


BLACK PAGE



LENOVO.PND  
NB system design section

TitleEC HISTORY

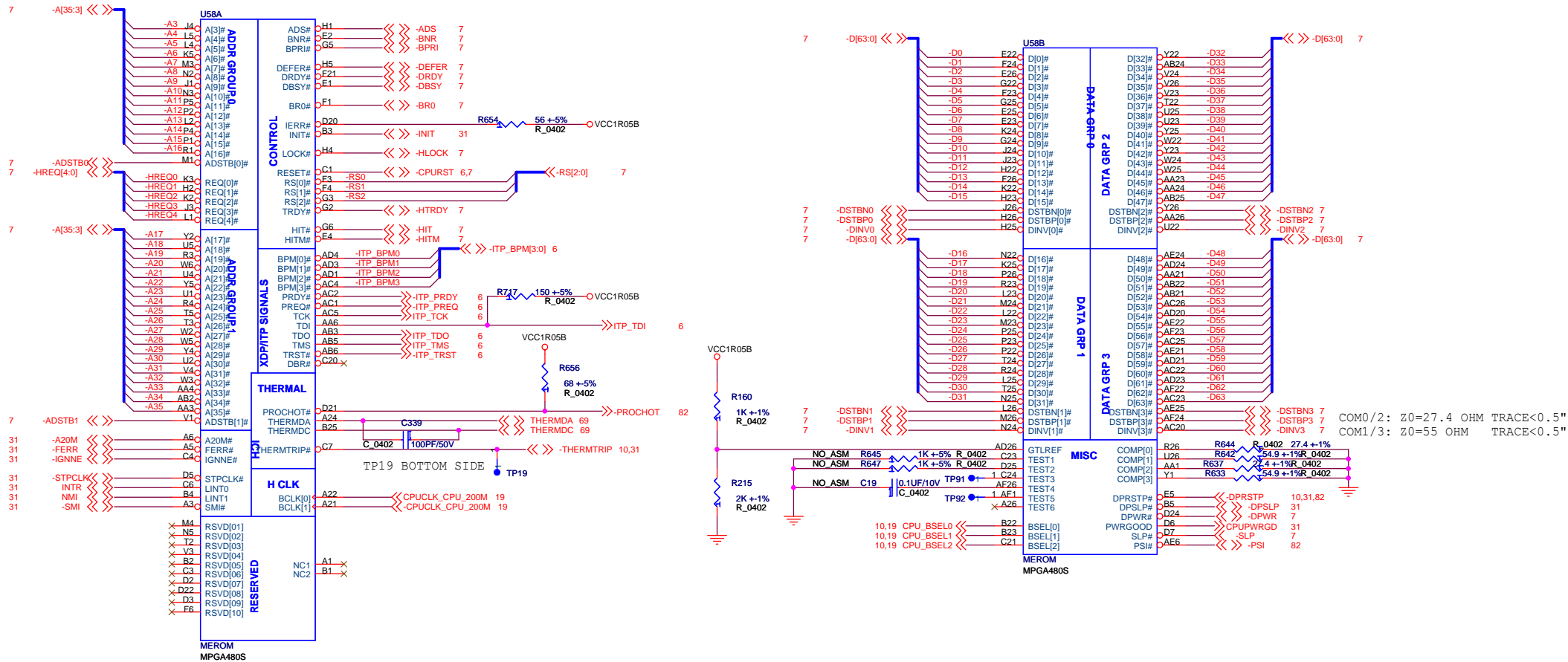
Size  
Custom

Document Number  
WAIKIKI

Rev  
s1.1

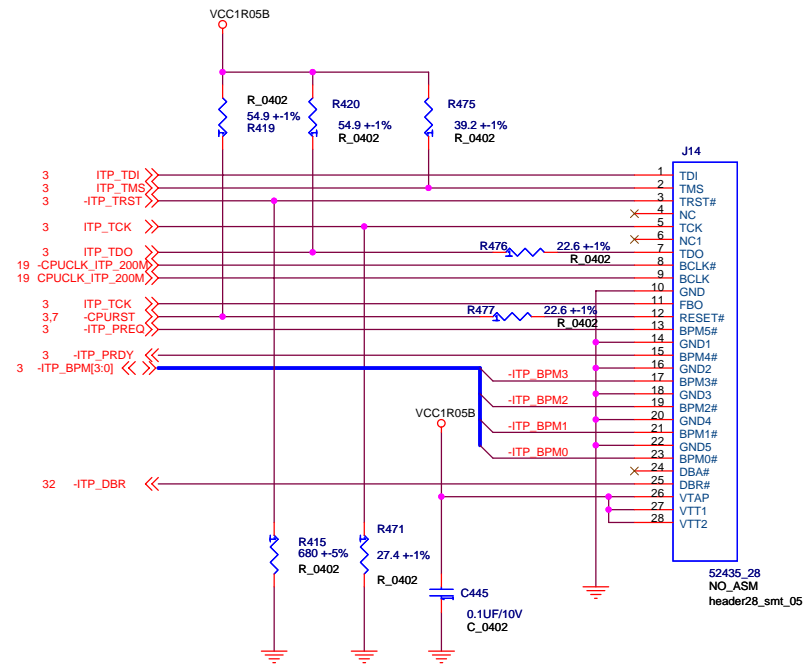
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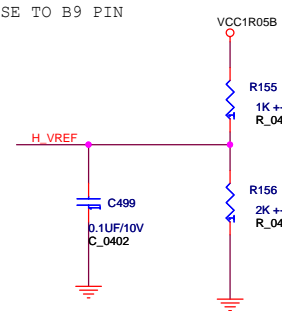
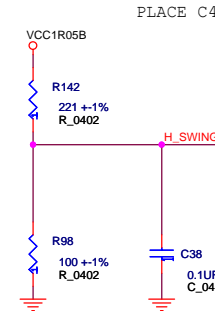
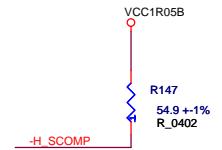
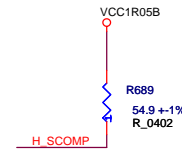
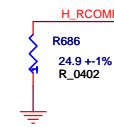
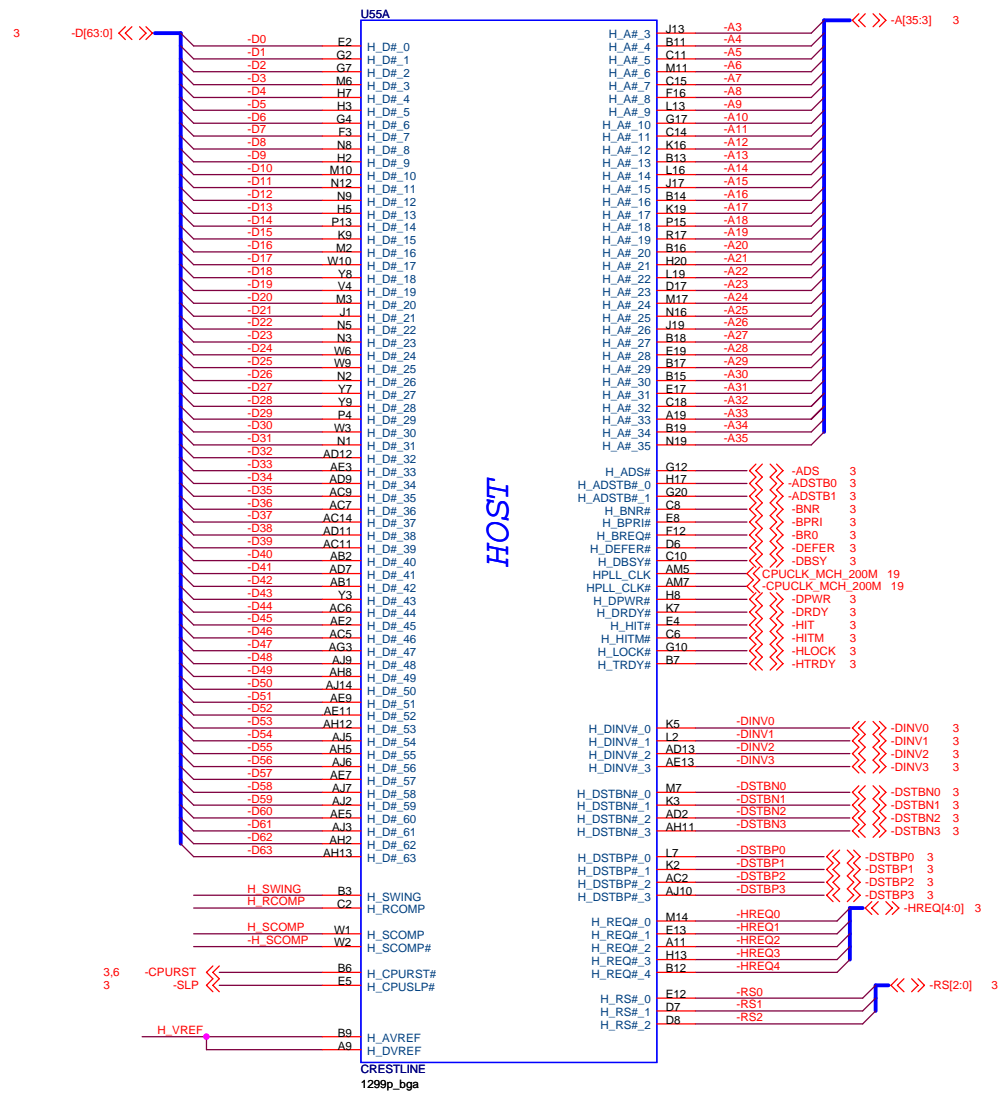


REF DES	FOR ITP700FLEX	FOR ITP-->XDP
J4	NO ASM---->ASM	NO ASM---->ASM
C445	ASM---->ASM	ASM---->ASM
R717	ASM(NO CHANGE)	ASM---->51 5% ASM
R476	ASM---->ASM	ASM---->ASM
R477	ASM---->ASM	ASM---->1K 5% ASM
R415	ASM(NO CHANGE)	ASM(NO CHANGE)
R419	ASM(NO CHANGE)	ASM-->NO ASM
R420	ASM(NO CHANGE)	ASM(NO CHANGE)
R471	ASM(NO CHANGE)	ASM---->51 5% ASM
R475	ASM(NO CHANGE)	ASM---->51 5% ASM

NOTE: C445,R476 AND R477 "ASM" BEFORE SIT, "NO\_ASM" FROM SIT.

- (\*1) TCK SIGNAL IS BRANCHED AT YONAH PIN
- (\*2) -CPURST SIGNAL IS BRANCHED AT CRESTLINE PIN

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Title ITP CONN			
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PLACE C499 CLOSE TO B9 PIN

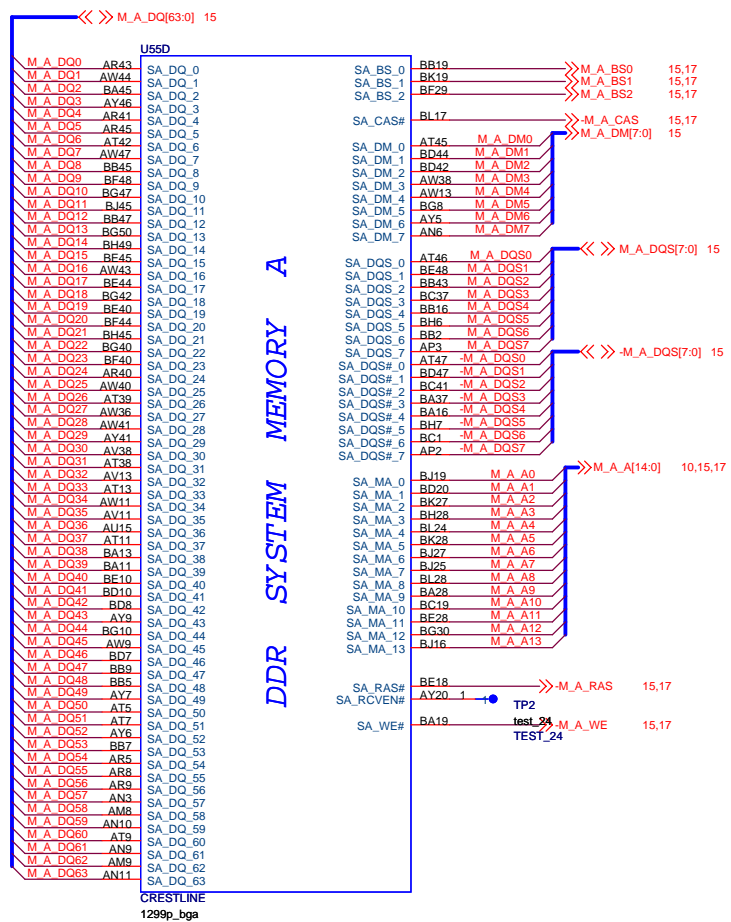
lenovo 联想 LENOVO.PND NB system design section

Title NB 965GM-1(HOST CFG)

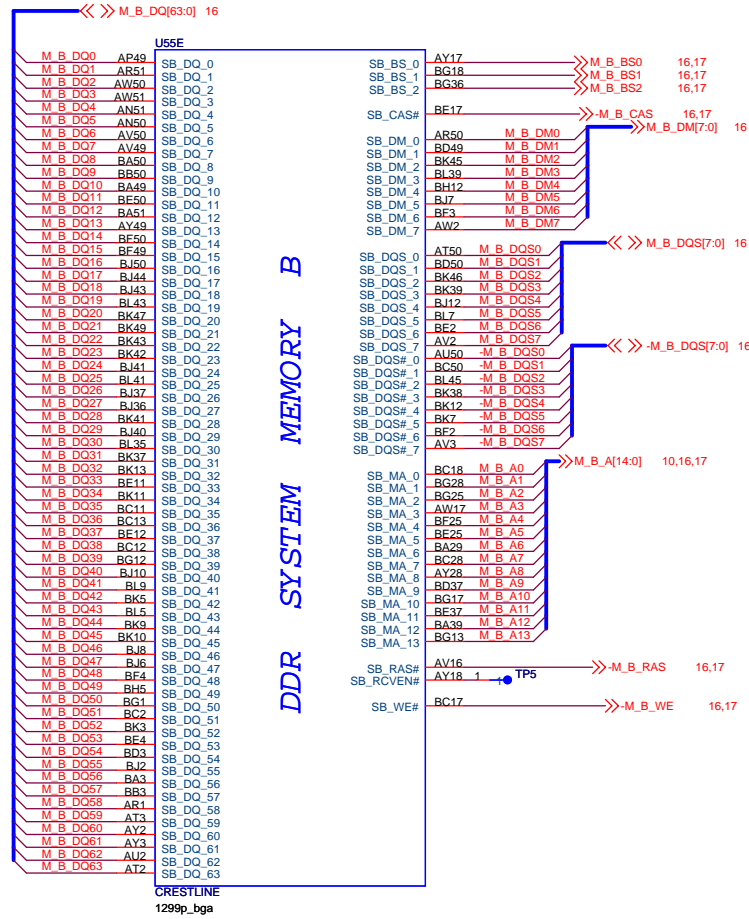
Size Custom Document Number WAIKIKI Rev s1.1

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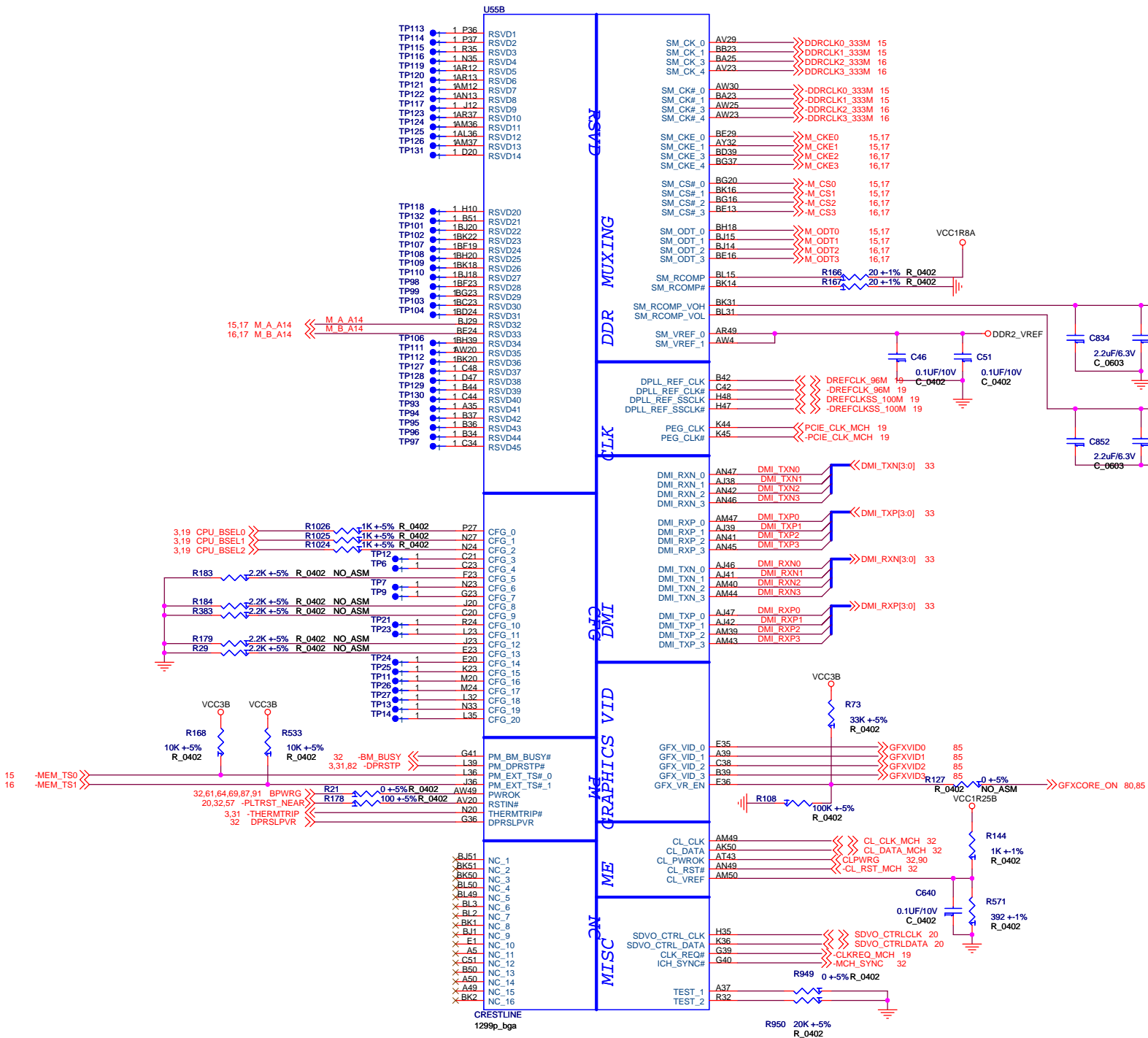
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Title NB 965 DDR2-B			
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	BSEL2	BSEL1	BSEL0
533M	0	0	1
667M	0	1	1
800M	0	1	0

CFG[17:3] INTERNAL PULLUP  
CFG[20:18] INTERNAL PULLDOWN

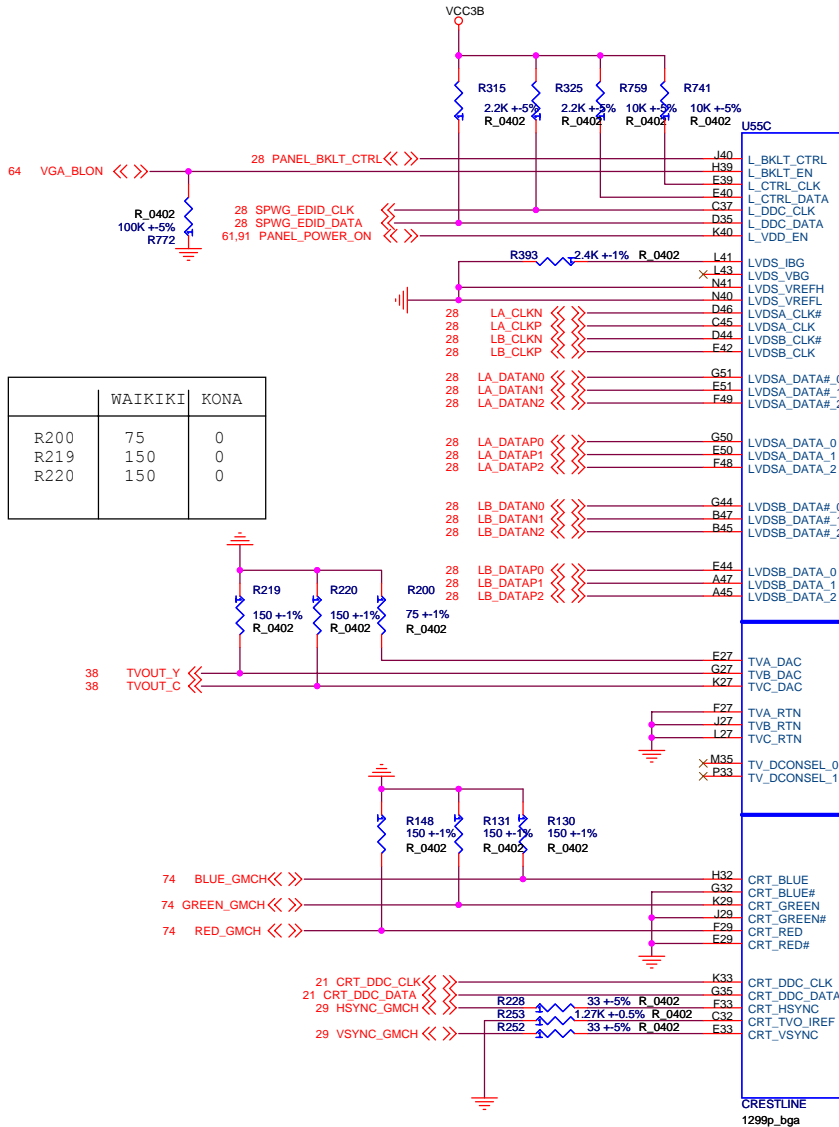
DMI	R183
	X2:ASM
	X4:NO_ASM
LOW POWER PCIE	R184
	NOMAL :ASM
LOW POWER PCIE :NO_ASM	
LANE REVERSEL	R383
	LANE REVERSEL:ASM
	NORMAL:NO_ASM



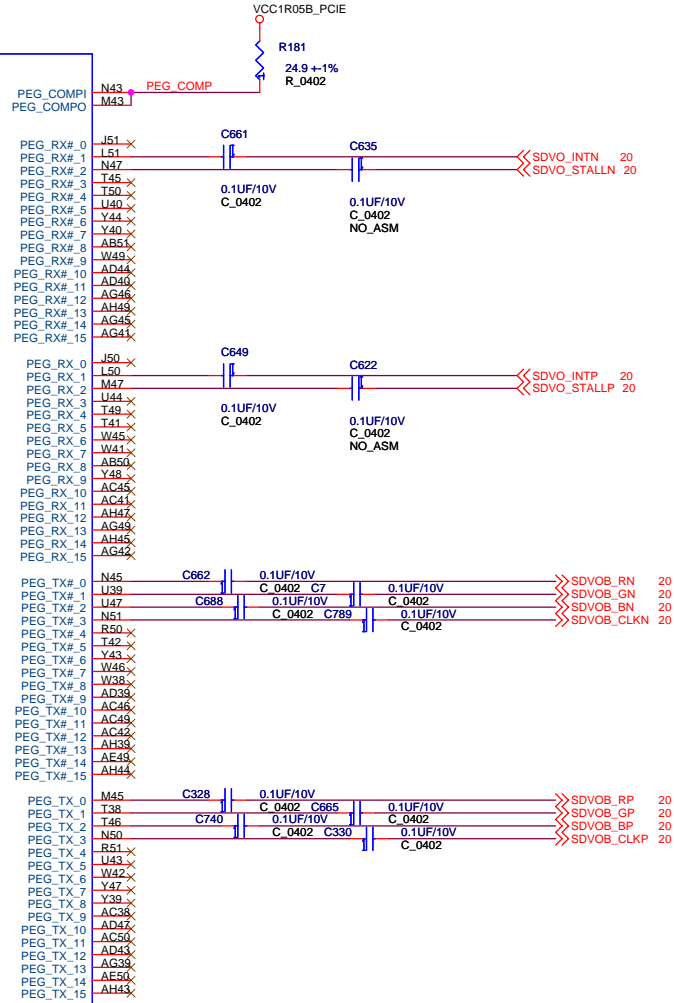
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Title	965 DMI
Size	Document Number
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PCI-EXPRESS GRAPHICS



	WAIKIKI	KONA
R200	75	0
R219	150	0
R220	150	0

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Title

GRAPHICS

Size

Document Number

Rev s1.1

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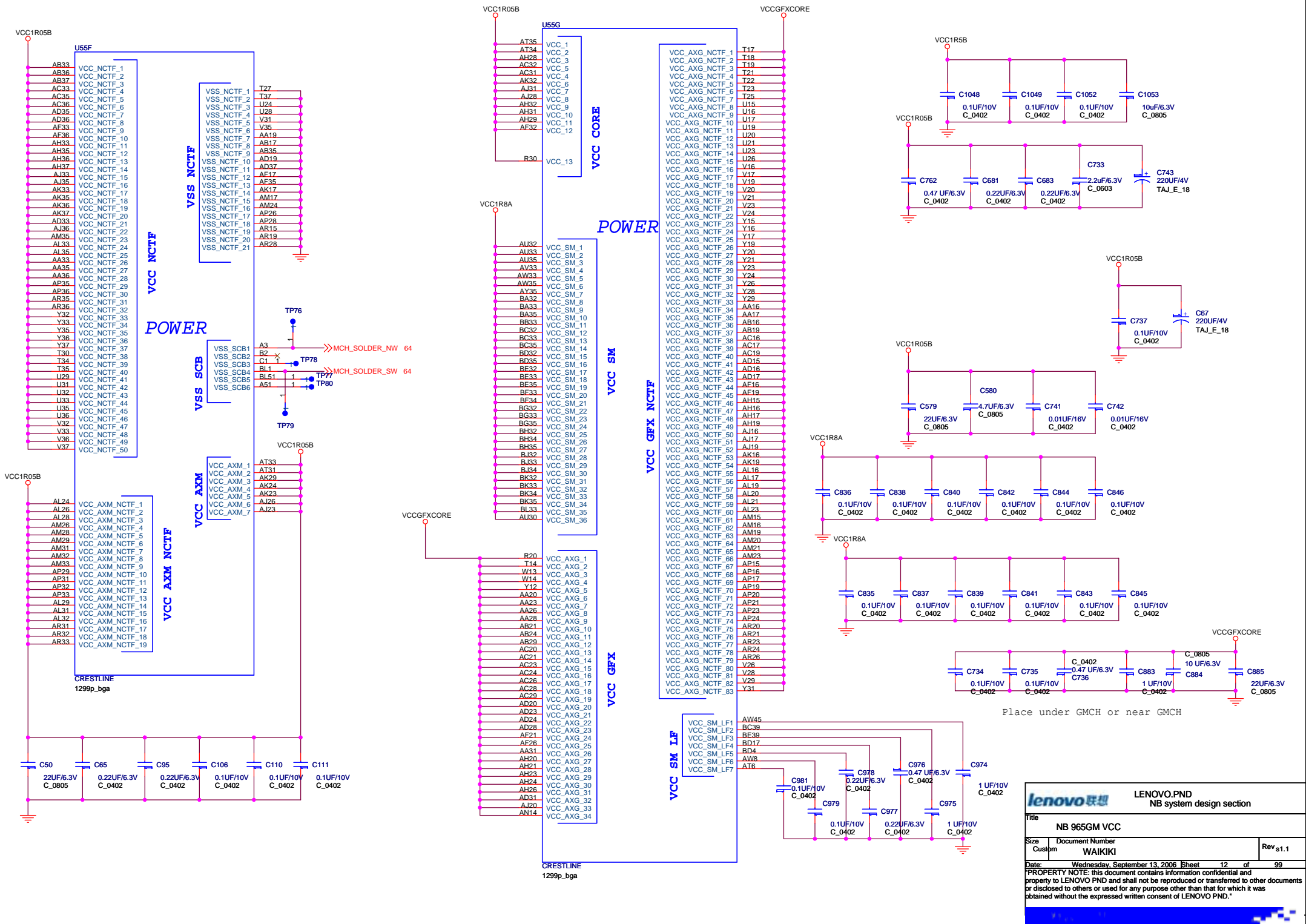
Sheet

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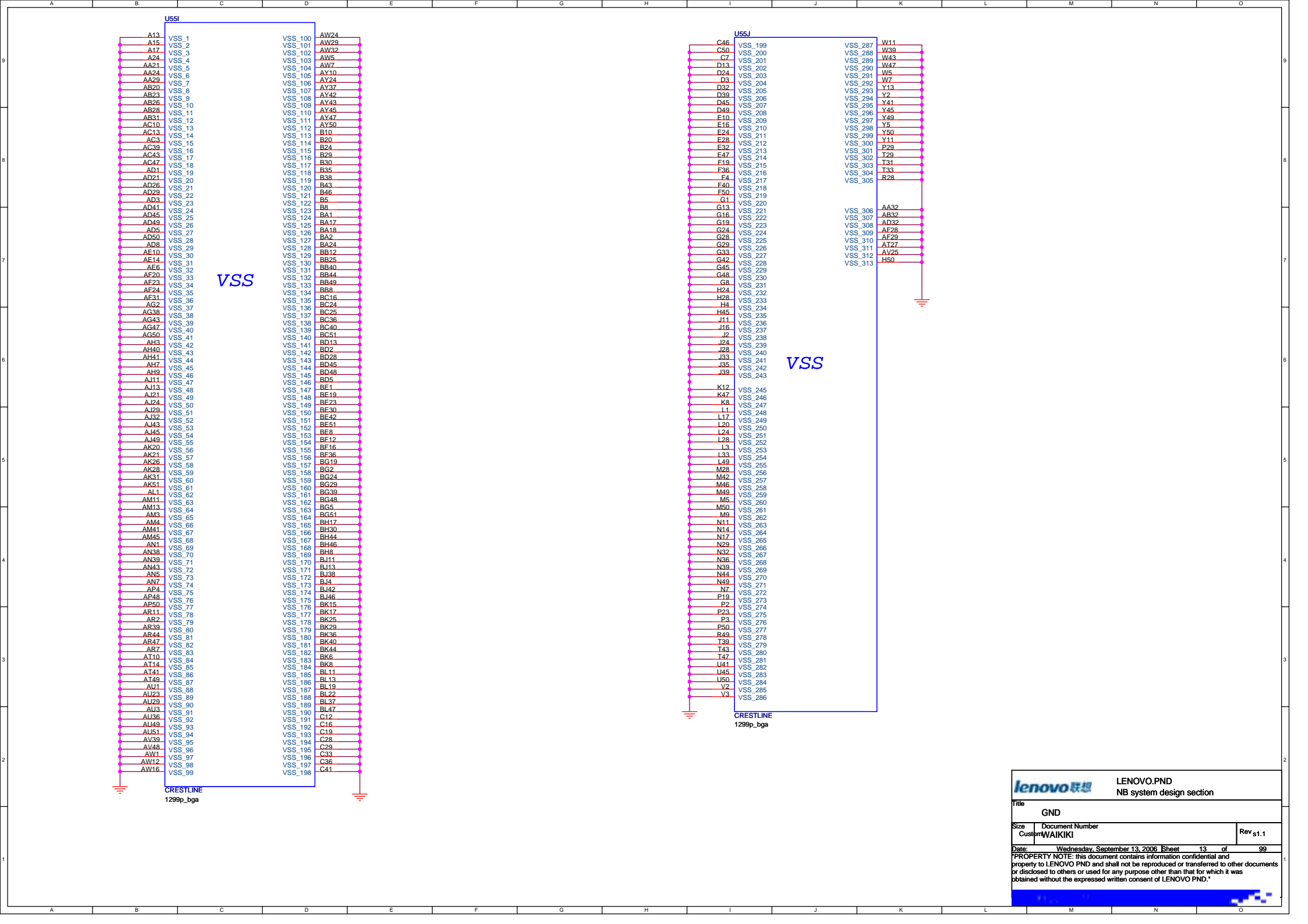
of

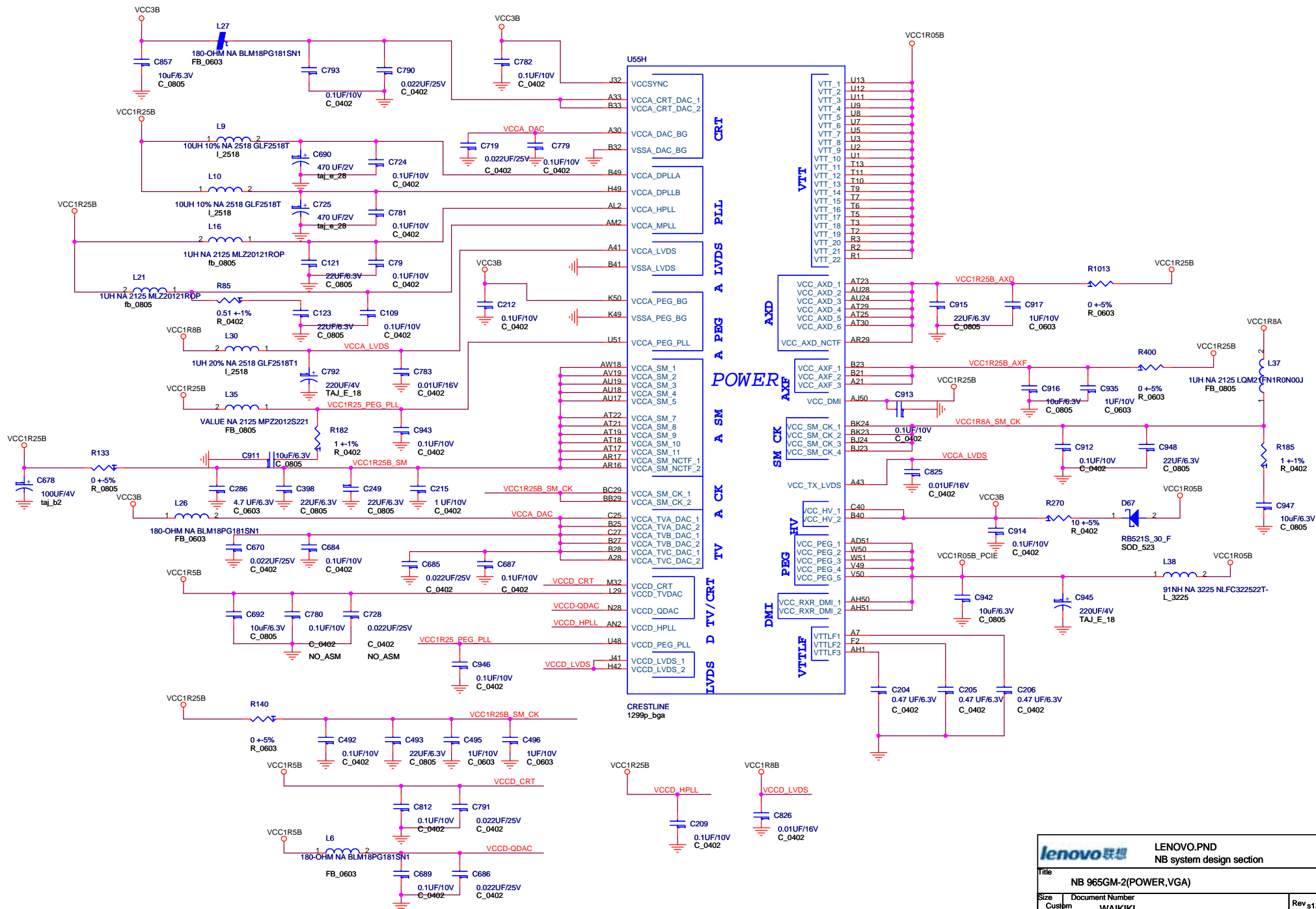
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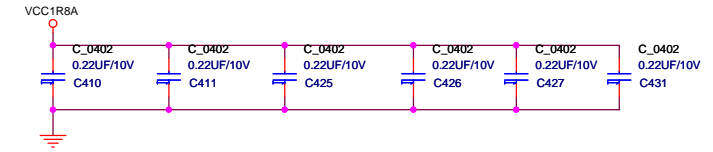
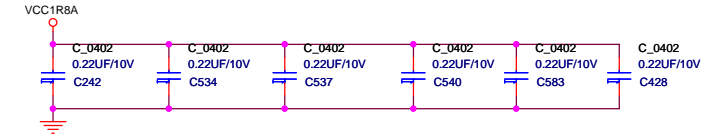
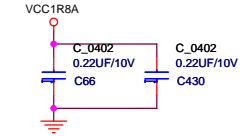
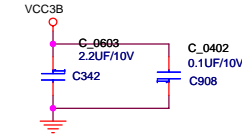
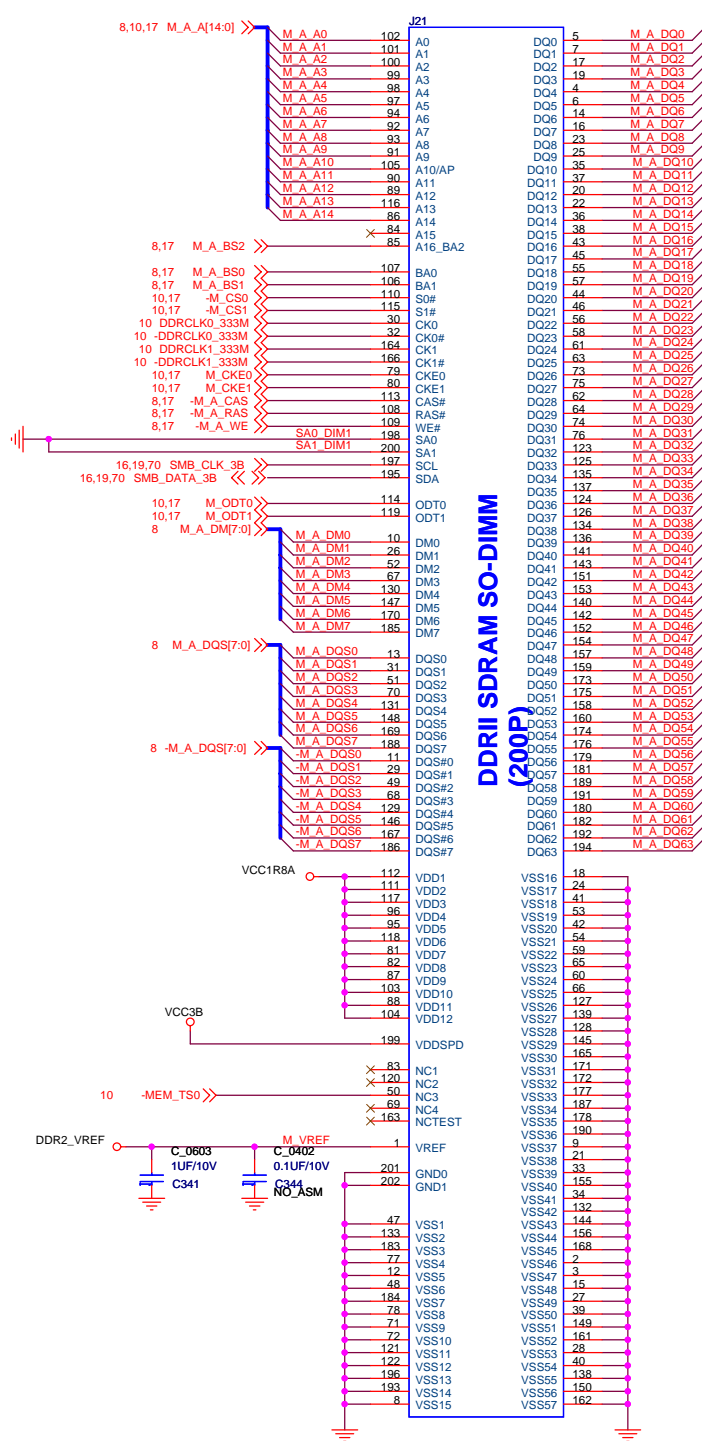


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NB 965GM VCC			
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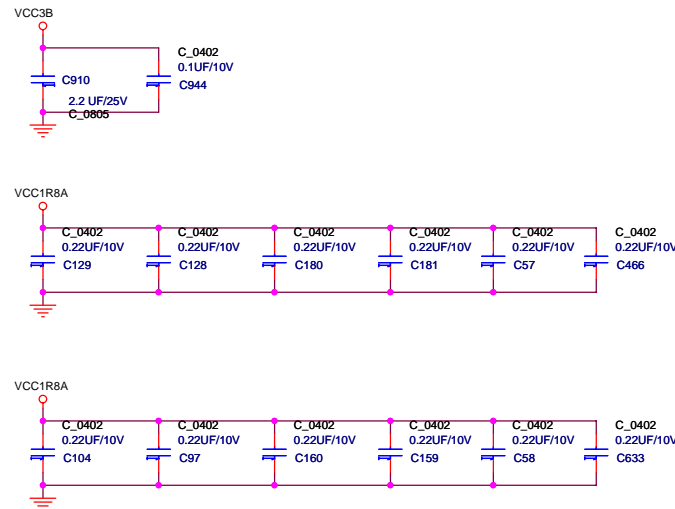







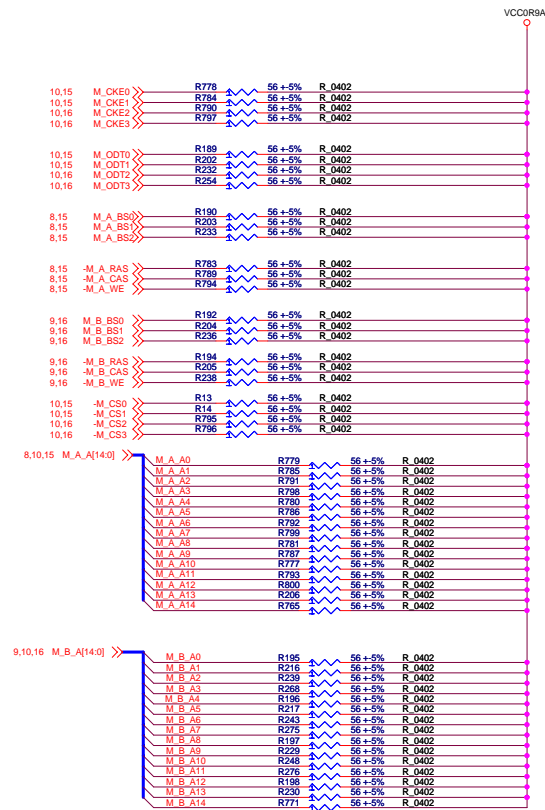
Add two more 0.22uf/10v for VCC1R8A at J21

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Title DDRII SO-DIMM			
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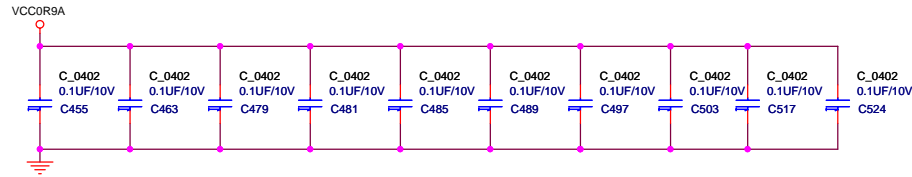
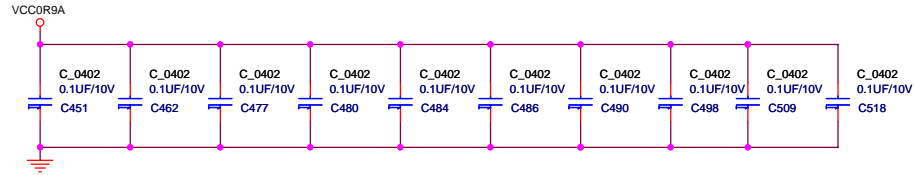



		<b>LENOVO.PND</b> <b>NB system design section</b>	
<b>Title</b> <b>DDR II 2</b>			
<b>Size</b>	<b>Document Number</b> <b>Custom WAIKIKI</b>		<b>Rev</b> s1.1
<b>Date:</b>	<b>Wednesday, September 13, 2006</b> <b>Sheet</b> <b>16</b> <b>of</b> <b>99</b>		
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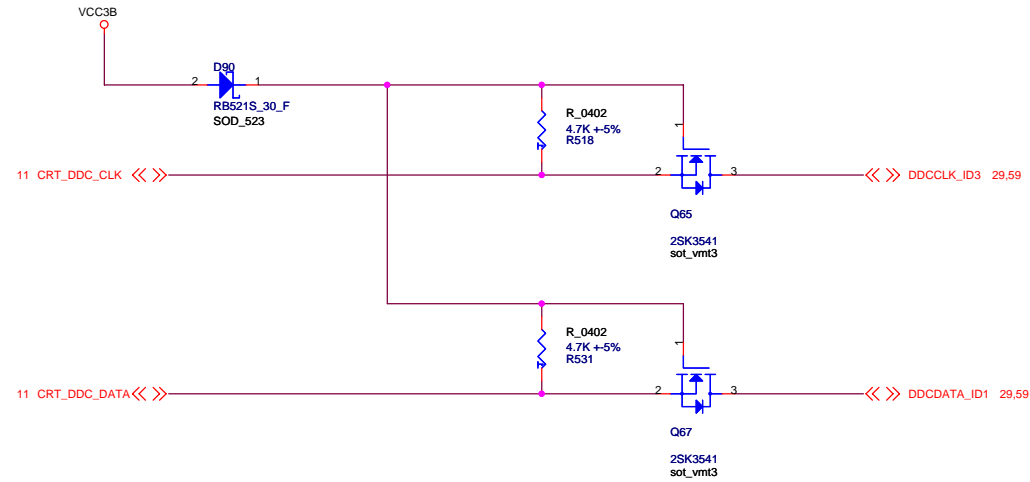
PLACE 1 CAP FOR EVERY 2 BITS TERMINATED TO VCC0R9B.



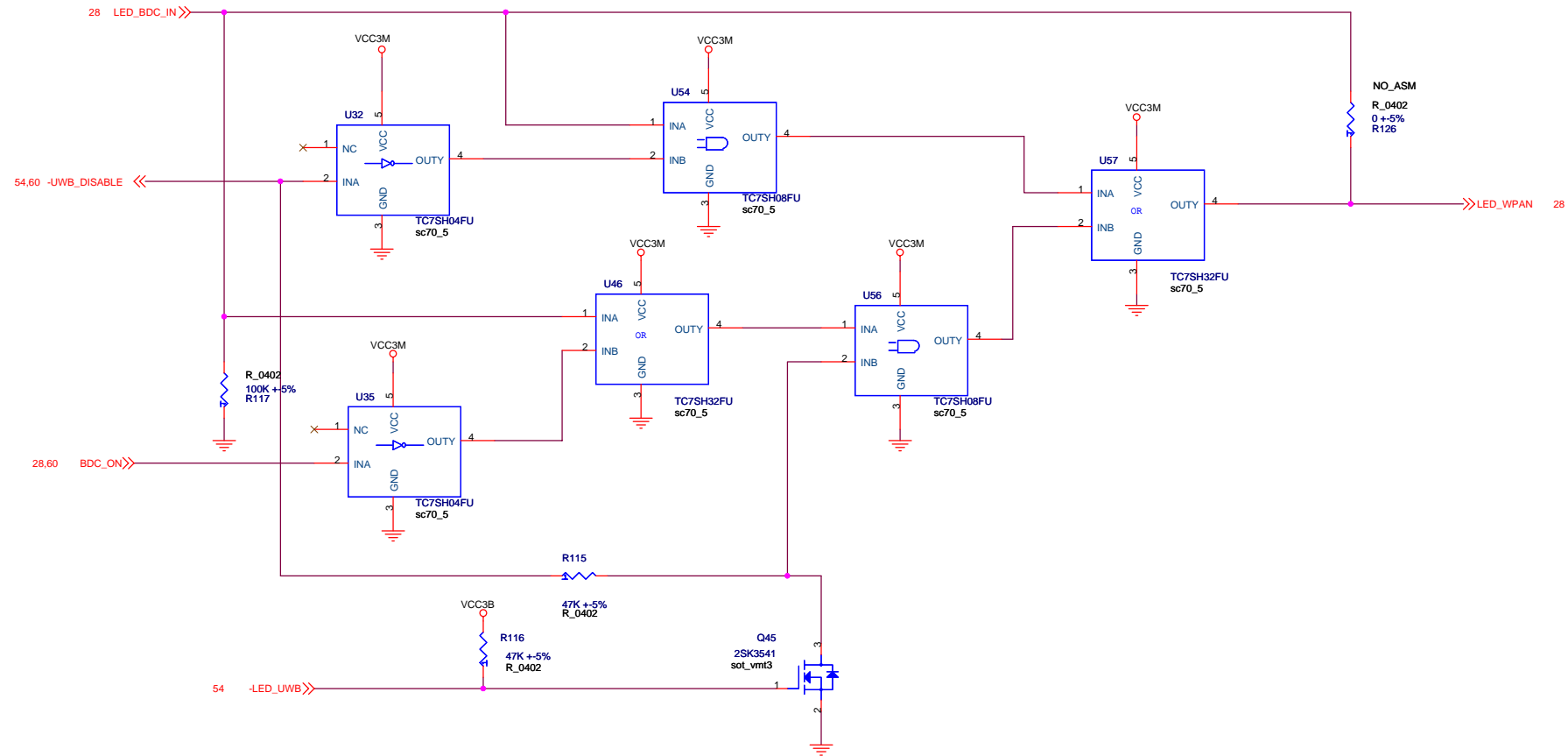
		LENOVO.PND NB system design section	
Title DDR2 DECOUPLING			
Size Custom	Document Number WAIKIKI		Rev s1.1
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Title CRT DDC CLK/DATA			
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Title WPAN LED CONTROL			
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	M	N	O

Notic:Change LED to right-angle type.

XD\_D[7..0],-XD\_RE,-XD\_WE should be the same trace length  
-XD\_RE(SD\_CLK and MS\_CLK) need GND guard



 <b>LENOVO.PND</b>		
NB system design section		
<b>Title</b> DAV3_INTEG_SIV_W		
<b>Size</b> Customer	<b>Document Number</b> WAIKIKI	<b>Rev</b> s1.1
<b>Date:</b> Wednesday, September 13, 2006 Sheet 23 of 99		
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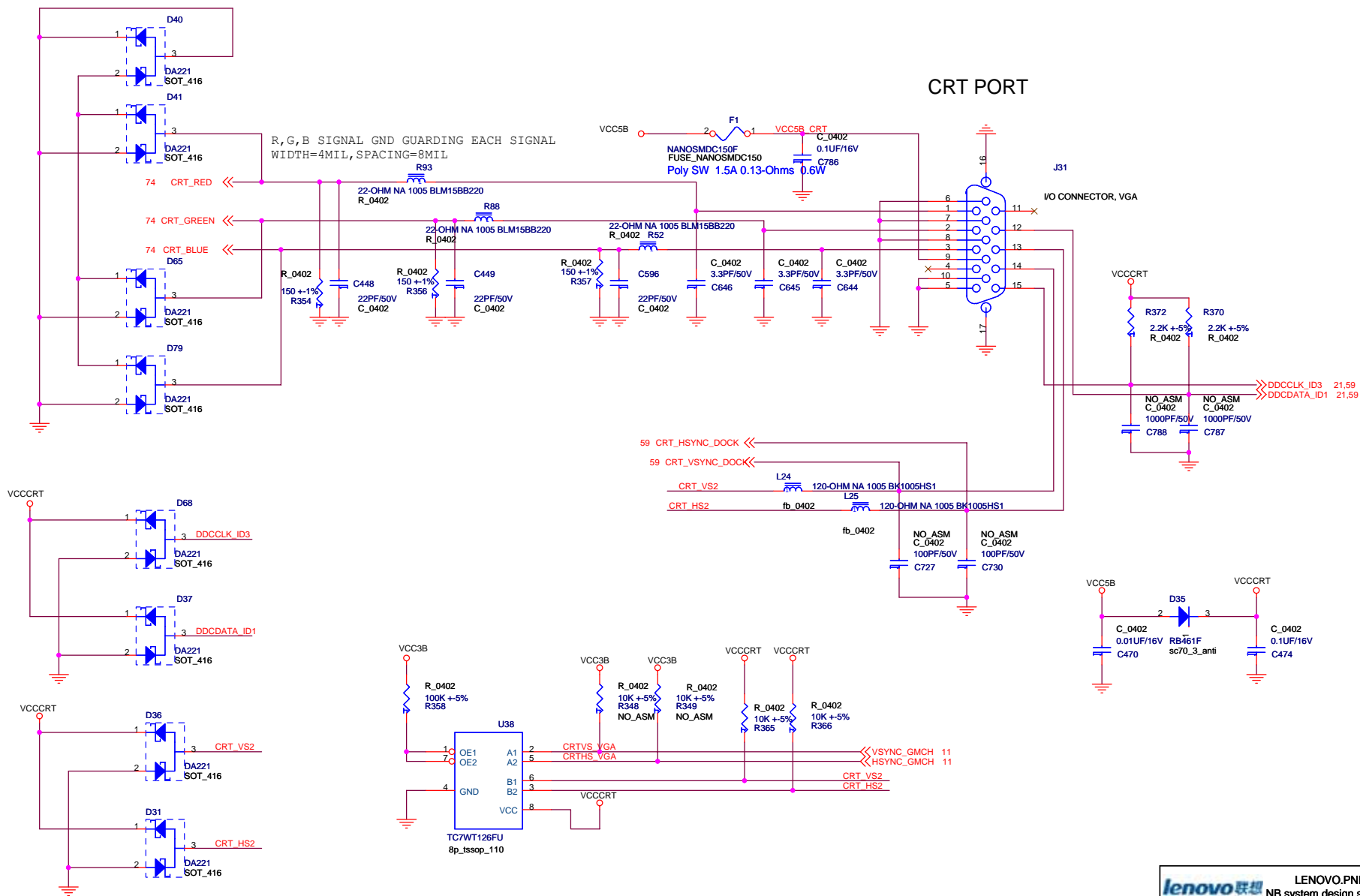
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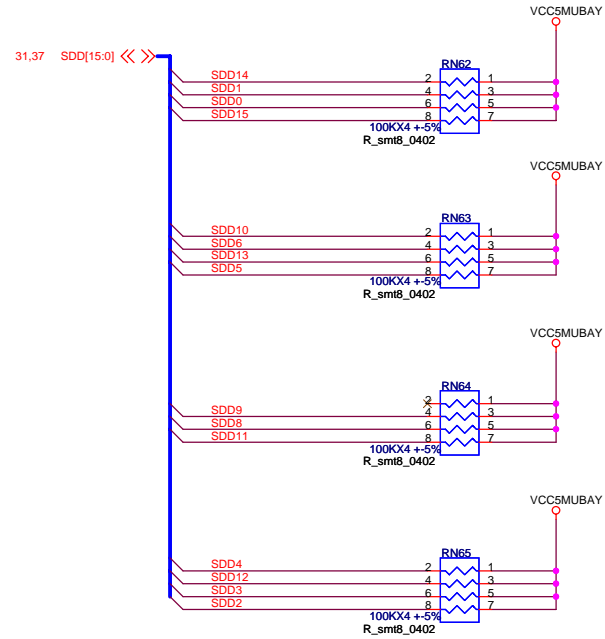
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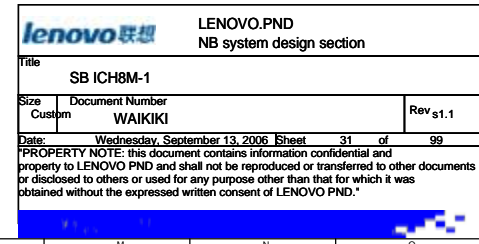


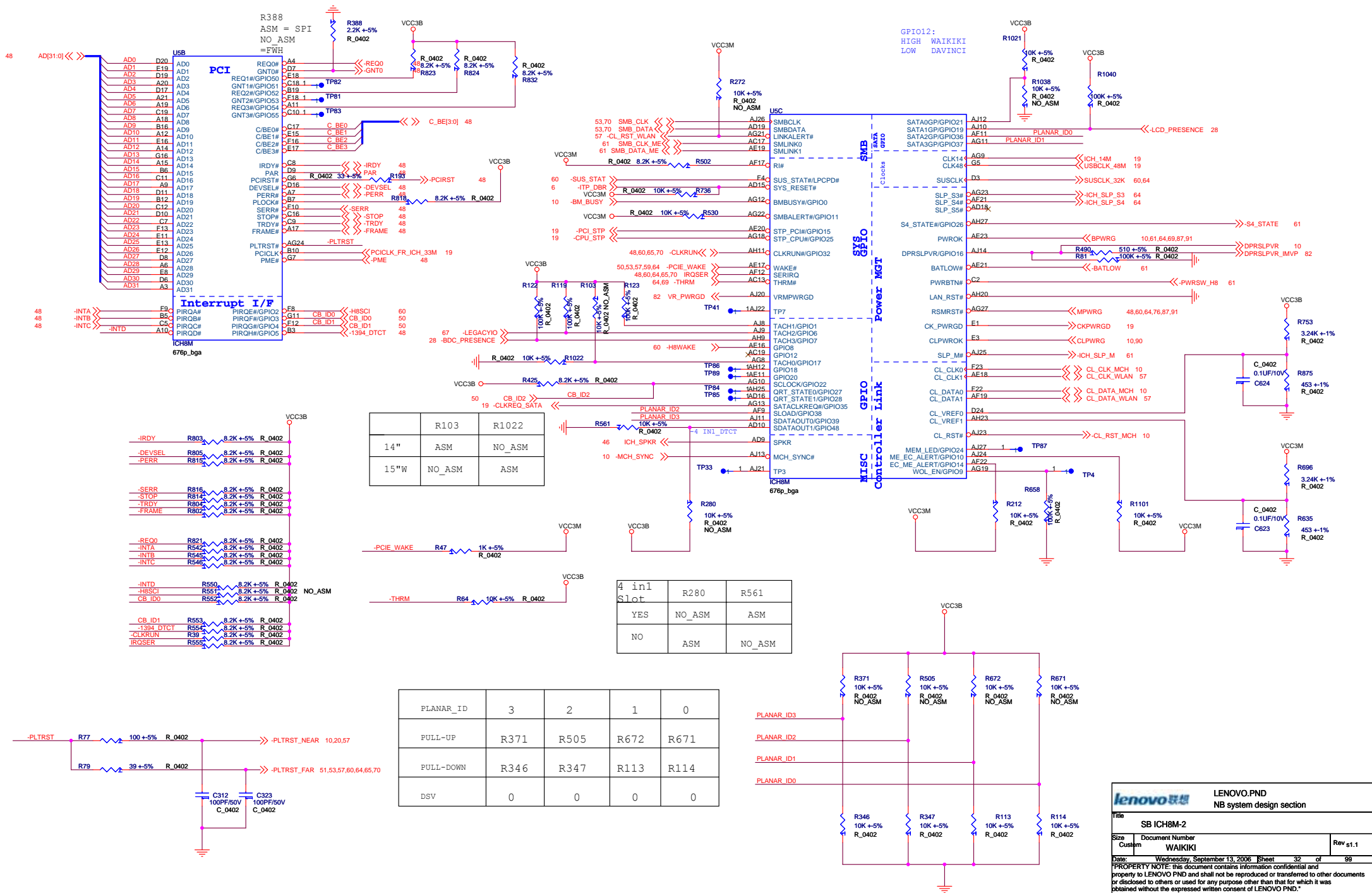
<b>lenovo</b> LENOVO.PND NB system design section		
File		
VGA_CONN		
Size	Document Number	Rev
Cust	WAIKIKI	s1.1
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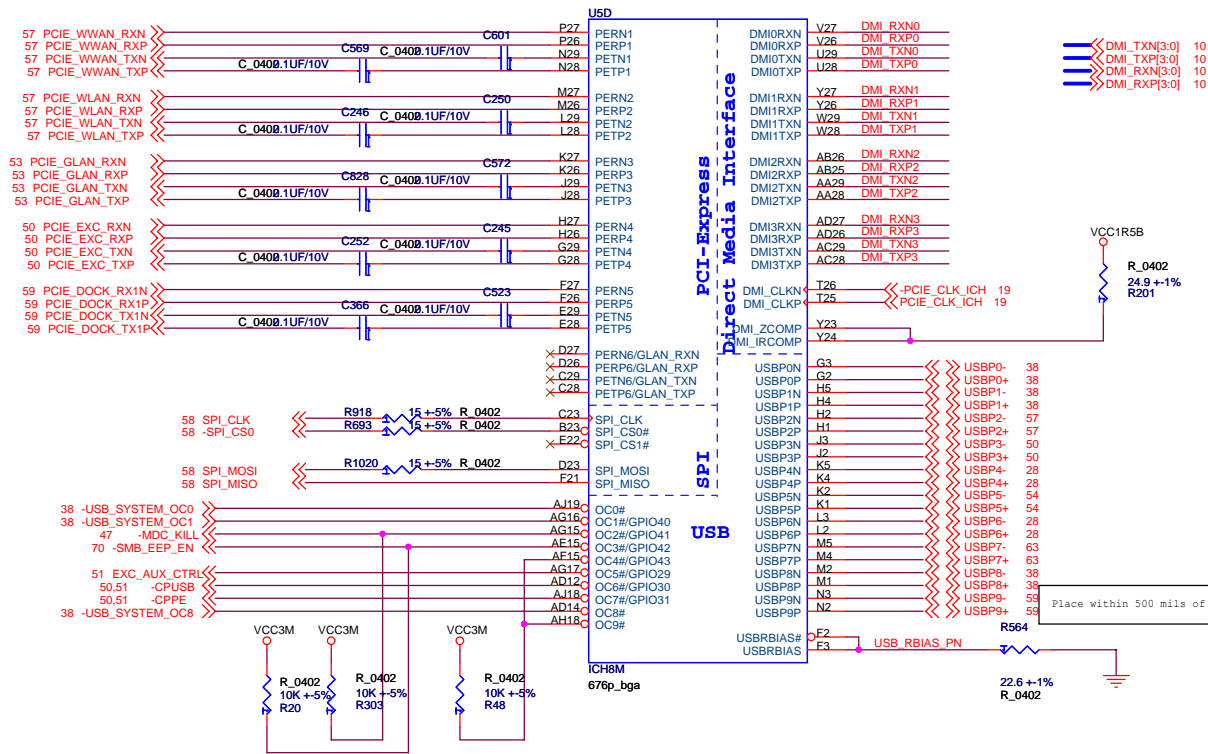
		LENOVO.PND NB system design section	
Title PATA I/F PU			
Size Custom	Document Number WAIKIKI		Rev s1.1
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Layout Note:  
Place all series  
resistors 0.6 to 2.6  
inches from the  
ICH







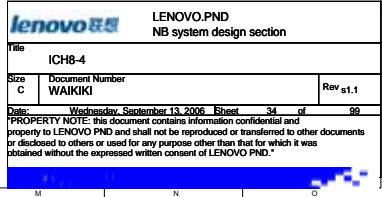


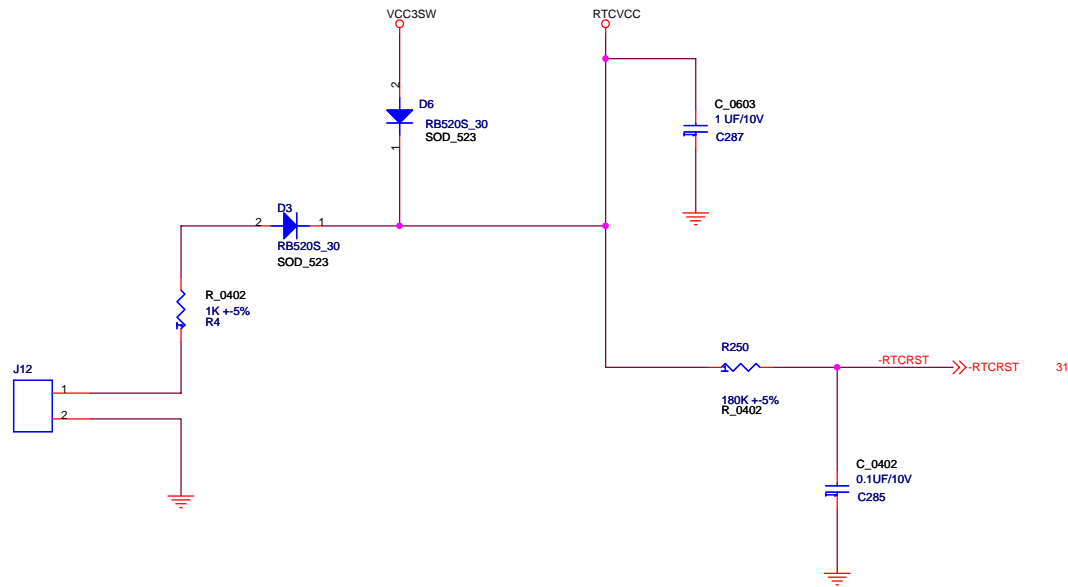
- 0 : USB Port
- 1 : USB Port
- 2 : PCIE MINICARD
- 3 : TO PCI EXPRESS CARD
- 4 : TO CAMERA (LCD CONN)
- 5 : TO PCIE MICROCARD
- 6 : TO BLUETOOTH
- 7 : TO FINGER PRINT
- 8 : USB PORT3
- 9 : TO DOCKING USB HUB

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NB system design section

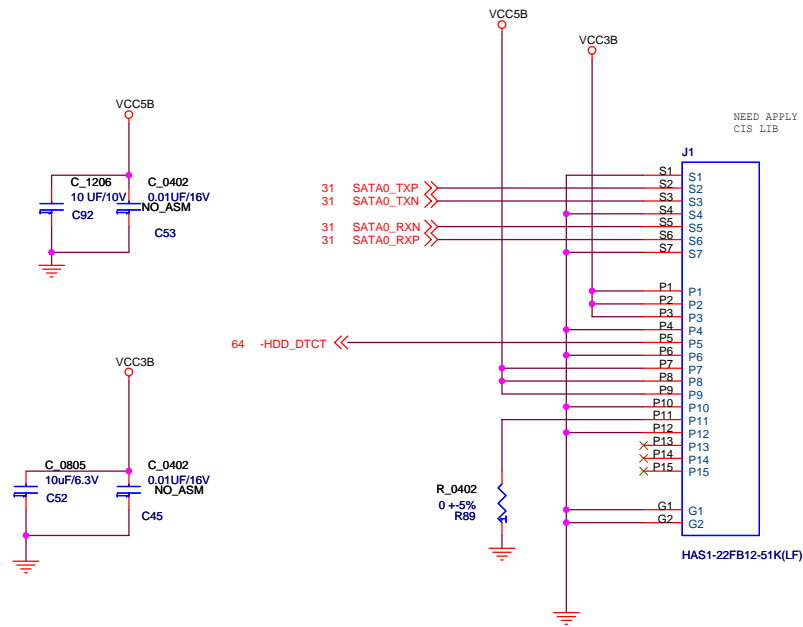
Title ICH8-3  
Size Custom WAIKIKI Document Number Rev s1.1  
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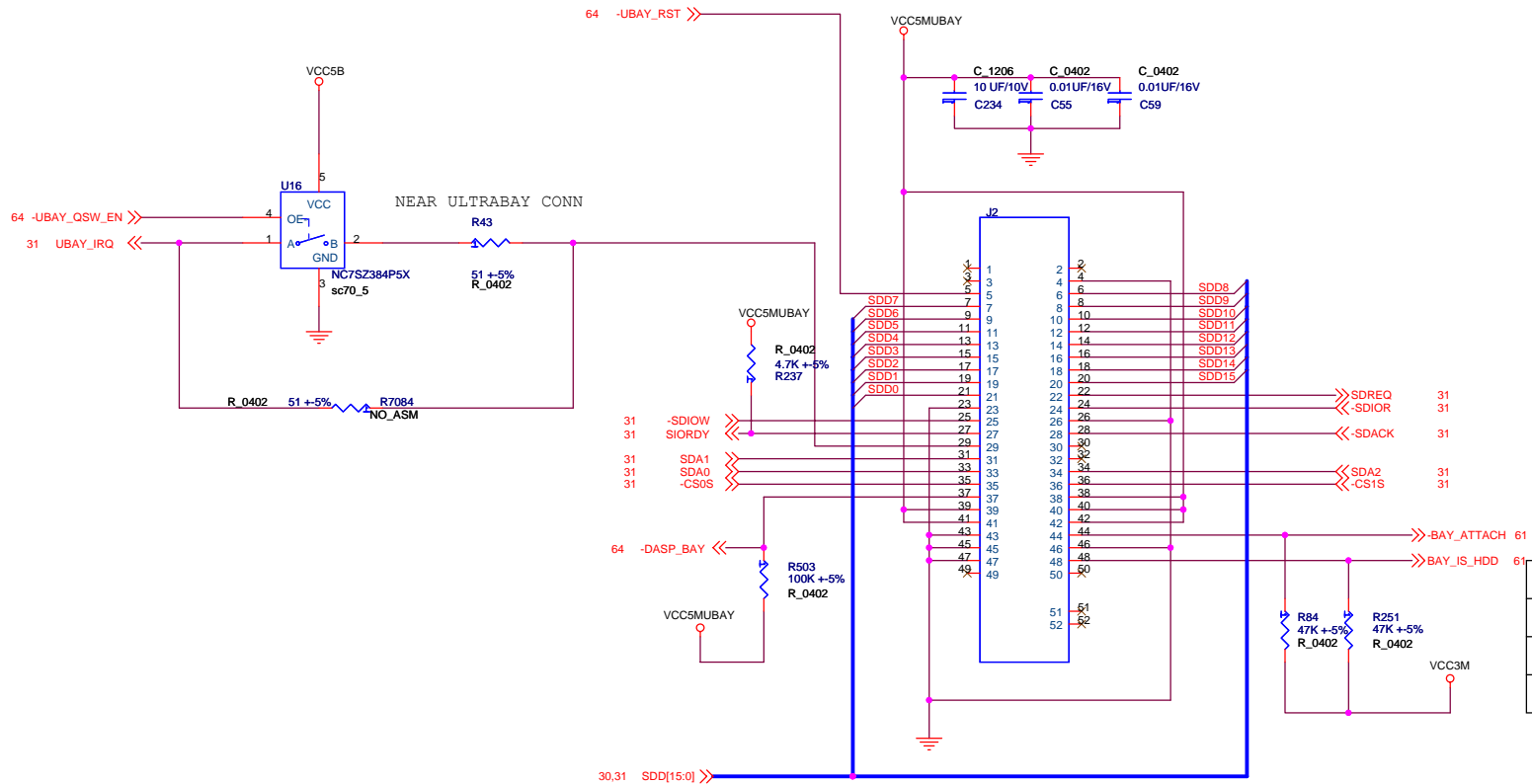




		LENOVO.PND NB system design section	
Title FWH,RTC BATTERY			
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	SWAP BAY	FIXED BAY
U16	ASM	NO_ASM
R43	ASM	NO_ASM
R7084	NO_ASM	ASM

	-BAY_ATTACH	BAY_IS_HDD
NO DEVICE	H	DON'T CARE
OPTICAL	L	L
HDD	L	H

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NB system design section

Title  
BAY I/F CONN

Size  
Customer

Document Number  
WAIKIKI

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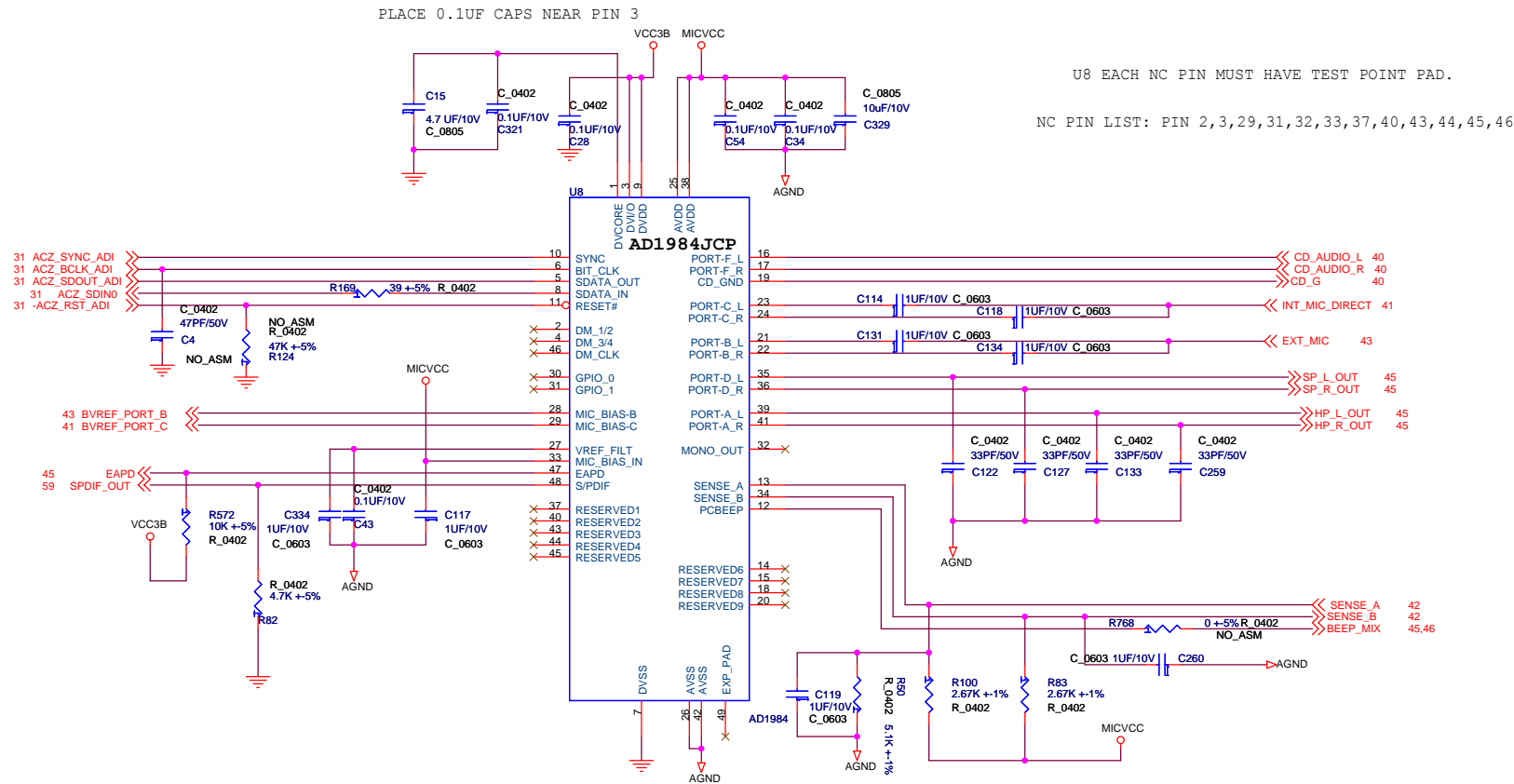
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Wednesday, September 13, 2006

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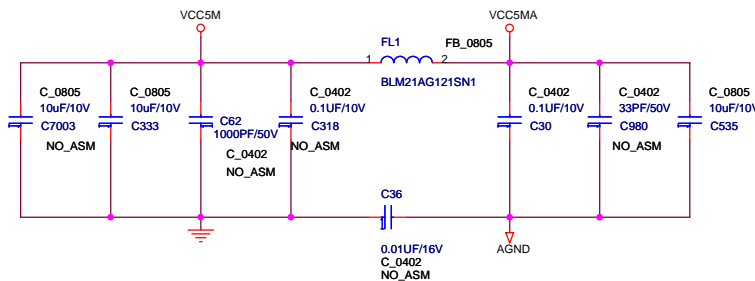
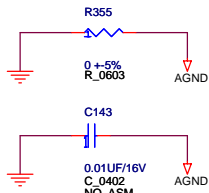
of  
99

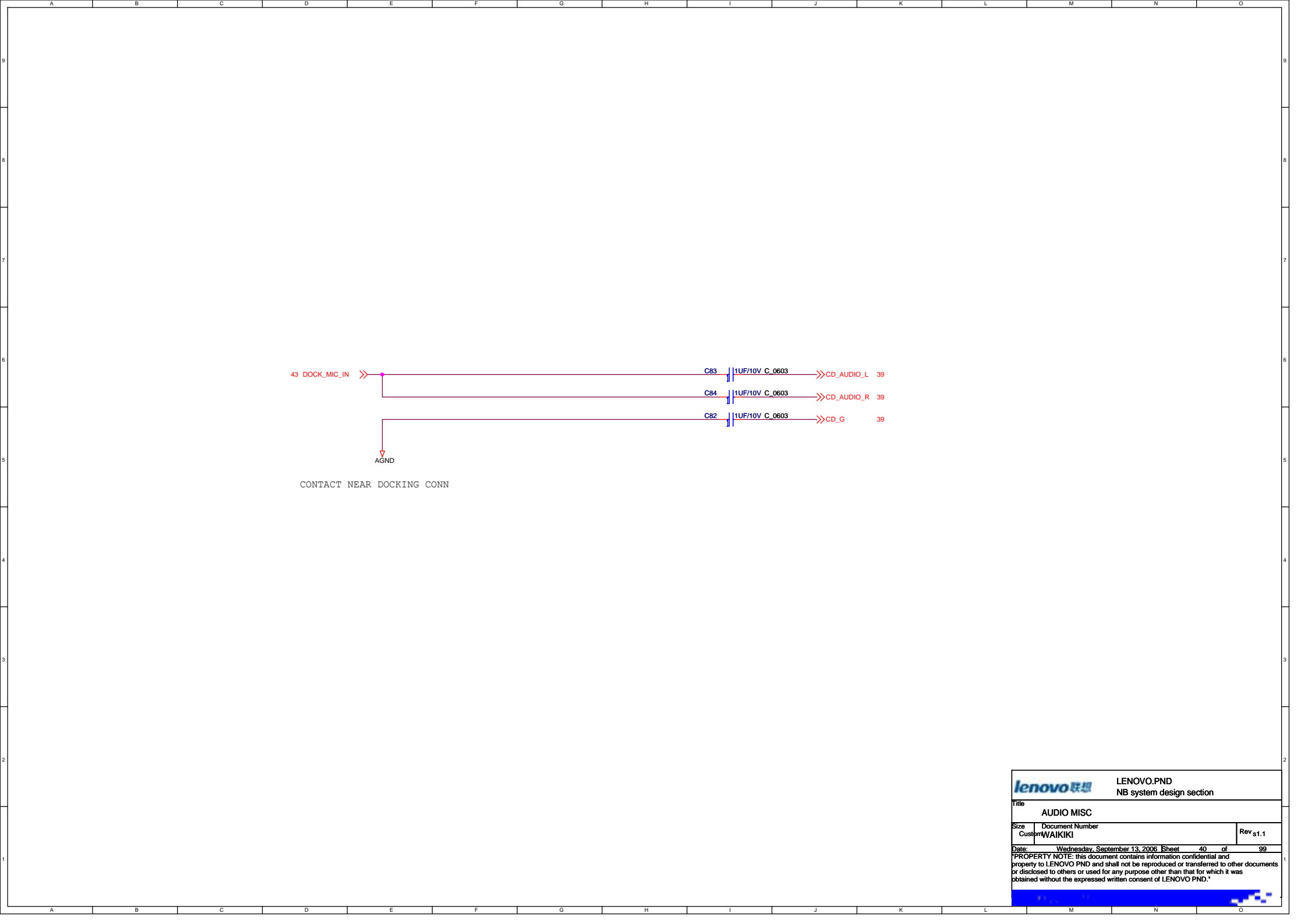
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LENOVO.PND  
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Title

AUDIO MISC

Size  
Custom

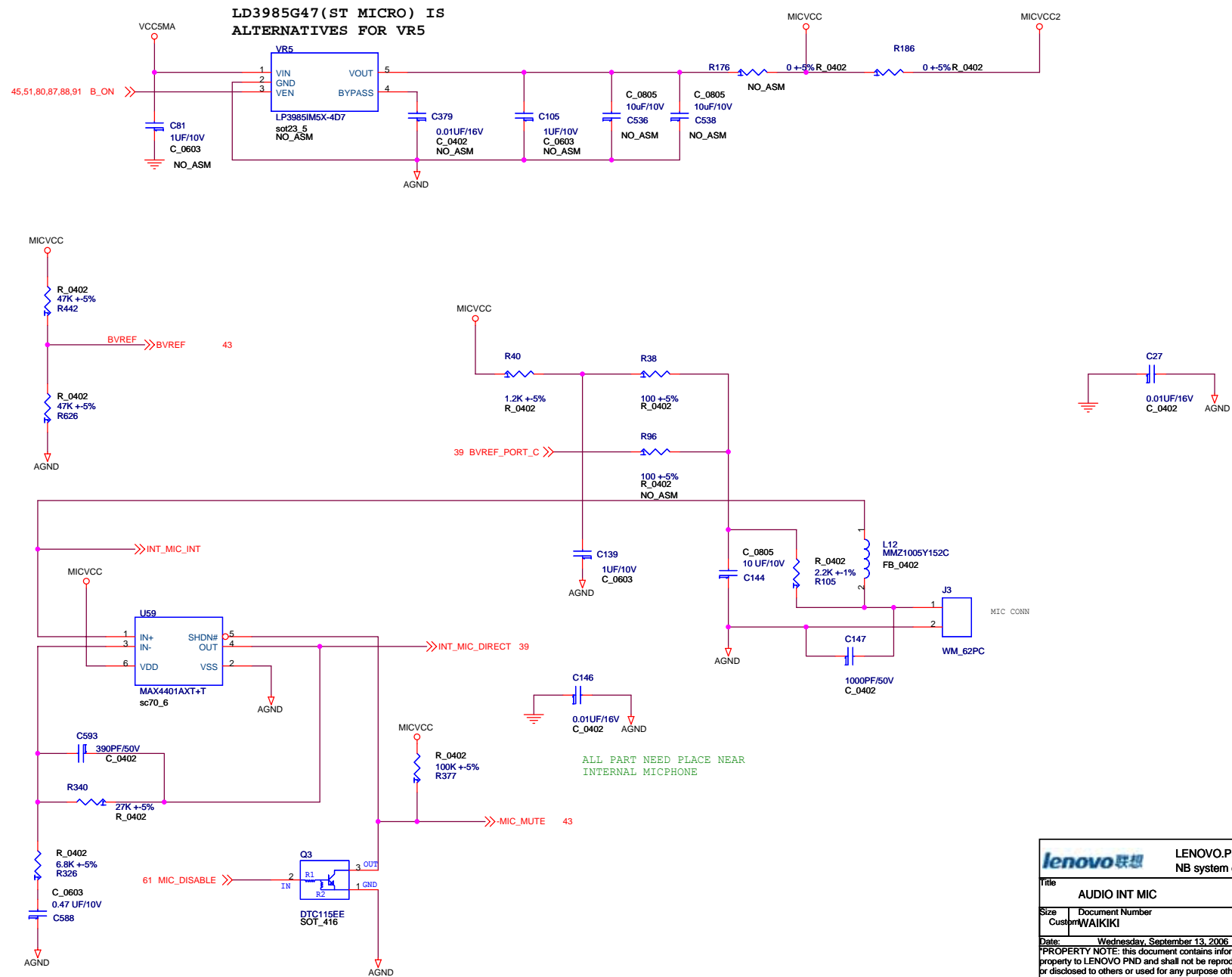
Document Number  
WAIKIKI

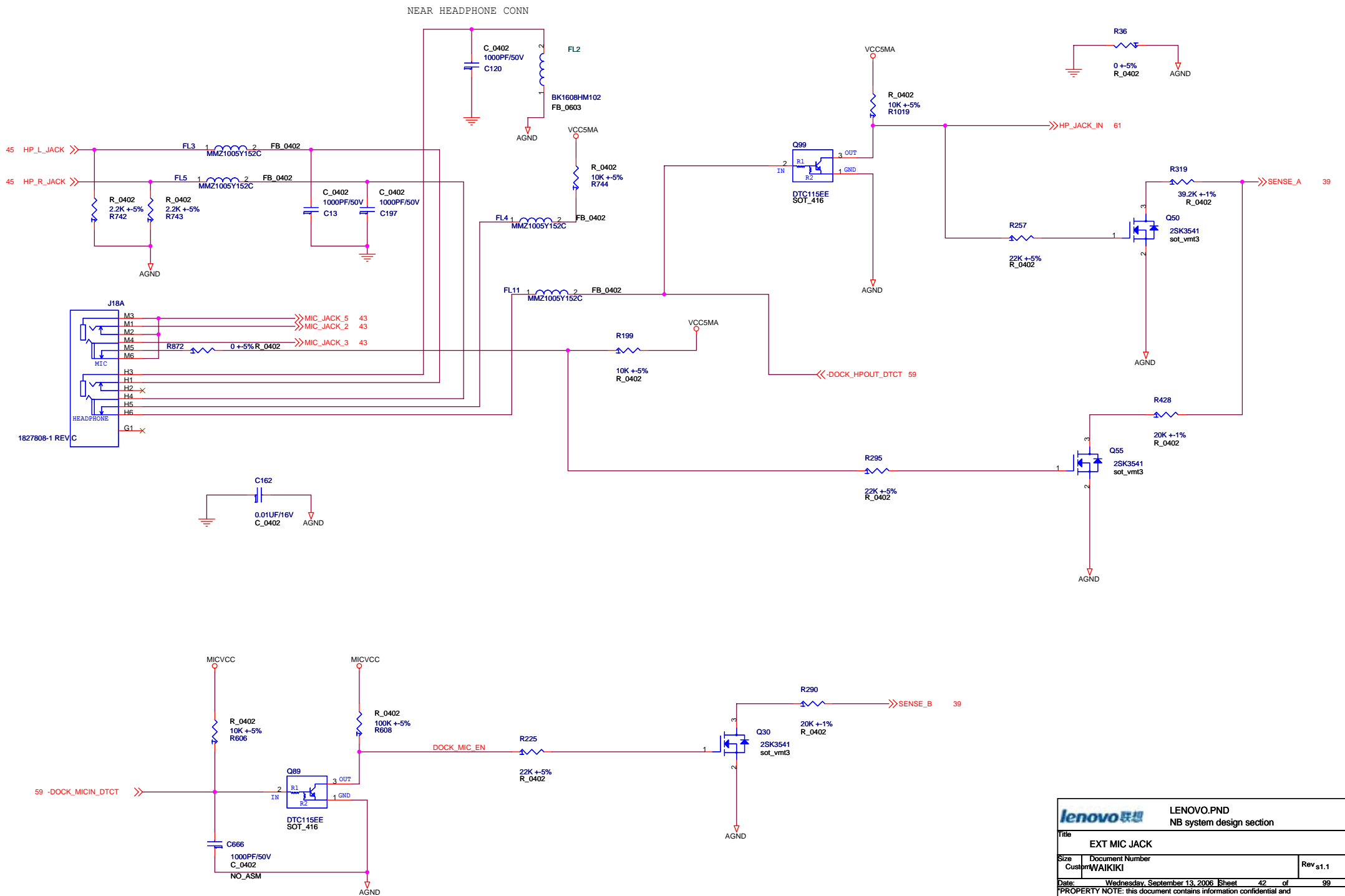
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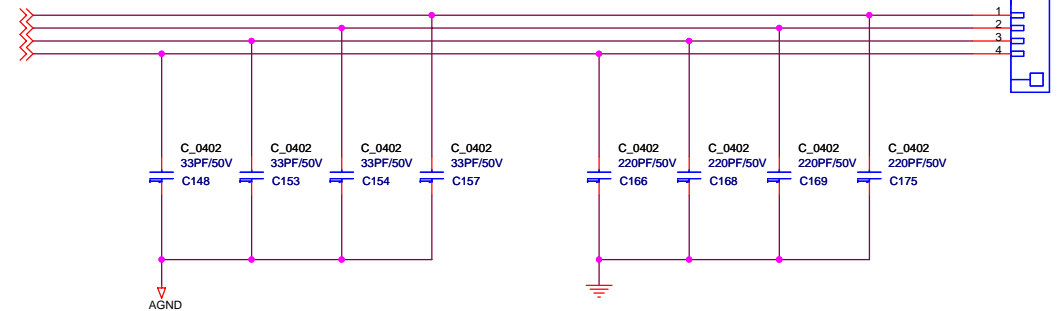




		<b>LENOVO.PND</b> NB system design section	
<b>Title</b> EXT MIC JACK			
<b>Size</b> Custom	<b>Document Number</b> WAIKIKI		<b>Rev</b> s1.1
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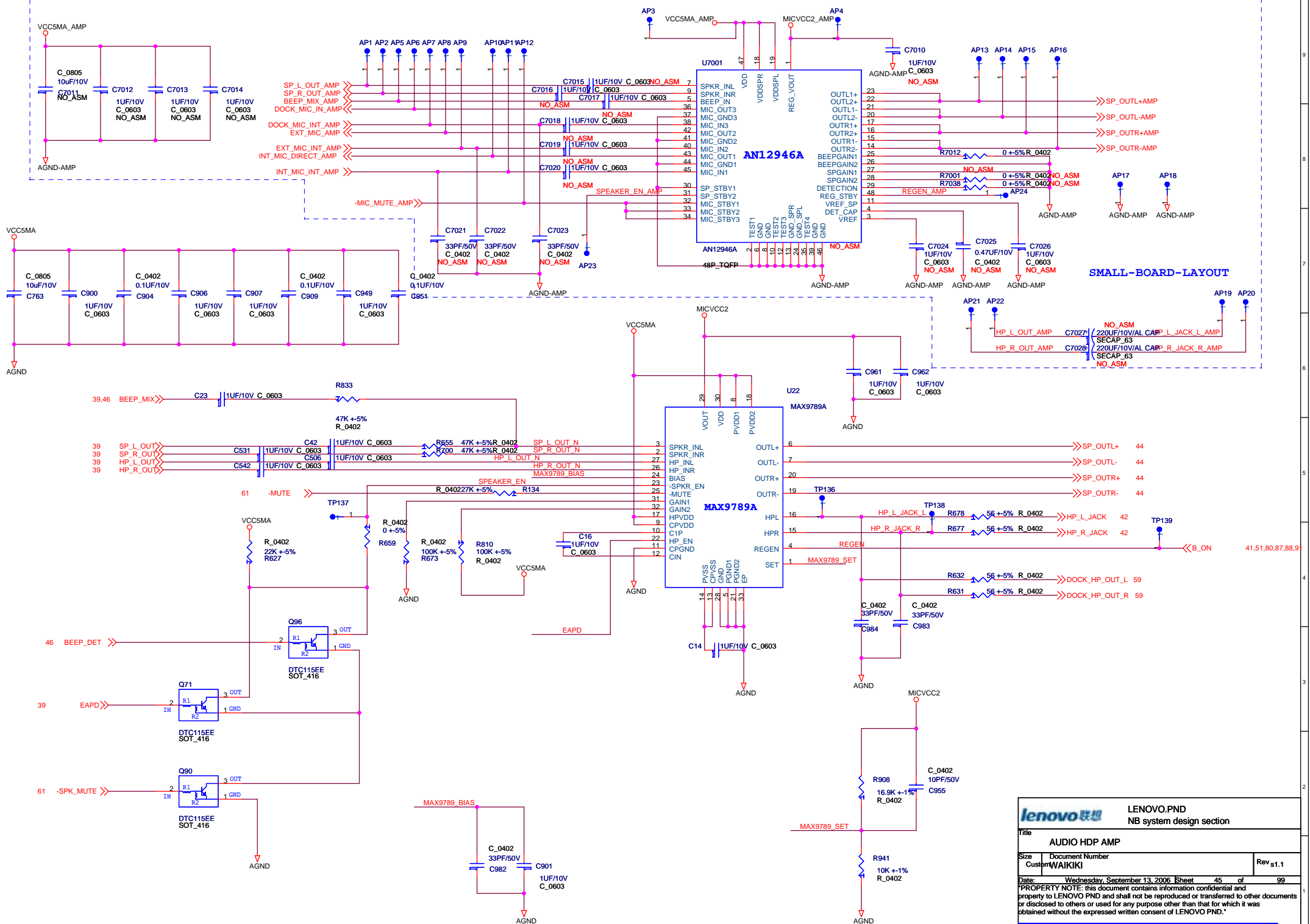
45 SP\_OUTR-  
45 SP\_OUTR+  
45 SP\_OUTL-  
45 SP\_OUTL+

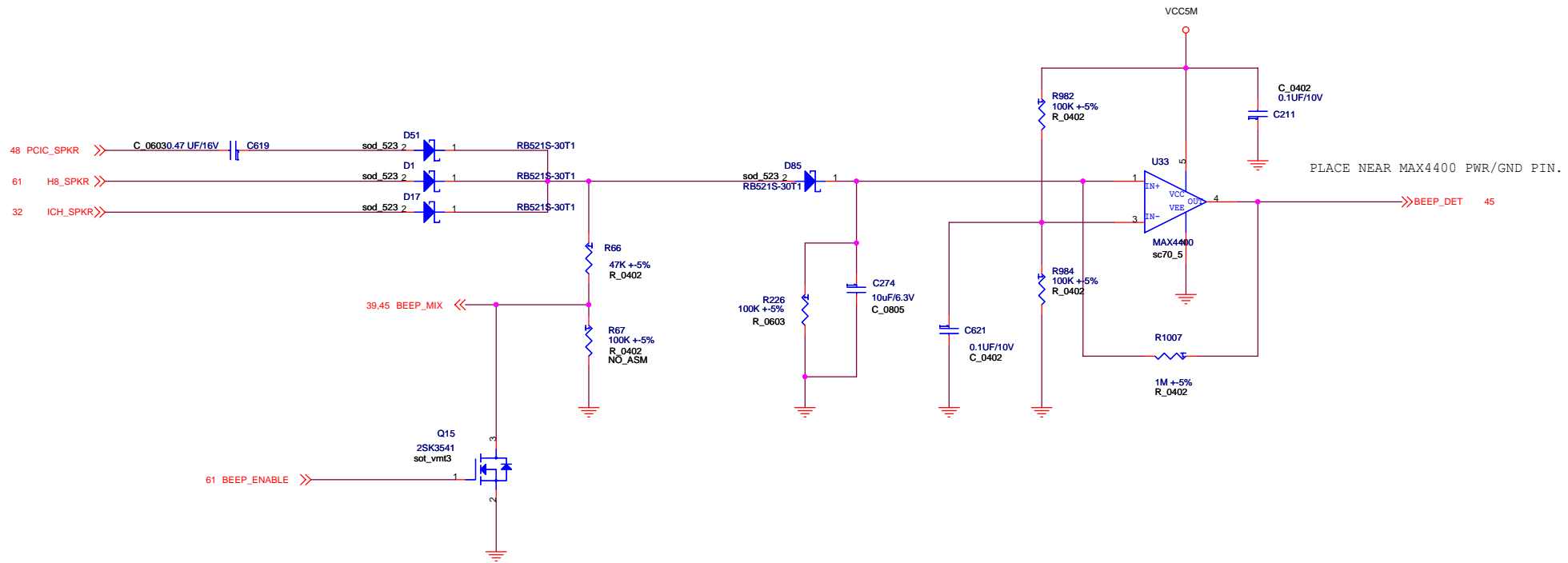


PLACE NEAR SPEAKER AMPLIFIER

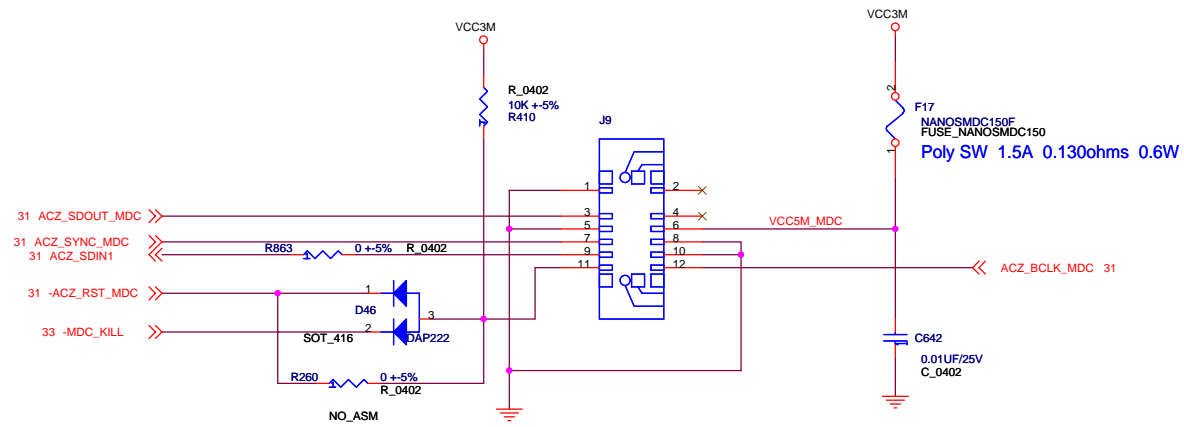
PLACE NEAR SPEAKER CONNECTOR

		LENOVO.PND NB system design section	
Title AUDIO SPEAKER			
Size Custom	Document Number WAIKIKI		Rev s1.1
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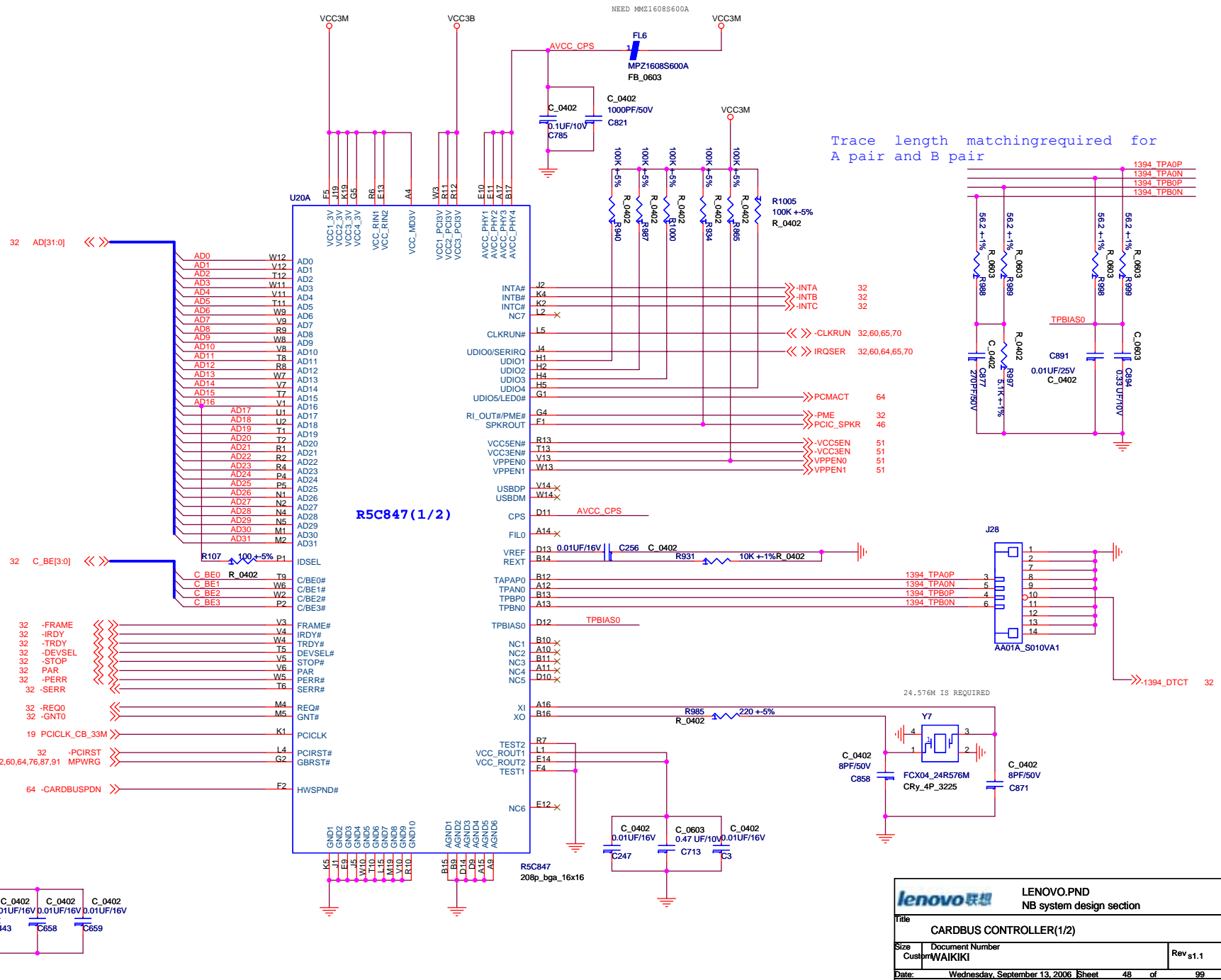


lenovo 联想		LENOVO.PND NB system design section	
Title AUDIO HDP BEEP			
Size Custom	Document Number WAIKIKI		Rev s1.1
Date: Wednesday, September 13, 2006 Sheet 46 of 99			
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lenovo 联想		LENOVO.PND NB system design section	
Title MDC CONN			
Size Custom	Document Number WAIKIKI		Rev s1.1
Date:	Wednesday, September 13, 2006 Sheet 47 of 99		
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	N-1394	Y-1394
U20	R5C804	R5C847/R5C803
FL6	0ohm	MPZ1608S600A
R931	NO_ASM	ASM
C256	NO_ASM	ASM
R988	NO_ASM	ASM
R989	NO_ASM	ASM
R998	NO_ASM	ASM
R999	NO_ASM	ASM
C877	NO_ASM	ASM
R997	NO_ASM	ASM
C891	NO_ASM	ASM
C894	NO_ASM	ASM
J28	NO_ASM	ASM
R985	NO_ASM	ASM
Y7	NO_ASM	ASM
C858	NO_ASM	ASM
C871	NO_ASM	ASM



**LENOVO.PND**  
 NB system design section

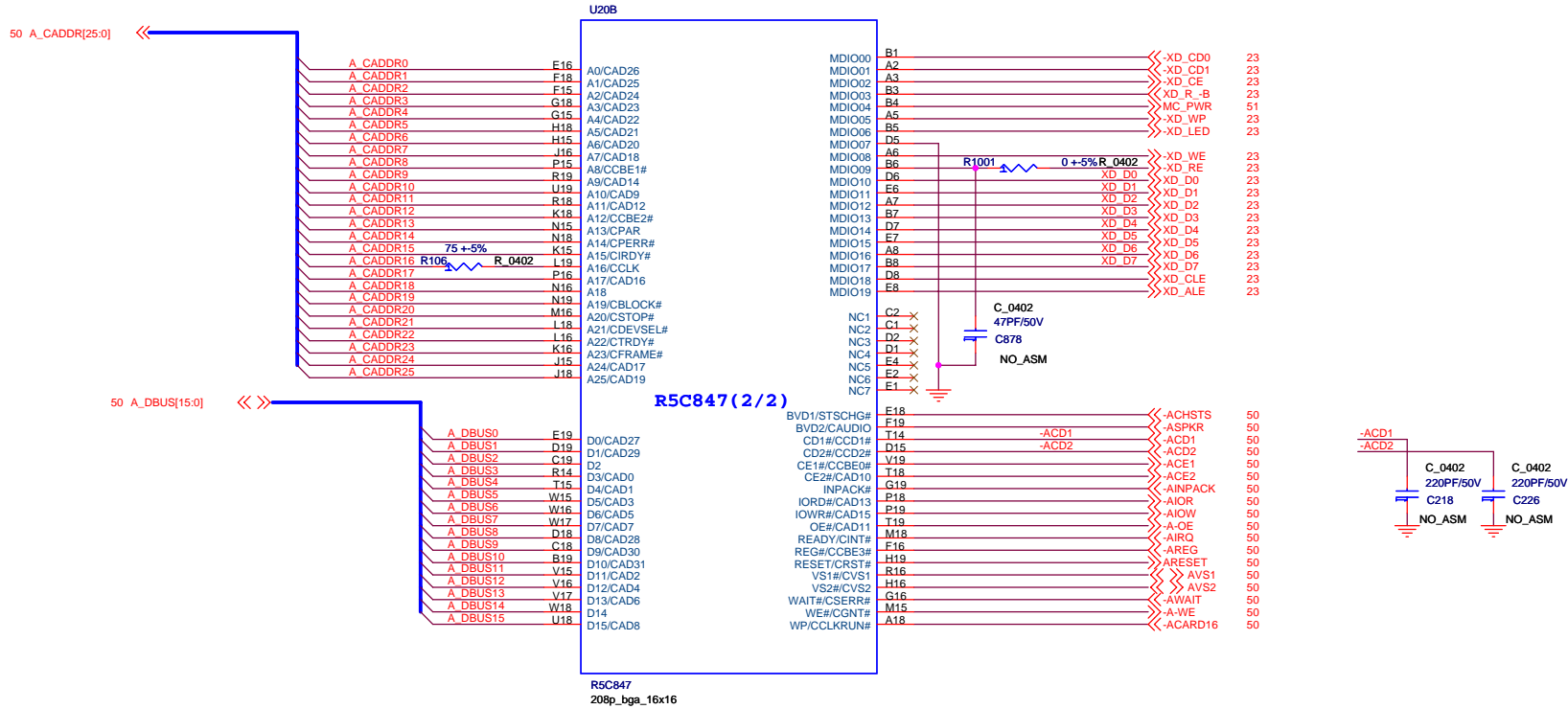
Title: CARDBUS CONTROLLER(1/2)

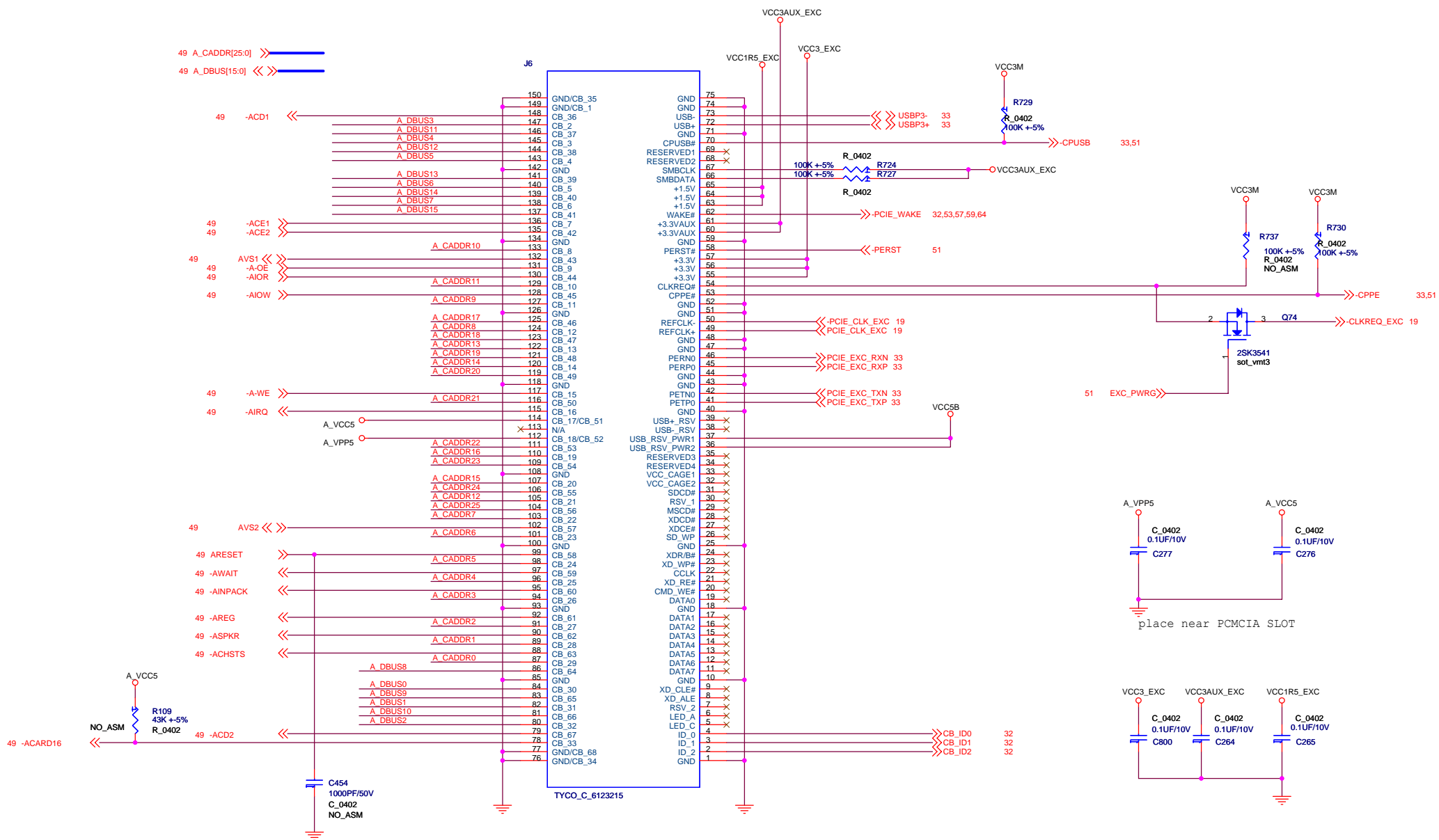
Size: Document Number  
 Customer: WAIKIKI


Date: Wednesday, September 13, 2006 Sheet 48 of 99

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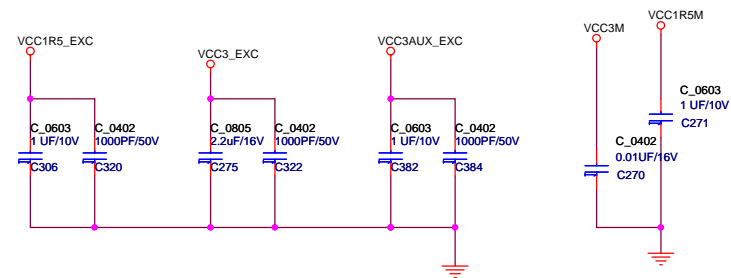
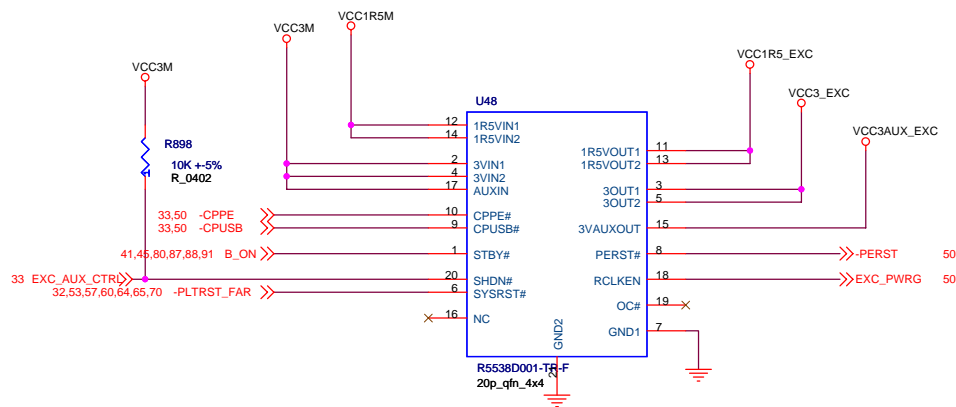
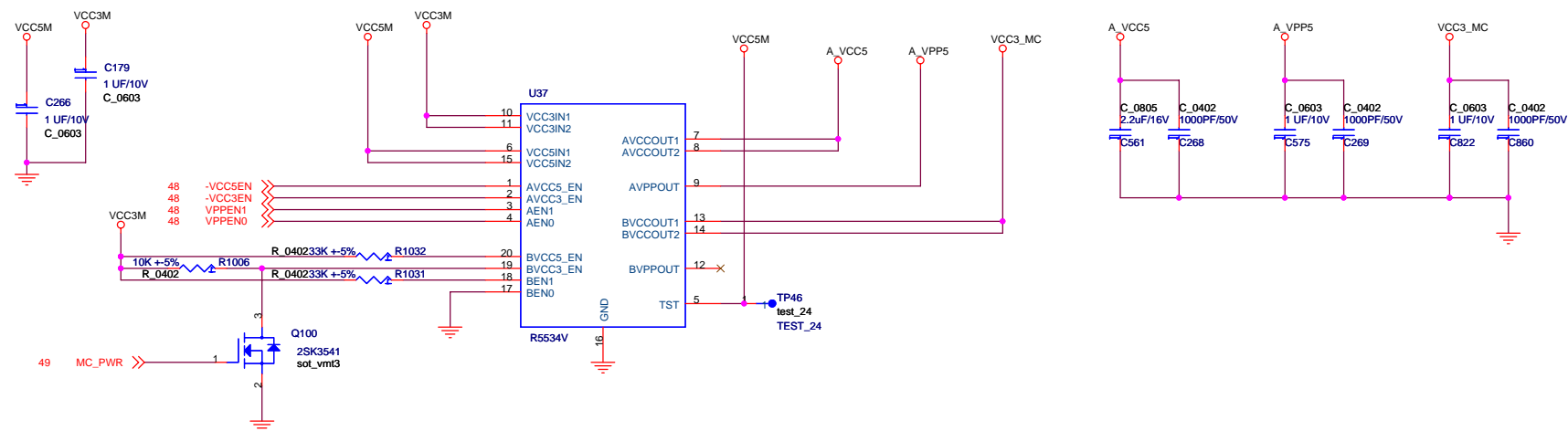


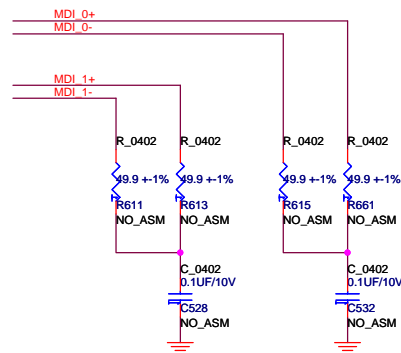
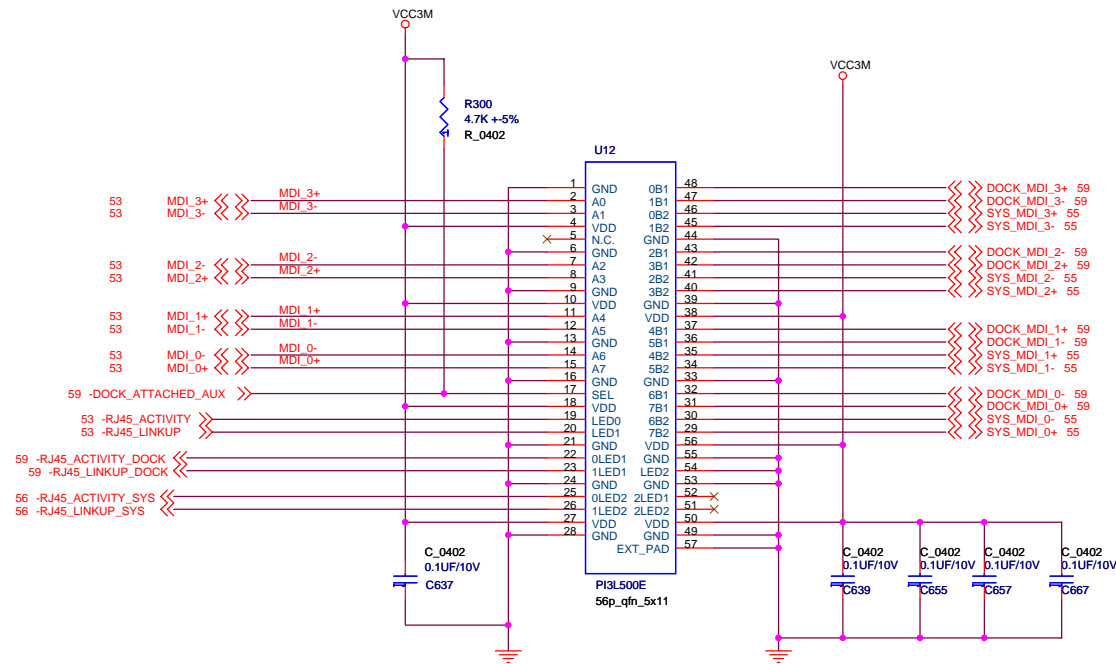




LENOVO.PND  
NB system design section

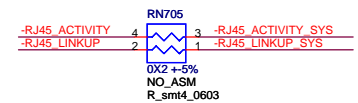
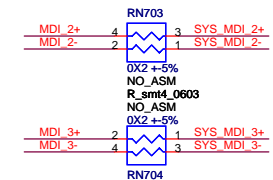
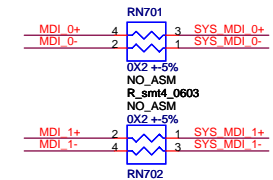
Title CARDBUS SLOT		
Size Customer	Document Number WAIKIKI	Rev s1.1
Date:	Wednesday, September 13, 2006 Sheet 50 of 99	
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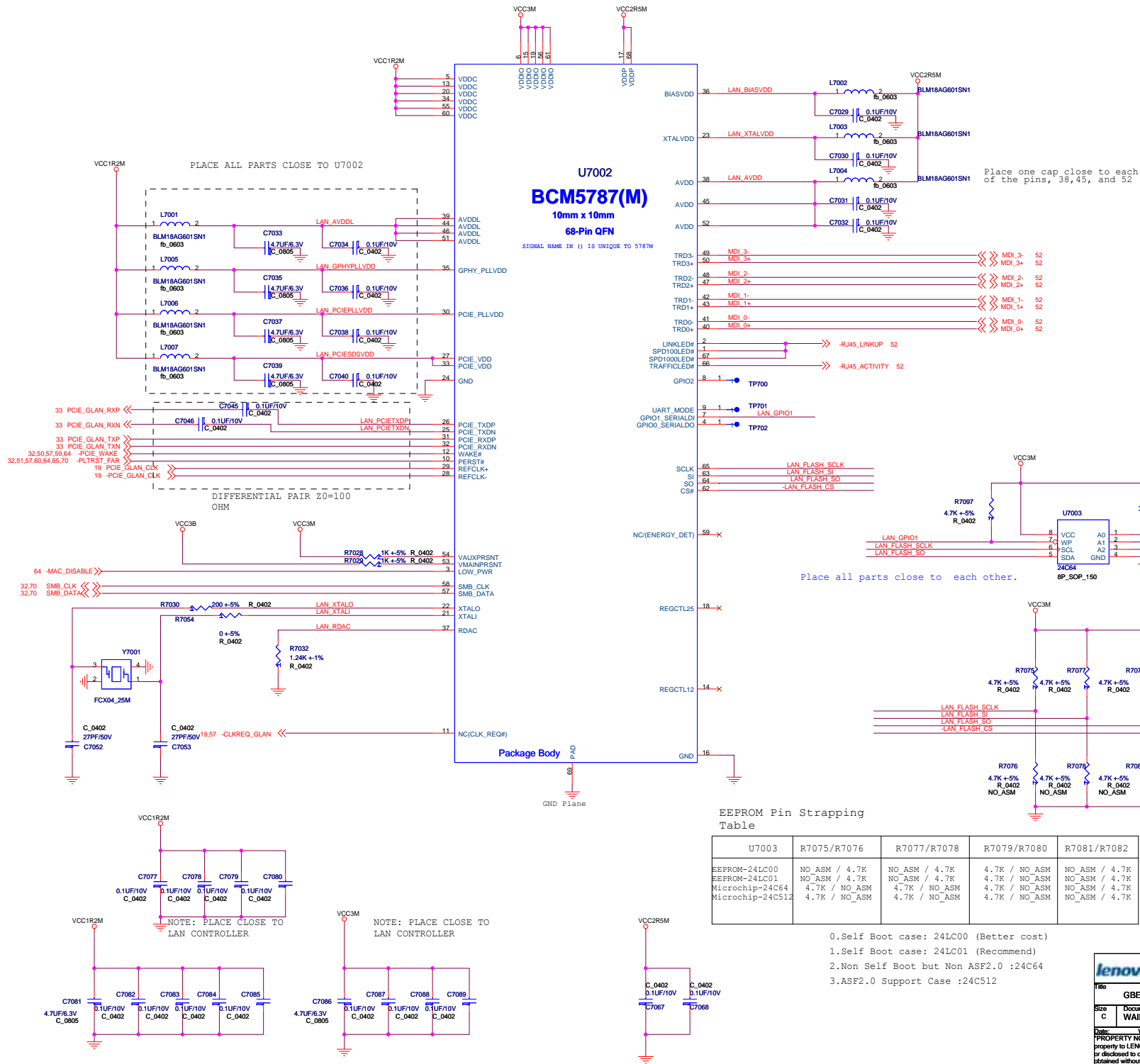
Place all parts close to U7002.  
Trace very shortly so that the termination can be NO\_ASM.  
Do not install RC as the 5787 (M) has on-chip RC terminations.

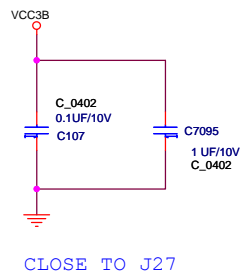
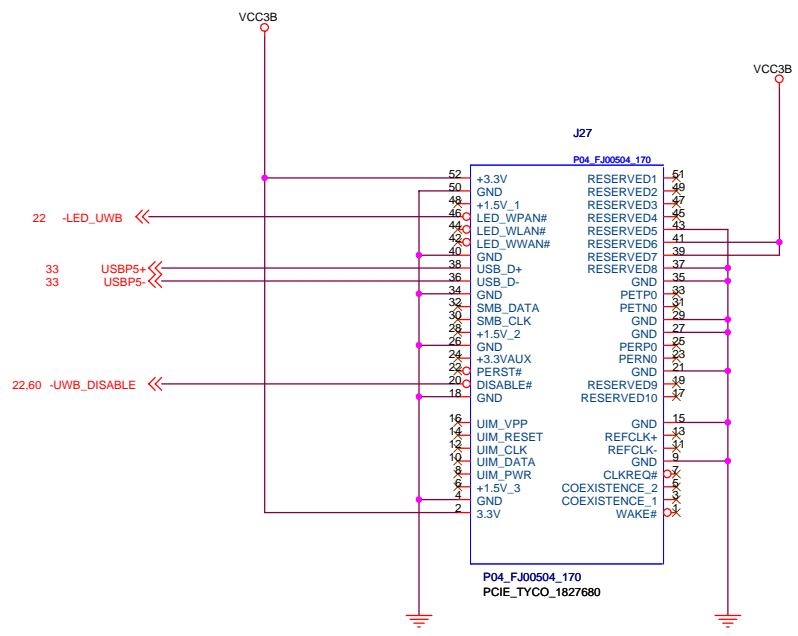
	DOCK	NO DOCK
U12	ASM	NO_ASM
C639	ASM	NO_ASM
C655	ASM	NO_ASM
C657	ASM	NO_ASM
C667	ASM	NO_ASM
R300	ASM	NO_ASM
RN701	NO_ASM	ASM
RN702	NO_ASM	ASM
RN703	NO_ASM	ASM
RN704	NO_ASM	ASM
RN705	NO_ASM	ASM
C637	ASM	NO_ASM




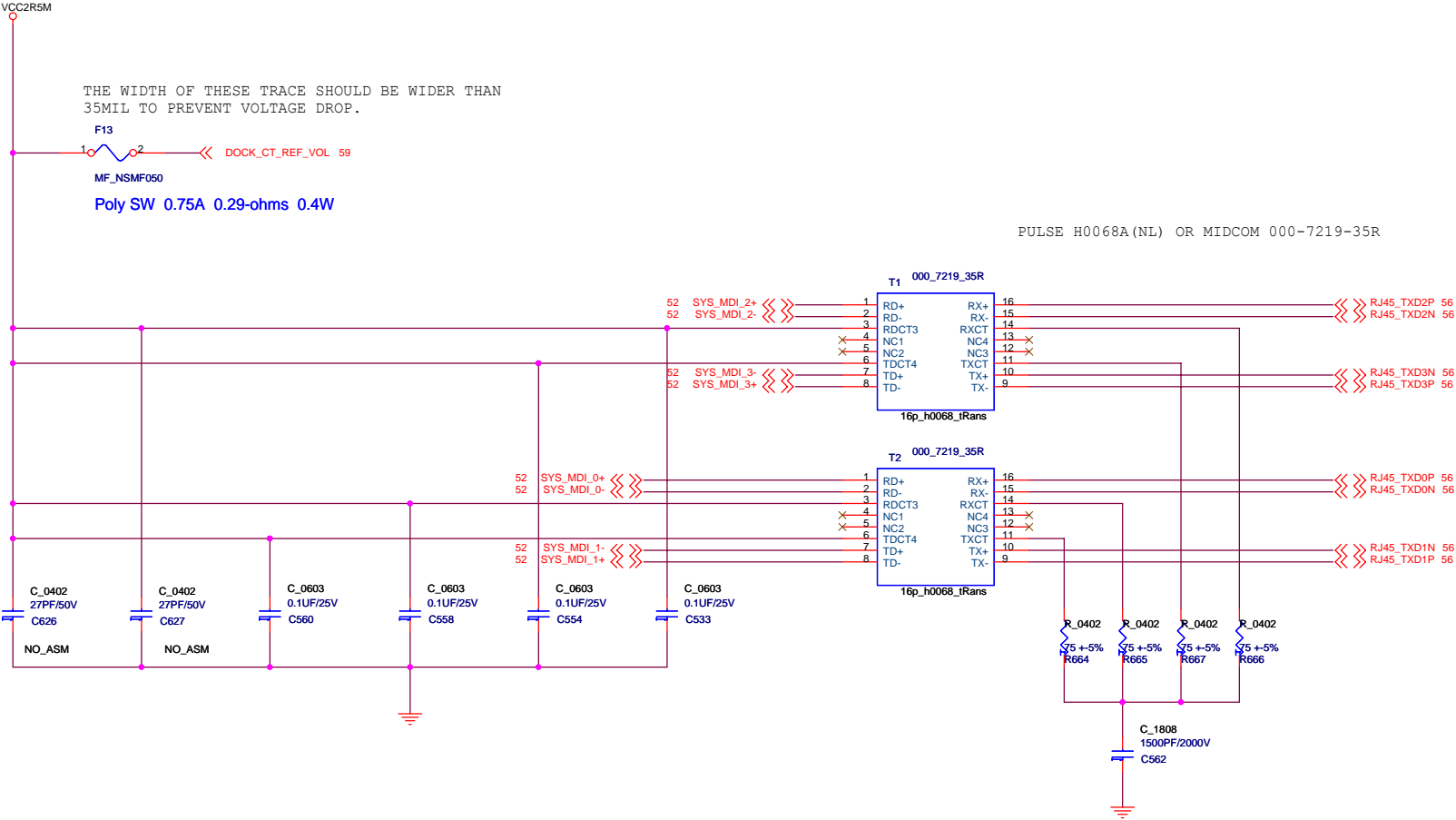
		LENOVO.PND	
		NB system design section	
Title			
GBE LAN SW			
Size	Document Number		Rev s1.1
Customer	WAIKIKI		
Date:	Wednesday, September 13, 2006	Sheet	52 of 99


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		LENOVO.PND	
		NB system design section	
Title MICRO PCIE CONN			
Size	Document Number		Rev s1.1
Custom	WAIKIKI		
Date:	Wednesday, September 13, 2006 Sheet 54 of 99		
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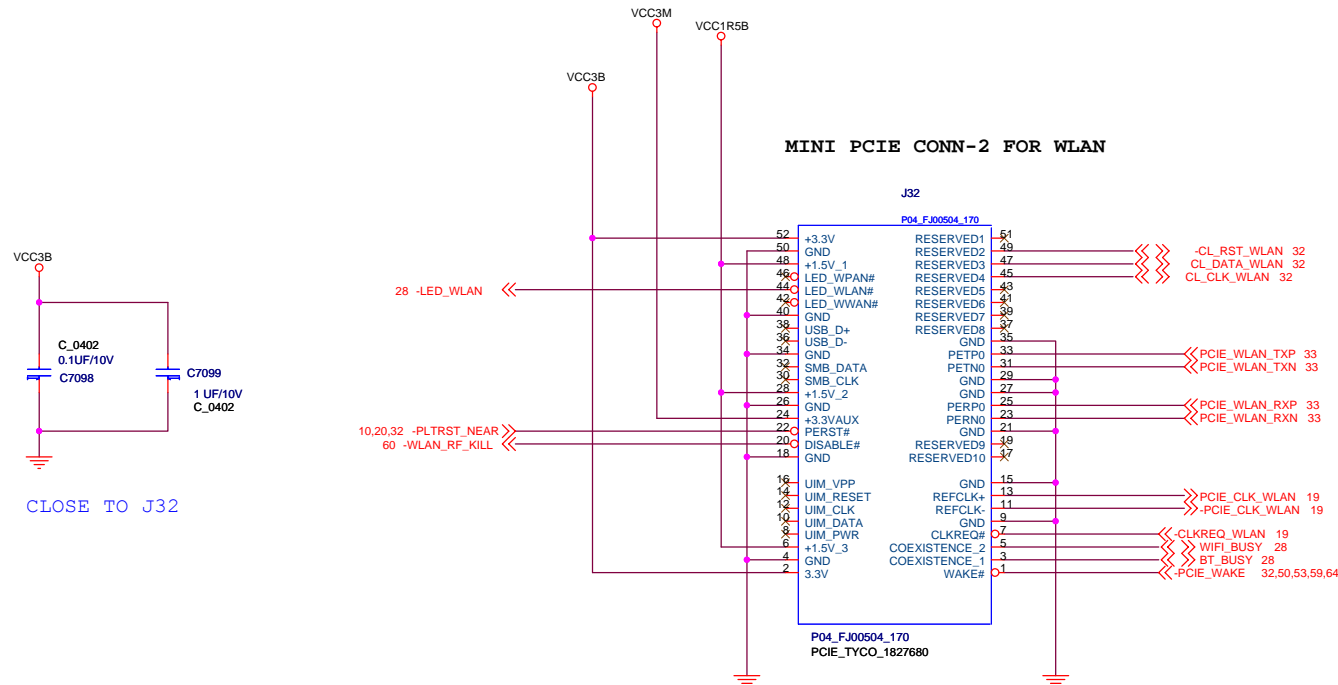
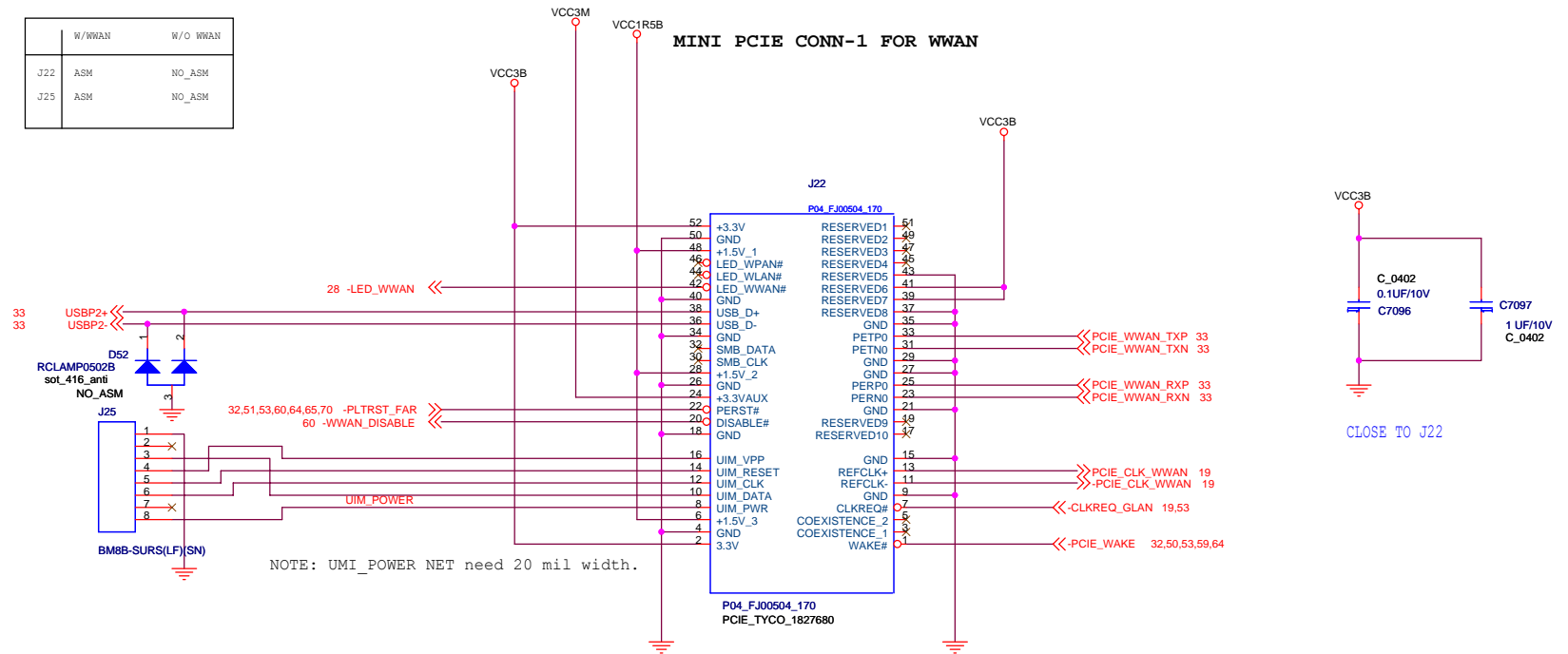
 LENOVO.PND  
NB system design section

Title GBE MAGNETICS		
Size Customer	Document Number WAIKIKI	Rev s1.1
Date: Wednesday, September 13, 2006 Sheet 55 of 99		
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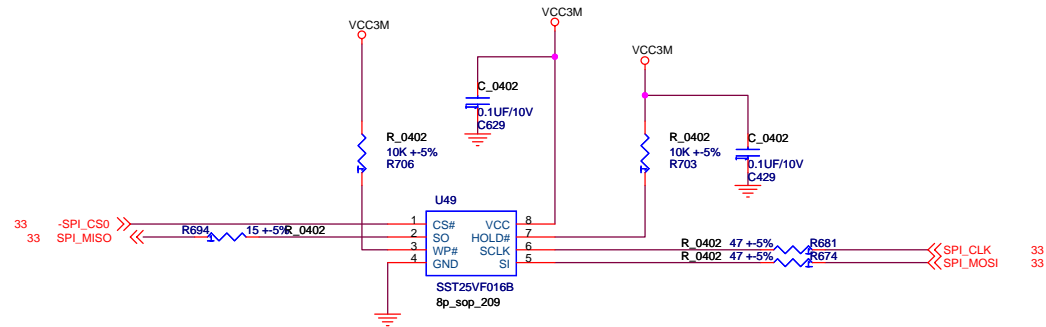


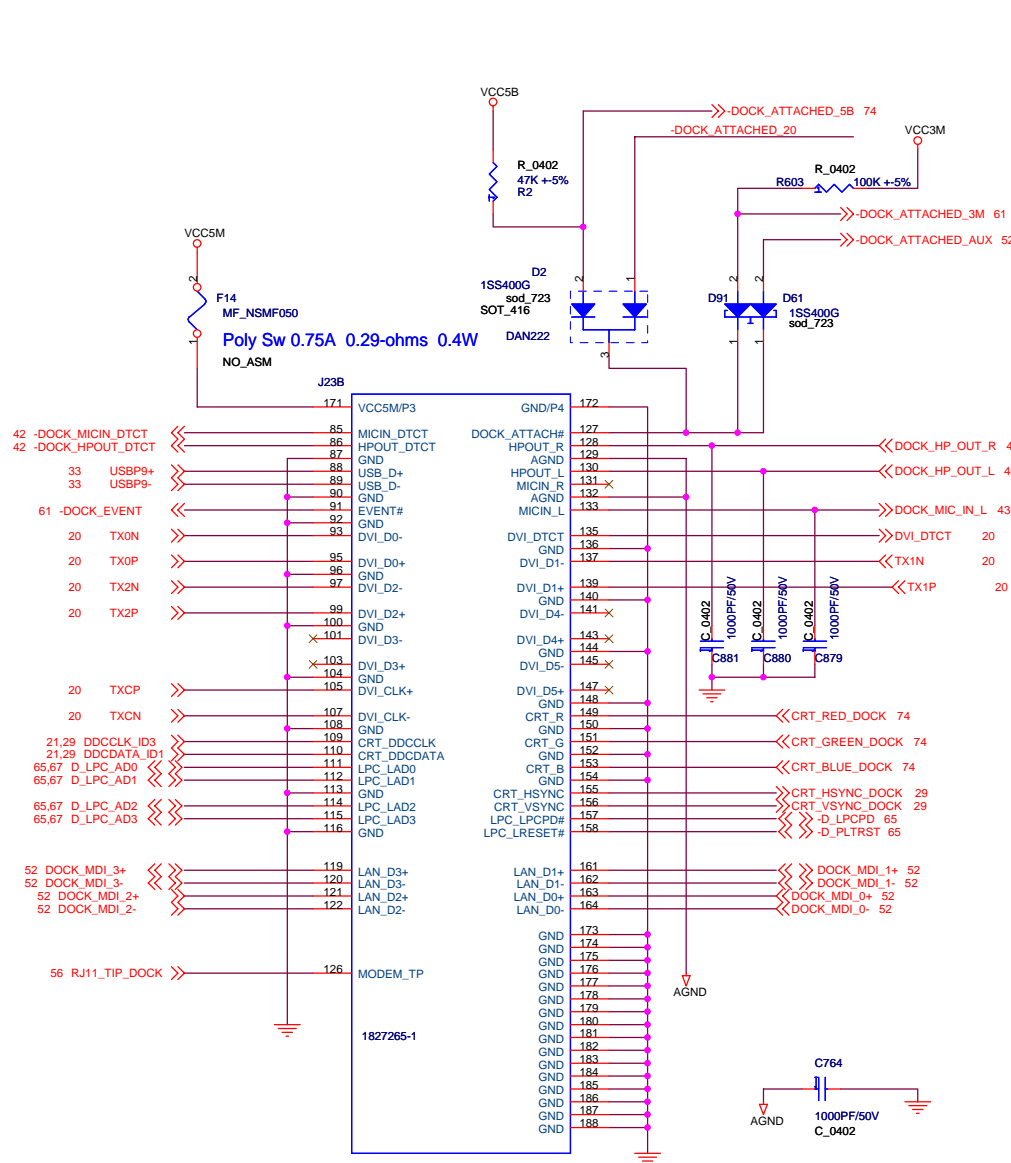


	W/WWAN	W/O WWAN
J22	ASM	NO_ASM
J25	ASM	NO_ASM

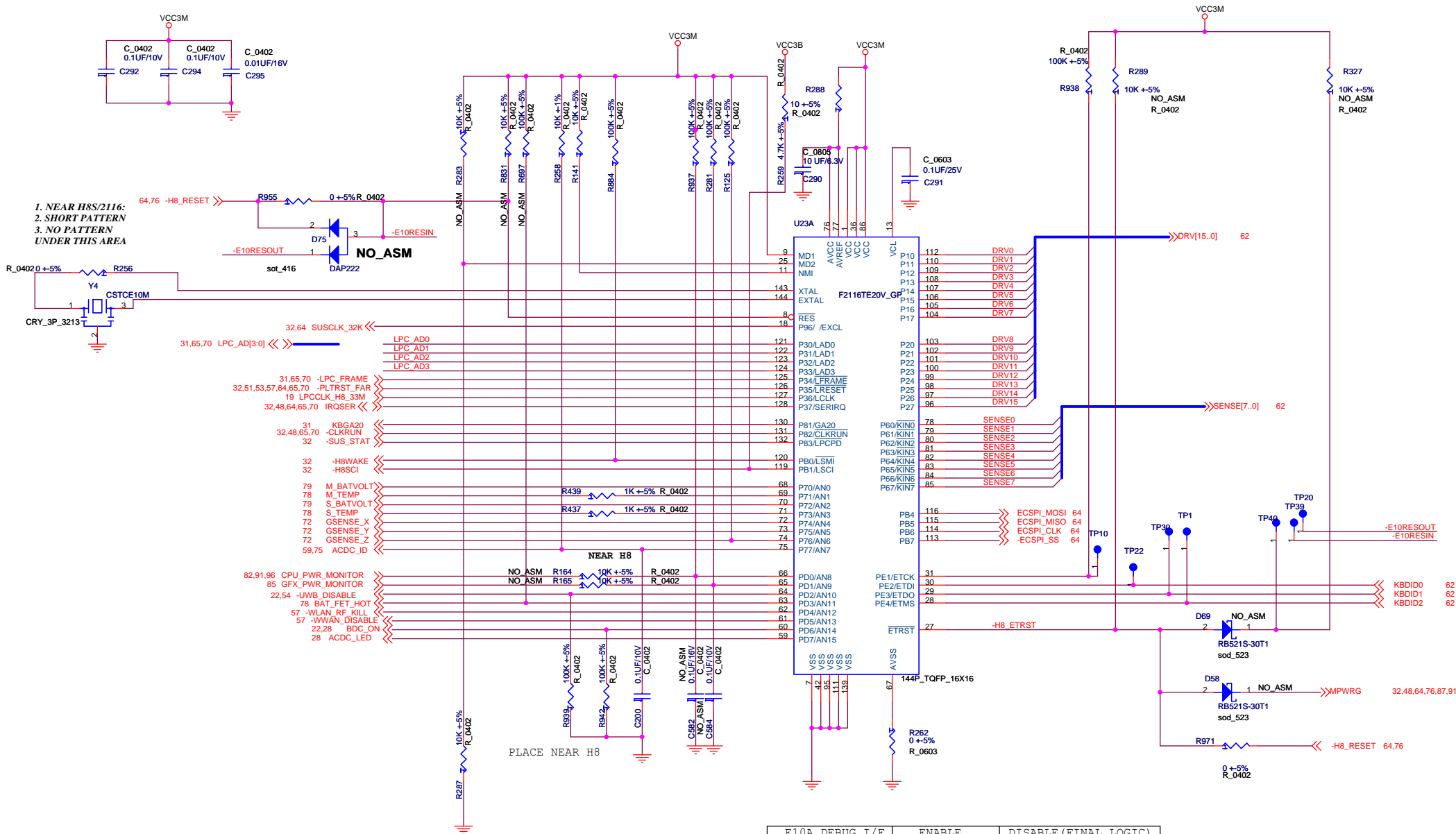


<b>lenovo</b> 联想		<b>LENOVO.PND</b>	
		NB system design section	
Title: <b>MINI PCIE SLOT(WWAN/WLAN)</b>			
Size	Document Number	Rev	
Custom	WAIKIKI	s1.1	
Date:	Wednesday, September 13, 2006	Sheet	57 of 99
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TX	TX Type	TX ID	TX Name	TX ID
20	TXCP	R763	R_0402 NO_ASM 330 +5%	TXCN
20	TXOP	R841	R_0402 NO_ASM 330 +5%	TX0N
20	TX1P	R844	R_0402 NO_ASM 330 +5%	TX1N
20	TX2P	R845	R_0402 NO_ASM 330 +5%	TX2N



E10A DEBUG I/F	ENABLE	DISABLE (FINAL LOGIC)
R0405	NO_ASM	ASM
R0407	NO_ASM	ASM
D0408	ASM	NO_ASM
R0409	ASM	NO_ASM
R0410	ASM	NO_ASM
D0411	ASM	NO_ASM
R0412	ASM	NO_ASM
R0413	ASM	NO_ASM

LENOVO.PND  
NB system design section

Title  
H8S(1/2)

Size  
Custom

Document Number  
WAIKIKI

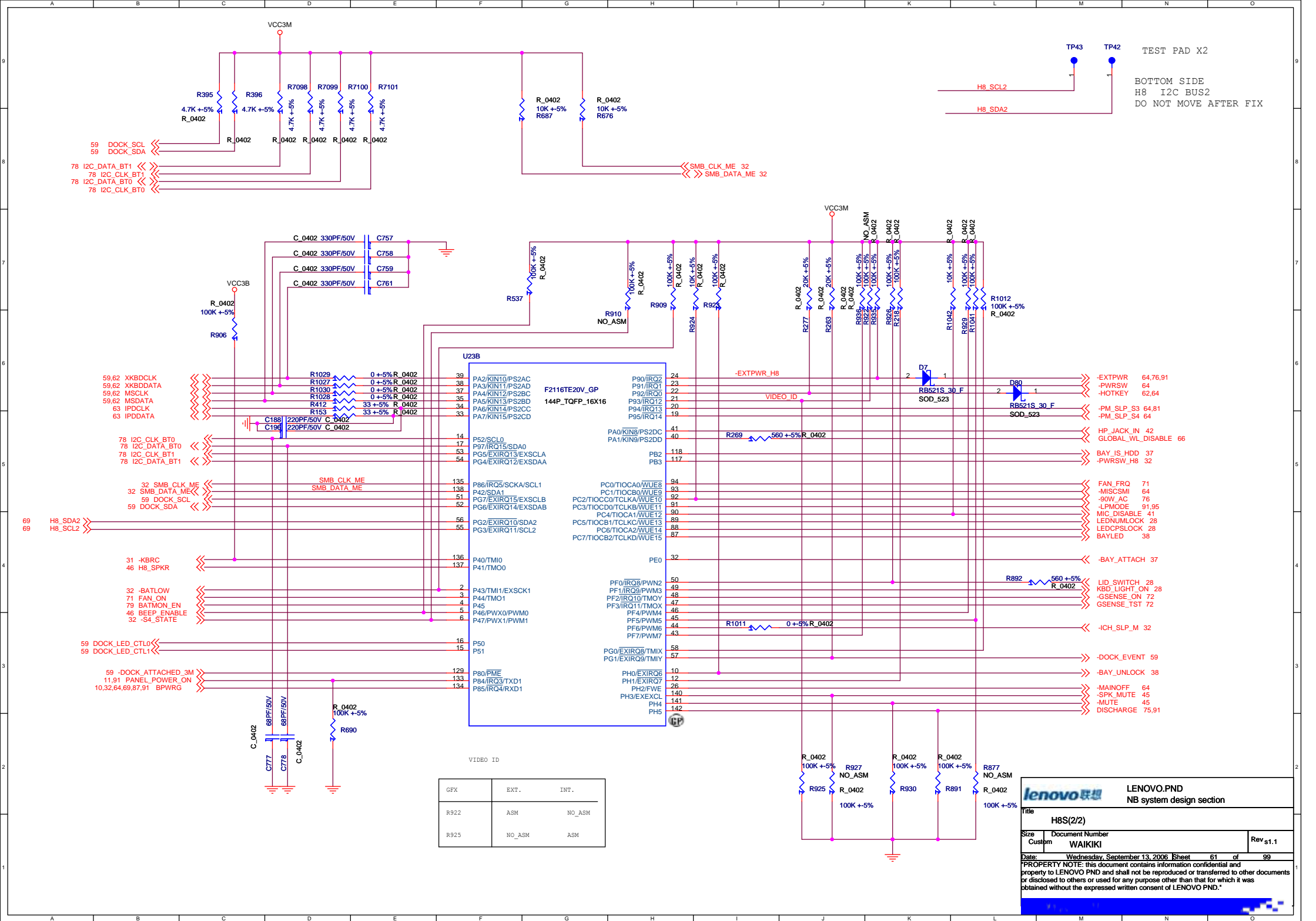
Date  
Wednesday, September 13, 2006

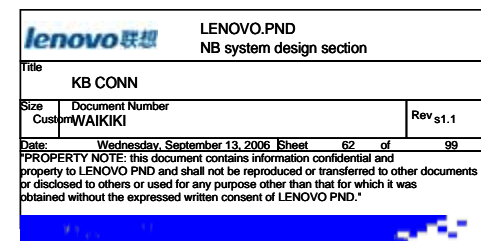
Sheet  
60

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99

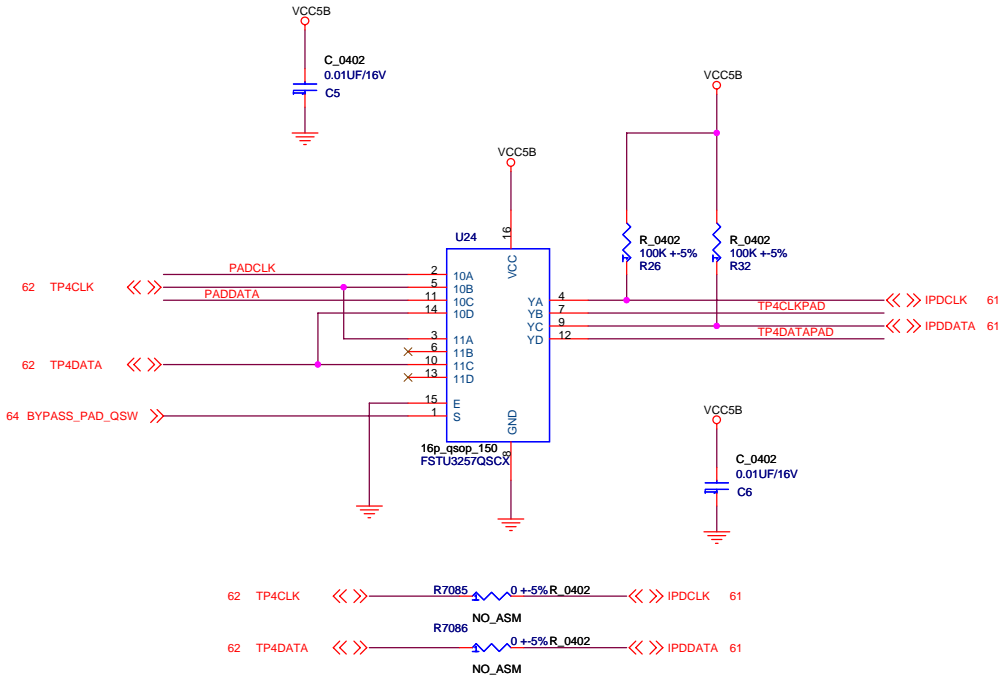
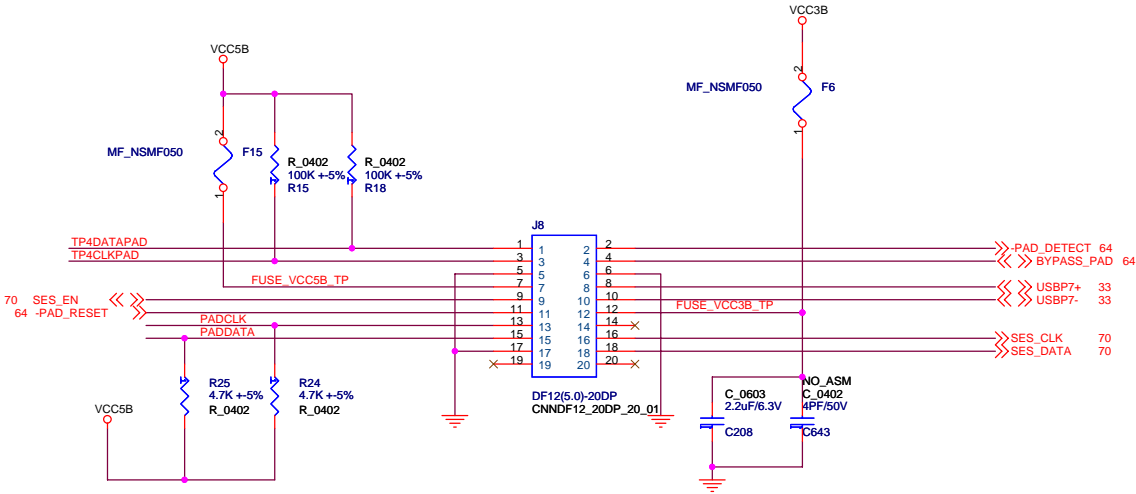
Rev  
s1.1

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TouchPad/FingerPrint Support		
	SUPPORT	NO SUPPORT
F15	ASM	NO-ASM
R15	ASM	NO-ASM
R18	ASM	NO-ASM
R24	ASM	NO-ASM
R25	ASM	NO-ASM
J8	ASM	NO-ASM
F6	ASM	NO-ASM
C208	ASM	NO-ASM
C643	NO-ASM	NO-ASM
C636	NO-ASM	NO-ASM
C631	NO-ASM	NO-ASM
C6	ASM	NO-ASM
U24	ASM	NO-ASM
R26	ASM	ASM
R32	ASM	ASM
R7085	NO-ASM	ASM
R7086	NO-ASM	ASM



LENOVO.PND

NB system design section

Title

TP CONN

Size

Document Number

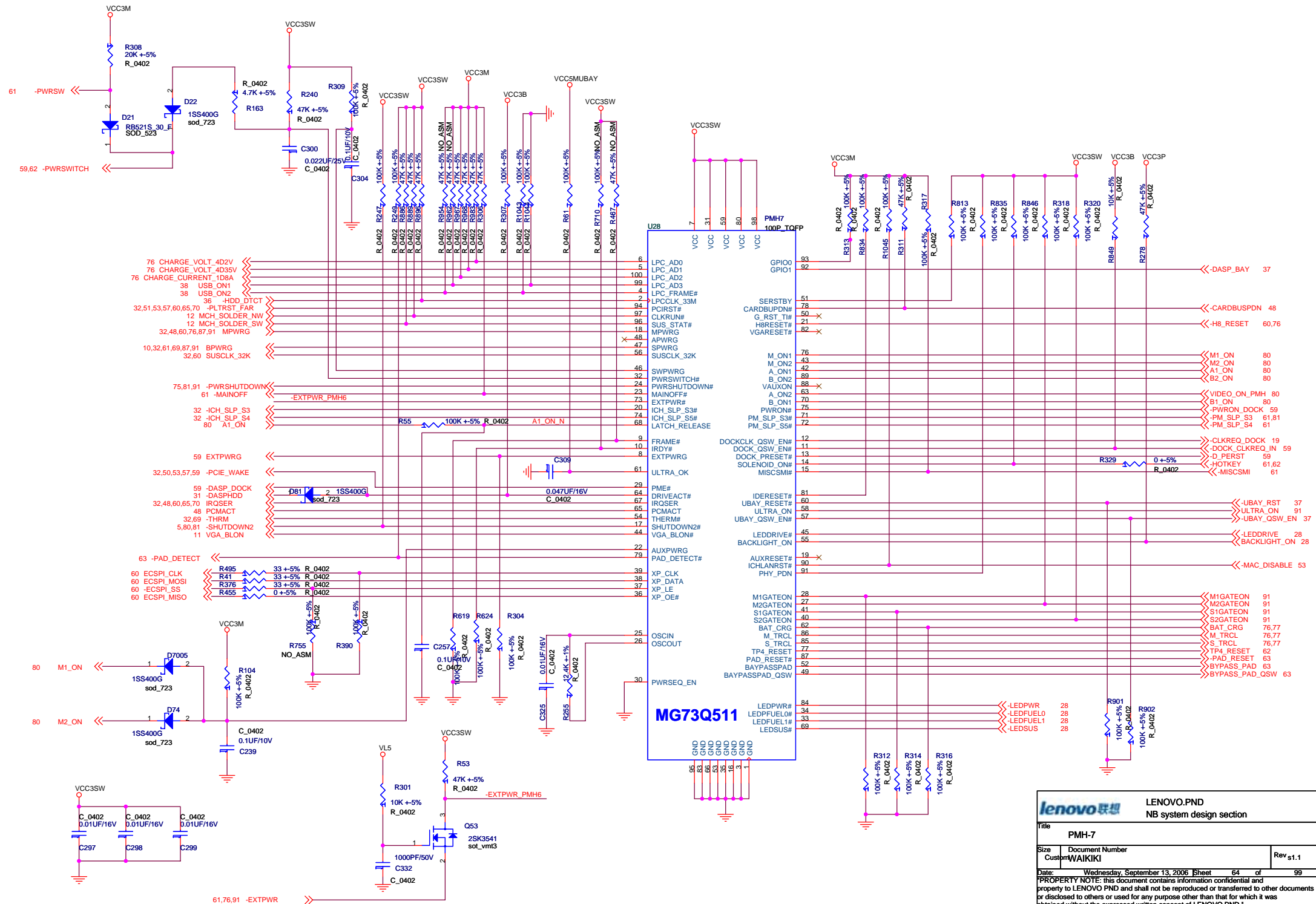
Rev s1.1

Date:

Wednesday, September 13, 2006

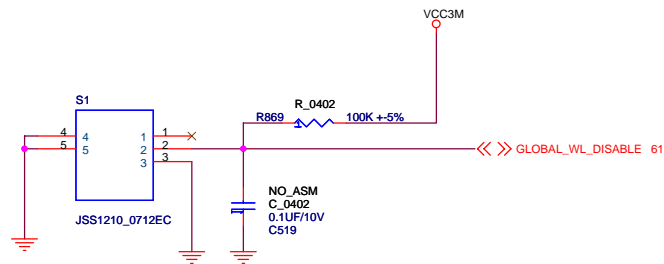
Sheet 63 of 99


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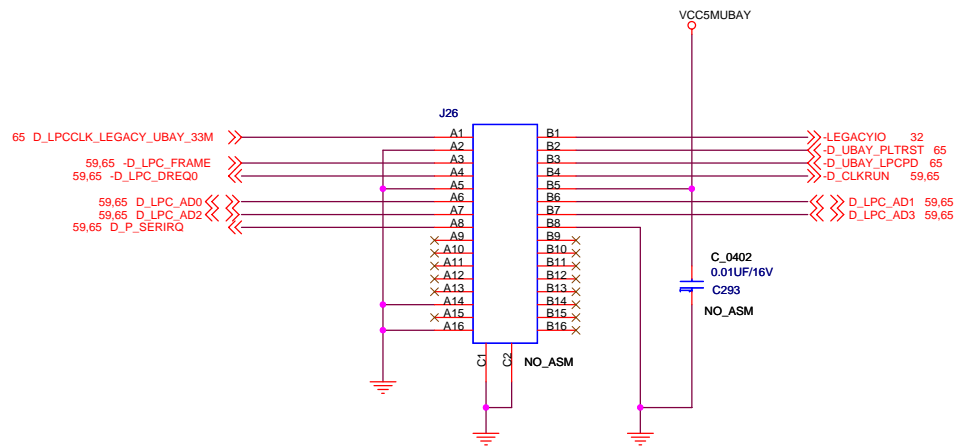











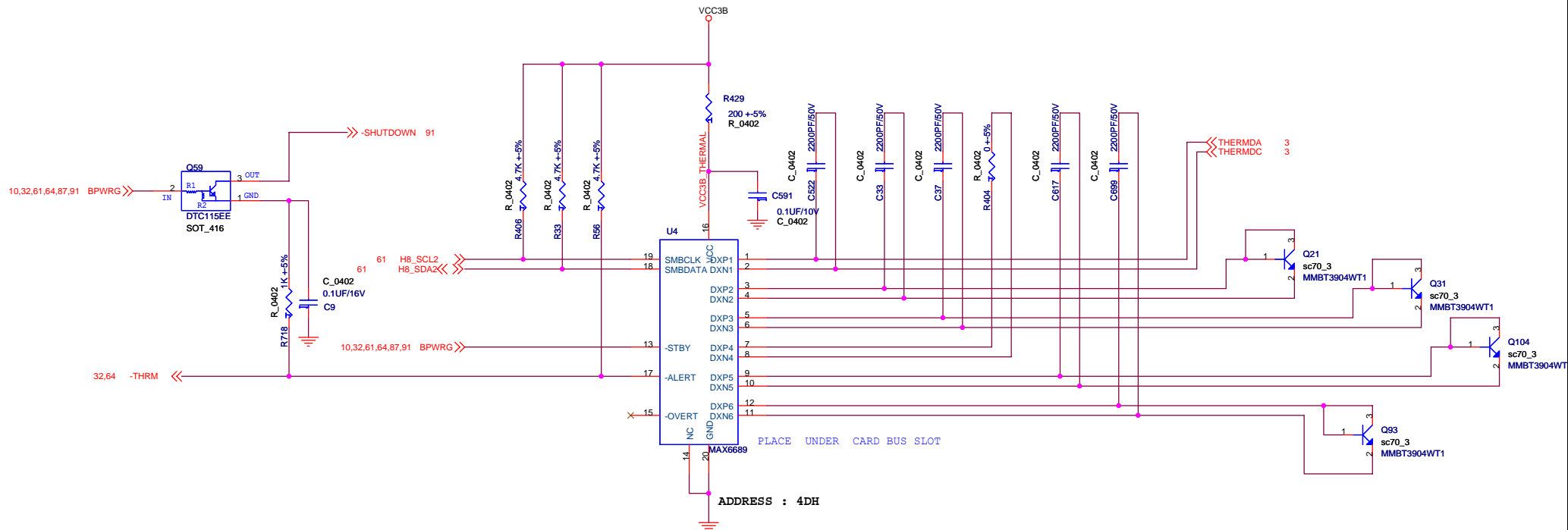
		LENOVO.PND NB system design section	
Title WIRELESS DISABLE SW			
Size Customer	Document Number WAIKIKI		Rev s1.1
Date: Wednesday, September 13, 2006 Sheet 66 of 99			
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		LENOVO.PND NB system design section	
Title SERIAL INTERFACE			
Size	Document Number	Rev s1.1	
Customer	WAIKIKI		
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		NB system design section	
Title			
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Size	Document Number		Rev
Custom	WAIKIKI		s1.1
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LENOVO.PND  
NB system design section

Title

THERMAL SENSOR

Size

Document Number

Rev s1.1

Custom

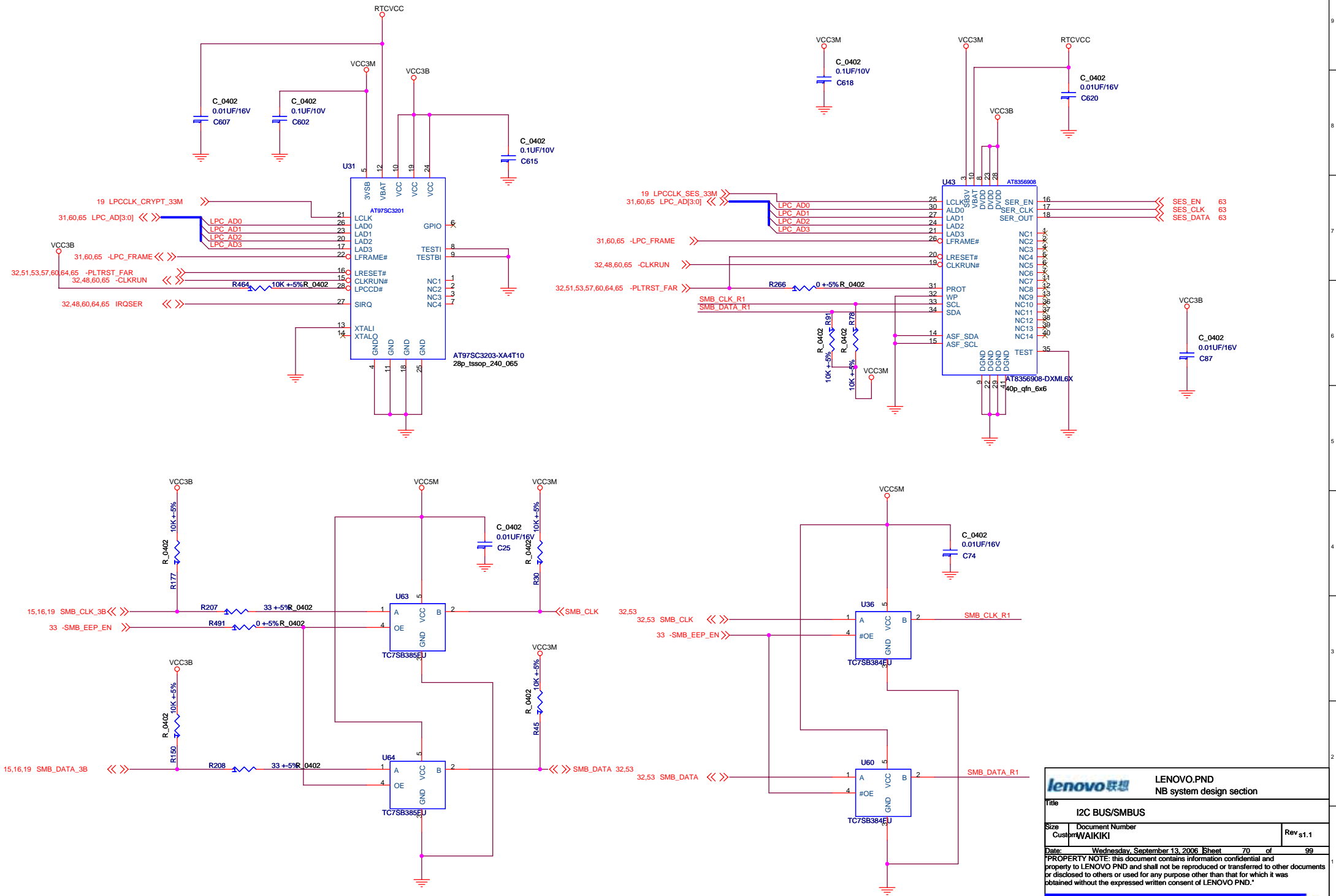
WAIKIKI


Date:

Wednesday, September 13, 2006

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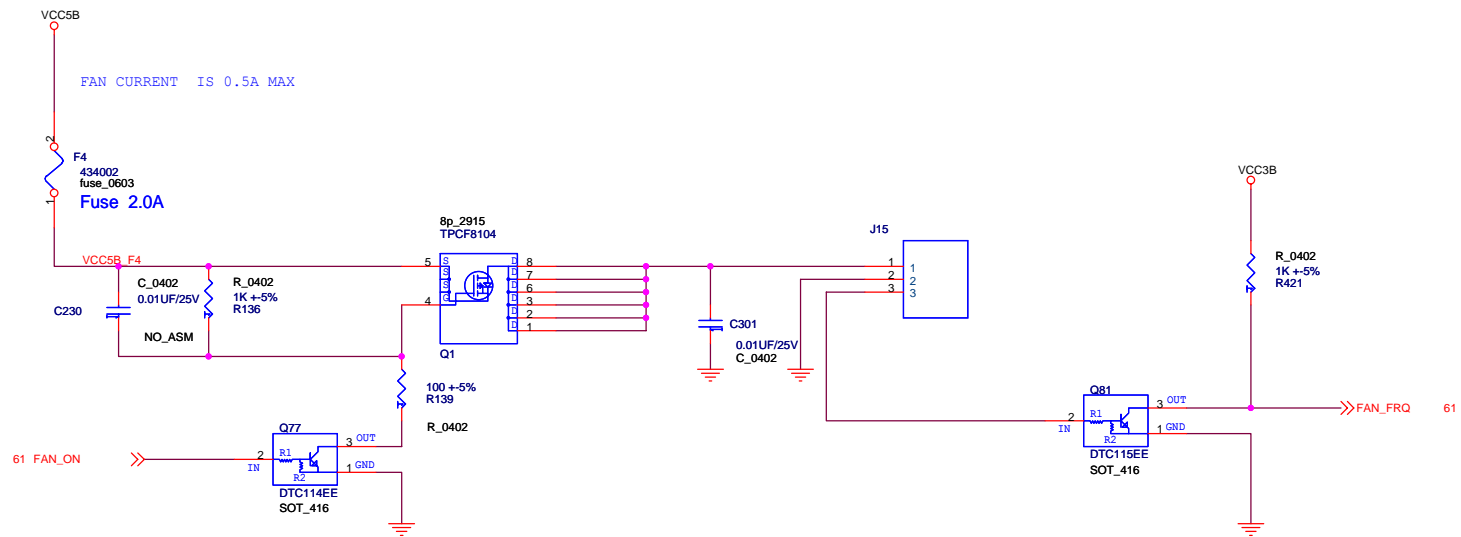
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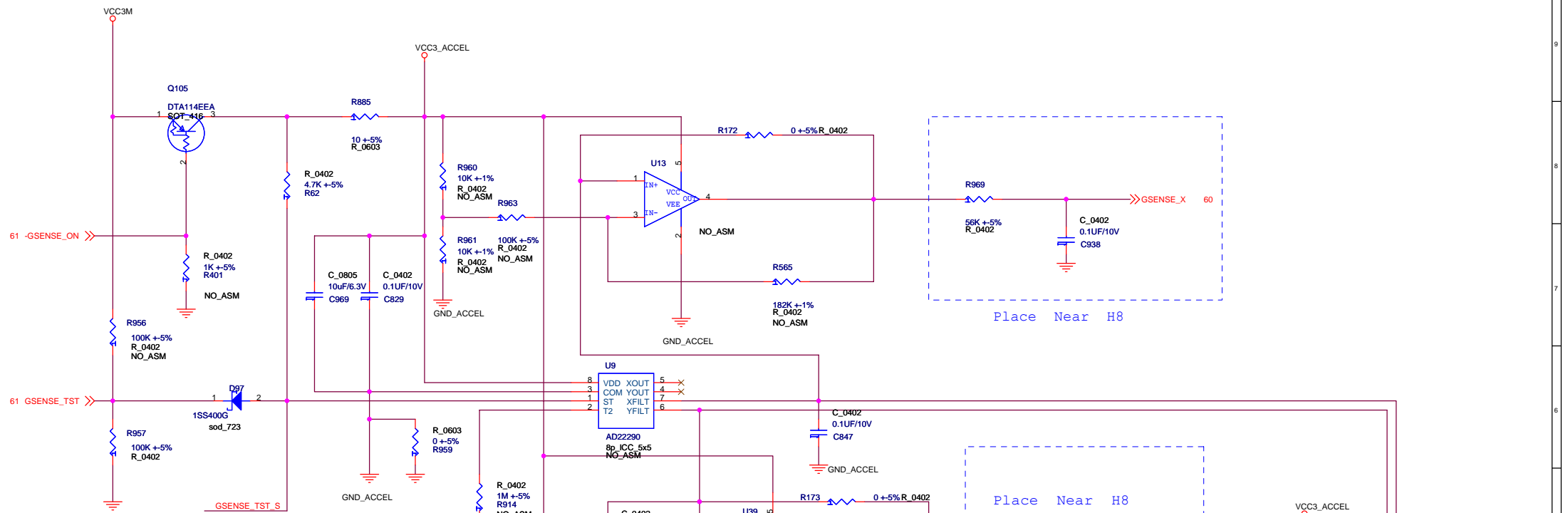


LENOVO.PND  
NB system design section

Title I2C BUS/SMBUS		
Size Custom	Document Number WAIKIKI	Rev s1.1
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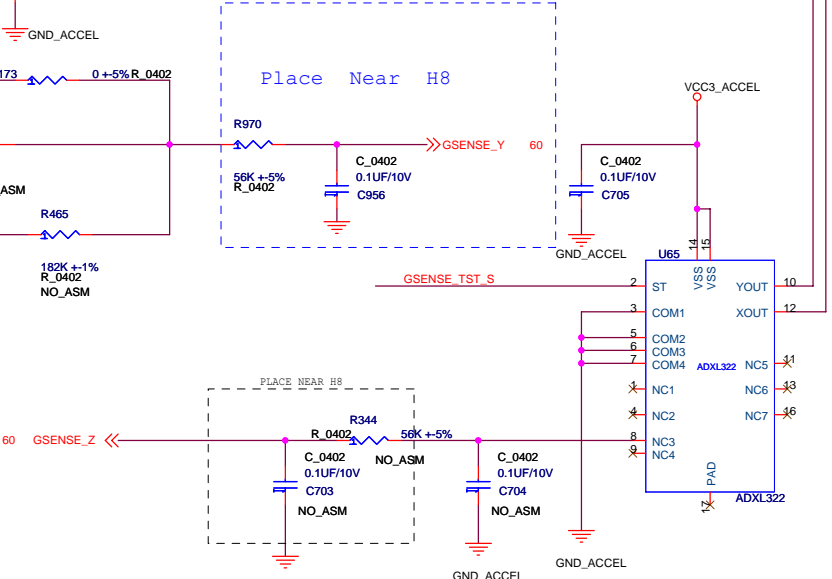



lenovo 联想		LENOVO.PND NB system design section	
Title FAN CONN			
Size Custom	Document Number WAIKIKI		Rev s1.1
Date:	Wednesday, September 13, 2006 Sheet 71 of 99		
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	AD22290	ADXL322/ LIS244AL	LIS2L02AL	NO ACC
R172	NO_ASM	ASM	ASM	NO_ASM
R173	NO_ASM	ASM	ASM	NO_ASM
R401	NO_ASM	NO_ASM	ASM	ASM
R957	NO_ASM	ASM	NO_ASM	ASM
U9	AD22290	NO_ASM	LIS2L02AL	NO_ASM
U65	NO_ASM	ASM	NO_ASM	NO_ASM
U13	NO_ASM	ASM	NO_ASM	NO_ASM
U39	ASM	NO_ASM	NO_ASM	NO_ASM
Q105	ASM	ASM	ASM	NO_ASM
D97	ASM	ASM	ASM	NO_ASM
R956	ASM	NO_ASM	ASM	NO_ASM
R62	ASM	ASM	ASM	NO_ASM
R885	47-OHM	10-OHM	10-OHM	NO_ASM
C829	ASM	ASM	ASM	NO_ASM
C969	ASM	ASM	ASM	NO_ASM
R914	ASM	NO_ASM	NO_ASM	NO_ASM
R959	ASM	ASM	ASM	NO_ASM
C830	0.1UF	0.1UF	0.033UF	NO_ASM
C847	0.1UF	0.1UF	0.033UF	NO_ASM
R960	ASM	NO_ASM	NO_ASM	NO_ASM
R961	ASM	NO_ASM	NO_ASM	NO_ASM
R963	ASM	NO_ASM	NO_ASM	NO_ASM
R565	ASM	NO_ASM	NO_ASM	NO_ASM

	AD22290	ADXL322/ LIS244AL	LIS2L02AL	NO ACC
R964	ASM	NO_ASM	NO_ASM	NO_ASM
R965	ASM	NO_ASM	NO_ASM	NO_ASM
R966	ASM	NO_ASM	NO_ASM	NO_ASM
R465	ASM	NO_ASM	NO_ASM	NO_ASM
R969	56K	56K	56K	NO_ASM
C938	ASM	ASM	NO_ASM	NO_ASM
R970	56K	56K	56K	NO_ASM
C956	ASM	ASM	ASM	NO_ASM



 LENOVO.PND  
NB system design section

Title

G-SENSOR

Size

Document Number

Rev s1.1

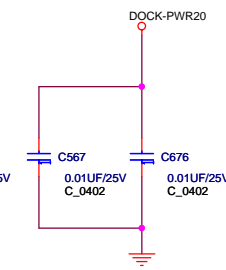
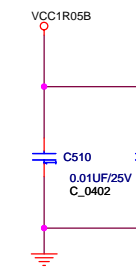
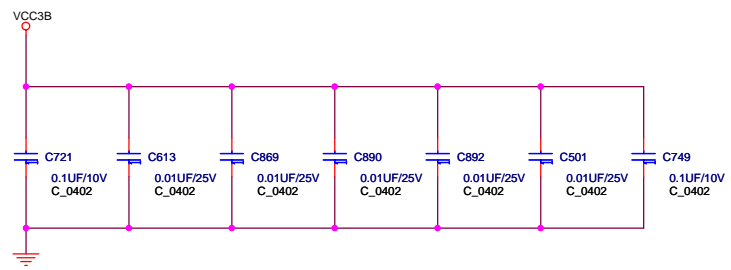
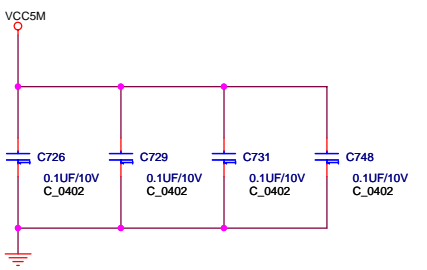
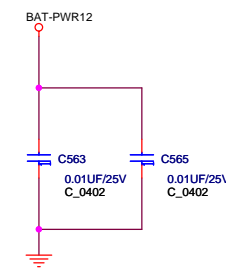
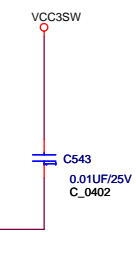
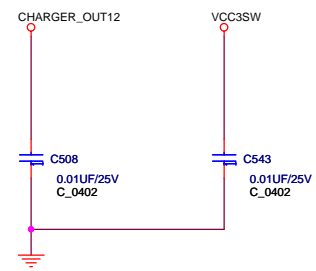
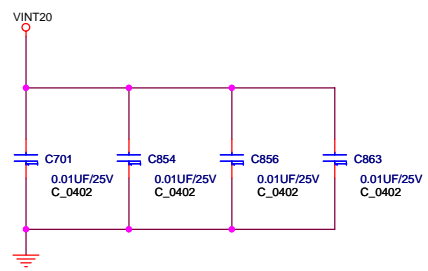
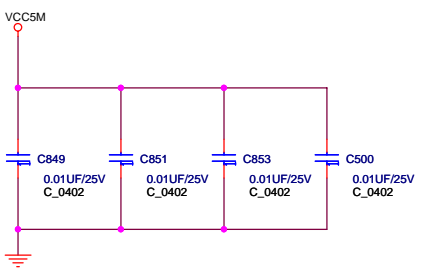
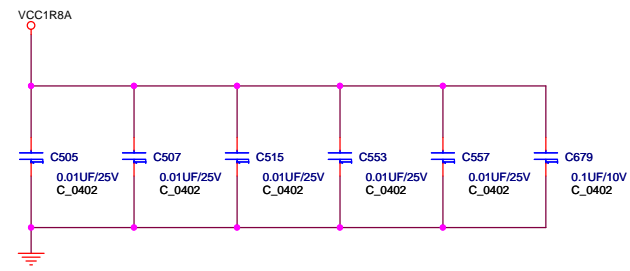
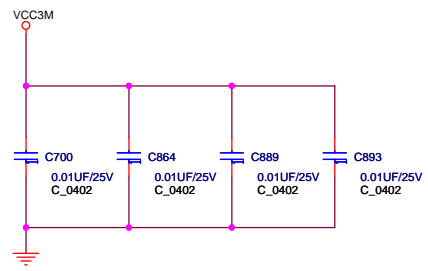
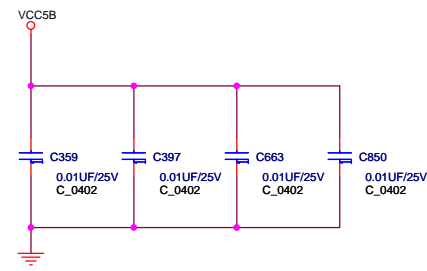
Date:


Wednesday, September 13, 2006

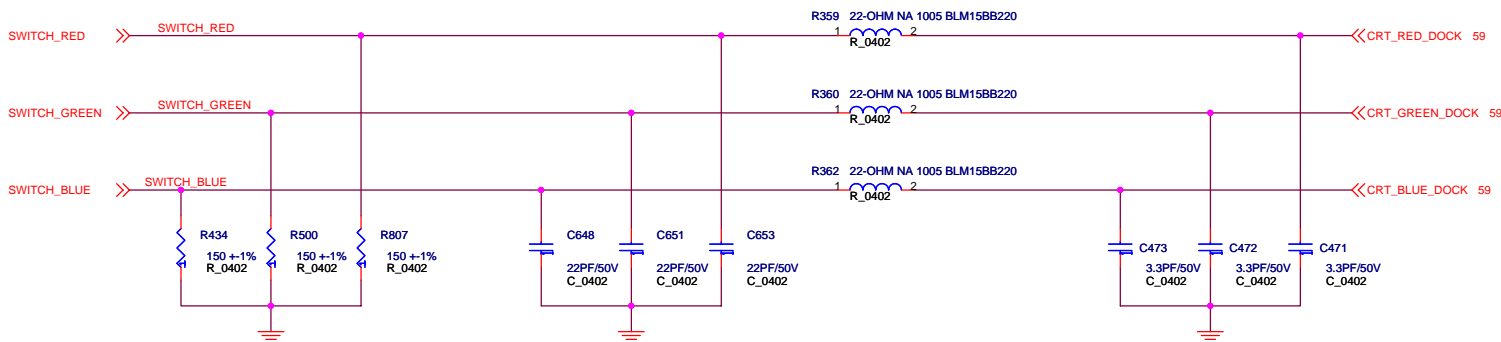
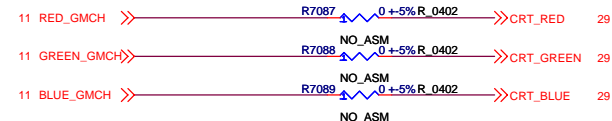
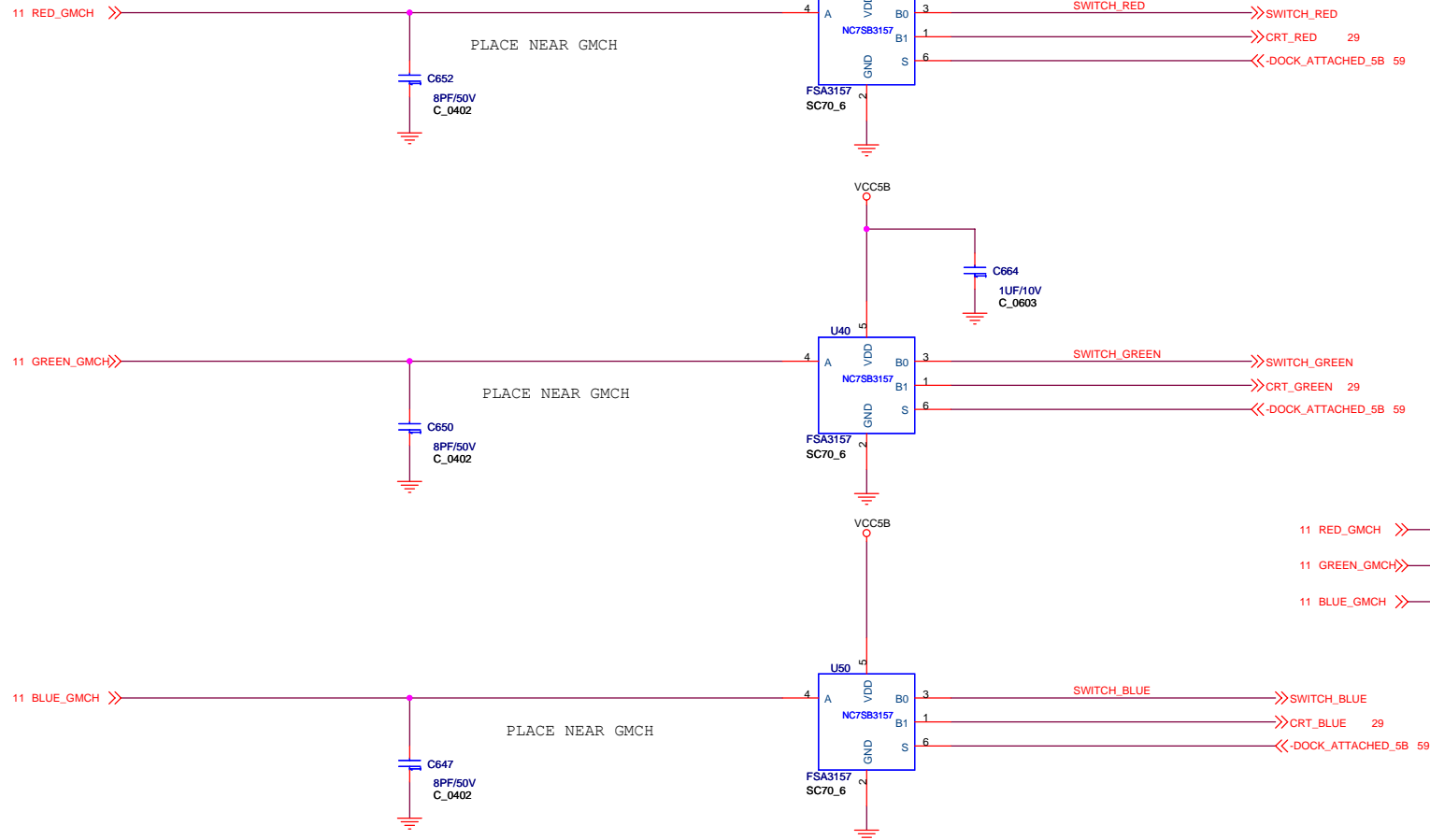
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		LENOVO.PND	
		NB system design section	
Title			
EMC DECOUPLING			
Size	Document Number		Rev s1.1
Custom	WAIKIKI		
Date: Wednesday, September 13, 2006 Sheet 73 of 99			
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	DOCK	NO DOCK
C471	ASM	NO_ASM
C472	ASM	NO_ASM
C473	ASM	NO_ASM
C648	ASM	NO_ASM
C651	ASM	NO_ASM
C653	ASM	NO_ASM
C656	ASM	NO_ASM
C664	ASM	NO_ASM
R359	ASM	NO_ASM
R360	ASM	NO_ASM
R362	ASM	NO_ASM
R434	ASM	NO_ASM
R500	ASM	NO_ASM
R807	ASM	NO_ASM
U14	ASM	NO_ASM
U40	ASM	NO_ASM
U50	ASM	NO_ASM
R7087	NO_ASM	ASM
R7088	NO_ASM	ASM
R7089	NO_ASM	ASM

LENOVO.PND  
NB system design section

Title  
RGB SWITCH

Size  
Custom

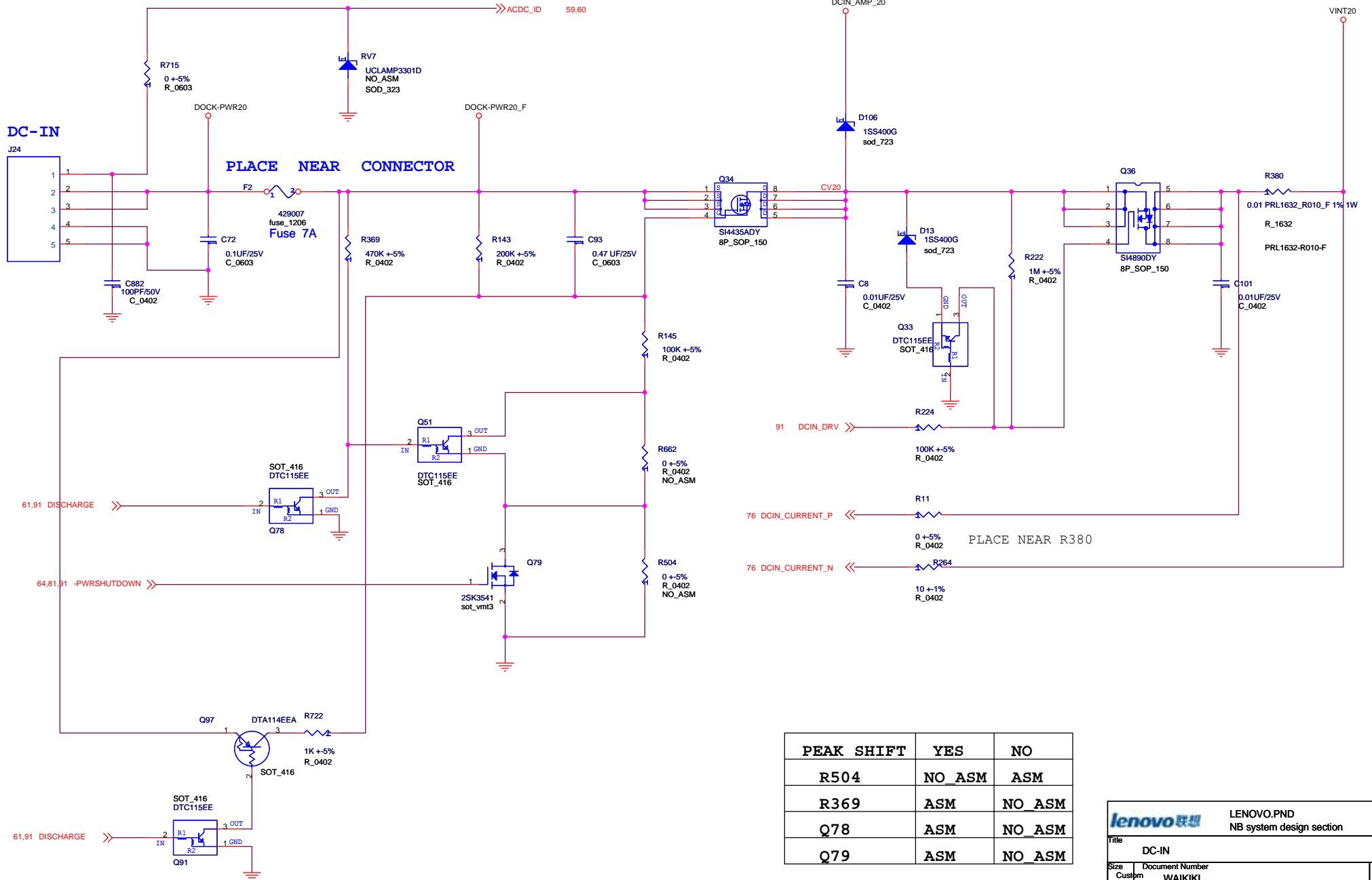
Document Number  
WAIKIKI

Rev  
s1.1

Date  
Wednesday, September 13, 2006

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PEAK SHIFT	YES	NO
R504	NO_ASM	ASM
R369	ASM	NO_ASM
Q78	ASM	NO_ASM
Q79	ASM	NO_ASM

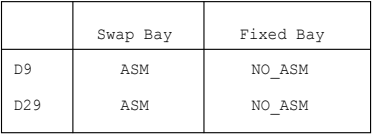
**lenovo** LENOVO.PND  
NB system design section

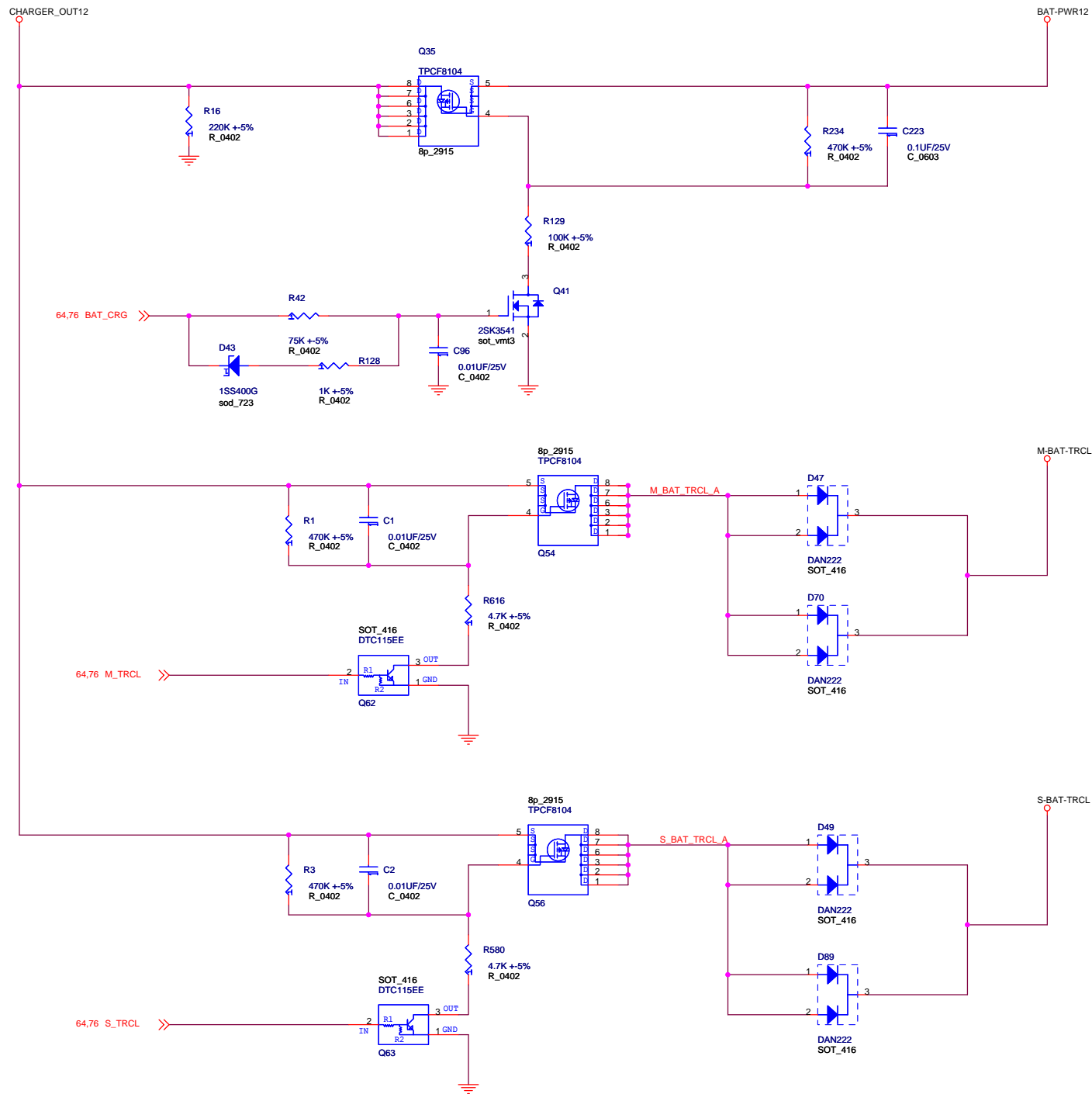
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Size: Custom Document Number: WAIKIKI Rev s1.1


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	Swap Bay	Fixed Bay
R3	ASM	NO-ASM
C2	ASM	NO-ASM
Q56	ASM	NO-ASM
Q63	ASM	NO-ASM
R580	ASM	NO-ASM
D49	ASM	NO-ASM
D89	ASM	NO-ASM

 LENOVO.PND  
NB system design section

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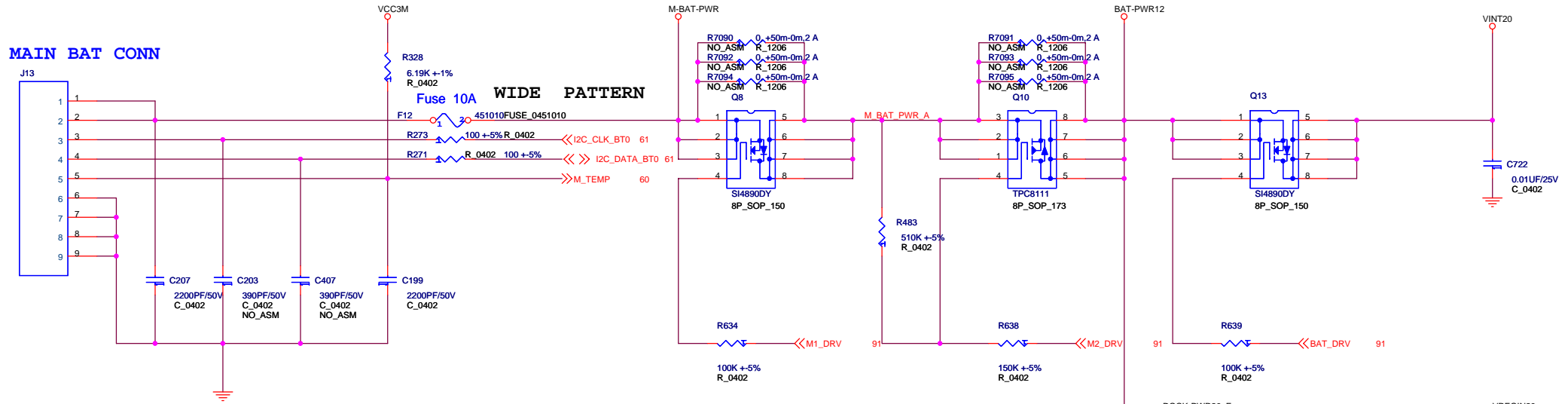
Rev s1.1

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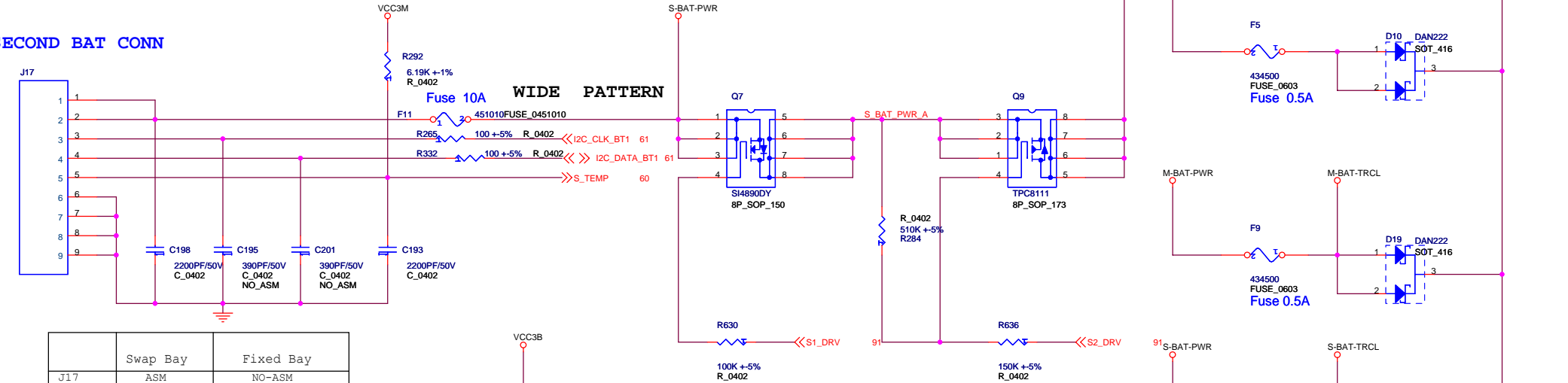
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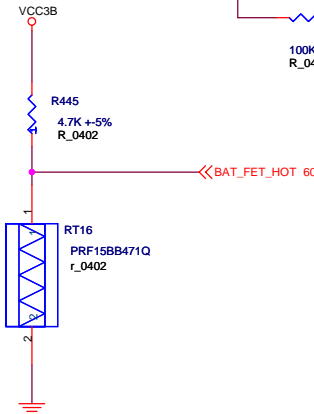
# MAIN BAT CONN



# SECOND BAT CONN



	Swap Bay	Fixed Bay
J17	ASM	NO-ASM
F11	ASM	NO-ASM
R265	ASM	NO-ASM
R332	ASM	NO-ASM
C198	ASM	NO-ASM
C195	ASM	NO-ASM
C201	ASM	NO-ASM
Q7	ASM	NO-ASM
Q9	ASM	NO-ASM
Q8	ASM	NO-ASM
Q10	ASM	NO-ASM
R630	ASM	NO-ASM
R284	ASM	NO-ASM
R636	ASM	NO-ASM
R7090	NO-ASM	ASM
R7091	NO-ASM	ASM
R7092	NO-ASM	ASM
R7093	NO-ASM	ASM
R7094	NO-ASM	ASM
R7095	NO-ASM	ASM
F10	ASM	NO-ASM
D23	ASM	NO-ASM



LENOVO.PND

NB system design section

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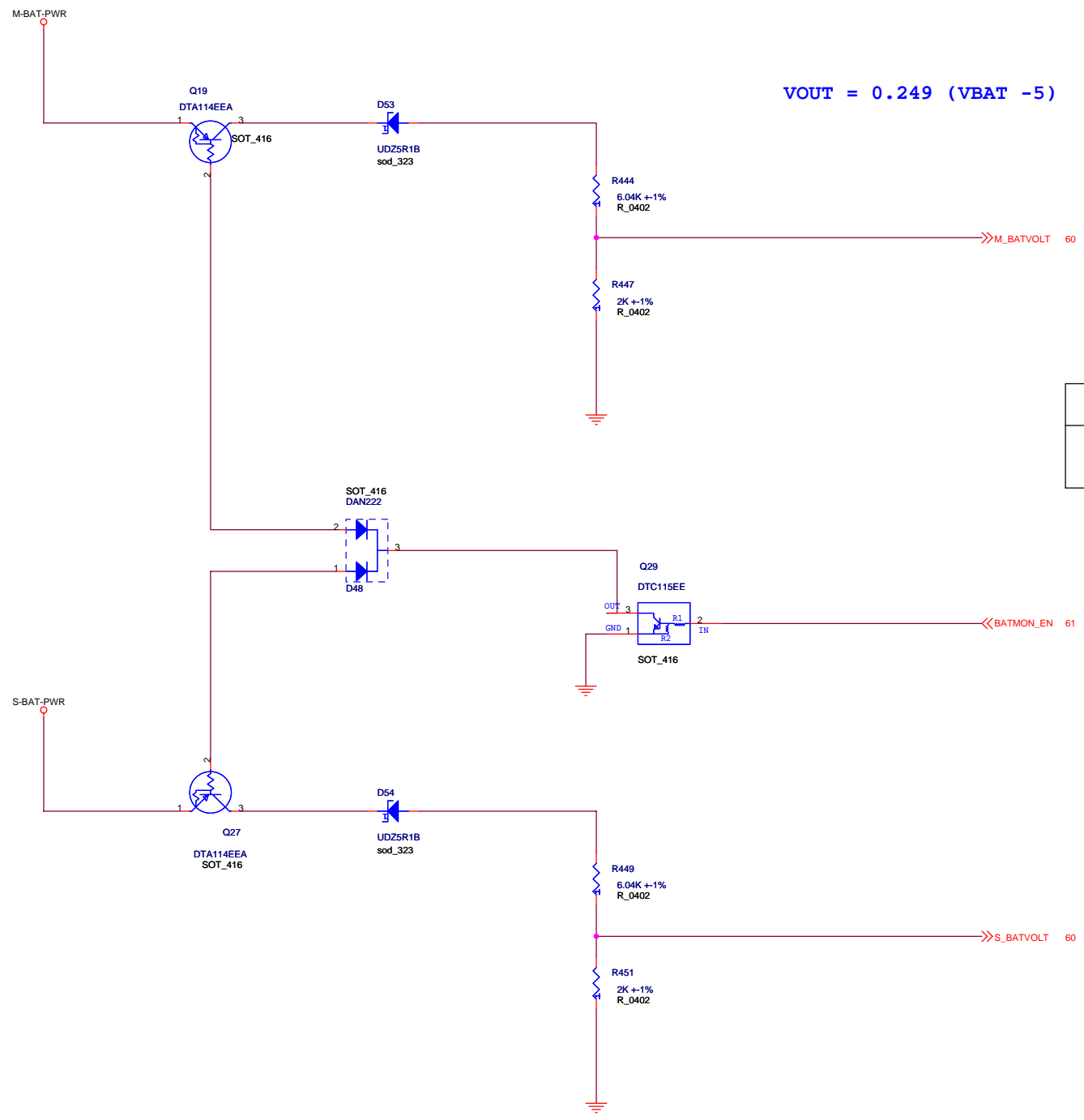
Rev s1.1

Date:


Wednesday, September 13, 2006

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	Swap Bay	Fixed Bay
Q27	ASM	NO-ASM
D54	ASM	NO-ASM
R449	ASM	NO-ASM



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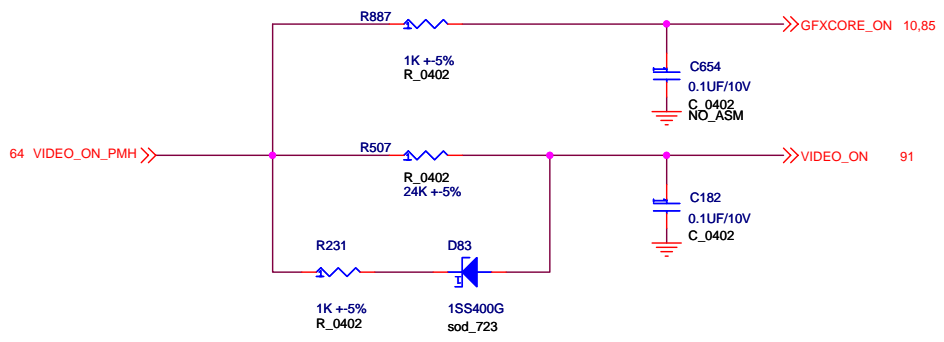
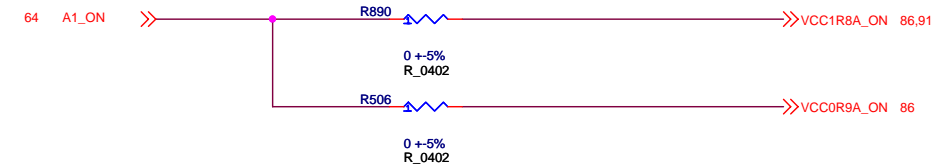
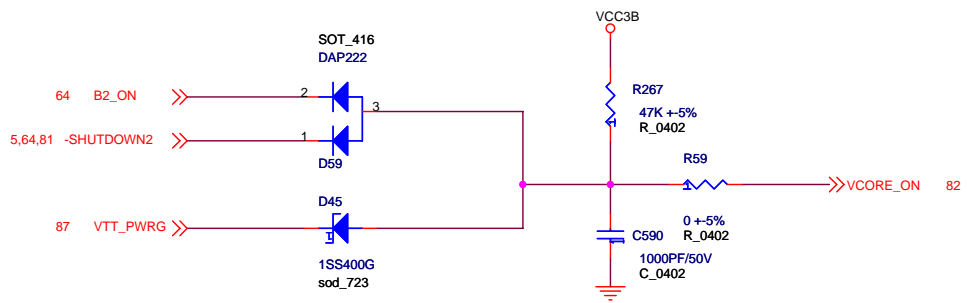
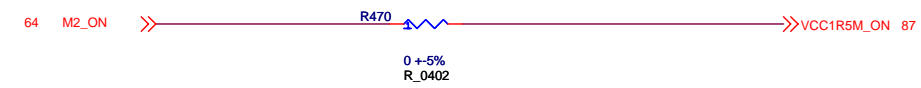
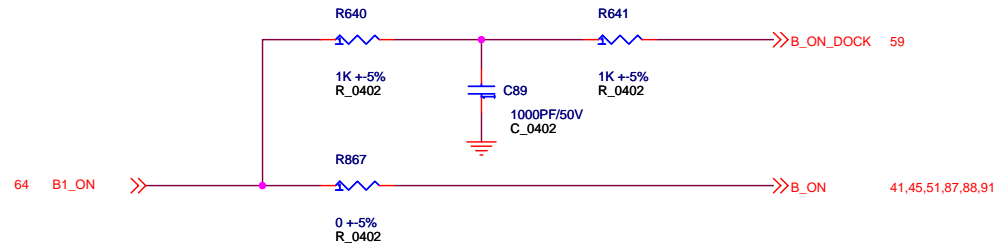
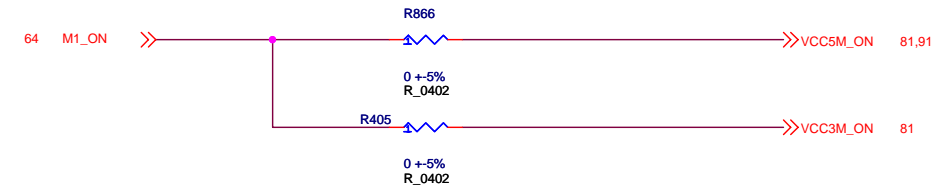
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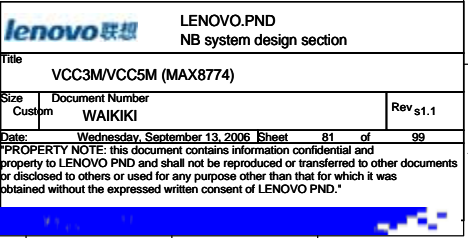
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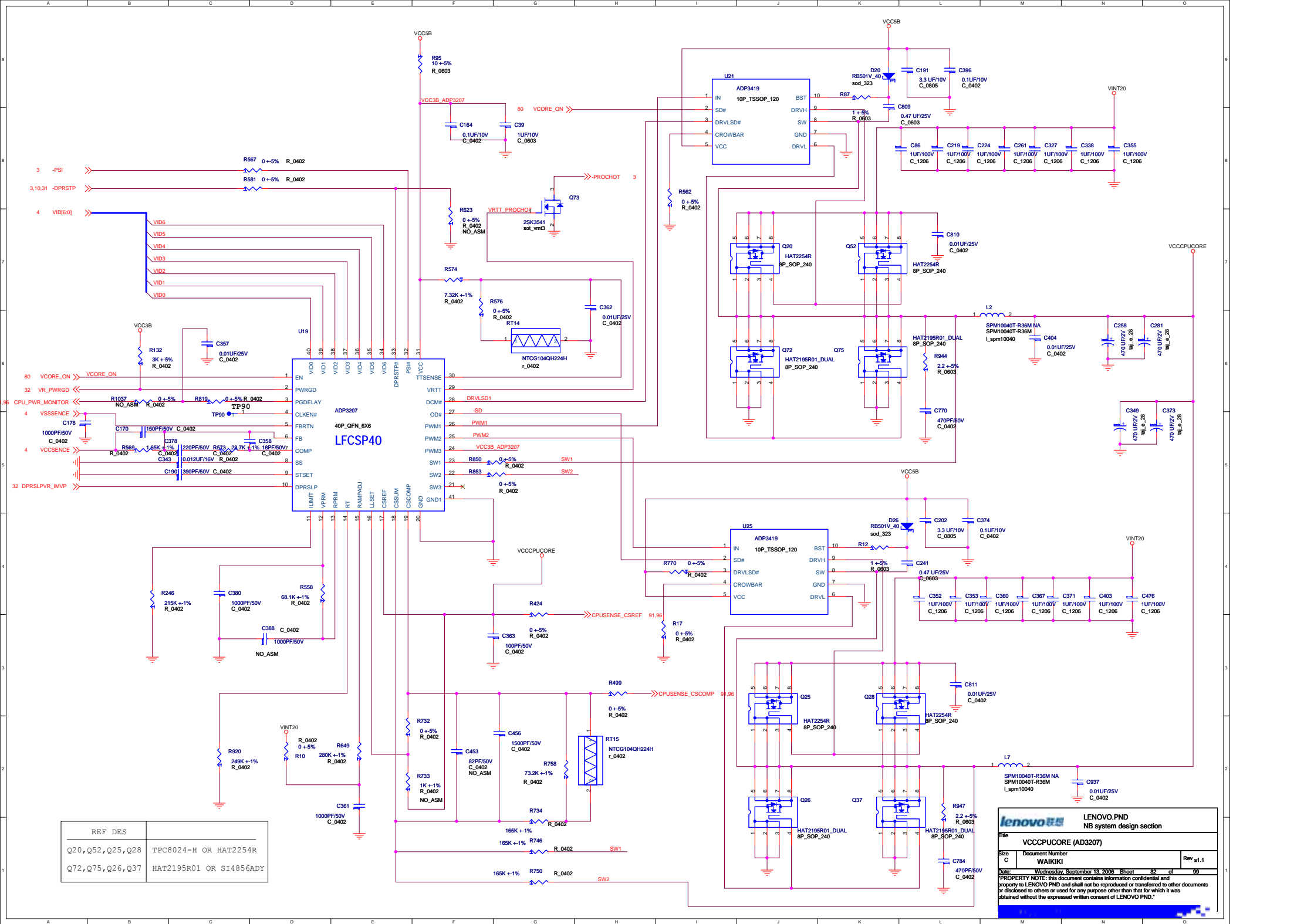


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NB system design section

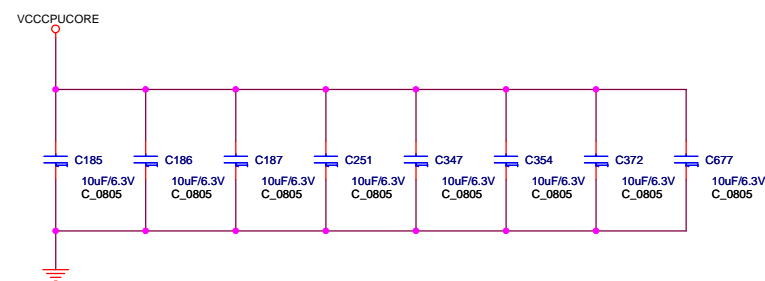
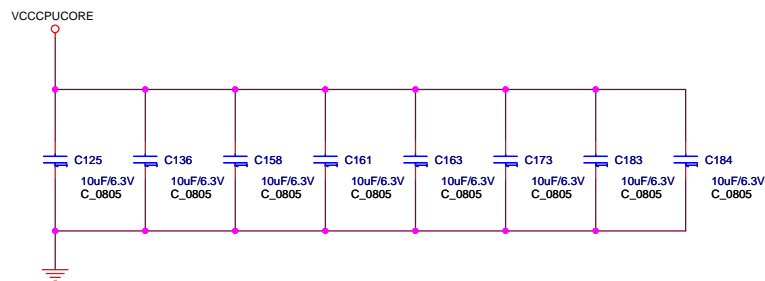
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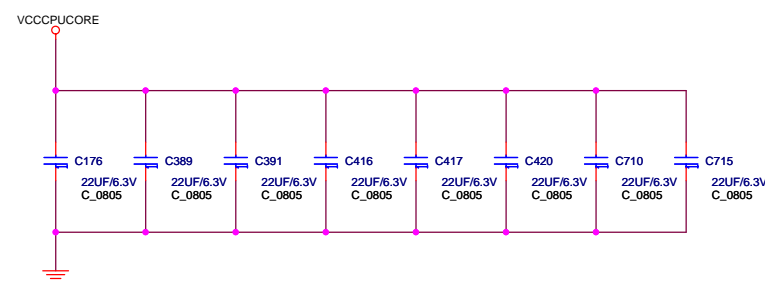
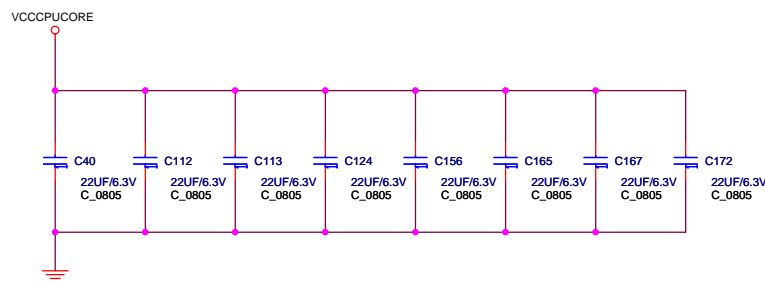




REF DES	
Q20,Q52,Q25,Q28	TPC8024-H OR HAT2254R
Q72,Q75,Q26,Q37	HAT2195R01 OR SI4856ADY





10UF 6.3V X5R 0805 1/16W X 16PCS



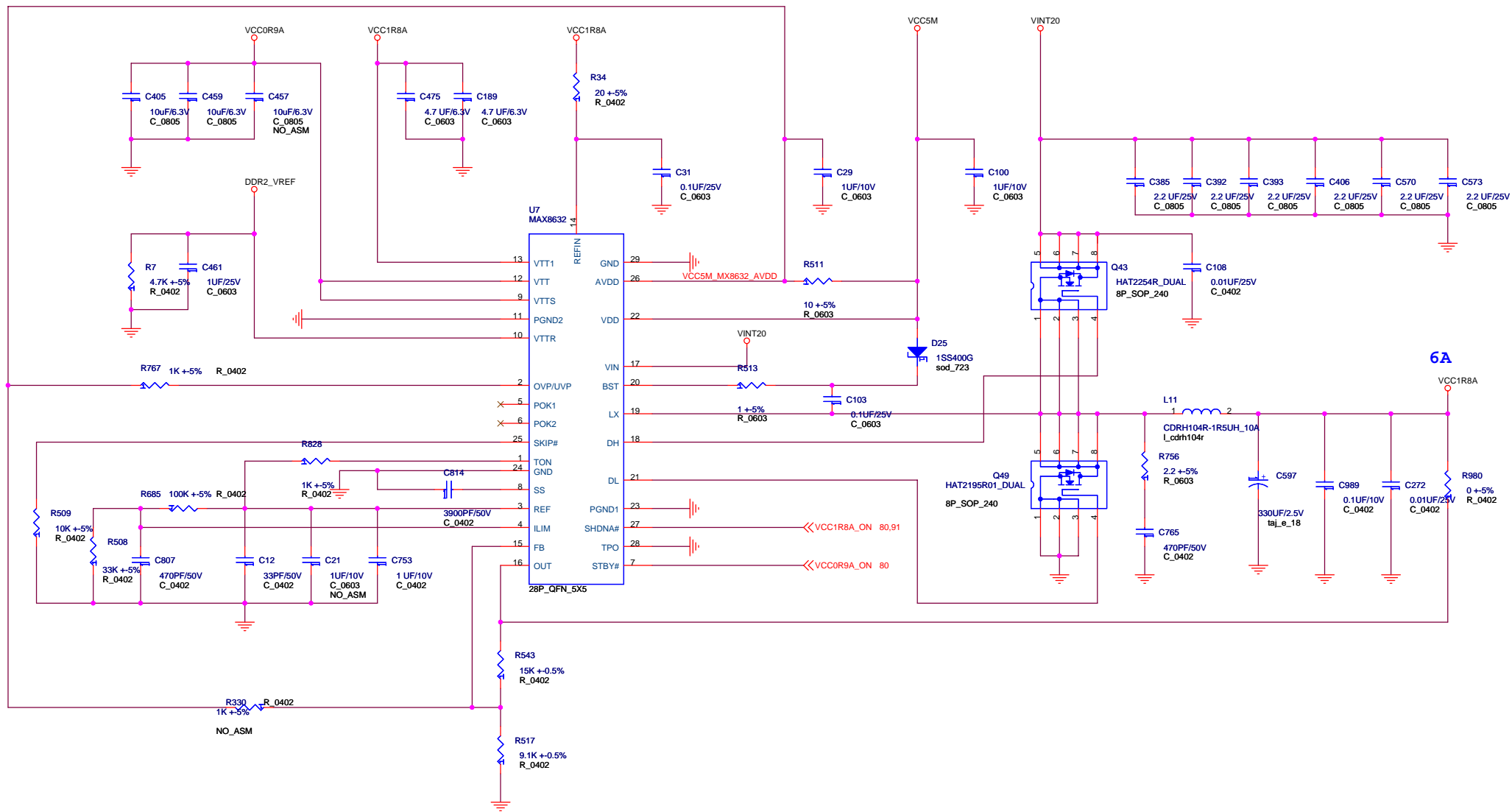
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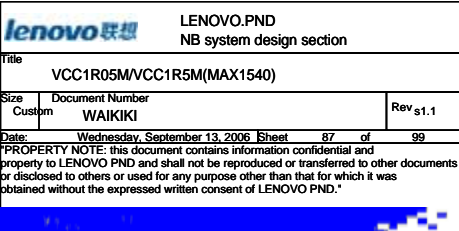
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Size Custom	Document Number WAIKIKI		Rev s1.1
Date: Wednesday, September 13, 2006 Sheet 83 of 99			
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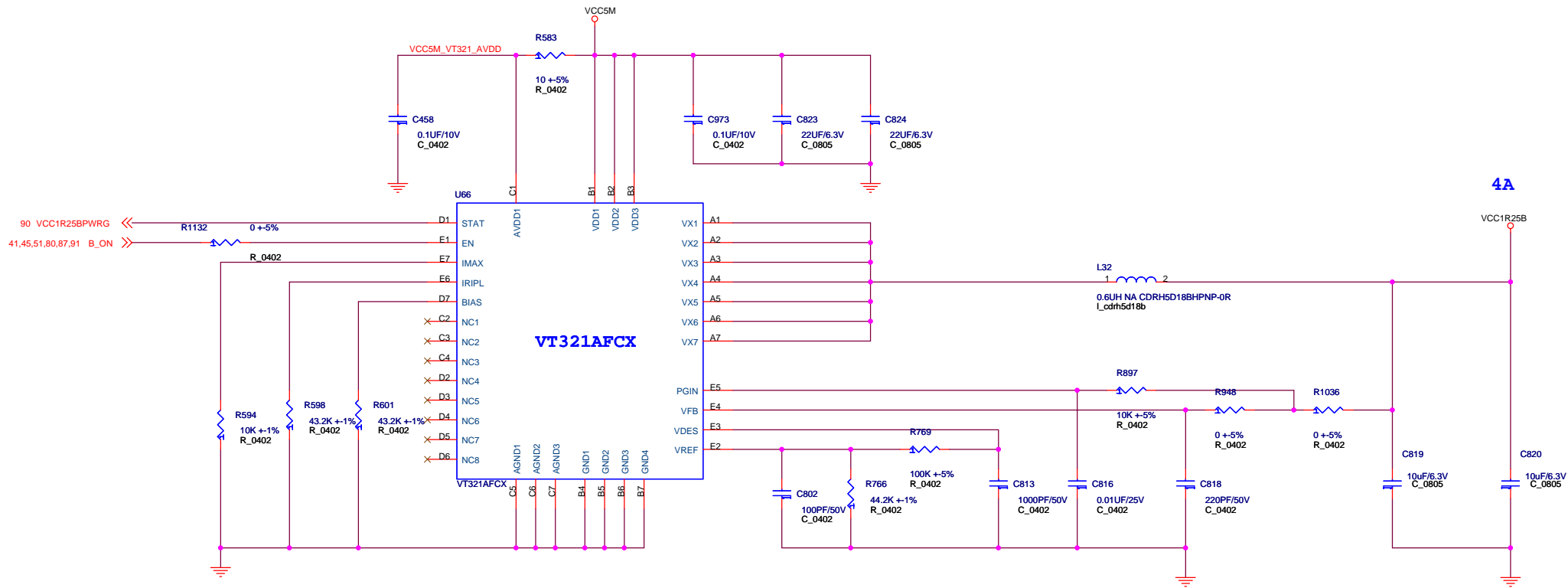
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Size	Document Number		Rev s1.1
Custom	WAIKIKI		
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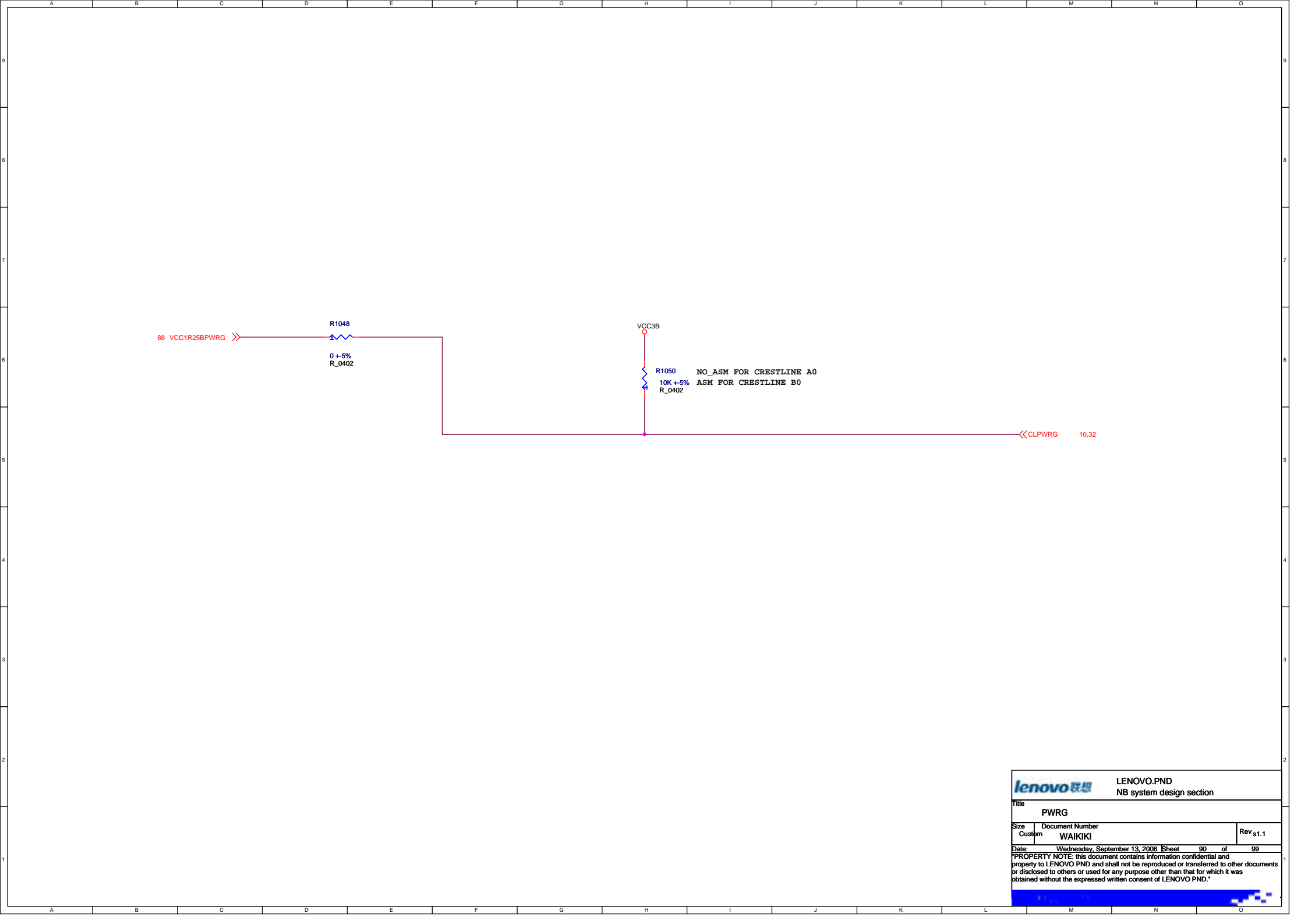








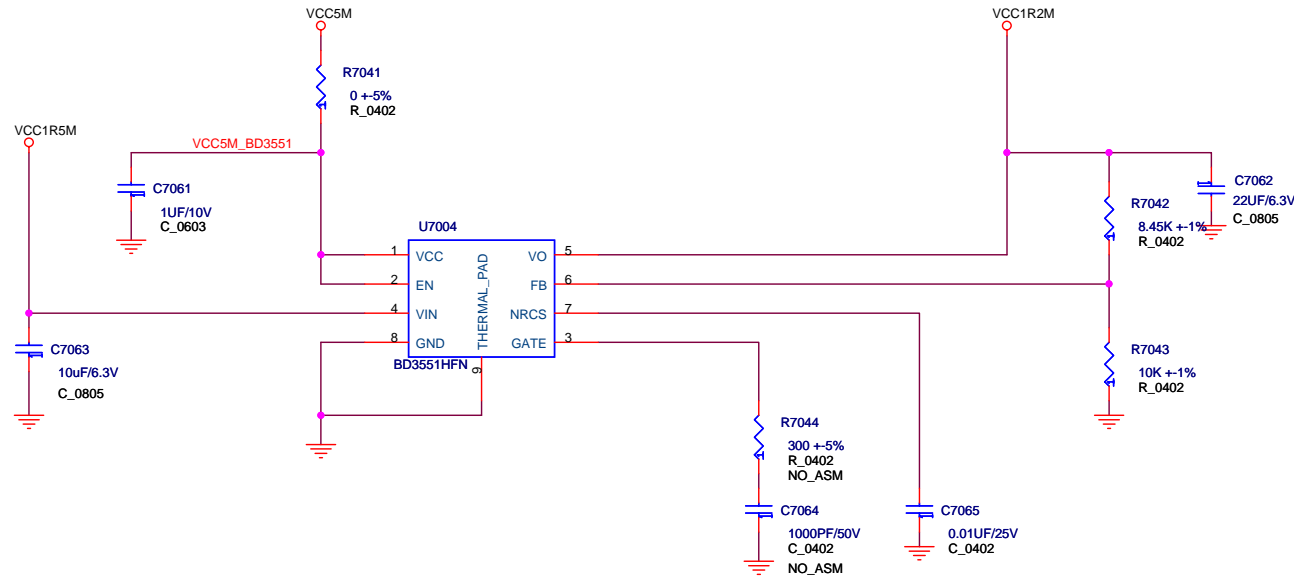
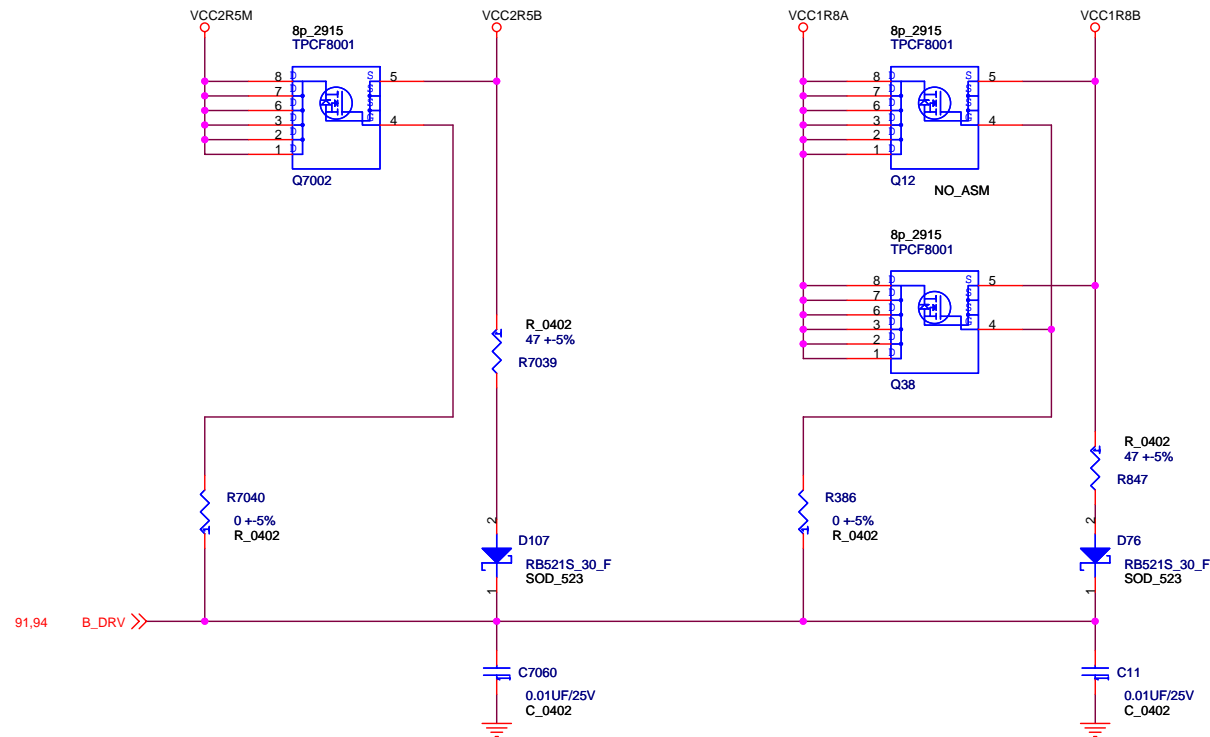
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Title			
LOAD SW AMT			
Size	Document Number		Rev s1.1
Custom	WAIKIKI		
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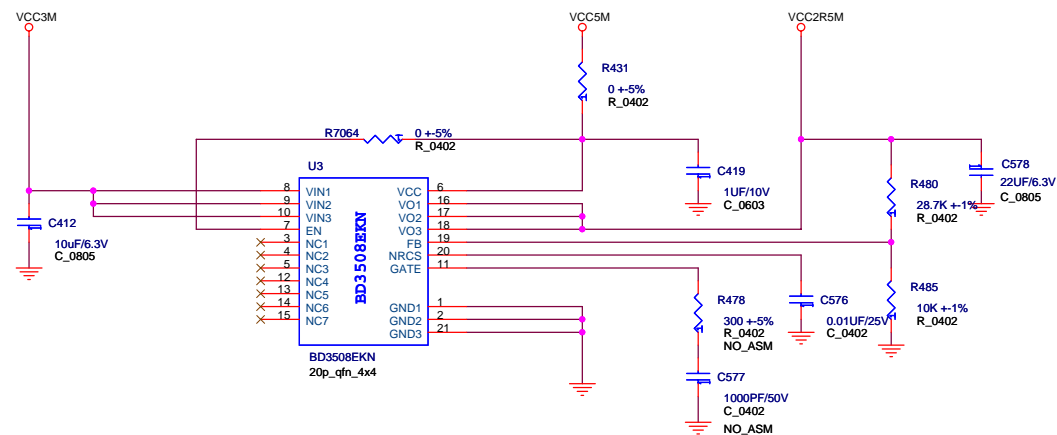


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Title PWRG			
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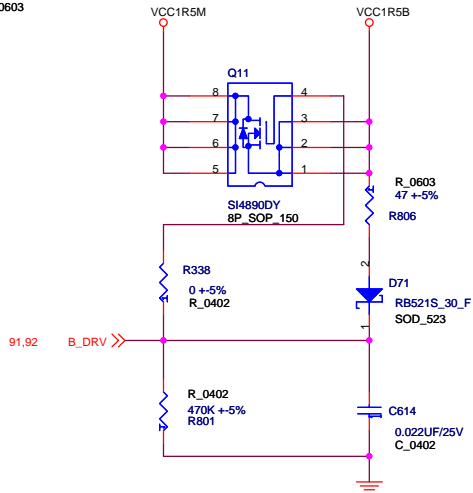
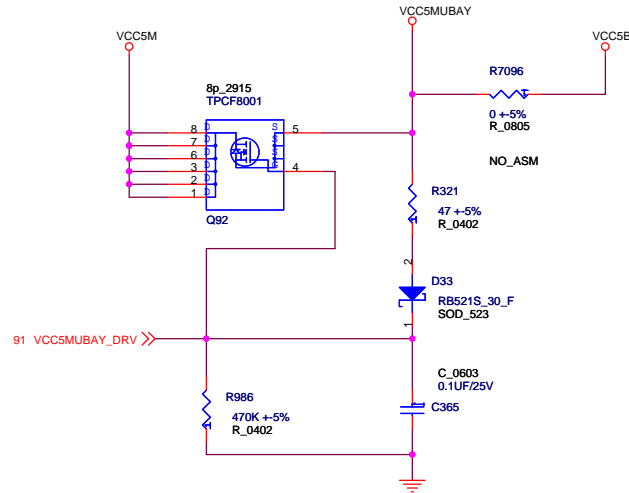
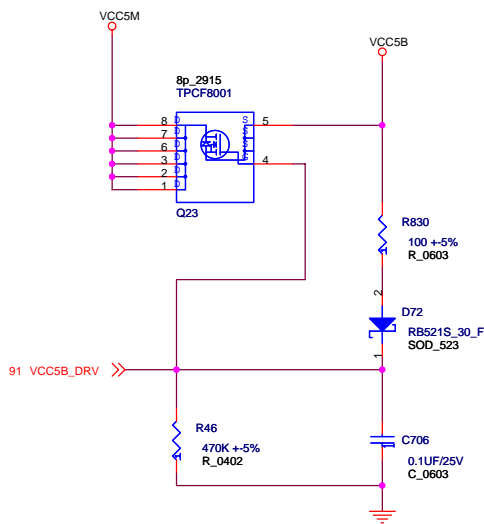
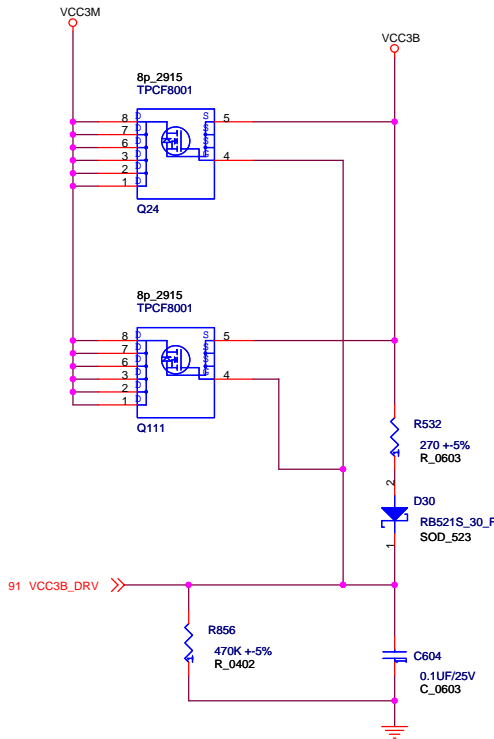



lenovo 联想		LENOVO.PND NB system design section	
Title LOAD SW VIDEO			
Size B	Document Number WAIKIKI		Rev s1.1
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Title LOAD SW AUX1 AUX2			
Size Custom	Document Number WAIKIKI		Rev s1.1
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	SWAP BAY	FIXED BAY
Q92	ASM	NO_ASM
R986	ASM	NO_ASM
R321	ASM	NO_ASM
D33	ASM	NO_ASM
C365	ASM	NO_ASM
R7096	NO_ASM	ASM



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NB system design section

TitleLOAD SW B&UBAY

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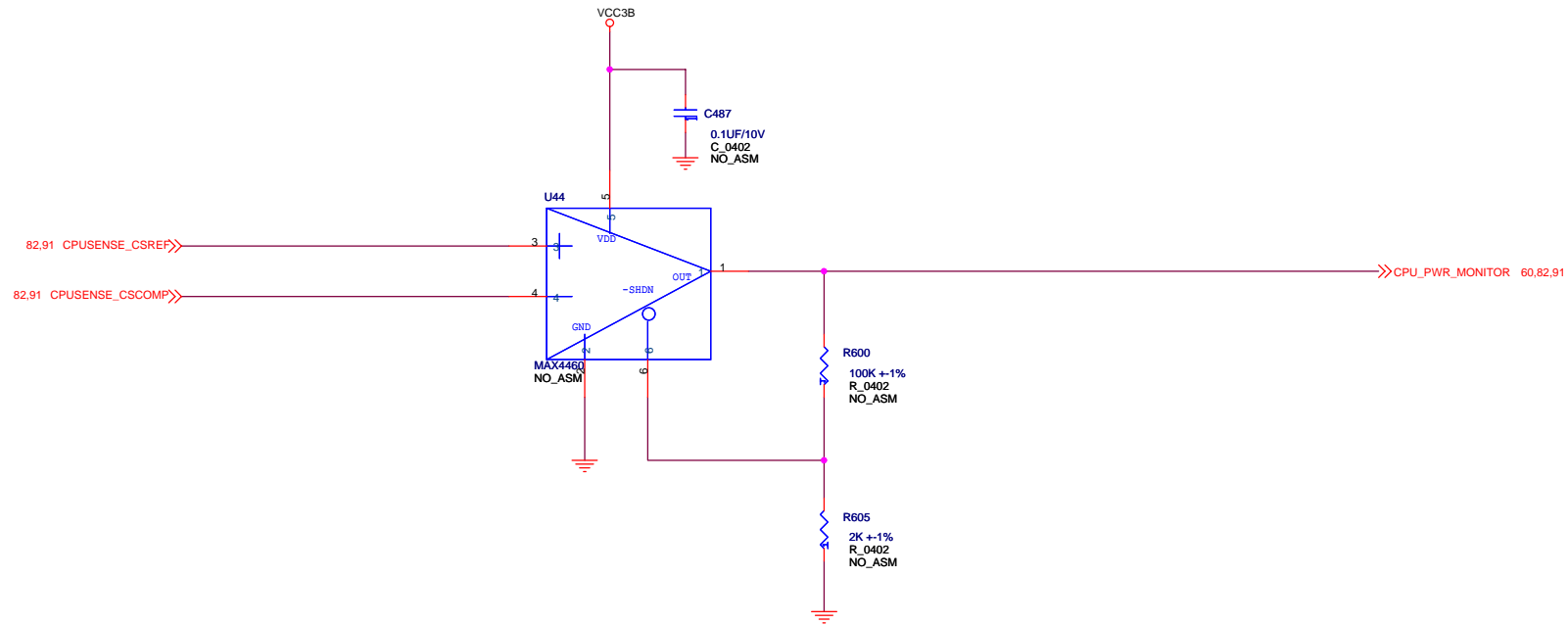
Document NumberWAIKIKI

Rev s1.1

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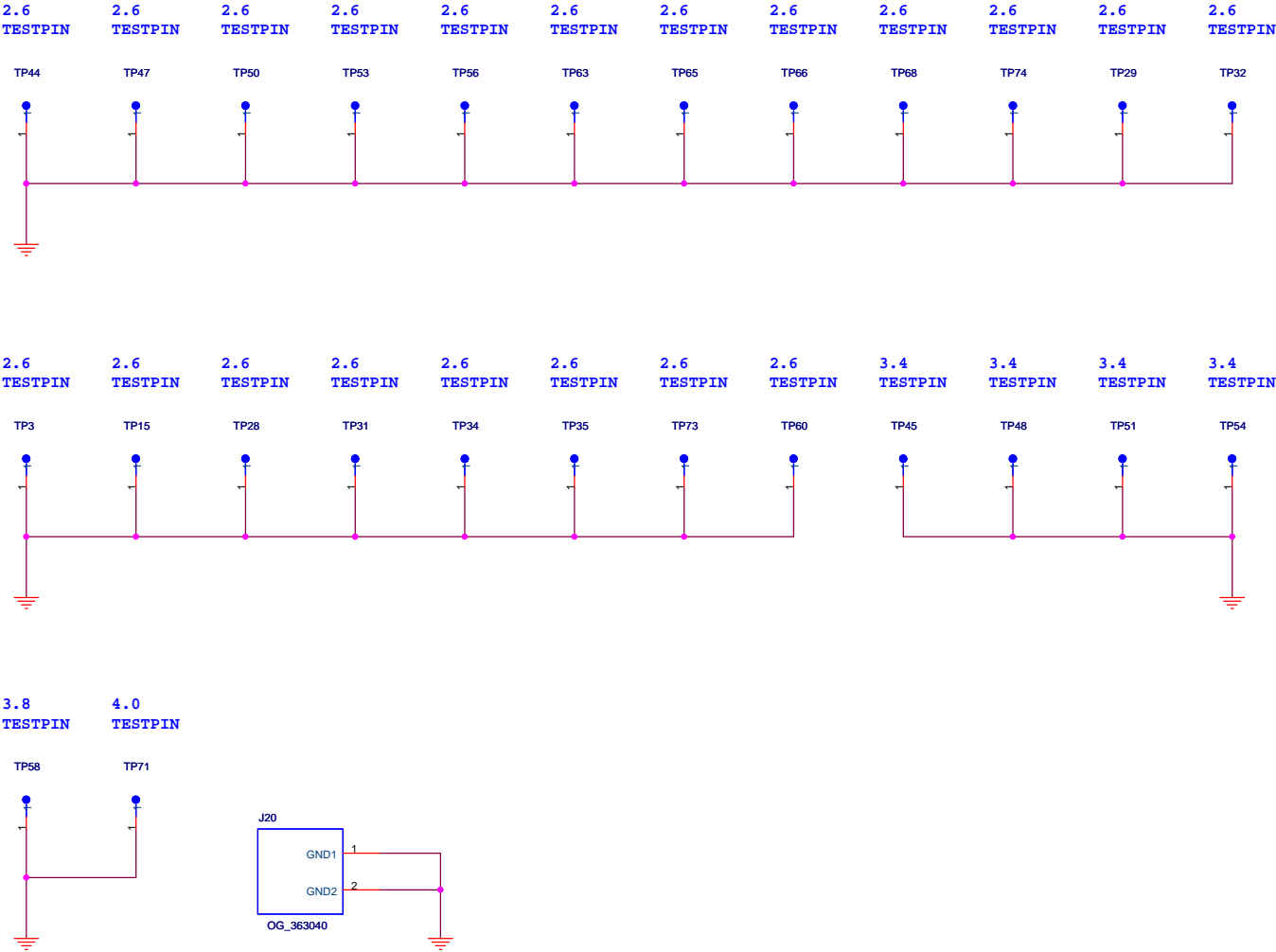
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PTH FOR SCREW HOLE




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## EC History List

EC#	PAGE	Description
WK_SDV_EC001	20	R341 & R27 : 2.2K => 4.7K for DVI ID read fail W /A EC; SDVO_CTRL_DATA/CLK Pullup value change,
WK_SDV_EC002	88	R598 : 47K 5% => 43.2K 1%,C819 & C820 : 22UF => 10UF/6.3V/10%/2125,to correct value of VCC 1R25AMT
WK_SDV_EC003	10	R184 : ASM=>NO ASM,to set default to LOW POWER PCIE (same as DaV -1-Integ/Disc, DaV-3-Disc )
WK_SDV_EC004	12	C762 : 0.1UF => 0.47UF/6.3V/10%/1005,C681, C683 : 0.1UF => 0.22UF/6.3V/10%/1005, C733 : 0.1UF => 2.2UF/6.3V/10%/1608, C579 : 10UF +> 22UF/6.3V/20%/2125, C580 : 10UF +> 4.7UF/6.3V/10%/2125, to meet Intel Design Review results R829 : 6.19K 1% 1005 to adjust voltage of VCC 1R05M Change R1005 from pull down to pull up in order to support MS card R939 ASM(100kQ) -> No ASM R690 : NOASM => 100K ASM,Add 100Kohm pulldown to PANEL_POWER_ON (to meet Intel D.G) R908 : 27K => 16.2K/1%/1005,to change MICVCC from 4.7V To 3.3V for AD1984 R345 ASM -> NO_ASM (0ohm),R1009 NO_ASM -> ASM (0ohm, 83G3823) to enable GbE Disabling from BIOS Setup Menu
WK_SDV_EC005	84	Romove Q108 and nets to remove Security LED Completely
WK_SDV_EC006	48	Q45 : Change NchMOSFET from 2SK3019 to 2SK3541
WK_SDV_EC007	60	Add R772 100K/1% pulldown resistor to VGA_BLON (to meet Intel D.G)
WK_SDV_EC008	63	Add C764,C879,C880,C981,(1000pF x 4) at audio lines near the Dock connector.
WK_SDV_EC009	45	Add C882 100pF at ACDC ID near the DC connector(J24)
WK_SDV_EC010	32	Add Q91,Q97,R773 to prevent inrush current from AC adapter to Battery Change placement note of R11 and R264 in order to prevent connection with VINT 20 Remove Q15,R403,R425,R429,R521,R537,R538,R540,R579,R586,R587,R590,R602,r73 change R494 to 22K 5%, ASM, Change R501,R541,R519,R549,R520 from No ASM to ASM, for GFX_VID PULLUP W/A until BIOS start for Crestline ES1, ES2(Parmanent EC ) Delete R1014,R1015, R1017, R1018, This Resiser prevent to on these FET due to low voltage level
WK_SDV_EC012	28	Add R943 100K 5% pullup
WK_SDV_EC013	22	Change Cap. Parameter C204,C205,C205 from 10V 10% TO 6.3V, 10%
WK_SDV_EC014	11	ADD C883,C884,C885,C886,C887,C888, AND Remove C734,C735,C736, FOR Add de-cap to VCCGFXCORE
WK_SDV_EC015	59	Change C732,C672,C673,C213,C739,C708,C716 to NO_ASM, and change R335,R336,R76 to 1 Ohm to follow supplier 's recommendation
WK_SDV_EC016	75	Change R668 to NO_ASM and R764 to ASM, because GMCH PWM problem was fixed by BIOS correction
WK_SDV_EC017	75	add five 0 ohm resistors: R1053,R1054,R403,R425,R429 in order to follow AMT NON Support
WK_SDV_EC018	75	Insert two blank pages between Page 26 and Page27.
WK_SDV_EC019	85	52,56,59,74 Add Docking BOM Option Tables
WK_SDV_EC020	44	Add R540,R579, 0 Ohm NO_ASM for no Touch Pad support
WK_SDV_EC022	61	Change R305,C293,J26 to NO_ASM for no legacy IO
WK_SDV_EC023	14	Add R586,R587, and add FixedBay or SwapBay BOM Option tables
WK_SDV_EC024	12	
WK_SDV_EC025	19	
WK_SDV_EC026	28	
WK_SDV_EC027	88	
WK_SDV_EC028	26,27	
WK_SDV_EC029	52,56,59,74	
WK_SDV_EC030	63	
WK_SDV_EC031	65,67	
WK_SDV_EC032	37,94	
WK_SDV_EC033	73	Change Q97 from DTC115EE to DTC114EE.
WK_SDV_EC034	19	Change C672 C213 to NO_ASM
WK_SDV_EC035	69,91	Change Q59.3PIN from PWRSHUTDOWN to -SHUTDOWN,add net -SHUTDOWN on U61.16PIN for to divide -pwrshutdown signal into IN and OUT
WK_SDV_EC036	28,32,61	Del GPIO_BT signal and add -S4 STATE signal between H8 and ICH8 for detect S4 state.
WK_SDV_EC037	94	Change Q11 from HAT2195R01 to S14890DY for cost reduction
WK_SDV_EC038	29,74	Change RGB signal CAP C448, C449, C596,C648, C651, C653 from 10pF to 22pF.
WK_SDV_EC039	32	Change R79 from 100ohm 5% to 39ohm 5% to improve waveform of -PLTRST_FAR
WK_SDV_EC040	85	Change R462 =>ASM,R595 100K=>200K,C744=>ASM,C895 2200PF=>1800PF,R905 59K=>43.2K,C660 =>NO_ASM,L23 0.88uh=>0.56uh for meet INTEL GMCH SPEC.
WK_SDV_EC041	78,81	CHANGE C722 0.01uF 25V 1005 10%,C214 C520 C386 C272 C324 C549 C750: 0.01uF 25V 1005 10% in order to reflect cap.
WK_SDV_EC042	86	Change R330 =>NO_ASM,R685 150K=>100K,Q43,Q49 =>HAT2195R01_DUAL in order to raise OCP point and output voltage of VCC 1R8M for DDR2
WK_SDV_EC043		Delete AMT support circuit,Include change AMT power rail,to B power rail,modify AMT signal,detail information please see EC_FOR_AMT_DISABLE_2.PDF
WK_SDV_EC044	63	ADD TouchPad/FingerPrint Support information.
WK_SDV_EC045	78,79,80	ADD SWAP_BAY/FIXED_BAY BATTERY option information when built bom
WK_SDV_EC046	11,14	Add ASM/NOASM table for S -Video support when built bom.
WK_SDV_EC047	11	change R253 from 1.3K to 1.27K, 0.5%
WK_SDV_EC048	80	change R507 from 12K to 24K,and change R507 to R7035
WK_SDV_EC049	91	R855 change to NO_ASM, R852 chang to 49.9K 1%, change R855 to R9036
WK_SDV_EC050	32	Add Cardbus ID
WK_SDV_EC051	53,71	Add R7034, 200ohm, chang C591(C7051) to 0.1UF, 1V, 10%, change C301(C7059) to 0.01UF, 25V, 10%
WK_SDV_EC052	61	Change H8 pin assign of S4_STATE#
WK_SDV_EC053	72	Change default AP3 logic for ADXL322.
WK_SDV_EC054	32	Change to Wailiki ID from Davinci ID, R1021 NO_ASM to ASM, and R1038 ASM to NO_ASM
WK_SDV_EC055	32	Planar ID 0000b for SDV, R113 NO_ASM to ASM, and R672 ASM to NO_ASM
WK_SDV_EC056	45	Co-Layout Headphone amplifier AN12946A
WK_SDV_EC057	45	Replace Ethernet from Nineveh to BCM 5787M (U7002)
WK_SDV_EC058	46	Change system speaker function with BEEP.
WK_SDV_EC059		Add comments for rating of power components.
WK_SDV_EC060	42	C7002 : should be 4.7uF 10V X5R,Q99 : 2SK3541 -> DTC115
WK_SDV_EC061	61,32	Delete 0ohm jumpers and connect directly of H8,and Delete NETDETECT signal.
WK_SDV_EC062	70	Change from D1 to R7052 0ohm and Delete R212.
WK_SDV_EC063	48	To remove hook for R5C584,and circuit for 2nd port of IEEE1394 termination and non-internal regulator
WK_SDV_EC064	48	To update ASM option for IEEE1394 support case(R5C847, R5C803) and non support case (R5C804)
WK_SDV_EC065	32	Pull-up R272 for CL_RST WLAN# -> NO_ASM : Intel's recommendation
WK_SDV_EC066	31	Add 4in1 Slot Presence Detection ID.
WK_SDV_EC067	48	To implement feedback of Circuit review by Ricoh
WK_SDV_EC068	60	ADD RC circuit for fix G-sensor noise problem

		LENOVO.PND NB system design section	
Title PTH SCREW HOLES			
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## EC History List 2

EC#	PAGE	Description
WK_SDV_EC069	70	DEL C41 to remove stub of LPCCLK for TATER
WK_SDV_EC070	81	ADD R7051 R7052 in order to support MAX17003 for Discharge function of VCC3M and VCC5M
WK_SDV_EC071	91	ADD R7045 DEL R868,R820,R876,R900 for change RINKAN-1 circuit for correction.
WK_SDV_EC072	70	Change TC7WB125FK to TC7SB384FU,TC7WB126FK to TC7SB385FU due to cost reduction
WK_SDV_EC073	55	Change C626,C627 to 0402.
WK_SDV_EC074	59,61	Change F14 : ASM -> NOASM,C7073,C7074,C7075,C7076 from 470pf to 330pf.
WK_SDV_EC075	76	Change C99,C90,C233 from 1uF/16V -> 1uF/10V in order to reflect cap.
WK_SDV_EC076	86	U7.5 add MPWRG signal workaround for backup
WK_SDV_EC077	55	Remove MDI_DETECT circuit and Change the power source of Magnetics from VCC1R8AUX to VCC2R5M.
WK_SDV_EC078	53	Modify NVRAM strapping and Connect ENERGY_DET to MDI_DETECT.
WK_SDV_EC079	53	Change BCM power source and add some decoupling Cap.
WK_SDV_EC080	53	VMAINPRSNNT connects VCC3B through R7029,LOW_PWR connects -GBE_DISABLE,Add 0ohm on LAN_XTALI.
WK_SDV_EC081	92	Add VCC1R2M Logic for Broadcom BCM5787M support
WK_SDV_EC082	93	Add VCC2R5M Logic for Broadcom BCM5787M support
WK_SDV_EC083	86,87,91,93	Delete VCC1R8AUX and VCC1R8M,ADD VCC1R8A.
WK_SDV_EC084	72	Make device option for G-SENSOR.
WK_SDV_EC085	32	Follow EC043 correct some singnal.
WK_SDV_EC086	92	Del Q86 for del VCC3VIDEO power.
WK_SDV_EC087	32,64	ADD -PHY_PD ICH signal then use it and -GBE_DISABLE to generate AUX_ON.
WK_SDV_EC088	7	Del R135,R387.
WK_SDV_EC089	64	Change SPI interface signal jumper resistor from 0 ohm to 33 ohm.
WK_SDV_EC090	85	Add pull-down resistor on Gfx_VID signal.
WK_SDV_EC091	11	change R772 to 100k +-5%,change R7071/R7072 to 33 +-5% for cost reduce.
WK_SDV_EC092	38	change FL7 to ACM2012-900-2P,R650/651 to 0 ohm 0603.
WK_SDV_EC093	10	Add R7073/R7074 on GFXCORE_ON signal for INTEL comment.
WK_SDV_EC094	50	Change PCMCIA Slot to new one.
WK_SDV_EC095	80,93	Change Q12.1 to VCC1R8A,add R7064 0ohm between U3.7 to U3.6,del R373 and VCC1R8M_ON signal.

WK_SDV_EC096	53	Correct the broadcom lan circuit,like change Ferrite bead to bigger type.
WK_SDV_EC097	31,32,64	Terminate opened pin and unused pin on ICH8M,and del -GBE_DISABLE net.
WK_SDV_EC098	84	Delete of VCC1R05M power rail.
WK_SDV_EC099	84	Change assignment of PC card slot connector.
WK_SDV_EC0100	19,33,54	Modify PCIE interface of BCM5787M,remove Robson interface.
WK_SDV_EC0101	63	Del C631 and C636 on the FPR USB lines.
WK_SDV_EC0102	54,57	Add bypass capacitors for VCC3B at miniPCIE slots.
WK_SDV_EC0103	32	Change R7006 from NO_ASM to ASM.
WK_SDV_EC0104	52,53,54,57	Change MID* netname to MDI* ,correct PCIE slot connect,add Lan eeprom table.
WK_SDV_EC0105	37	Del CD_IN function on ODD.
WK_SDV_EC0106	23	Change R7083 to 130 ohm,D7006 to SML_A10MT.TC7WB125AFK to TC7SBL384AFU.
WK_SDV_EC0108	32	Correct 4_in_1 slot ID.
WK_SDV_EC0109	91	Enable Rinkan_1 -LPMORE function.
WK_SDV_EC0110	53	Follow WK_SDV_EC096. Add strap option for Broadcom.
WK_SDV_EC0111	50	Delete VCC3_MC at PC card slot.
WK_SDV_EC0112	23	Add comments for xd function route.
WK_SDV_EC0113		Location ID to be consistent to DaVinci-3.Only change Location IDs . NO CHANGE in connection in schematics.
WK_SDV_EC0114	86	Change L11 to CDRH104R 1R5UH 10A follow DV3.
WK_SDV_EC0115	31,53	Connect LAN_RSTSYNC on ICH8 to GND,and modify LAN strapping table.

WK_SDV_EC0116	45,46,61	Add beep enable circuit and correct connection.
WK_SDV_EC0117	53	Add pull-up resistor R7097 for EEPROM WP signal.
WK_SDV_EC0118	23	Change J29 to Tyco from FOXCONN.
WK_SDV_EC0119	85	Correct discription.
WK_SDV_EC0120	85	Change R7062 rename to R212.
WK_SDV_EC0121	86	Del R17 and not connect MPWRG on U7.5 PIN
WK_SDV_EC0122	23	Correct slot pin.
WK_SDV_EC0123	38	R650/R651 marge to 1 network resistor RN5.
WK_SDV_EC0124	52,53,55	Change pin definition of T1/T2/U12.
WK_SDV_EC0125	61	Change RN54 to discrete resistor .
WK_SDV_EC0126		Parameter change and Error Correction.
WK_SDV_EC0127	84	R598,R601,C819,C820 Part value change.
WK_SDV_EC0128	58	DEL U53,U6 function for Live SPI rom one chip only.
WK_SDV_EC0129	95	No asm R996,Q64 change LPMODE EC.
WK_SDV_EC0130	52	ADD C637 ASSEMBLE STATION ON TABLE.
WK_SDV_EC0131	45	Move Panasonicamplifier from planar layout to dummy subcard.
WK_SDV_EC0132	36	BOM change,J1 change to HAS1-2DFB12-51K.
WK_SDV_EC0133	81	BOM change,ADP3209 update to X5 version.
WK_SDV_EC0134	81	Change C482 to R1072 for use ADP3209 X5.
WK_SDV_EC0135	61	Change R1042 to 10K 5%.
WK_SDV_EC0136	91	Change C609 1U 0603 to 2.2U 0805 for prevent power shutdown when AC Adapter attached.
WK_SDV_EC0137	41	Disconnect U8 centerpad from AGND.
WK_SDV_EC0138	53	Add test point to U8 pin4,8,9.
WK_SDV_EC0139	52	RC termination should be NO_ASM.R611, R613, R615, R661, R610, R612, R660, R614, C528, C532, C527, C529 -> NO_ASM
WK_SDV_EC0140	56	Please stuff the TVS diode D103,D104 of Ethernet.
WK_SDV_EC0141	53	Change U7003 to 24C64 for the evaluation on DV4.After the evaluation without any issue, we plan to use 24L01 for Volume production to reduce the cost.stuff
R7075 R7077,no asm R7076		R7078.
WK_SDV_EC0142	45	Change R655,R833,R700,R66 to 47K 5%,R67 No_asm.
WK_SDV_EC0143	85	Change ADP3209 X5 P/N to #DV3190.

EC#	PAGE	Description
WK_SDV_EC0144	72	Update logic table to support new G_sensor ST Micro LIS244AL.
WK_SDV_EC0145	85	Update L23 to #DV3127 SPM10040T-R56M150
WK_SDV_EC0146	85	Update L11 to #DV3014
WK_SDV_EC0147		Update #DV3123 description to CHIP 0.6UH +-20% 8.2A SMT 2PIN .
WK_SDV_EC0148		Just correct/update description of schematic.
WK_SDV_EC0149	23	Change J29 P/N to 42W3121.

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