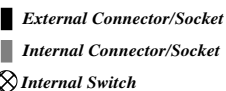


Project Code: 91.4LY01.001
PCB(Raw Card): 12298-2



RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805.

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

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Title			
EC HISTORY			
Size	Document Number		Rev
C	Mystique-1		-2
Date: Tuesday, November 12, 2013		Sheet 2 of	79

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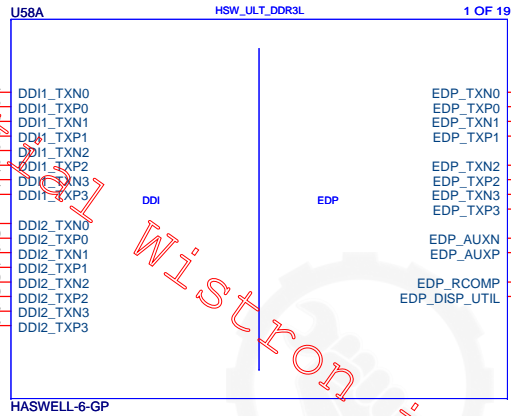
2013/11/17

27 DDIP1_0N
27 DDIP1_0P
27 DDIP1_1N
27 DDIP1_1P
27 DDIP1_2N
27 DDIP1_2P
27 DDIP1_3N
27 DDIP1_3P

37 DDIP2_0N
37 DDIP2_0P
37 DDIP2_1N
37 DDIP2_1P
37 DDIP2_2N
37 DDIP2_2P
37 DDIP2_3N
37 DDIP2_3P

C54
C55
B59
C58
B65
A55
A57
B57

C51
C50
C53
B54
C49
B50
A53
B53



VCCCPUIO_A

R5
24D9R1F-GP

C45
B46
A47
B47

C47
C46
A49
B49

A45
B45

D20 eDP_COMP
A43

eDP_TXN0 26
eDP_TXP0 26
eDP_TXN1 26
eDP_TXP1 26

eDP_TXN2 26
eDP_TXP2 26
eDP_TXN3 26
eDP_TXP3 26

eDP_AUXN 26
eDP_AUXP 26

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Title CPU(1/16) : DDI/EDP

Size B Document Number

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Rev -2

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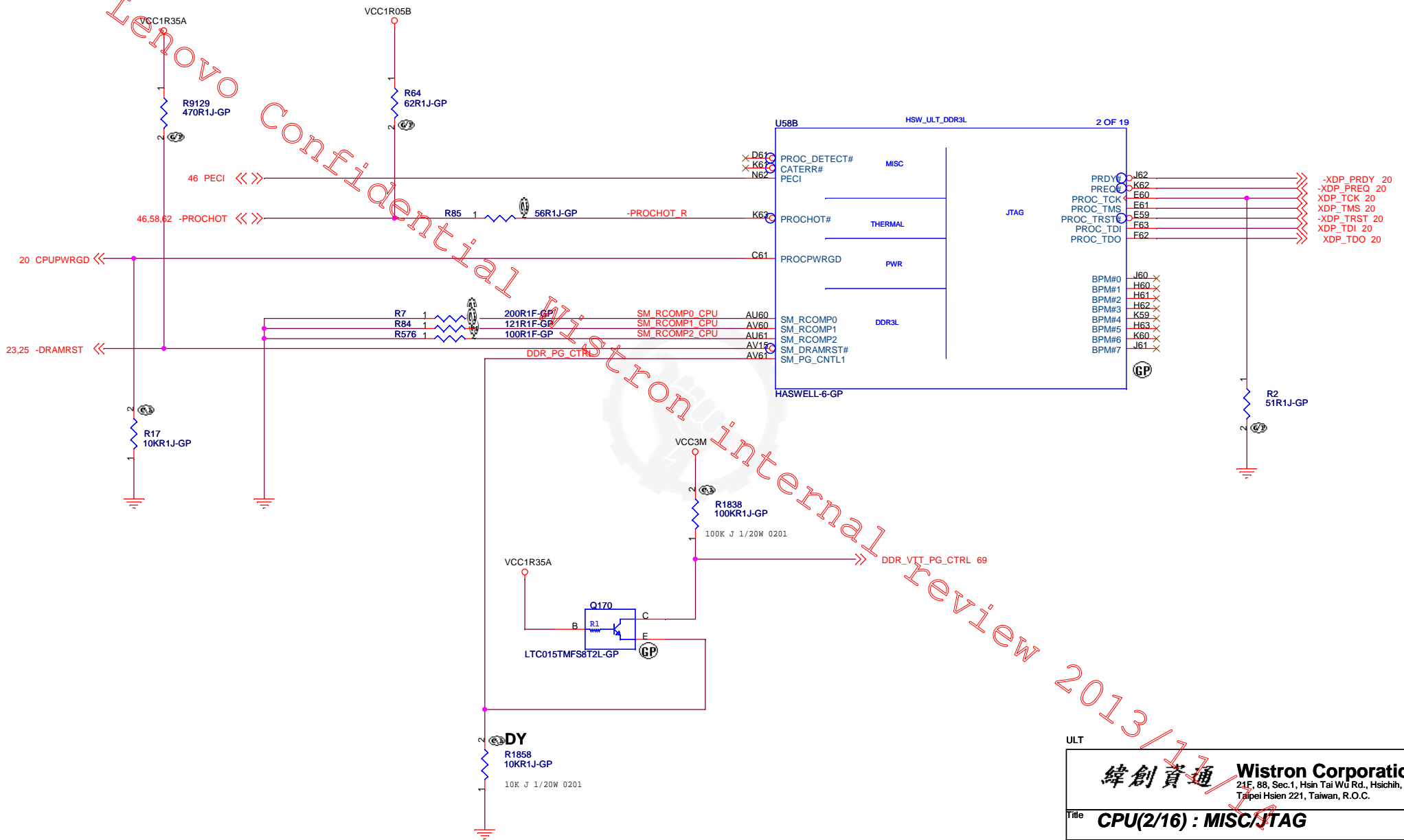
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Title CPU(2/16) : MISC/JTAG	
Size B	Document Number
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23 M_A_DQ[63:0] << >>

M_A_DQ0
M_A_DQ1
M_A_DQ2
M_A_DQ3
M_A_DQ4
M_A_DQ5
M_A_DQ6
M_A_DQ7
M_A_DQ8
M_A_DQ9
M_A_DQ10
M_A_DQ11
M_A_DQ12
M_A_DQ13
M_A_DQ14
M_A_DQ15
M_A_DQ16
M_A_DQ17
M_A_DQ18
M_A_DQ19
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M_A_DQ62
M_A_DQ63

AH63
AH62
AK63
AK62
AH61
AH60
AK61
AK60
AM63
AM62
AP63
AP62
AM61
AM60
AP61
AP60
AP58
AR58
AM57
AK57
AL58
AK58
AR57
AN57
AP55
AR55
AM54
AK54
AL55
AK55
AR54
AN54
AY58
AW58
AY56
AW56
AV58
AU58
AV56
AU56
AY54
AW54
AY52
AW52
AV54
AU54
AV52
AU52
AK40
AK42
AM43
AM45
AK45
AK43
AM40
AM42
AM46
AK46
AM49
AK49
AM48
AK48
AM51
AK51

SA_DQ0
SA_DQ1
SA_DQ2
SA_DQ3
SA_DQ4
SA_DQ5
SA_DQ6
SA_DQ7
SA_DQ8
SA_DQ9
SA_DQ10
SA_DQ11
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SA_DQ13
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SA_DQ54
SA_DQ55
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SA_DQ57
SA_DQ58
SA_DQ59
SA_DQ60
SA_DQ61
SA_DQ62
SA_DQ63

U58C

HSW_ULT_DDR3L

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SA_CLK#0
SA_CLK#1
SA_CLK#1
SA_CLK#1
SA_CKE0
SA_CKE1
SA_CKE2
SA_CKE3
SA_CS#0
SA_CS#1
SA_ODT0
SA_RAS#0
SA_WE#0
SA_CAS#0
SA_BA0
SA_BA1
SA_BA2
SA_MA0
SA_MA1
SA_MA2
SA_MA3
SA_MA4
SA_MA5
SA_MA6
SA_MA7
SA_MA8
SA_MA9
SA_MA10
SA_MA11
SA_MA12
SA_MA13
SA_MA14
SA_MA15
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SA_DQS#1
SA_DQS#2
SA_DQS#3
SA_DQS#4
SA_DQS#5
SA_DQS#6
SA_DQS#7
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SA_DQSP3
SA_DQSP4
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SA_DQSP6
SA_DQSP7
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SM_VREF_DQ0
SM_VREF_DQ1

AU37
AV37
AW36
AY36
AU43
AW43
AY42
AY43
AP33
AR32
AP32
AY34
AW34
AU34
AU35
AV35
AY35
AU36
AY37
AR38
AP36
AU39
AR36
AV40
AW39
AY39
AU40
AP35
AU41
AR35
AV42
AU42
AJ61
AN62
AM58
AM55
AV57
AV53
AL43
AL48
AJ62
AN61
AN58
AN55
AW57
AW53
AL42
AL49
AP49
AR51
AP51

-M_A_DDRCLK0_800M 23,24
M_A_DDRCLK0_800M 23,24
M_A_CKE0 23,24
M_A_CKE1 23
-M_A_CS0 23,24
-M_A_CS1 23
-M_A_RAS 23,24
-M_A_WE 23,24
-M_A_CAS 23,24
M_A_BS0 23,24
M_A_BS1 23,24
M_A_BS2 23,24
M_A_A0
M_A_A1
M_A_A2
M_A_A3
M_A_A4
M_A_A5
M_A_A6
M_A_A7
M_A_A8
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M_A_A11
M_A_A12
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M_A_A14
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-M_A_DQS2
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-M_A_DQS5
-M_A_DQS6
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M_A_DQS2
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M_A_DQS7
DDR3_VREF_DQ M3 23
DDR3_VREF_DQ M3 23
DDR3_VREF_DQ M3 25

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M_A_A1
M_A_A2
M_A_A3
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M_A_A5
M_A_A6
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-M_A_DQS2
-M_A_DQS3
-M_A_DQS4
-M_A_DQS5
-M_A_DQS6
-M_A_DQS7
M_A_DQS0
M_A_DQS1
M_A_DQS2
M_A_DQS3
M_A_DQS4
M_A_DQS5
M_A_DQS6
M_A_DQS7

M_A_A[15:0] 23,24
-M_A_DQS[7:0] 23
M_A_DQS[7:0] 23

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Title CPU(3/16) : DDR3L CHANNEL-A		
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Mystique-1		
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25 M_B_DQ[63:0]

M_B_DQ0 AY31
M_B_DQ1 AW31
M_B_DQ2 AY29
M_B_DQ3 AW29
M_B_DQ4 AY31
M_B_DQ5 AU31
M_B_DQ6 AV29
M_B_DQ7 AU29
M_B_DQ8 AY27
M_B_DQ9 AW27
M_B_DQ10 AY25
M_B_DQ11 AW25
M_B_DQ12 AY27
M_B_DQ13 AU27
M_B_DQ14 AV25
M_B_DQ15 AU25
M_B_DQ16 AM29
M_B_DQ17 AK29
M_B_DQ18 AL28
M_B_DQ19 AK28
M_B_DQ20 AR29
M_B_DQ21 AN29
M_B_DQ22 AR28
M_B_DQ23 AP28
M_B_DQ24 AN26
M_B_DQ25 AR26
M_B_DQ26 AR25
M_B_DQ27 AP25
M_B_DQ28 AK26
M_B_DQ29 AM26
M_B_DQ30 AK25
M_B_DQ31 AL25
M_B_DQ32 AY23
M_B_DQ33 AW23
M_B_DQ34 AY21
M_B_DQ35 AW21
M_B_DQ36 AV23
M_B_DQ37 AU23
M_B_DQ38 AV21
M_B_DQ39 AU21
M_B_DQ40 AY19
M_B_DQ41 AW19
M_B_DQ42 AY17
M_B_DQ43 AW17
M_B_DQ44 AV19
M_B_DQ45 AU19
M_B_DQ46 AV17
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M_B_DQ52 AN22
M_B_DQ53 AP21
M_B_DQ54 AK21
M_B_DQ55 AK22
M_B_DQ56 AN20
M_B_DQ57 AR20
M_B_DQ58 AK18
M_B_DQ59 AL18
M_B_DQ60 AK20
M_B_DQ61 AM20
M_B_DQ62 AR18
M_B_DQ63 AP18

U58D

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SB_CK#0 AM38
SB_CK#1 AN38
SB_CK#1 AK38
SB_CK#1 AL38
SB_CKE0 AY49
SB_CKE1 AU50
SB_CKE2 AW49
SB_CKE3 AV50
SB_CS#0 AM32
SB_CS#1 AK32
SB_ODT0 AL32
SB_RAS0 AM35
SB_WE0 AK35
SB_CAS0 AM33
SB_BA0 AL35
SB_BA1 AM36
SB_BA2 AU49
SB_MA0 AP40
SB_MA1 AR40
SB_MA2 AP42
SB_MA3 AR42
SB_MA4 AR45
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SB_MA8 AY47
SB_MA9 AU46
SB_MA10 AK36
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SB_MA12 AU47
SB_MA13 AK33
SB_MA14 AR46
SB_MA15 AP46
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SB_DQSP2 AM28
SB_DQSP3 AM25
SB_DQSP4 AV22
SB_DQSP5 AW18
SB_DQSP6 AM21
SB_DQSP7 AM18

-M_B_DDRCLK0_800M 24,25
M_B_DDRCLK0_800M 24,25

M_B_CKE0 24,25
M_B_CKE1 25

-M_B_CS0 24,25
-M_B_CS1 25

-M_B_RAS 24,25
-M_B_WE 24,25
-M_B_CAS 24,25

M_B_BS0 24,25
M_B_BS1 24,25
M_B_BS2 24,25

M_B_A0
M_B_A1
M_B_A2
M_B_A3
M_B_A4
M_B_A5
M_B_A6
M_B_A7
M_B_A8
M_B_A9
M_B_A10
M_B_A11
M_B_A12
M_B_A13
M_B_A14
M_B_A15

M_B_A[15:0] 24,25

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-M_B_DQS1
-M_B_DQS2
-M_B_DQS3
-M_B_DQS4
-M_B_DQS5
-M_B_DQS6
-M_B_DQS7

-M_B_DQS[7:0] 25

M_B_DQS0
M_B_DQS1
M_B_DQS2
M_B_DQS3
M_B_DQS4
M_B_DQS5
M_B_DQS6
M_B_DQS7

M_B_DQS[7:0] 25



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Title CPU(4/16) : DDR3L CHANNEL-B

Size B Document Number Mystique-1 Rev -2

Date: Tuesday, November 12, 2013 Sheet 6 of 79

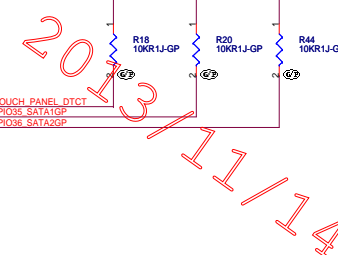
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.F61 [7P capacitor]
 .F31 [8P capacitor]
 .C01 [10P capacitor]

VCC SUB RTCVCC

R1009 1K R1J-GP R406 250K J-GP

GND

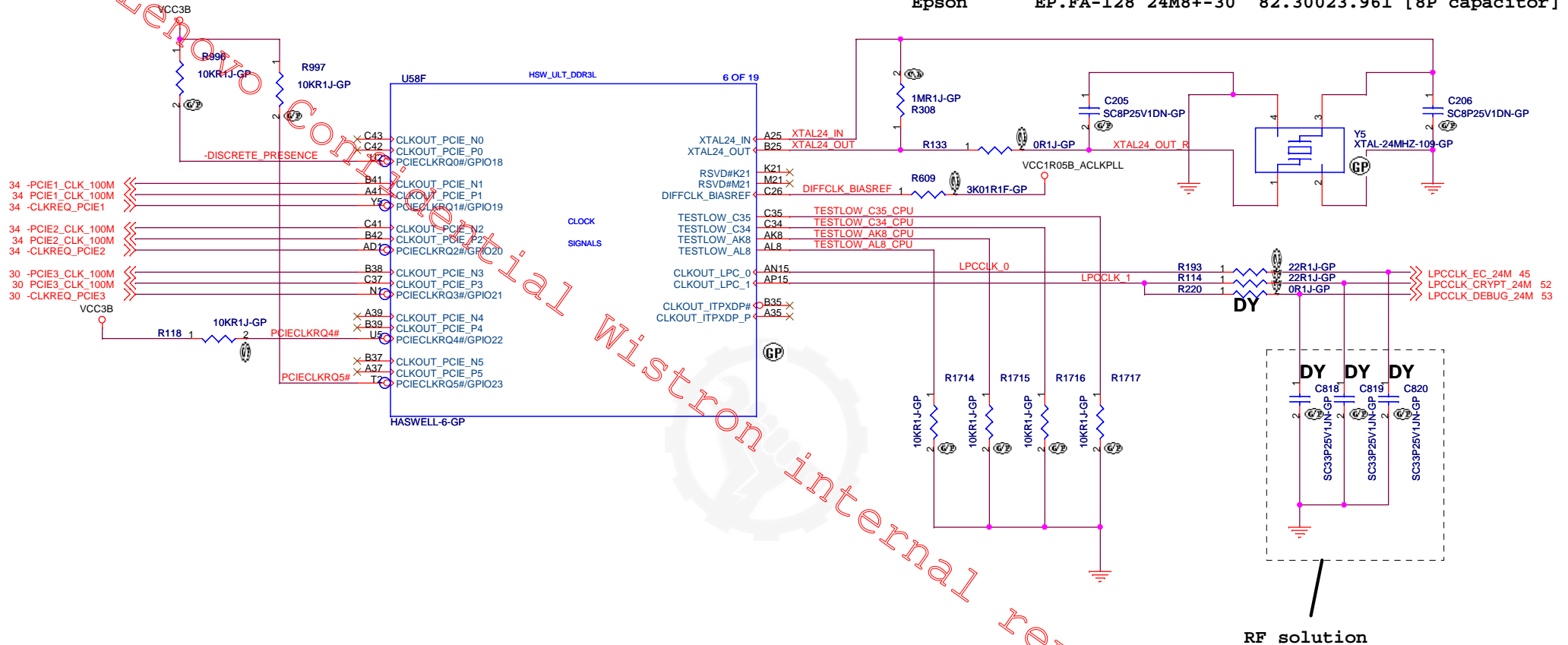


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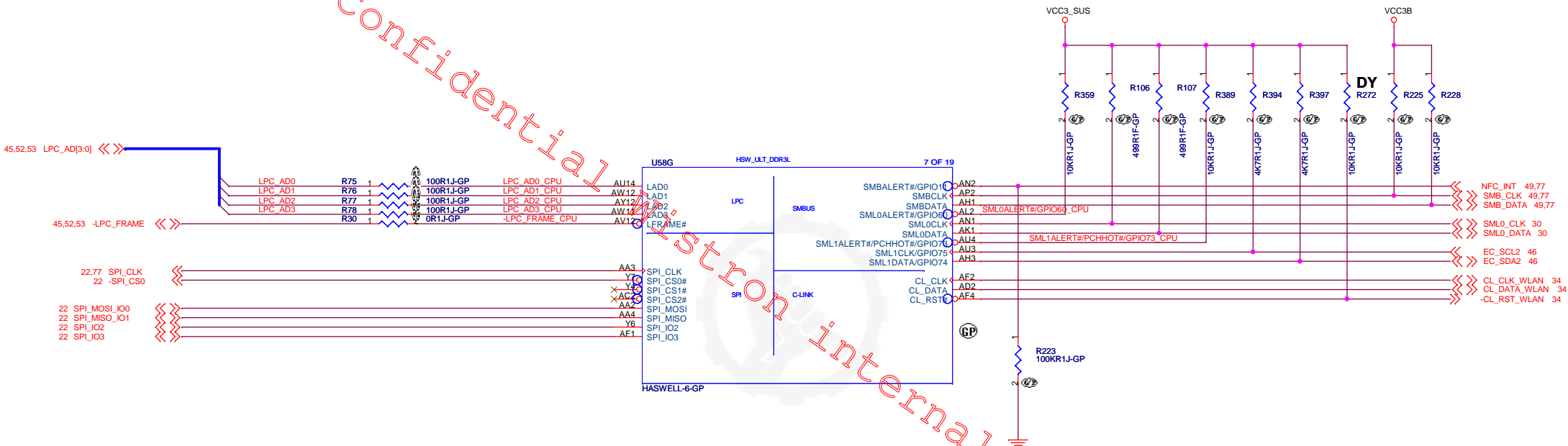
Size C	Document Number Mystique-1	Rev -2
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Date: Tuesday, November 12, 2013 Sheet 7 of 79

Y5
KDS 1ZZCAA24000CC0B 82.30023.991 [8P capacitor]
TXC 8Y24080002 82.30023.981 [8P capacitor]
Epson EP.FA-128 24M8+-30 82.30023.961 [8P capacitor]



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Wistron internal review 2013/11/14

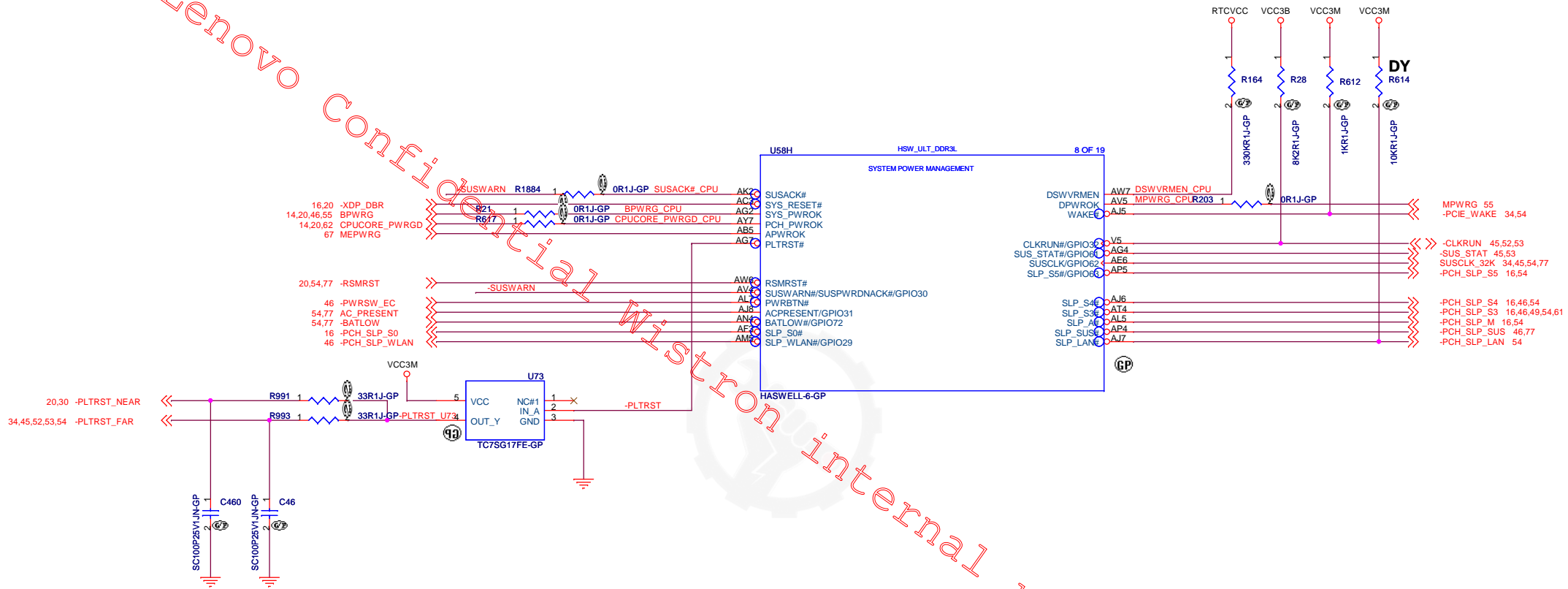


TABLE	
GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

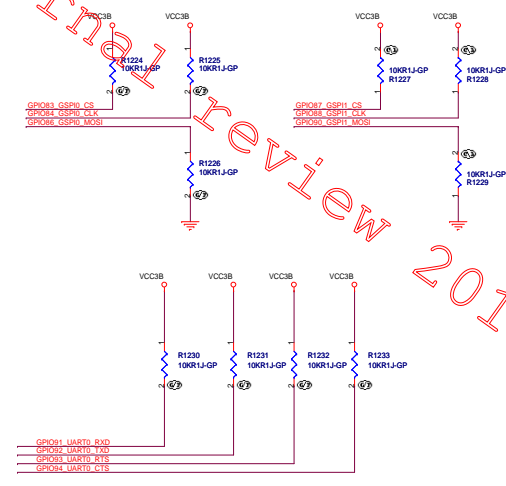
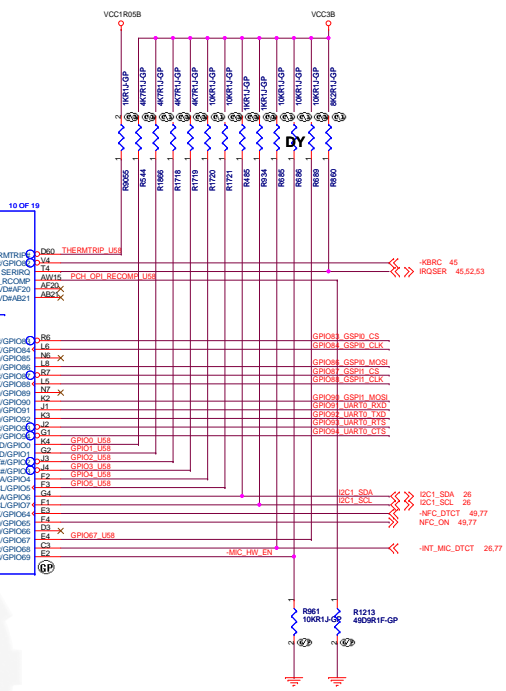
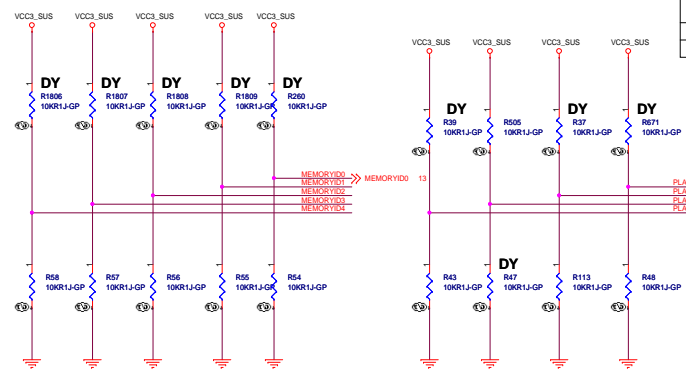
TABLE	
SPKR TCO TIMER SYSTEM REBOOT	
HIGH	DISABLED (NO REBOOT)
LOW	ENABLED

TABLE	
GPIO8	INTEGRATED CLOCKING
HIGH	DISABLED (BTM)
LOW	ENABLED (PCIM)

TABLE	
LEVEL	PLANAR ID
1	3 2 1 0
0	R39 R505 R37 R671 R43 R47 R113 R48

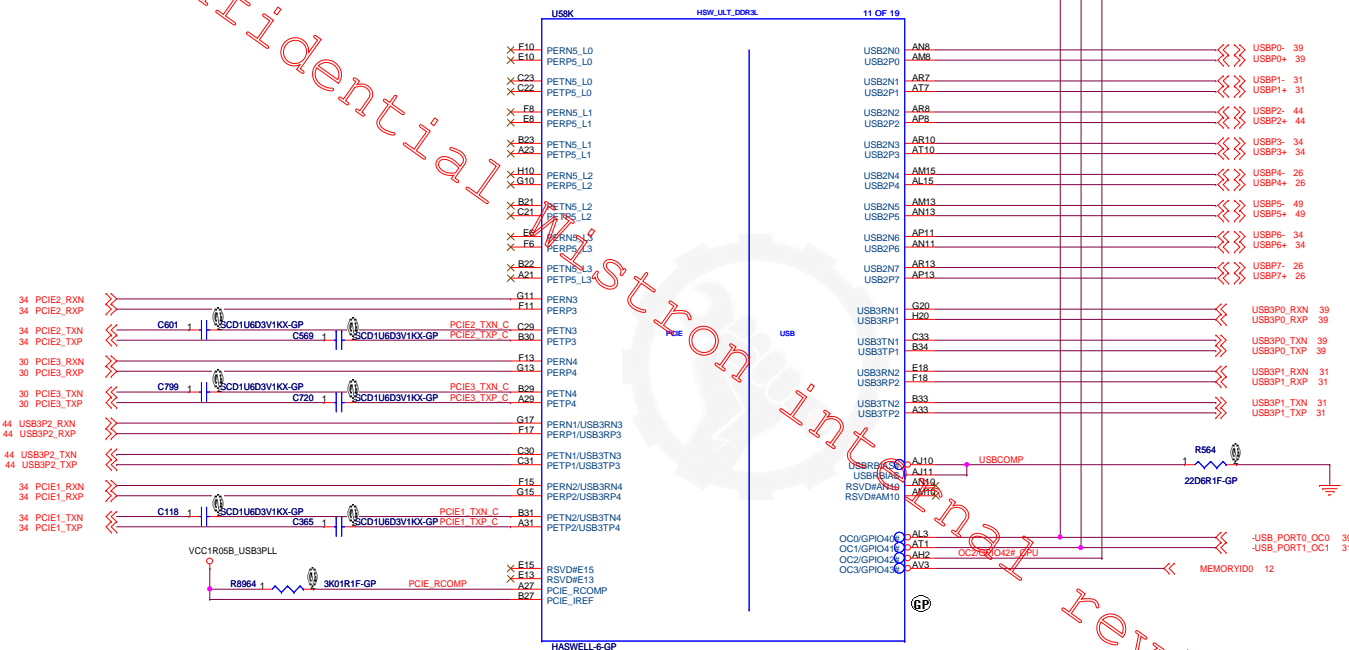
TABLE	
LEVEL	PLANARID[3..0]
SDV	0000B
FVT	0001B
SIT	0010B
SVT	0011B
SOVP	0100B

Meory Size	GPIO47	
MEMORYID4	0	4Gb <= LOGIC
	1	8Gb
Meory Supplier	GPIO[46:44]	
MEMORYID[3:1]	000	ELPIDA <= LOGIC
	001	HYNIX
	010	SAMSUNG
	011	MICRON
	[100:111]	(reserved)
Meory Die Revision	GPIO43	
MEMORYID0	0	initial <= LOGIC
	1	revised



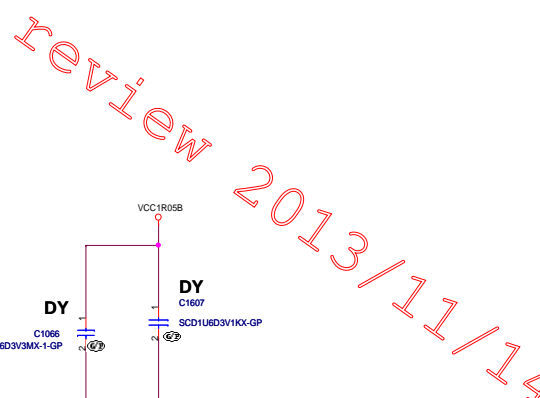
Flexible I/O Configuration			
I/O	High Speed Signals	Configuration	Net Name
Port 1	USB3 1	USB3 1	USB3P0
Port 2	USB3 2	USB3 2	USB3P1
Port 3	PCIE 1/USB3 3	USB3 3	USB3P2
Port 4	PCIE 2/USB3 4	PCIE 2	PCIE1
Port 5	PCIE 3	PCIE 3	PCIE2
Port 6	PCIE 4	PCIE 4	PCIE3
Port 7	PCIE 5 L0	PCIE 5 L0	PCIE4_0
Port 8	PCIE 5 L1	PCIE 5 L1	PCIE4_1
Port 9	PCIE 5 L2	PCIE 5 L2	PCIE4_2
Port 10	PCIE 5 L3	PCIE 5 L3	PCIE4_3
Port 11	PCIE 6 L0	PCIE 6 L0	PCIE5
Port 12	SATA 2/PCIE 6 L1	SATA 2	SATA 2
Port 13	SATA 1/PCIE 6 L2	SATA 1	SATA 1
Port 14	SATA 0/PCIE 6 L3	SATA 0	SATA 0

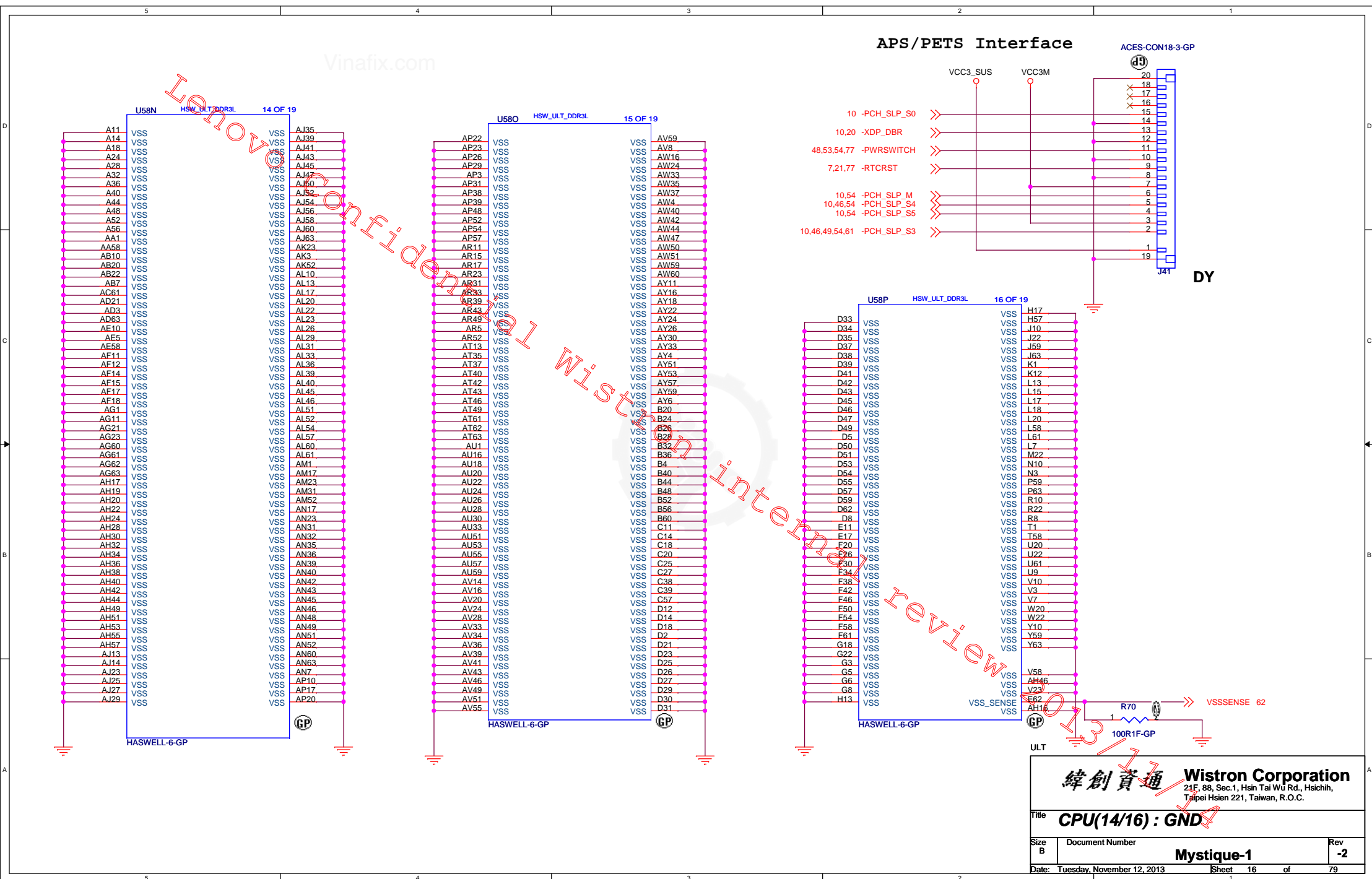
PCIe Port Assignment	
1	M.2 WLAN Slot Port 1
2	M.2 WLAN Slot Port 0
3	GbE PHY
4 (x4)	Reserved
5 (x1)	Reserved



USB Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking
3	M.2 WWAN Slot
4	Touch Panel
5	Fingerprint Reader
6	M.2 WLAN Slot
7	USB Camera

USB 3.0 Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking





FOR SOLDER CRACK DETECTION



DC TEST AY2 AW2	AY2
DC TEST AY3 AW3	AY3
TP DC TEST AY60	AY60
DC TEST AY61 AW61	AY61
DC TEST AY62 AW62	AY62
TP DC TEST B2	B2
DC TEST A3 B3	B3
DC TEST A61 B61	B61
DC TEST B62 B63	B62
DC TEST C1 C2	C1
	C2

U58Q

HSW_ULT_DDR3L

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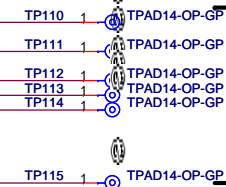
HASWELL-6-GP

DAISY_CHAIN_NCTF_AY2	DAISY_CHAIN_NCTF_A3
DAISY_CHAIN_NCTF_AY3	DAISY_CHAIN_NCTF_A4
DAISY_CHAIN_NCTF_AY60	
DAISY_CHAIN_NCTF_AY61	
DAISY_CHAIN_NCTF_AY62	
DAISY_CHAIN_NCTF_B2	
DAISY_CHAIN_NCTF_B3	
DAISY_CHAIN_NCTF_B61	
DAISY_CHAIN_NCTF_B62	
DAISY_CHAIN_NCTF_B63	
DAISY_CHAIN_NCTF_C1	
DAISY_CHAIN_NCTF_C2	

DAISY_CHAIN_NCTF_A3	A3
DAISY_CHAIN_NCTF_A4	A4
	A60
DAISY_CHAIN_NCTF_A60	A61
DAISY_CHAIN_NCTF_A61	A62
DAISY_CHAIN_NCTF_A62	AW1
DAISY_CHAIN_NCTF_AW1	AW2
DAISY_CHAIN_NCTF_AW2	AW3
DAISY_CHAIN_NCTF_AW3	AW61
DAISY_CHAIN_NCTF_AW61	AW62
DAISY_CHAIN_NCTF_AW62	AW63
DAISY_CHAIN_NCTF_AW63	

DC TEST A3 B3	A3
TP DC TEST A4	A4
TP DC TEST A60	A60
DC TEST A61 B61	A61
TP DC TEST A62	A62
TP DC TEST AW1	AW1
TP DC TEST AW2	AW2
DC TEST AY2 AW2	AW3
DC TEST AY3 AW3	AW61
DC TEST AY61 AW61	AW62
DC TEST AY62 AW62	AW63
TP DC TEST AW63	

FOR SOLDER CRACK DETECTION



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Title CPU(15/16) : NCTE

Size B Document Number

Mystique-1

Rev -2

Date: Tuesday, November 12, 2013

Sheet 17 of 79

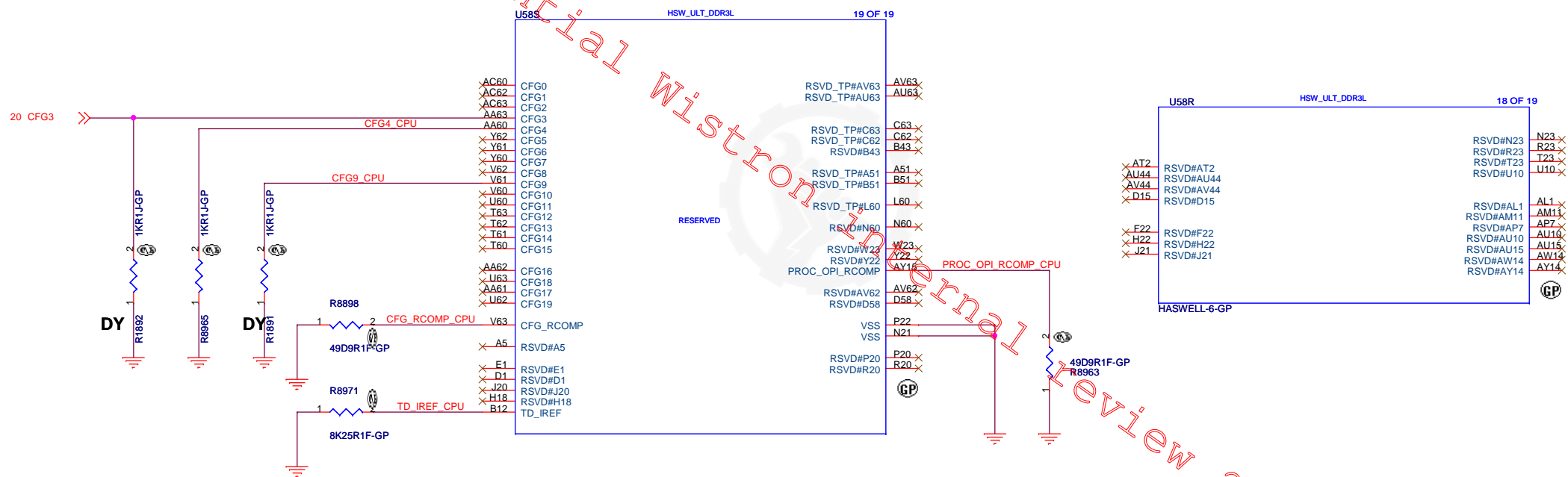
TABLE

CFG4 : Display Port Presence

- 1 : Disabled
0 : Enabled

CFG9 : SVID Bus Communication

- 1 : Enabled
0 : Disabled



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Taipei Hsien 221, Taiwan, R.O.C.

Title

CPU(16/16) : CFG/RESERVE

Size

Document Number

Mystique-1

Rev

-2

Date: Tuesday, November 12, 2013

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of 79

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Title

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Size
A

Document Number

Mystique-1

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-2

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XDP

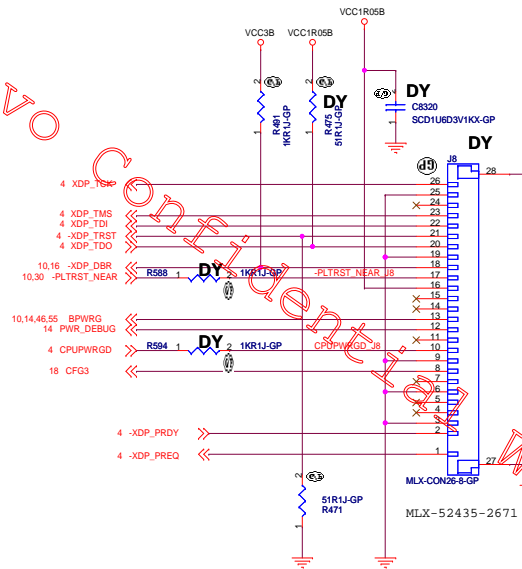


TABLE NOTE: J8 "ASM" FOR PDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
	J8	ASM	NO ASM
	C8320	ASM	NO ASM

LOGIC

JTAG

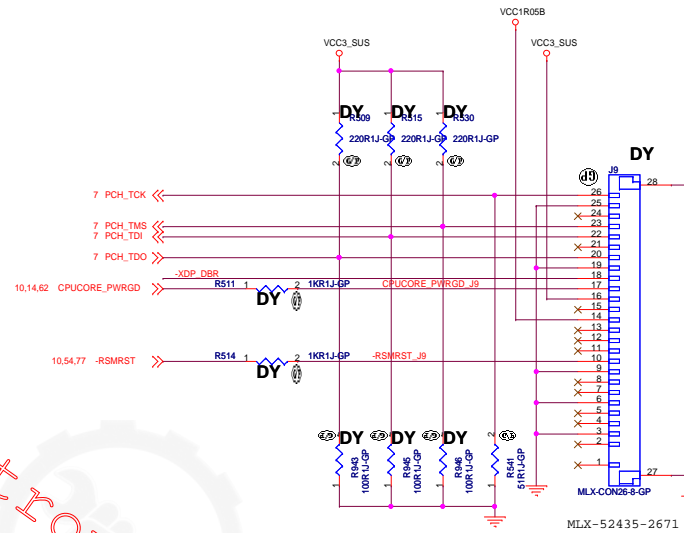
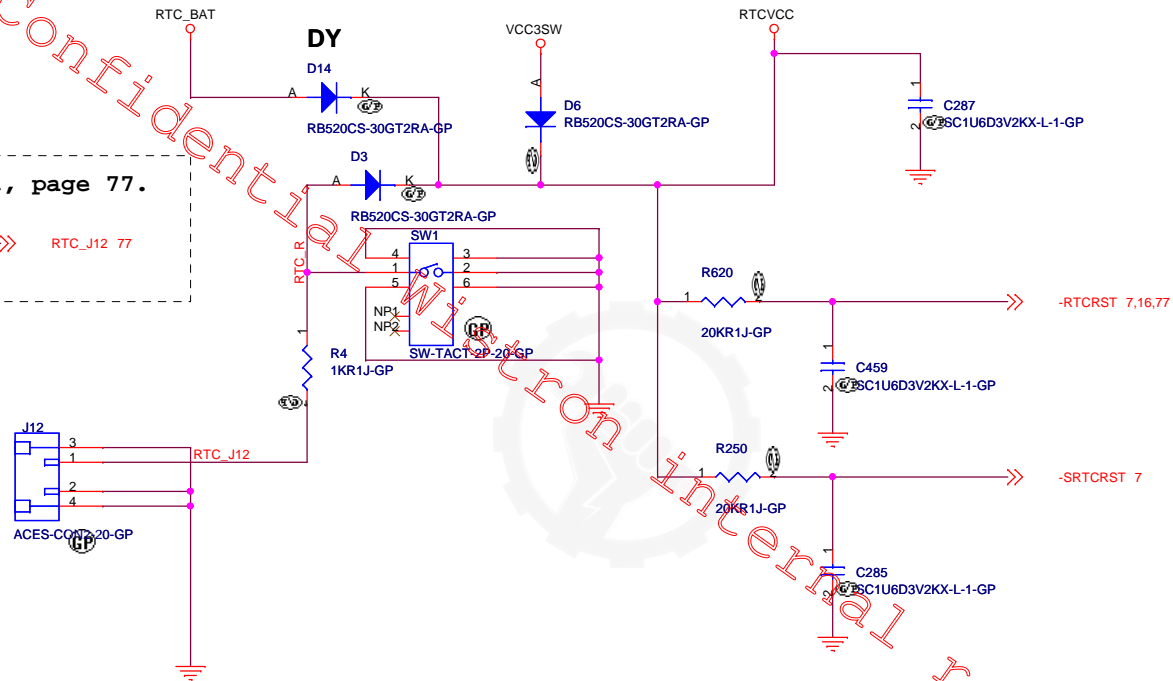


TABLE NOTE: J9 "ASM" FOR PDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	220	NO ASM
	R943	100	NO ASM
TMS	R530	220	NO ASM
	R946	100	NO ASM
TDI	R515	220	NO ASM
	R945	100	NO ASM
TCK	R541	51	51
CPUCORE_PWRGD	R511	ASM	NO ASM
-RSMRST	R514	ASM	NO ASM
	J9	ASM	NO ASM

LOGIC

RTC_J12 >> RTC_J12 77



緯創資通

Title **RTC BATTERY**

Mystique-1

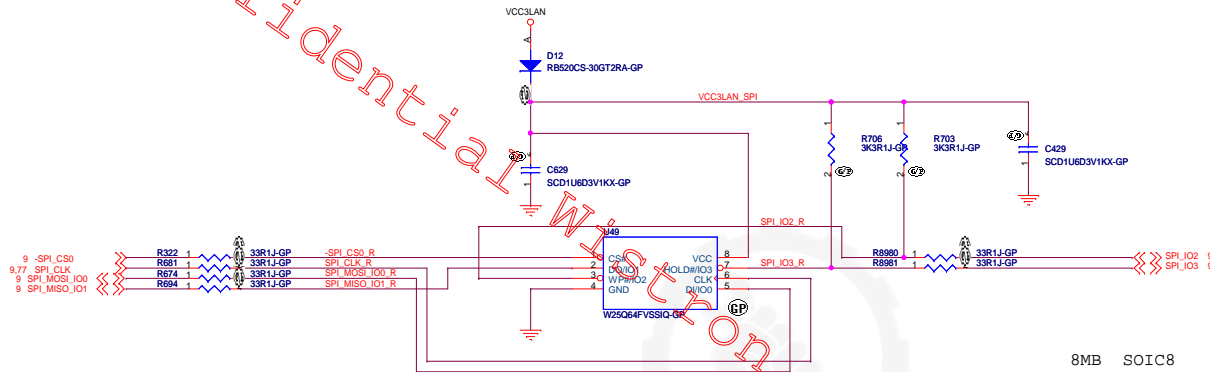
Date: Tuesday, November 12, 2013

Sheet	21
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-2

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SPI	vPRO	non-vPRO
U49	16MB	8MB

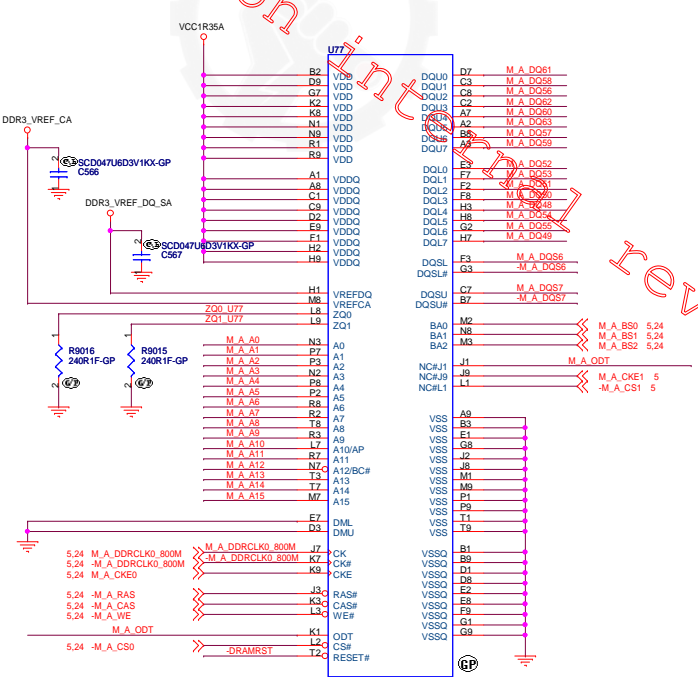
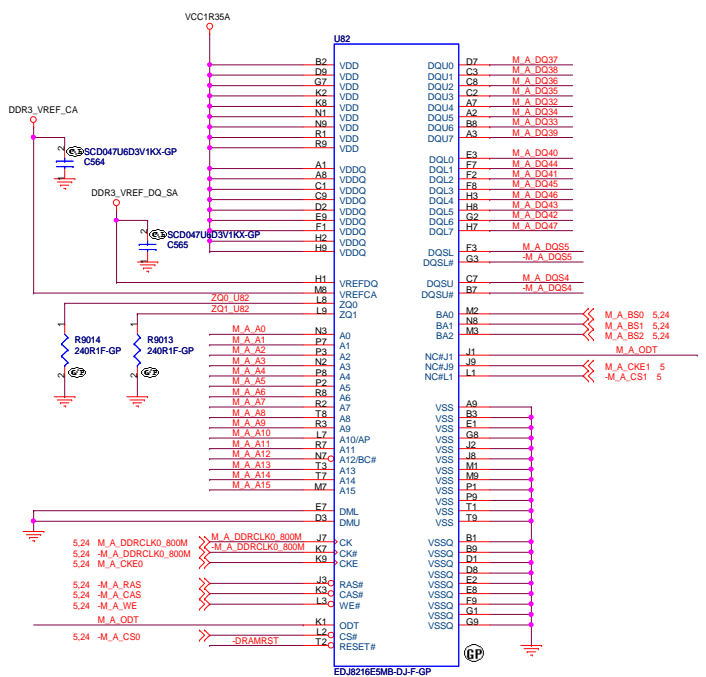
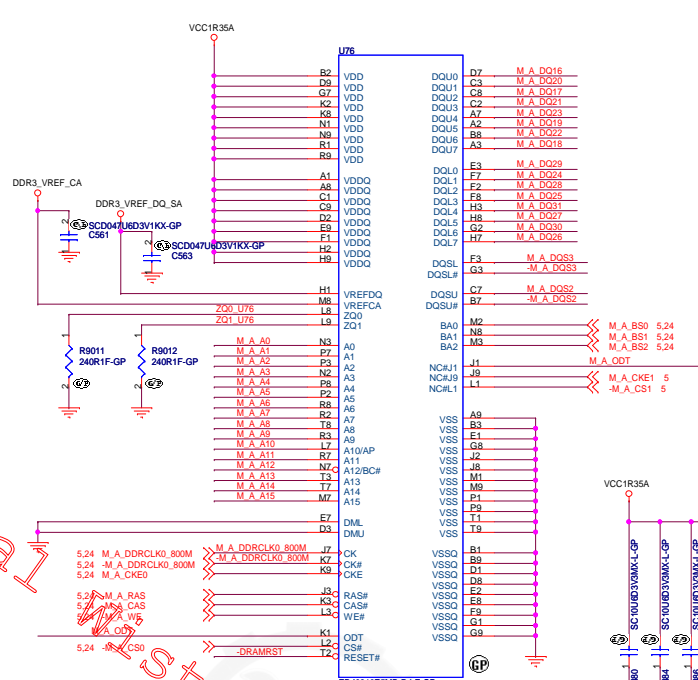


TABLE

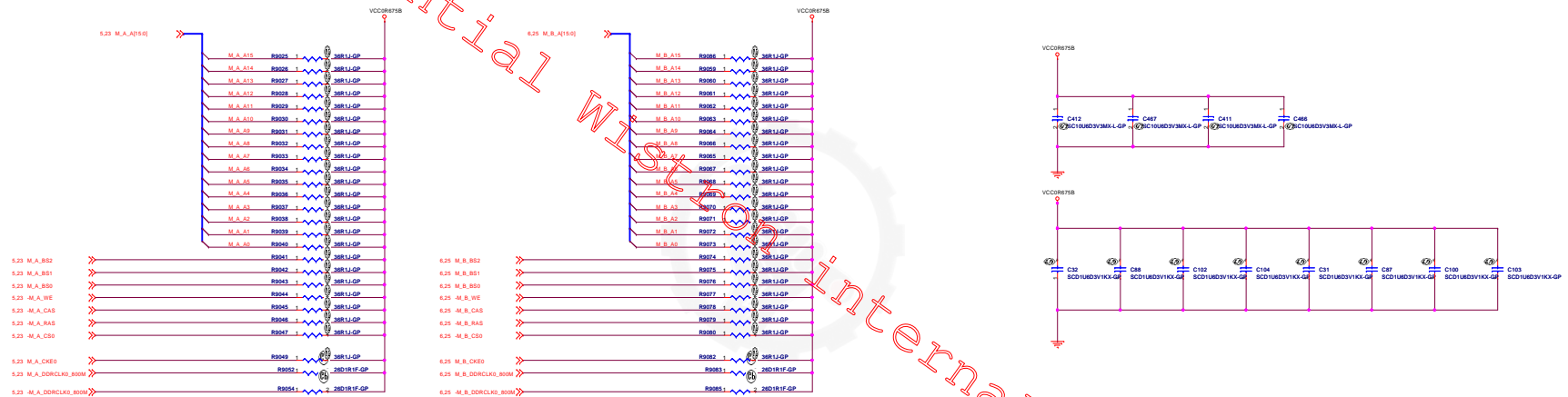
SF100	PIN	HEADER	INTERFACE	(TOP VIEW)
1	VCC	D12.1	GND	GND 2
3	CS#	R322.2	R681.2	CLK 4
5	MISO	R694.2	R674.2	MOSI 6
7	(KEY)	N/A	N/A	(RESET) 8

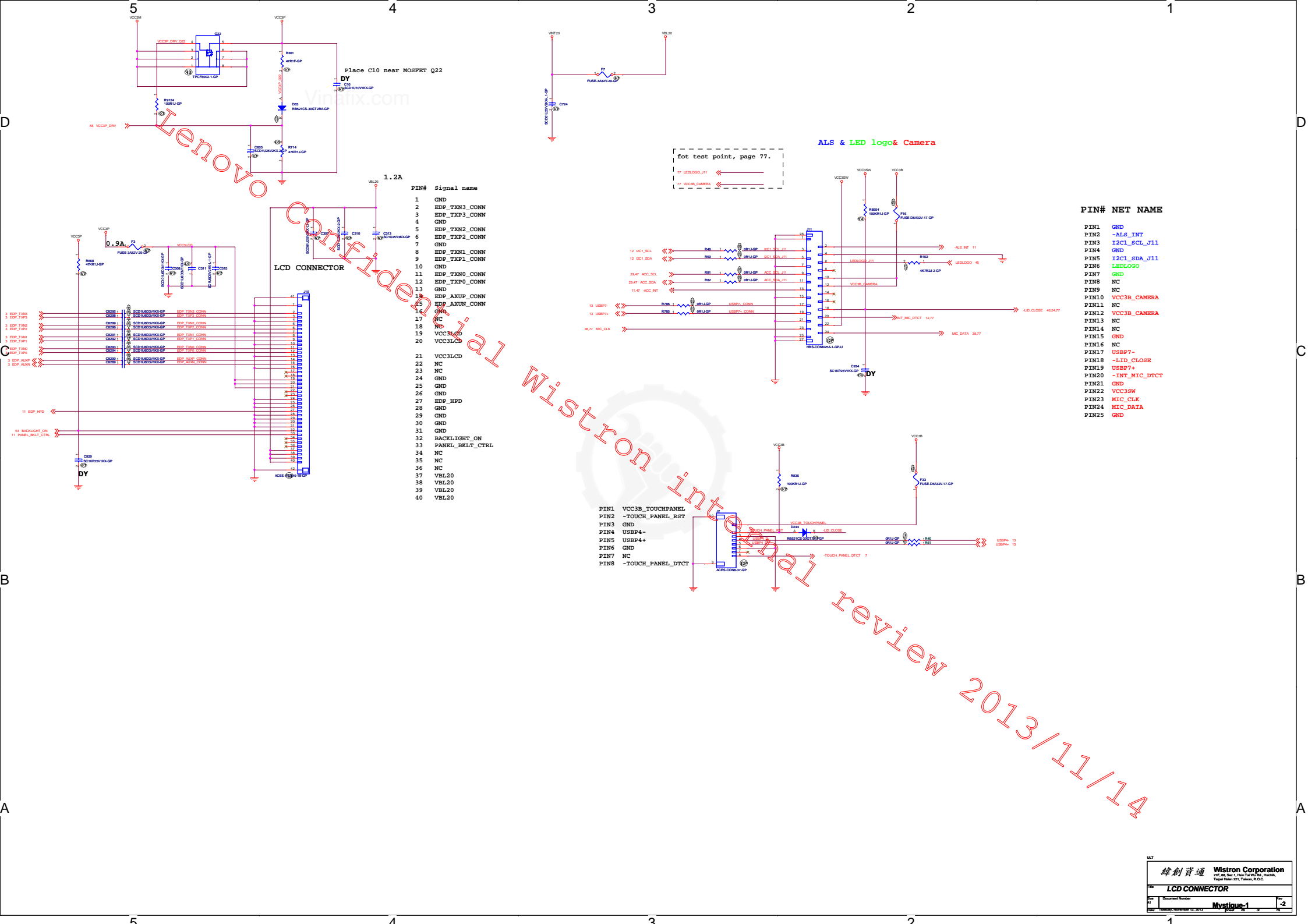
8MB SOIC8
 WINBOND W25Q64FVSSIQ 72.25Q64.K01
 MACRONIX MX25L6475EM2I-10G 72.25647.001
 MICRON N25Q064A13ESEC0F 72.25Q64.G01

16MB SOIC8
 WINBOND W25Q128FVSIQ 72.25128.0E1
 MACRONIX MX25L12875FM2I-10G 72.12873.001
 MICRON N25Q128A13ESEC0F 72.25128.0B1



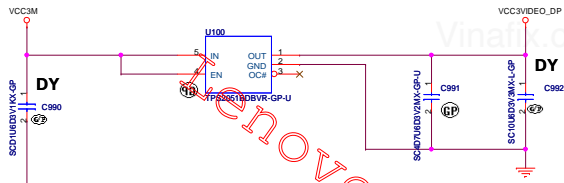
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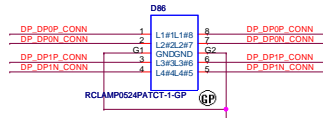


PLACE NEAR DP CONN

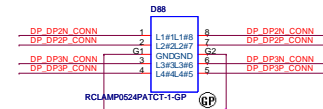
Typ. 500mA
Max 1.5A



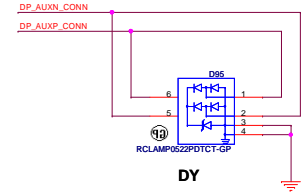
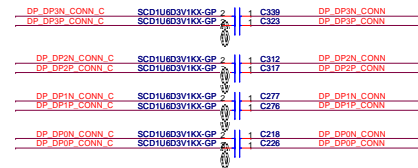
NEAR DP CONN



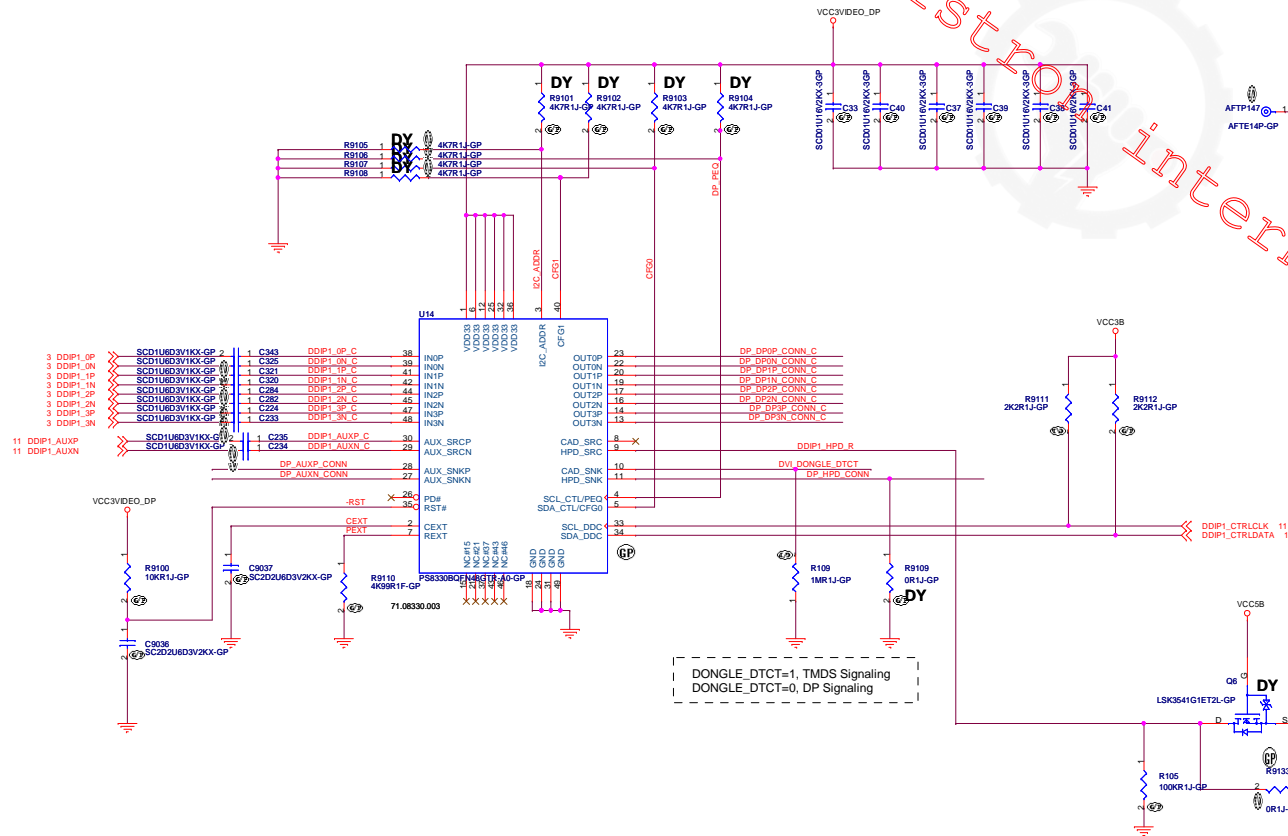
NEAR DP CONN

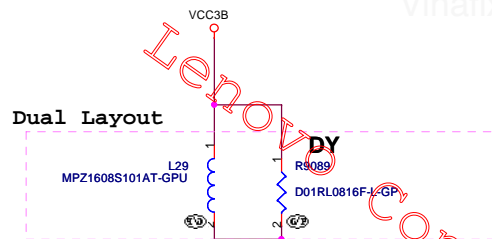


FOR SYSTEM DP NEAR DP CONN

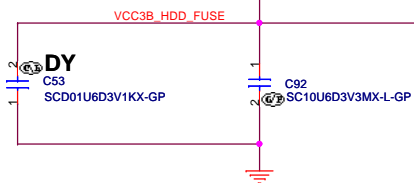


Add Demux for DP in Cable Dock Case

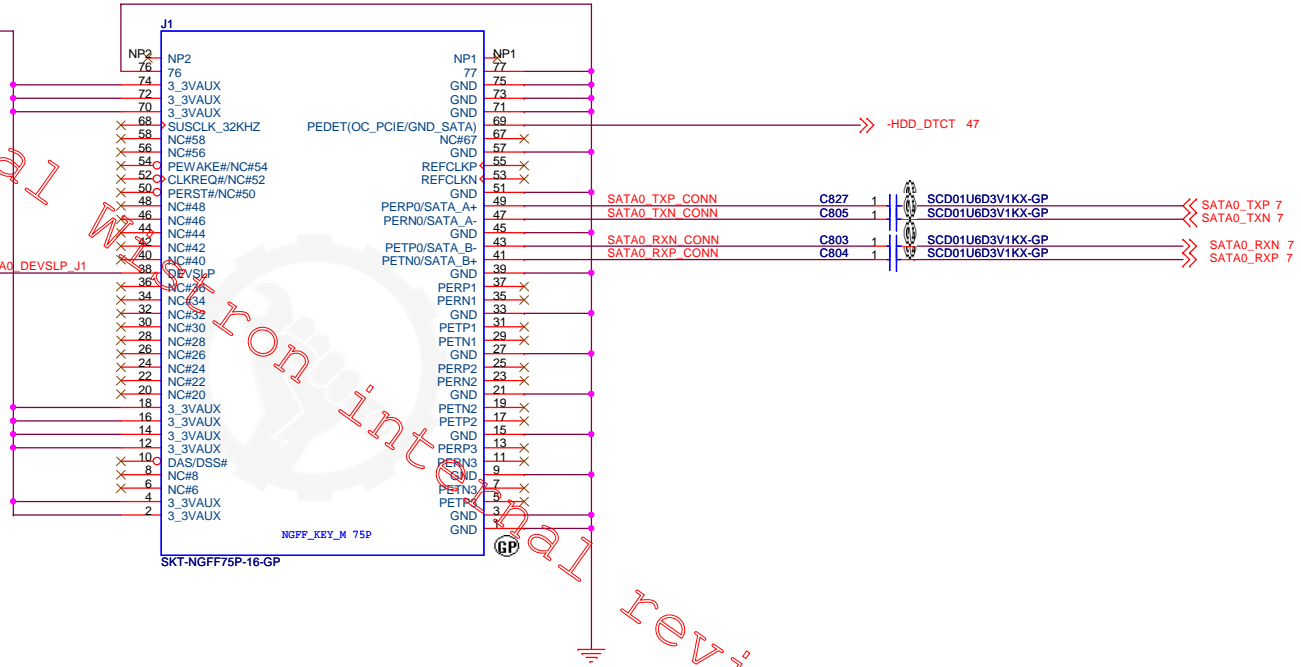




M.2 SSD Type-M 2280

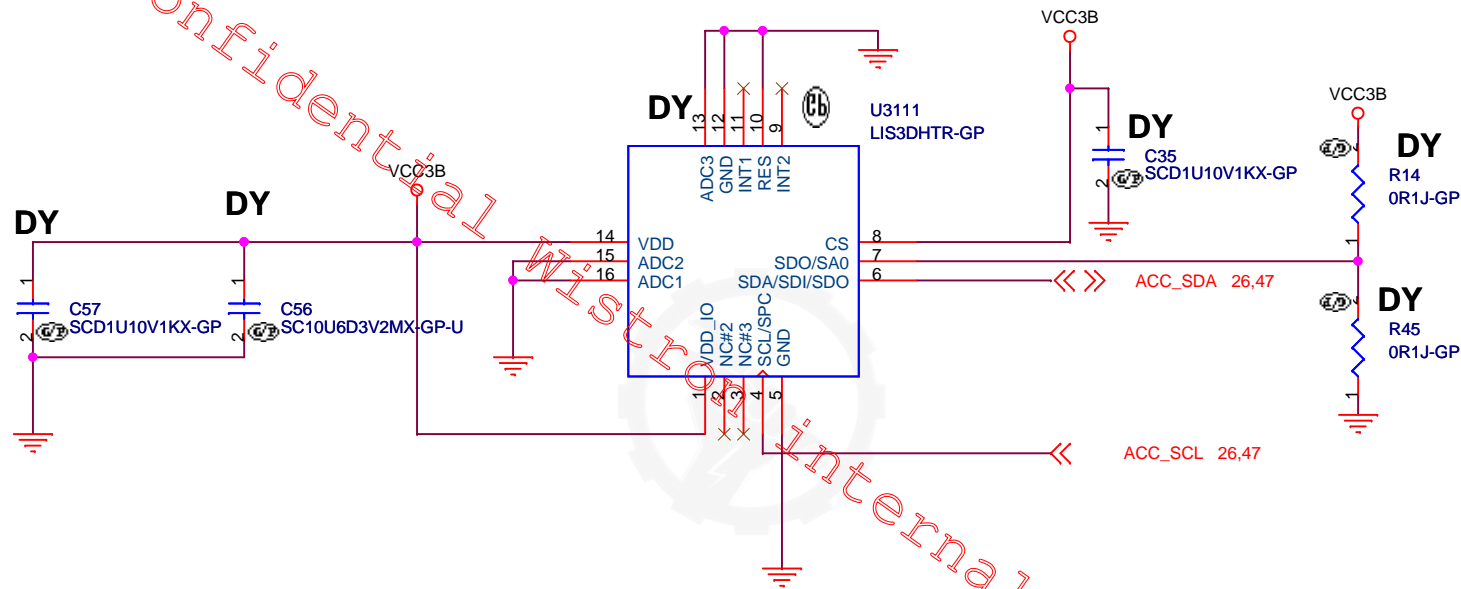


12 SATA0_DEVSLP >>> R100 1 >>> 0R1J-GP >>> SATA0_DEVSLP J1



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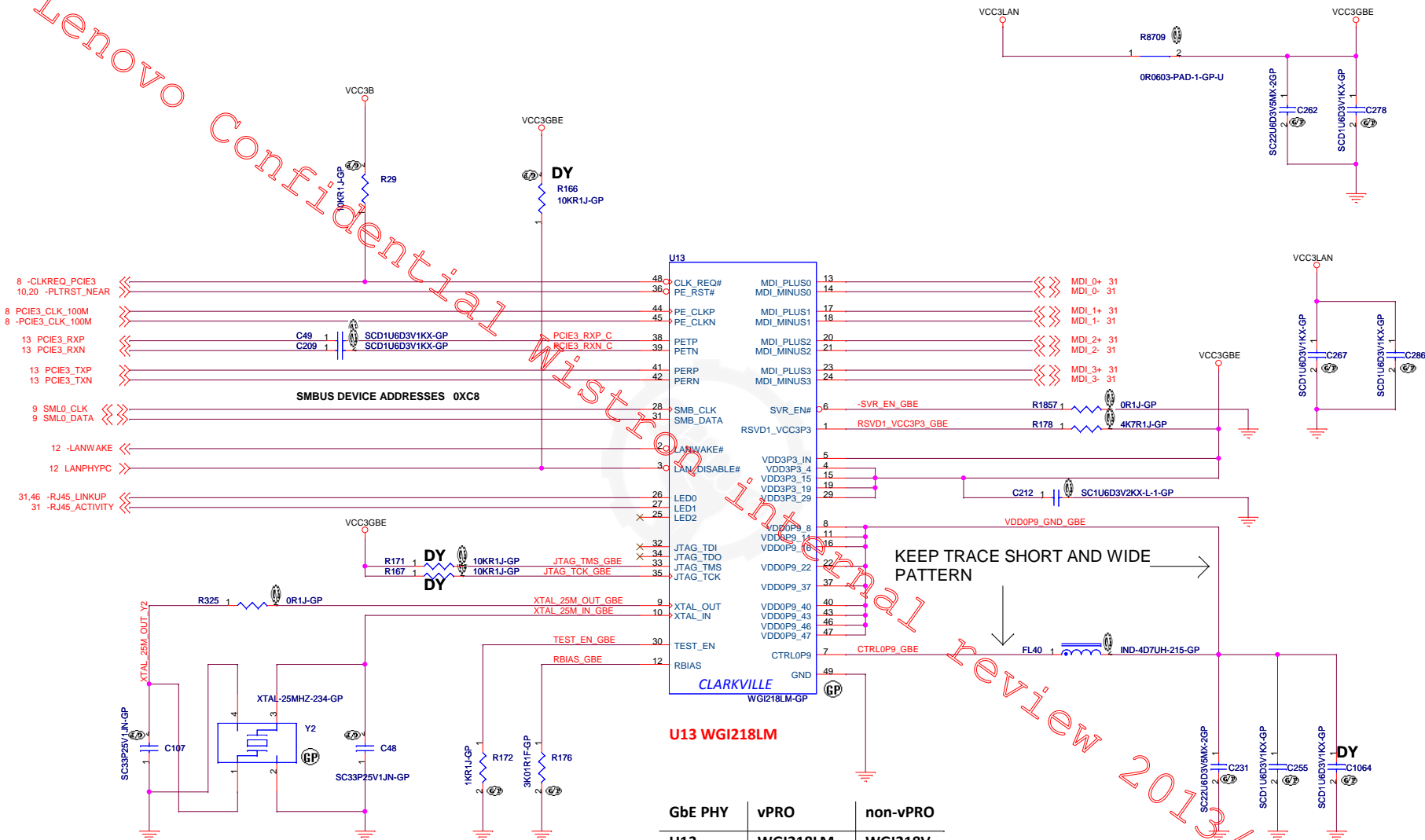
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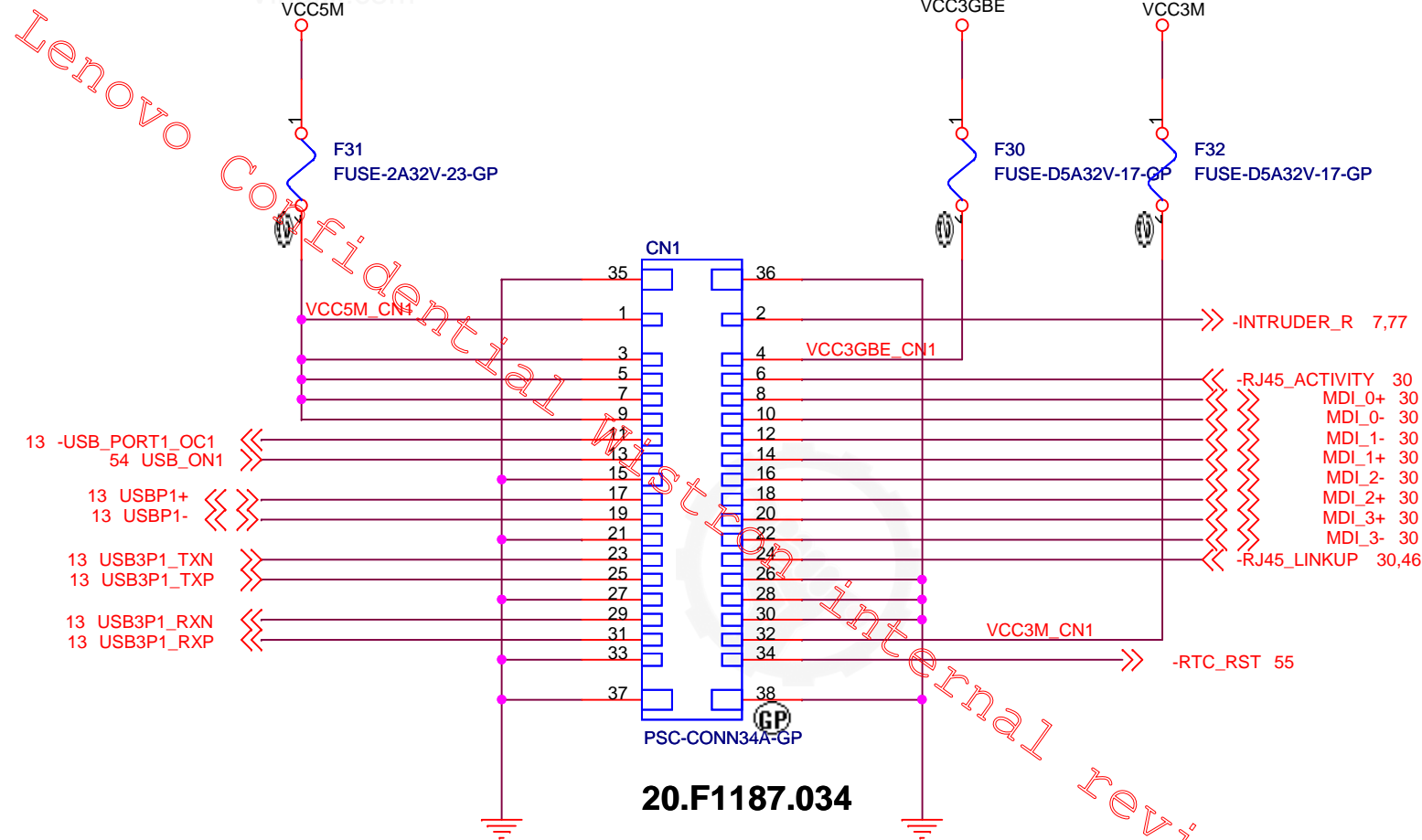
緯創資通		Wistron Corporation	
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Title			
G-SENSOR			
Size	Document Number	Rev	
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Date: Tuesday, November 12, 2013			
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Y2
KDS 1ZZCAA25000CC0C 82.30020.N91 [12P capacitor]
TXC 8Y25000004 82.20026.191 [33P capacitor]
Epson EP.FA-128 25M20+-30 82.30020.P51 [33P capacitor]

GbE PHY	vPRO	non-vPRO
U13	WGI218LM	WGI218V



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Taipei Hsien 221, Taiwan, R.O.C.

Title **GBE / USB3.0 BTB CONNECTOR**

Size A Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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Title BLANK

Size A4	Document Number	Rev -2
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Mystique-1

Date: Tuesday, November 12, 2013	Sheet 32 of 79
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Title

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Size
A4

Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

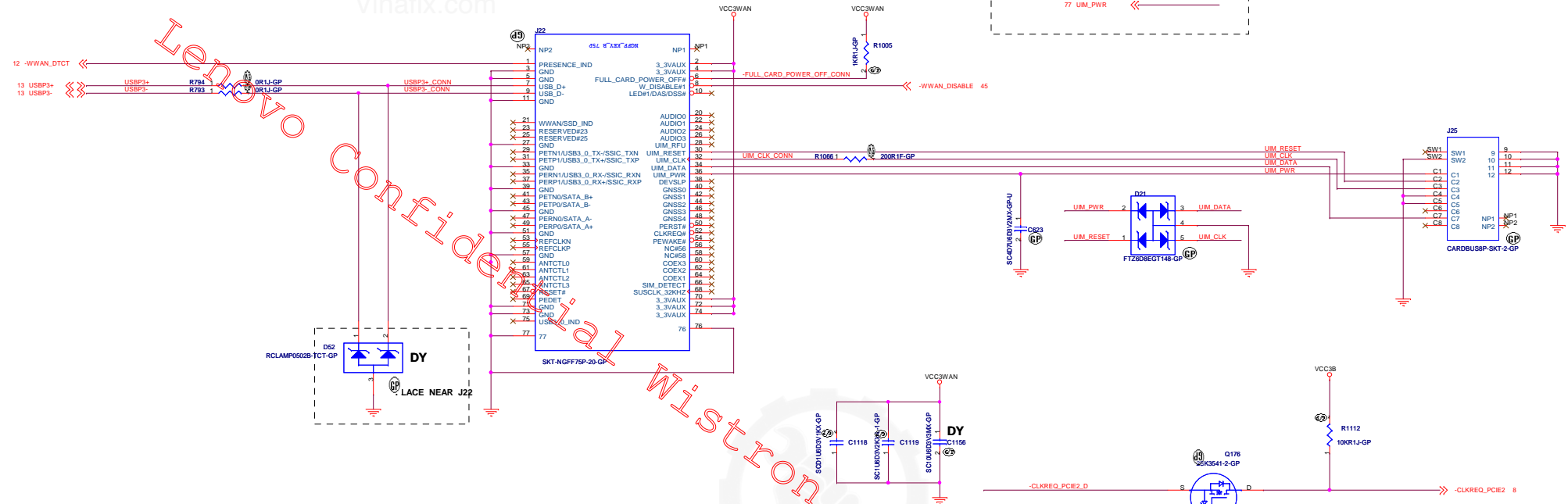
Sheet 33 of 79

TYPE-B M.2 CARD FOR WWAN/SSD

3.2H CONNECTOR

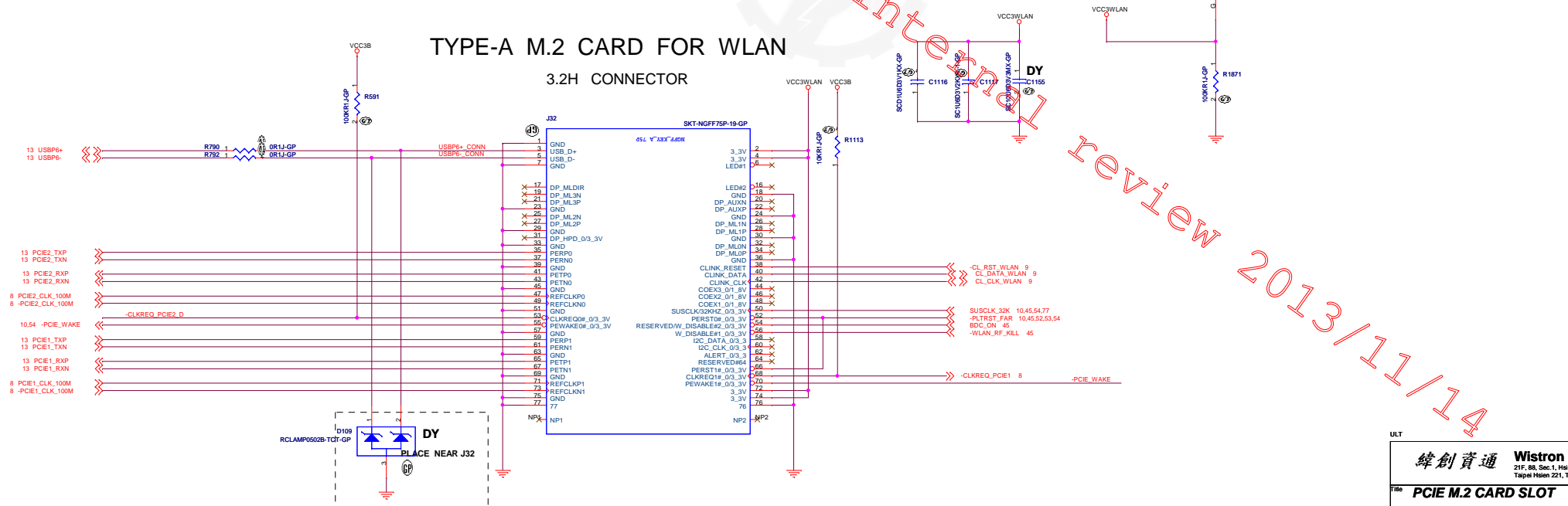
for test point, page 77.

77 UIM_RESET
77 UIM_CLK
77 UIM_DATA
77 UIM_PWR



TYPE-A M.2 CARD FOR WLAN

3.2H CONNECTOR



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Title **BLANK**

Size
A4

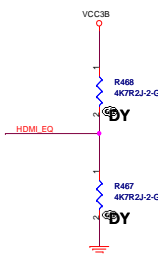
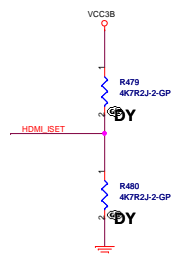
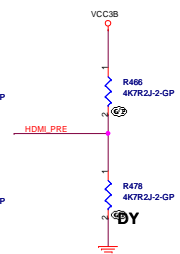
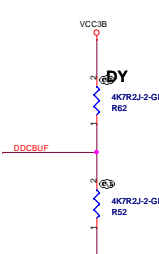
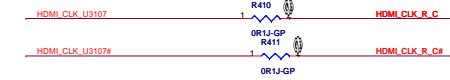
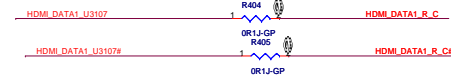
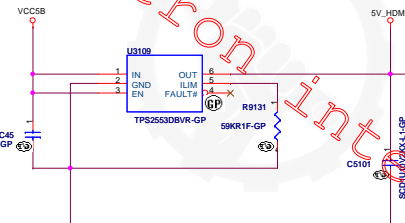
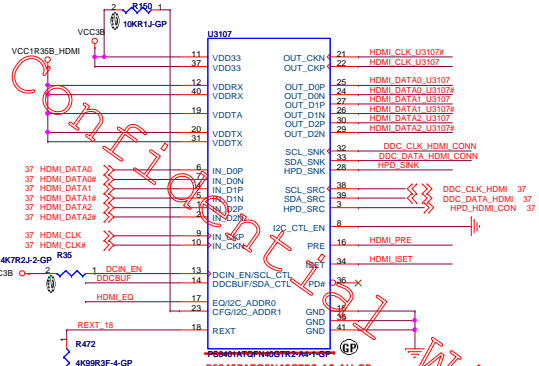
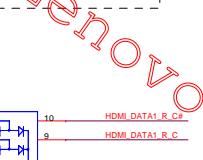
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Mystique-1

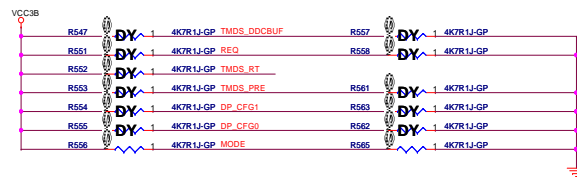
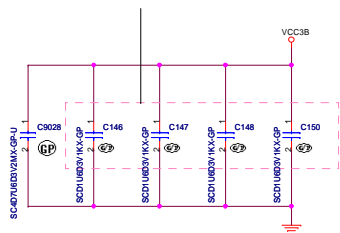
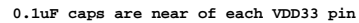
Rev
-2

Date: Tuesday, November 12, 2013

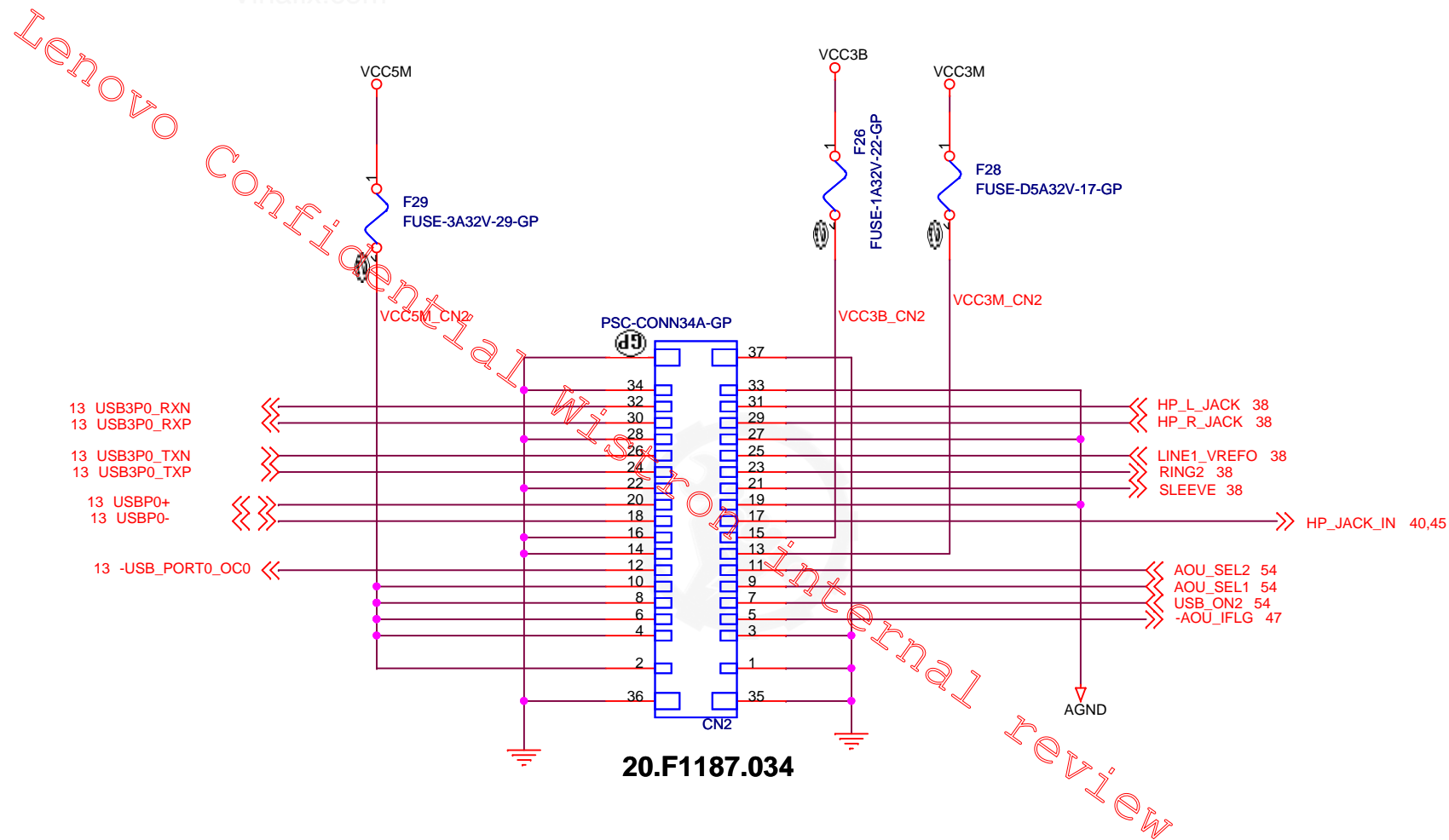
Sheet 35 of 79



```
H: HDMI is higher priority than DOCKDP
L: DOCKDP is higher priority than HDMI
```



For Automatic Switching:
SW1 = L: DP output has higher priority.
The pin Internal pull down at ~150K, 3.3V V_{CC}.
SW1 = H: TMS output has higher priority.



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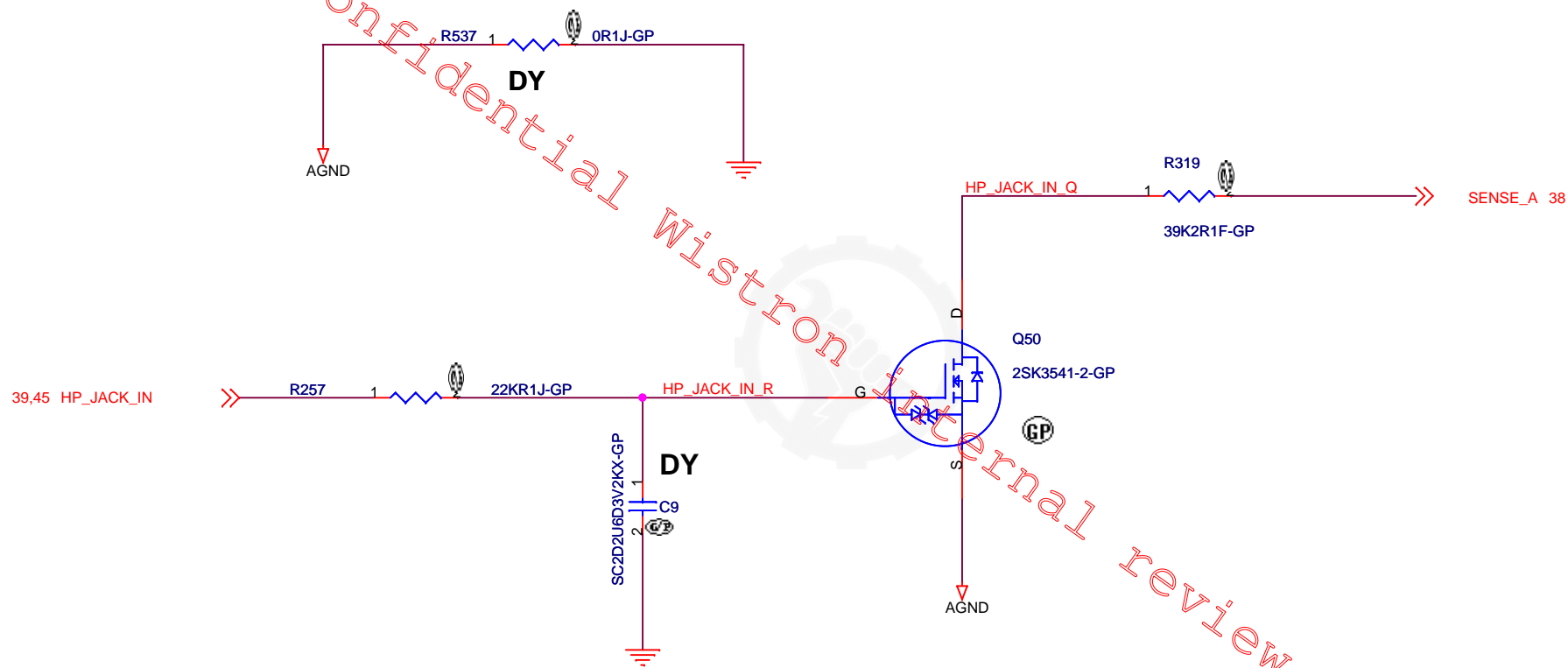
Title **AUDIO /USB3.0 BTB CONNECTOR**

Size A4 Document Number

Mystique-1 -2

Date: Tuesday, November 12, 2013 Sheet 39 of 79

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Title **AUDIO JACK SENSE**

Size A4 Document Number

Mystique-1

Rev **-2**

Date: Tuesday, November 12, 2013

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Size B Document Number

Mystique-1

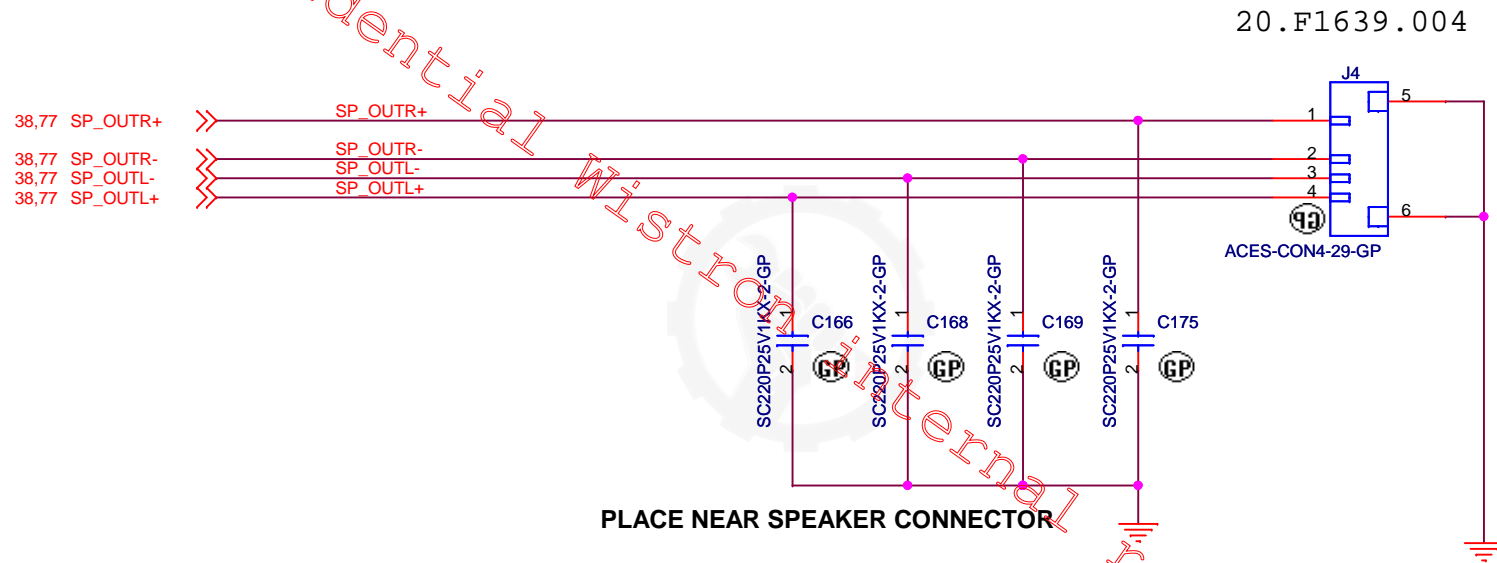
Rev
-2

Date: Tuesday, November 12, 2013

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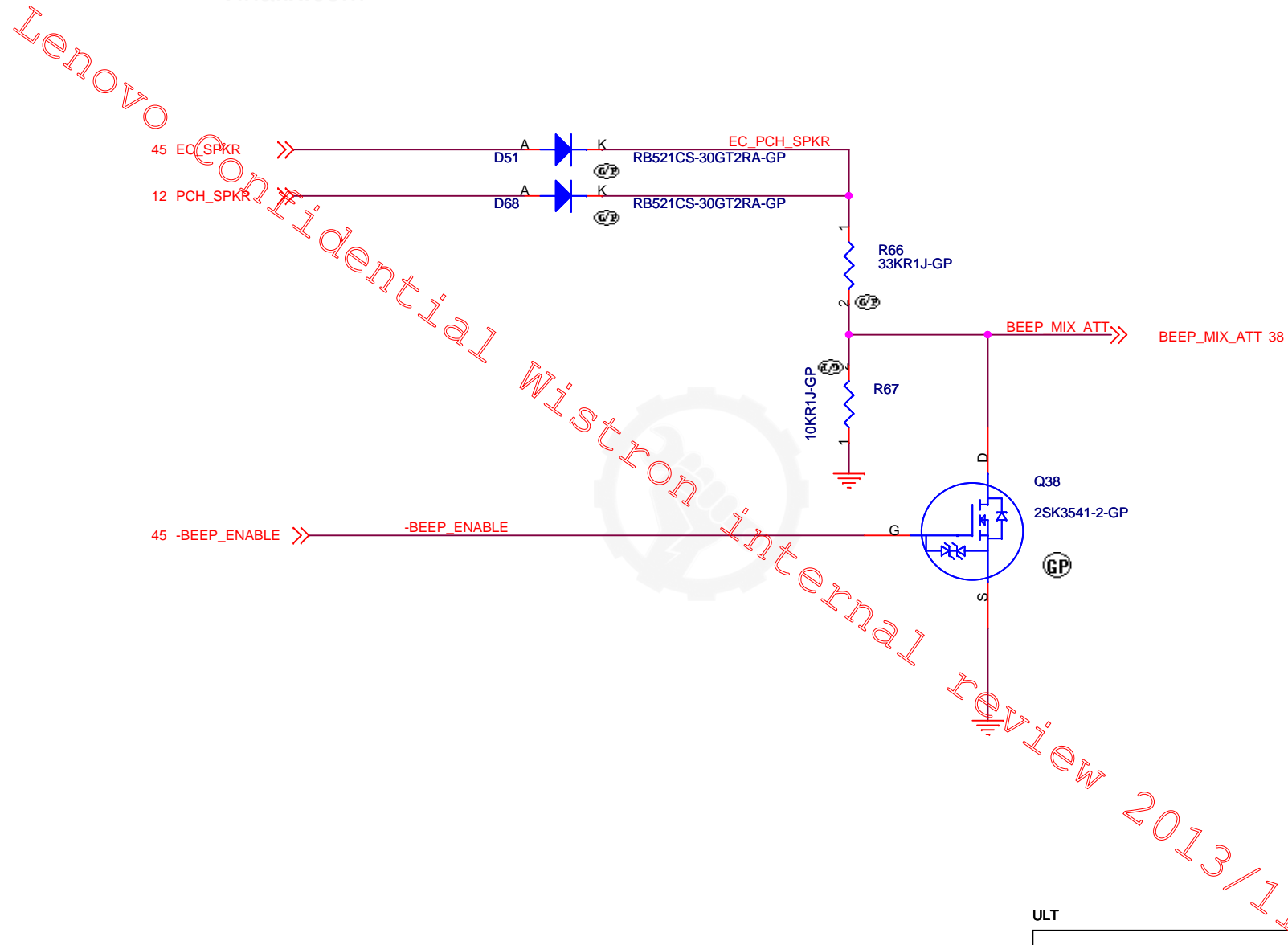
Title **AUDIO SPEAKER**

Size A4	Document Number	Rev -2
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Mystique-1

Date: Tuesday, November 12, 2013 Sheet 42 of 79

Vinafix.com

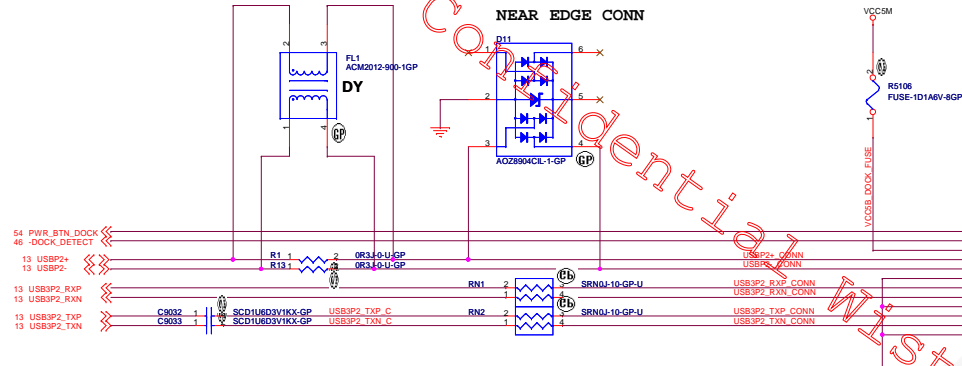


ULT

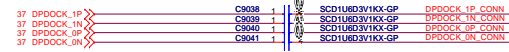
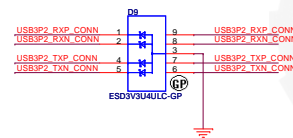
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Title	
AUDIO BEEP	
Size A4	Document Number Mystique-1
Date: Tuesday, November 12, 2013	Sheet 43 of 79

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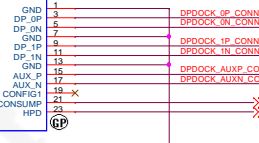
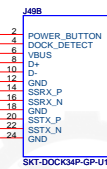
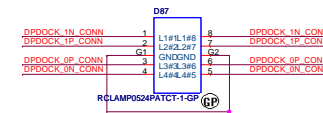
NEAR EDGE CONN



NEAR EDGE CONN

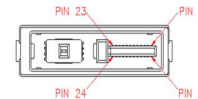


NEAR EDGE CONN



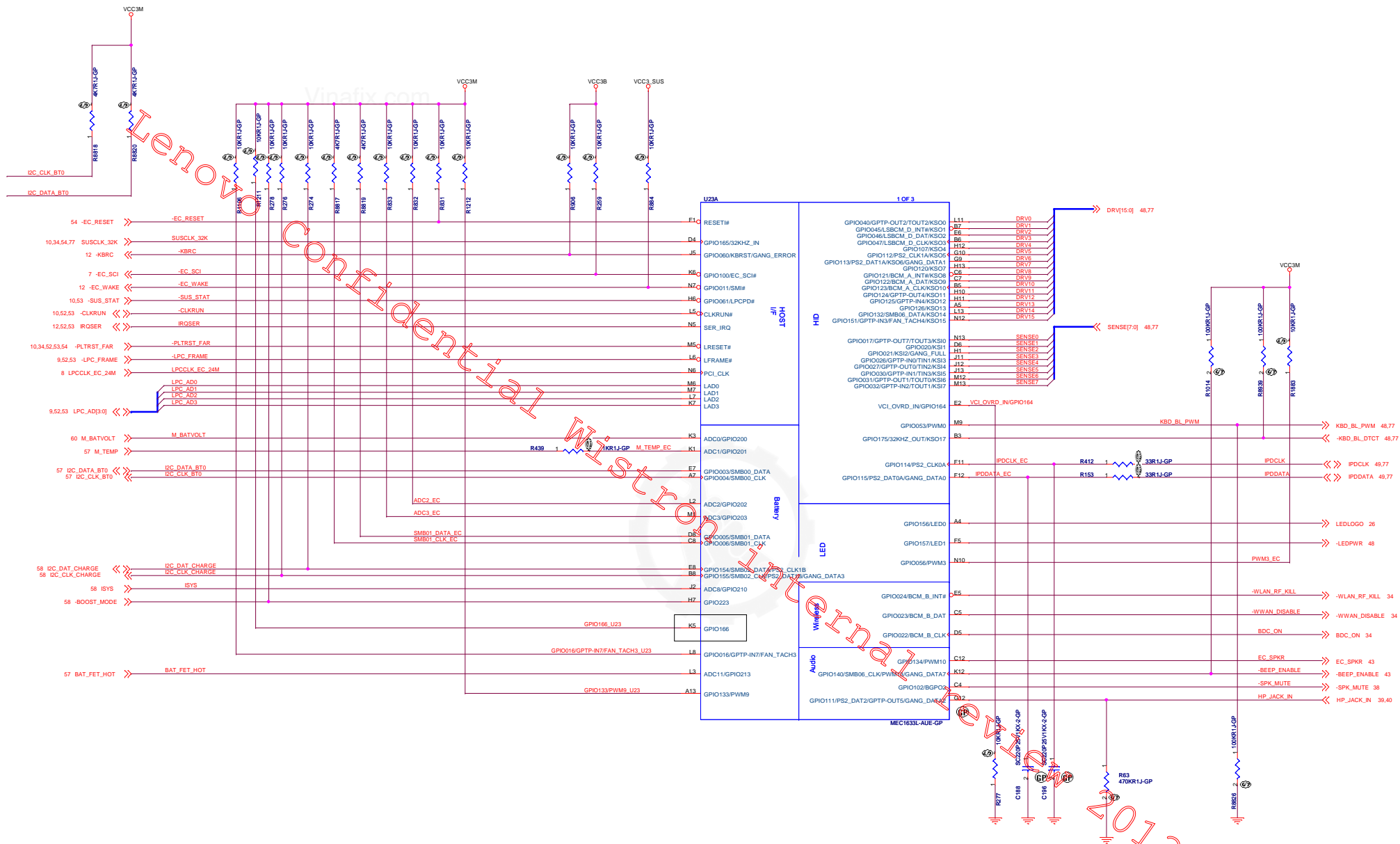
Because layout is fixed, just can change PR.
will change to "R9034" "R9035" if any layout time.

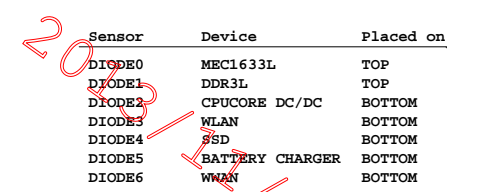
NEAR EDGE CONN



P/n	Signal
Pin#P24	Pin#P3 GND Hot Plug Detect
Pin#P22	Pin#P21 USB3.0 Tx(n) DOCK_CONSUMP
Pin#P20	Pin#P19 USB3.0 Tx(p) CONFIG1 (no connect)
Pin#P18	Pin#P17 GND AUX_CH(n)
Pin#P16	Pin#P15 USB3.0 Rx(n) AUX_CH(p)
Pin#P14	Pin#P13 USB3.0 Rx(p) GND
Pin#P12	Pin#P11 GND ML_Lane1(n)
Pin#P10	Pin#P9 USB2.0(n) ML_Lane1(p)
Pin#P8	Pin#P7 USB2.0(p) GND
Pin#P6	Pin#P5 VBUS ML_Lane0(n)
Pin#P4	Pin#P3 Return/Detect ML_Lane0(p)
Pin#P2	Pin#P1 Power button GND

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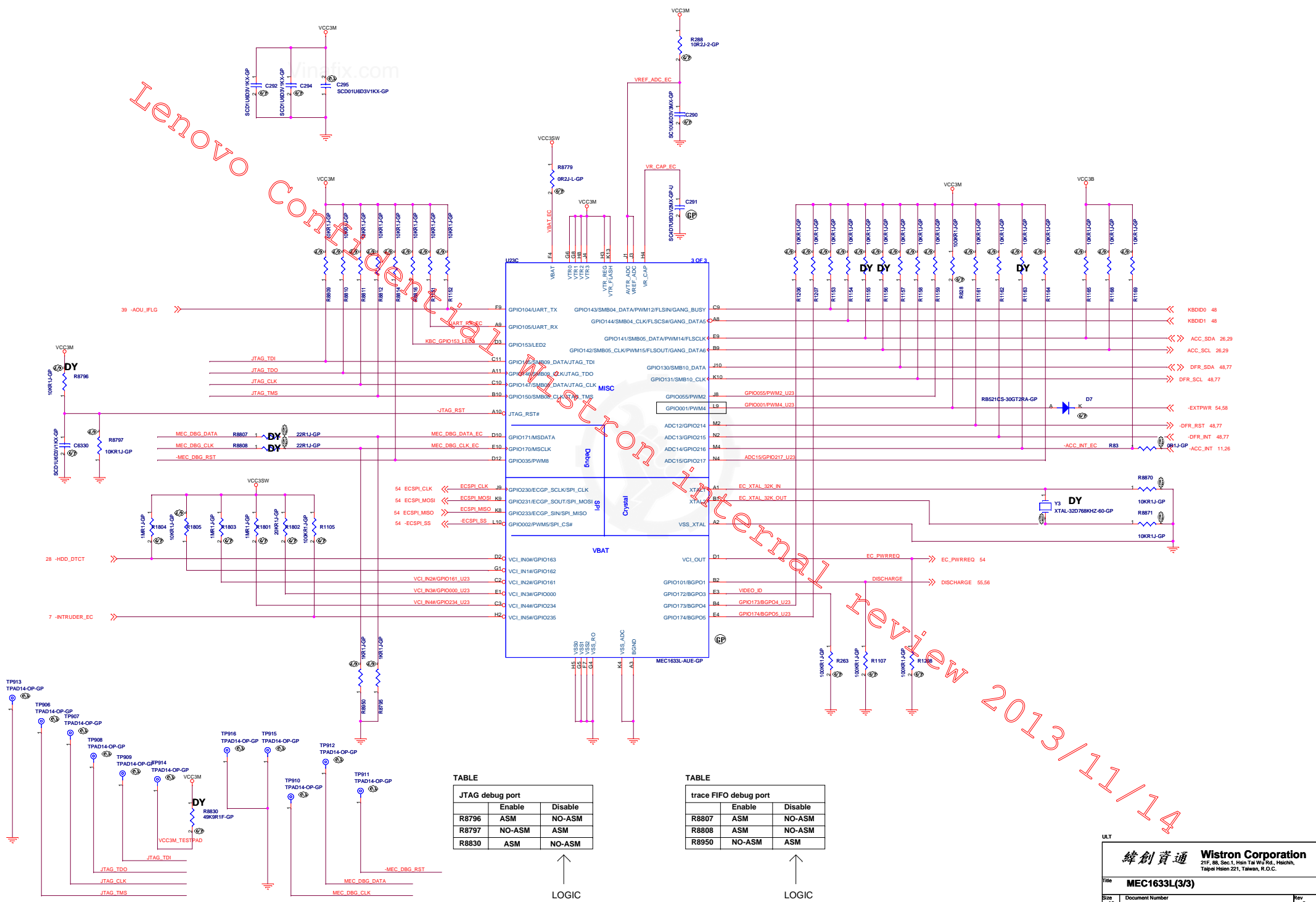
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Internal

Review

2013/11/14



TABLE

JTAG debug port		
	Enable	Disable
R8796	ASM	NO-ASM
R8797	NO-ASM	ASM
R8830	ASM	NO-ASM

LOGIC

TABLE

trace FIFO debug port		
	Enable	Disable
R8807	ASM	NO-ASM
R8808	ASM	NO-ASM
R8950	NO-ASM	ASM

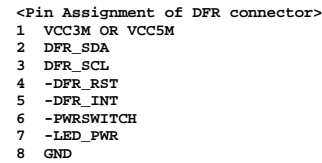
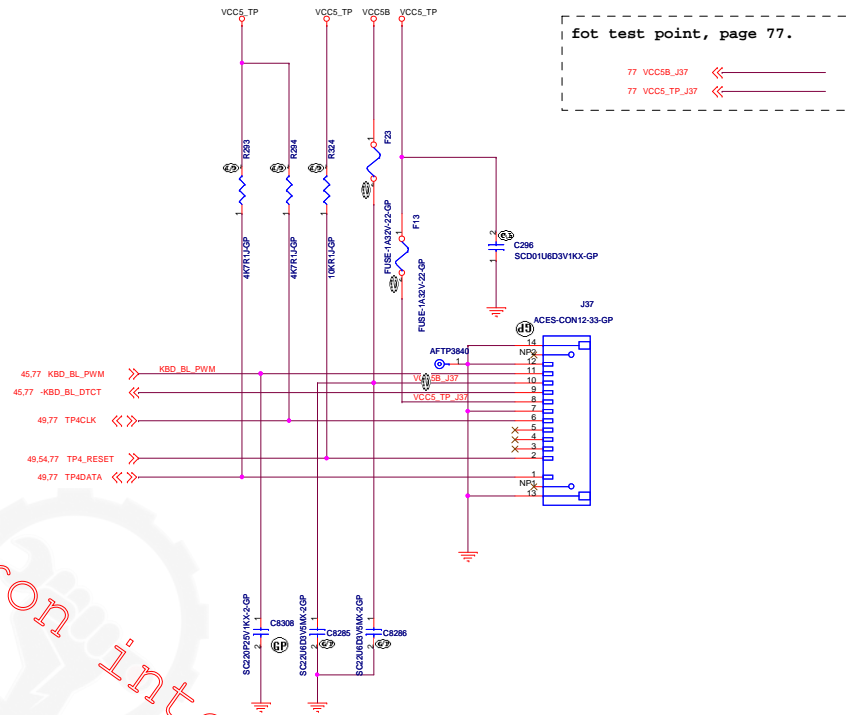
LOGIC

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 221, Taiwan, R.O.C.

Title	MEC1633L(3/3)		
Size	Document Number	Mystique-1	Rev 2
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TRACKPOINT (12PIN)



Clickpad + NFC 20.K0637.026

FP 8 pin ZIF
20.K0637.008

R799 and R800 should be placed for replacement by common mode choke.

for test point, page 77.

77 FUSEVCC3B
77 FUSEVCC3B

NFC

Clickpad

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Title TOUCH PAD CONNECTOR

Size C Document Number Mystique-1 Rev -2

Date: Tuesday, November 12, 2013 Sheet 49 of 79

for test point, page 77.

77 VCC5B_F4

FAN CURRENT IS 0.5A MAX
FUSE 2.0A

F4
FUSE-2A32V-23-GP

PIN# NET NAME

PIN1 VCC5B_F4
PIN2 NC
PIN3 FAN_ON
PIN4 GND
PIN5 FAN_FRQ
PIN6 FAN_ID

46,77 FAN_ON

FAN_ON
AFTP3847

J15

VCC5B_F4

H31 CON6-9-GP

FAN_ID 46,77

FAN_FRQ 46,77

ULT

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Wistron Corporation

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Title

FAN CONNECTOR

Size
A4

Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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Title

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Size

A4

Document Number

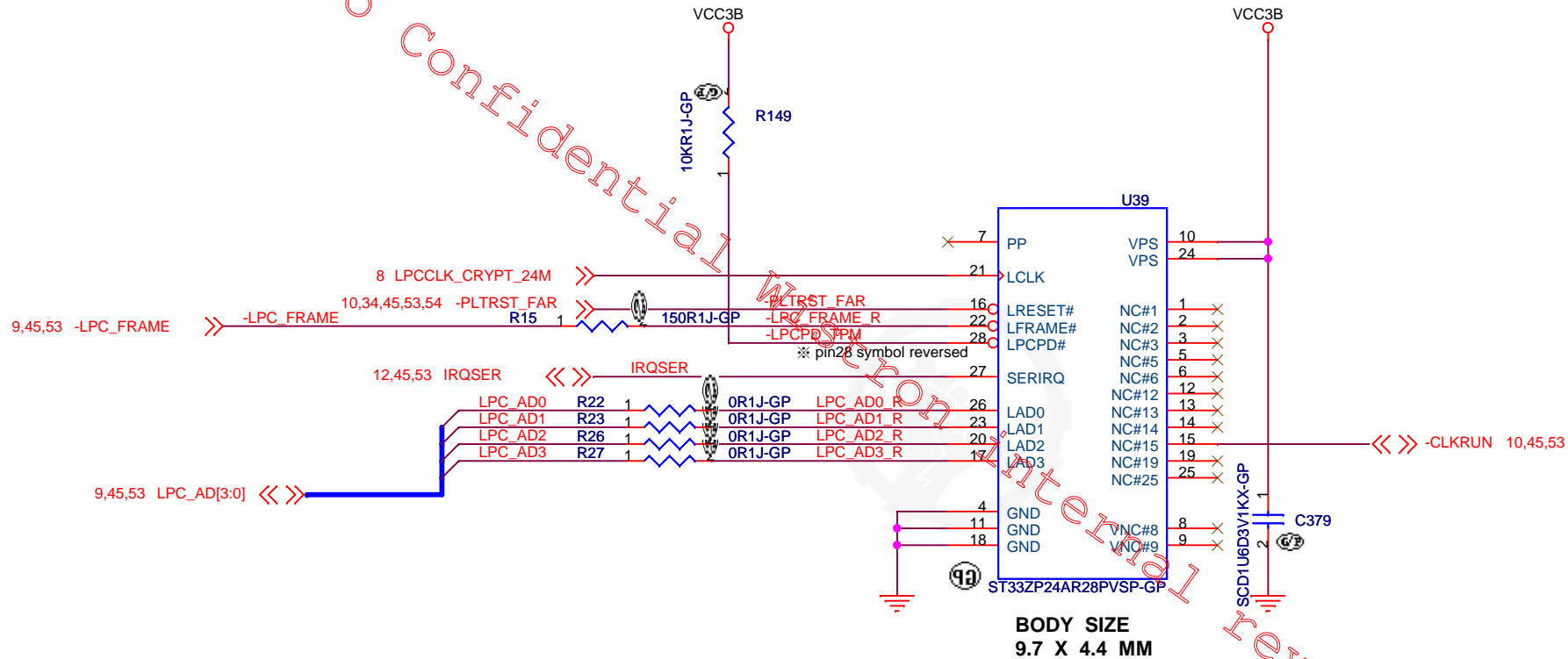
Mystique-1

Rev

-2

Date: Tuesday, November 12, 2013

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Taipei Hsien 221, Taiwan, R.O.C.

Title

TPMSize
A4

Document Number

Rev
2

Mystique-1

Date: Tuesday, November 12, 2013

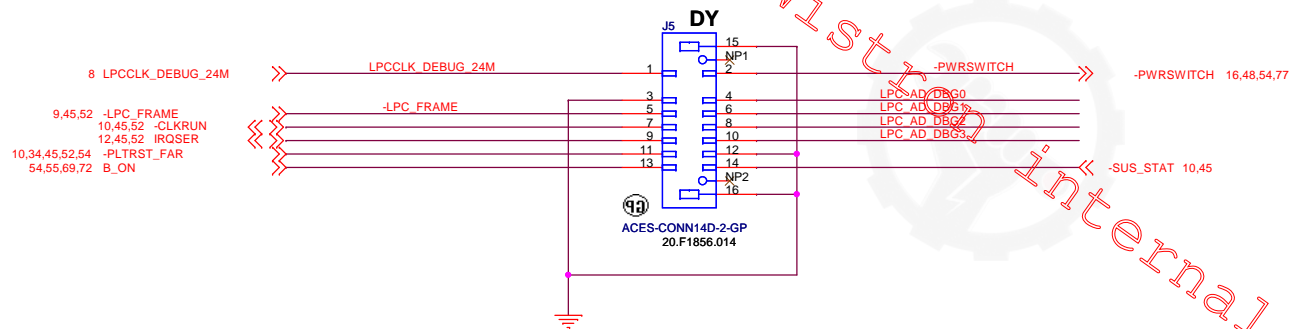
Sheet 52 of 79

TABLE

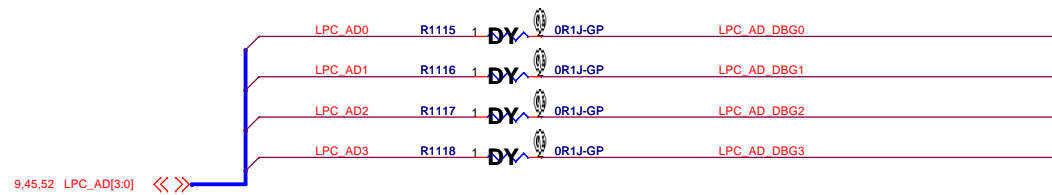
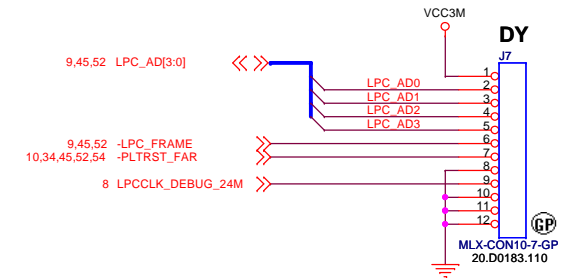
REF DES	ENABLE	DISABLE
J5	ASM	NO_ASM
R220	ASM	NO_ASM
R1115	ASM	NO_ASM
R1116	ASM	NO_ASM
R1117	ASM	NO_ASM
R1118	ASM	NO_ASM

LOGIC

Lenovo Debug Tool I/F



LPC for Debug Card Connector



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 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title **EEPROM/SMBUS SW**Size
A3 Document Number**Mystique-1**Rev
-2

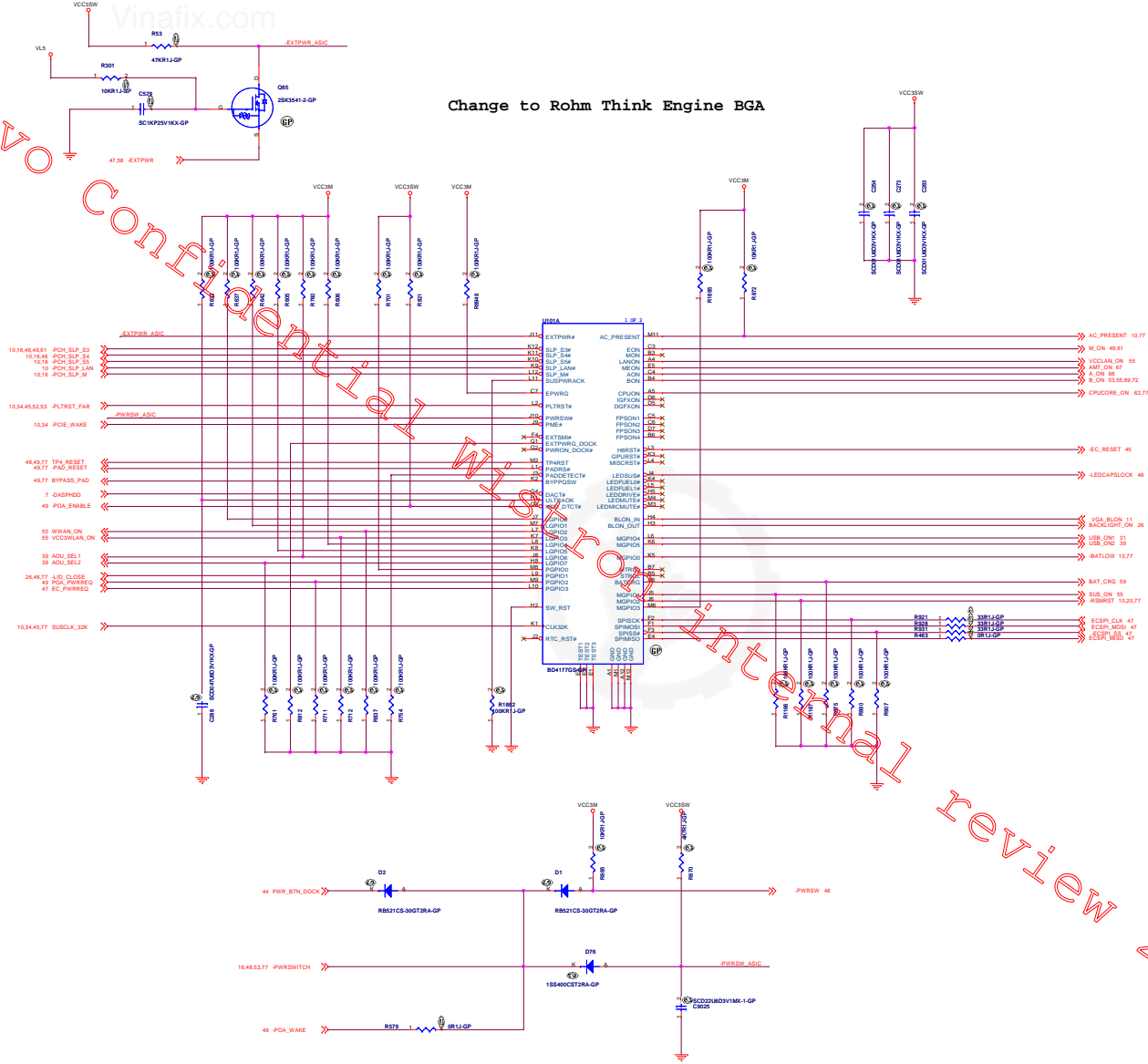
Date: Tuesday, November 12, 2013

Sheet 53 of 79

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Change to Rohm Think Engine BGA



review 2013/11/14

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CONFIDENTIAL

WATSON Internal

review 2023/11/14

TABLE

AOAC	YES	NO
D73	ASM	NO-ASM
R8931	ASM	ASM
	100K-ohm	0-ohm

LOGIC

TABLE

ID	Target
RT1	DCIN_PWR20_F to CV20 FET (Q9)
RT2	VCC3M Switching DC/DC High/Low Side FET (Q18)
RT3	CHARGER_OUT12 to BAT_PWR12FET (Q35)
RT4	M_BAT_PWR to M_BAT_PWR_A FET (Q8)
RT5	VCC5M Switching DC/DC High/Low Side FET (Q16)
RT6	CPU Die (U58)
RT7	VCCCPUCORE Volterra Driver IC VT1334S (U16)

RT1-RT7 is used THERMISTOR 540OHM PRF15BB541NB

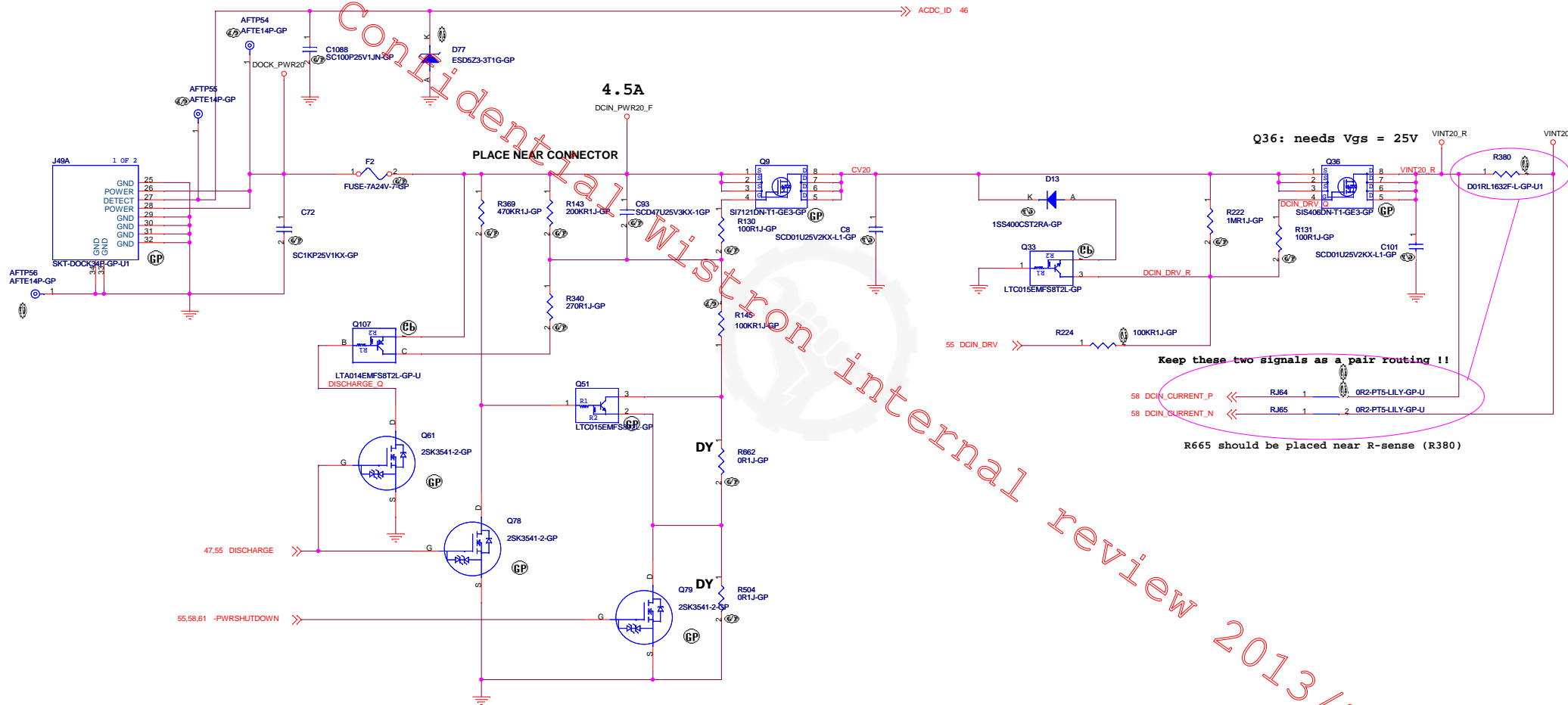
ULT

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title	THINK ENGINE(22)		
Size	Document Number		Rev
A2			+2
Date:	Tuesday, November 12, 2013	Sheet	79

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D77
ESD5Z3.3T1G/On Semi 83.ESD5Z.0AF
uClamp3301H/SEMTECH 83.03301.AAF



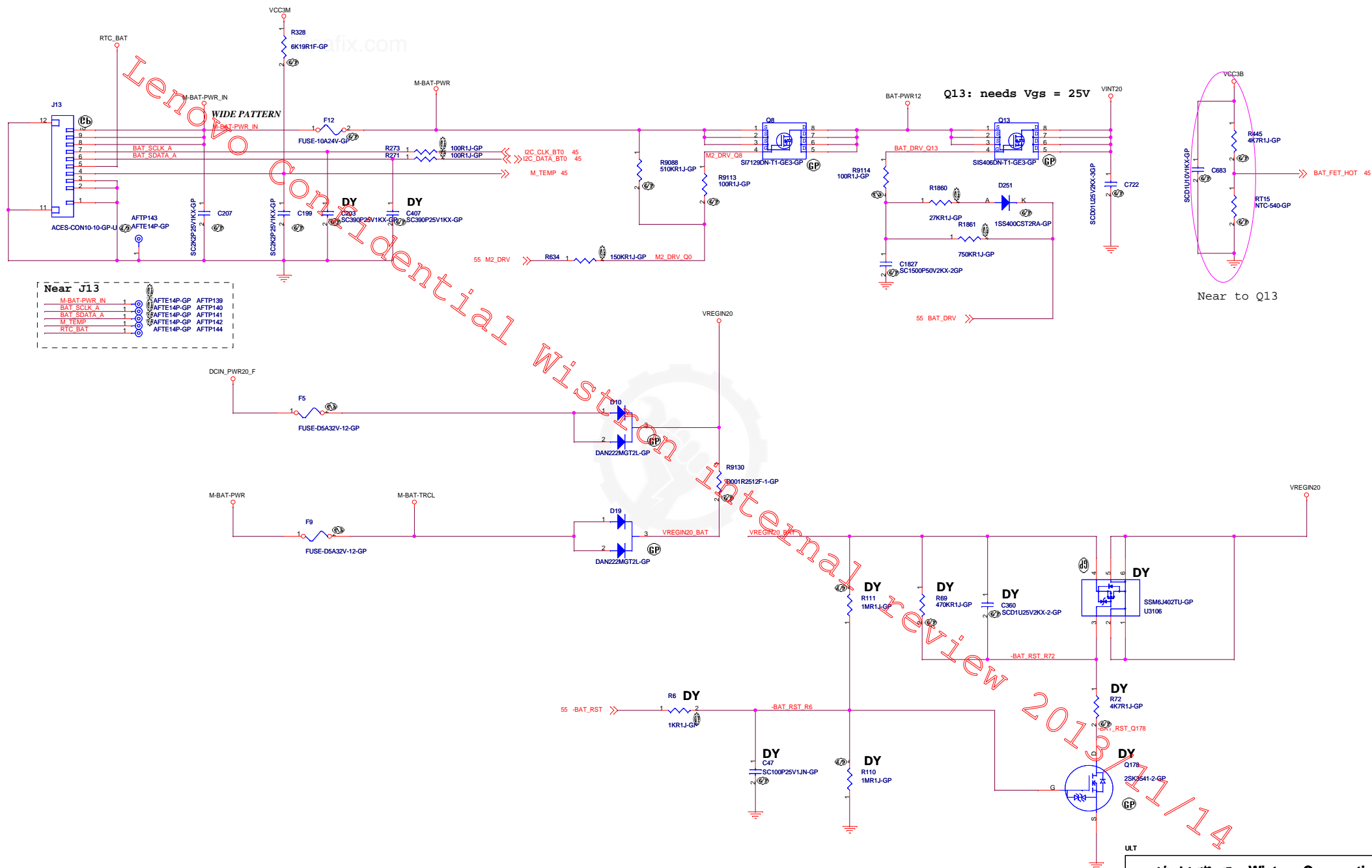
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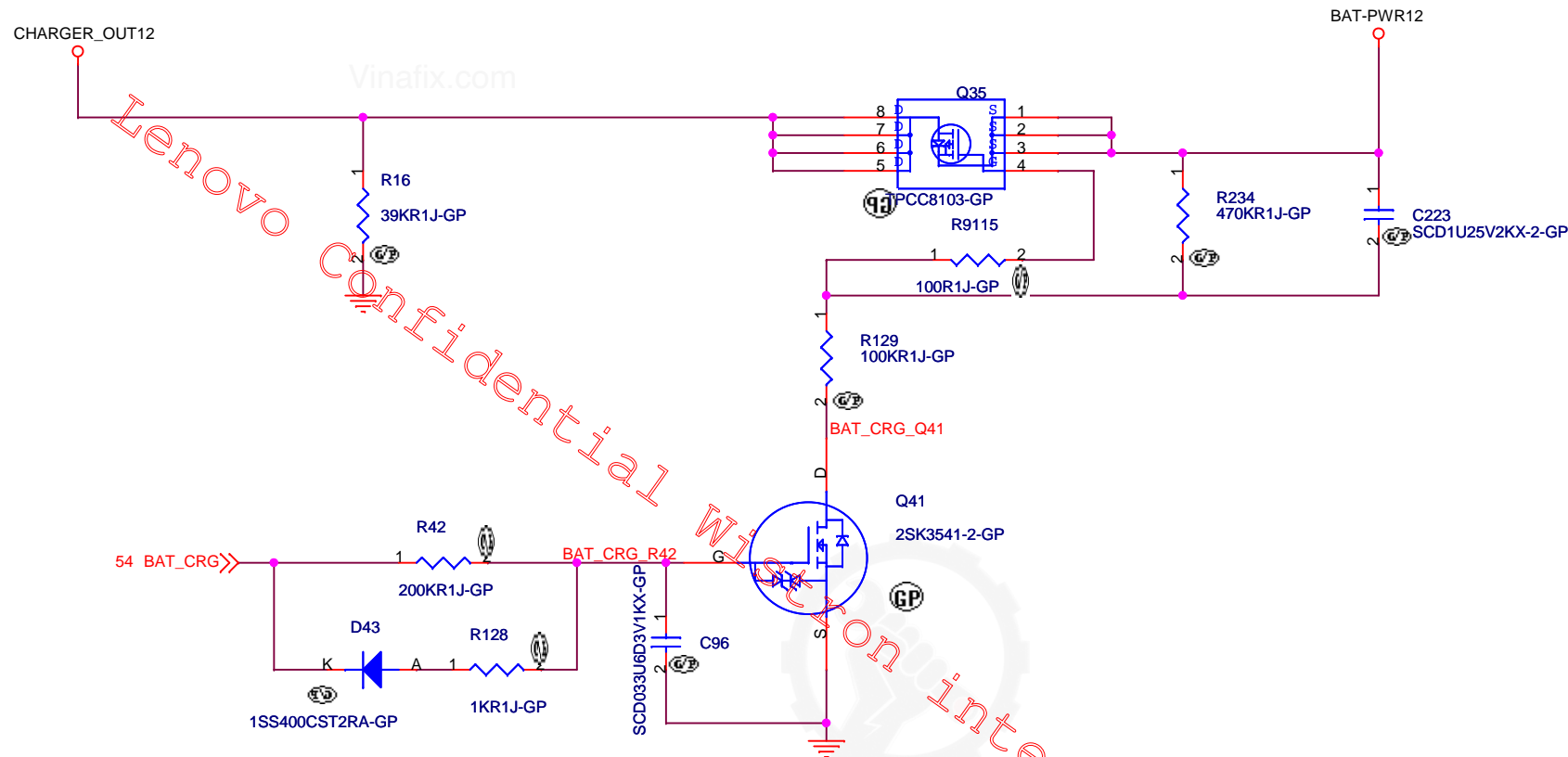
PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

Logic

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21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 221, Taiwan, R.O.C.			
File DC-IN			
Size	Document Number	Rev	
Custom	Mystique-1	-2	
Date: Tuesday, November 12, 2013	Sheet 56 of 79		





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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **CHARGER SELECTOR**

Size
A4

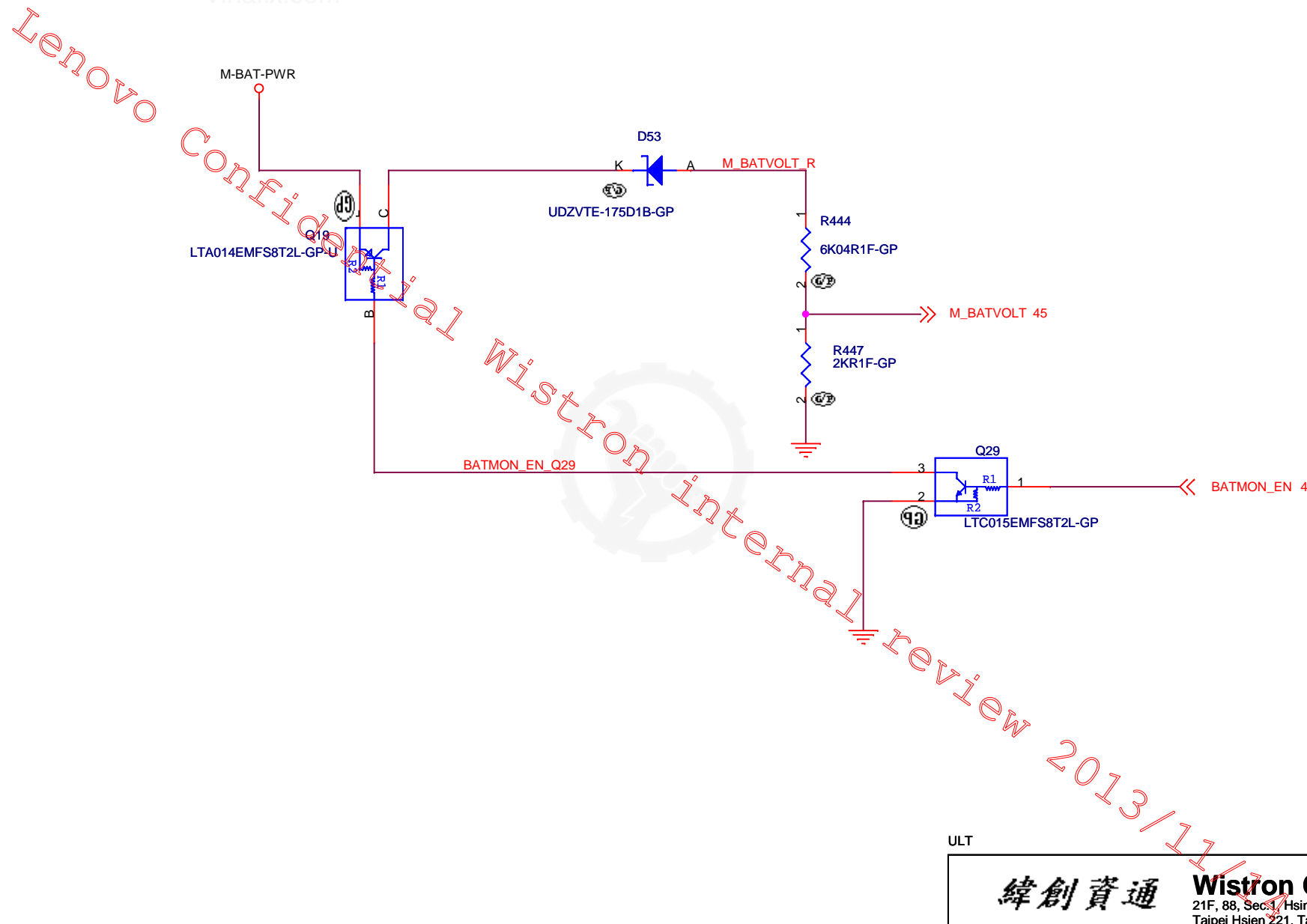
Document Number

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Rev
-2

Date: Tuesday, November 12, 2013

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Taipei Hsien 221, Taiwan, R.O.C.

Title
BATTERY MONITOR

Size
A4 Document Number

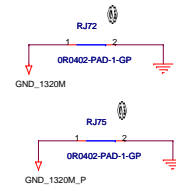
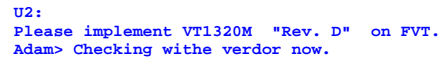
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Rev
-2

Date: Tuesday, November 12, 2013

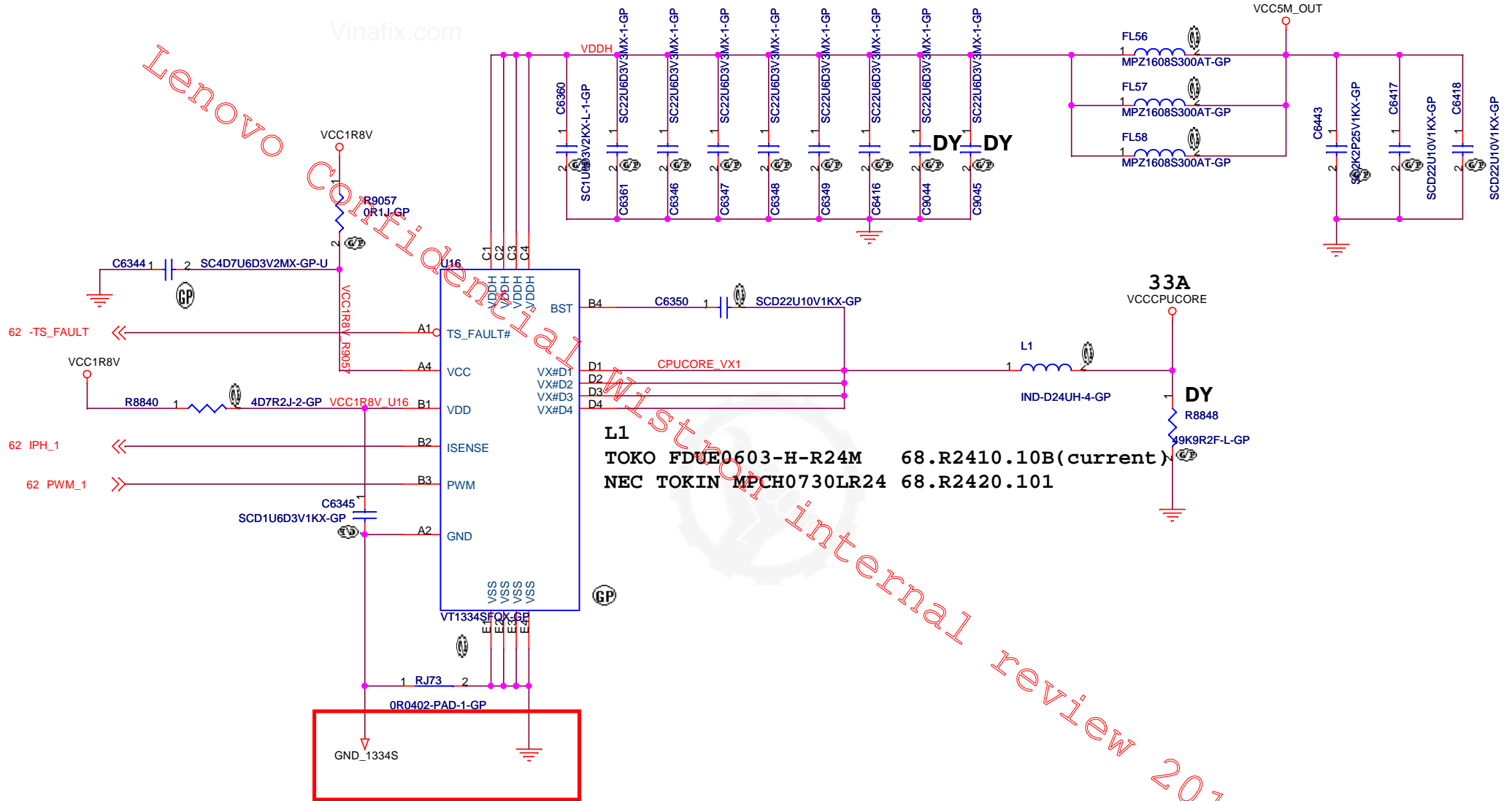
Sheet 60 of 79

R8831 if noise is visable,
option to place chip bead instead of resistor to reduce noise



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Title **DC/DC VCCCPUCORE(VT1334S)**

Size Custom Document Number

Mystique-1

Rev -2

Date: Tuesday, November 12, 2013

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Title

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Size
A4

Document Number

Mystique-1

Rev
-2

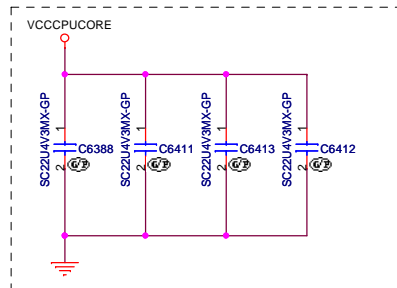
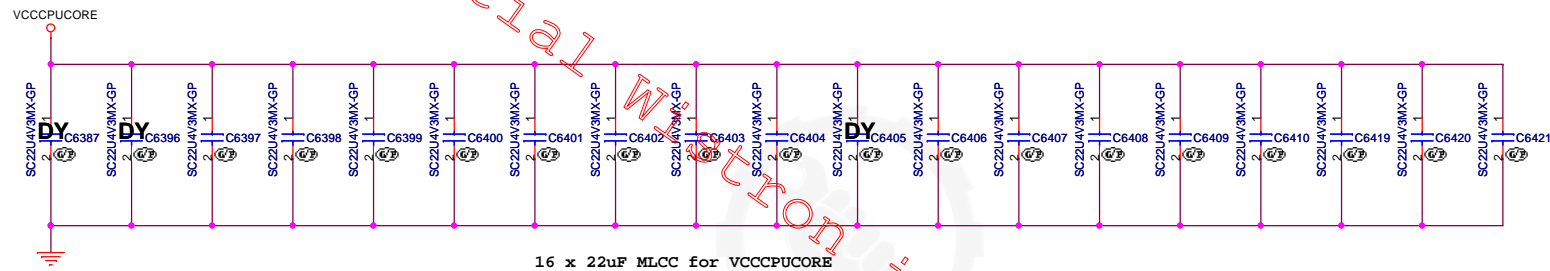
Date: Tuesday, November 12, 2013

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4 x 22uF MLCC for VCCCPUCORE

Lenovo: If change from 0805 to 0603 size, the number of capacitors must increased 20% minimum.
these four caps are the 25% additional.

ULT

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title VCCCPUCORE DECOUPLING			
Size A3	Document Number	Mystique-1	Rev -2
Date: Tuesday, November 12, 2013		Sheet 65	of 79

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Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
A4

Document Number

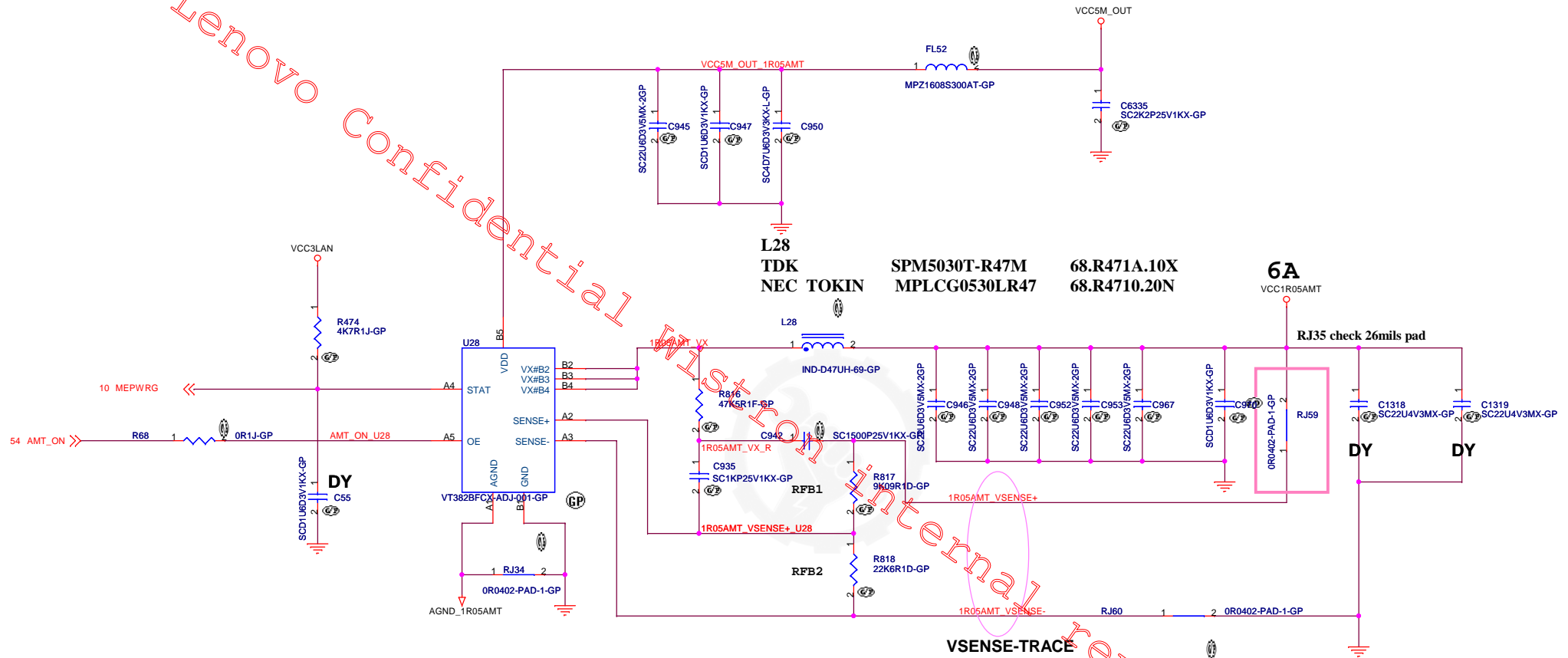
Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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VSENSE-TRACE
ROUTED
DIFFERENTIALLY
PARALLEL TO VSENSE+

TABLE

$V_{OUT} = 0.75 \times (1 + R817/R818)$		
RFB1	RFB2	
R817	R818	
9.31K	22.6K	for 1.059V

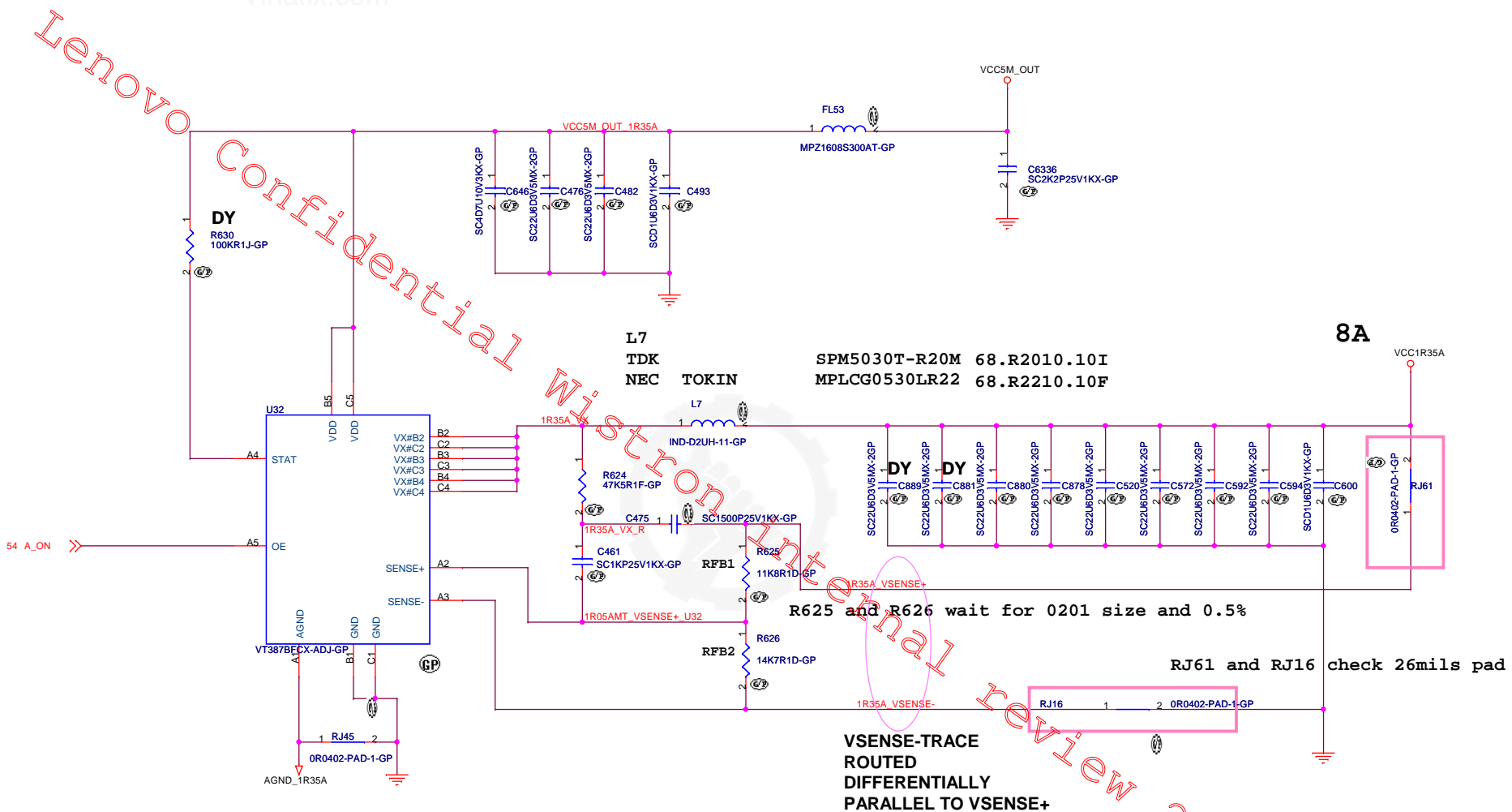
ULT

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **DC/DC VCC1R05AMT(VT382B)**

Size A3 Document Number **Mystique-1** Rev -2

Date: Tuesday, November 12, 2013 Sheet 67 of 79

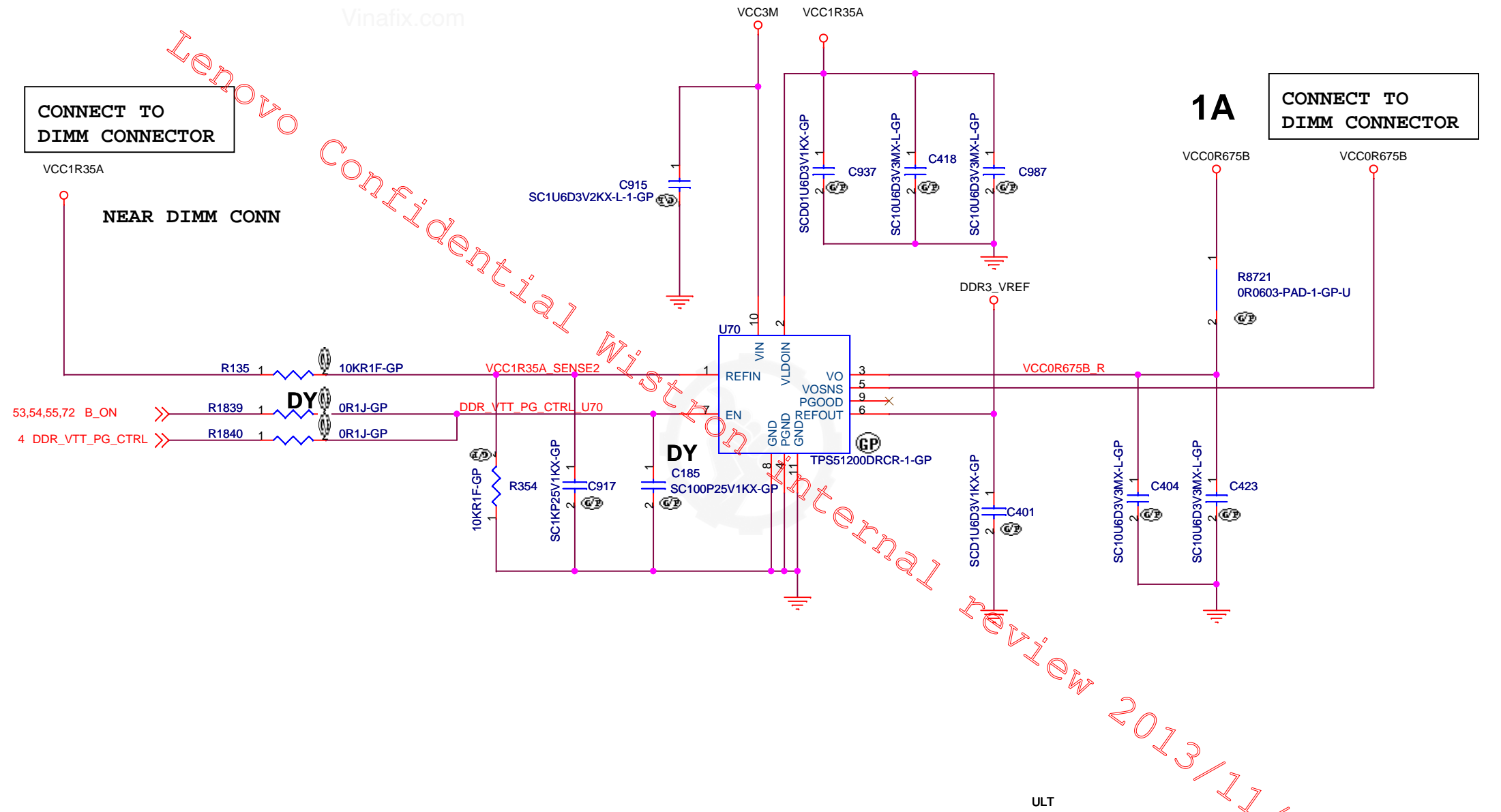


R625 and R626 wait for 0201 size and 0.5%

RJ61 and RJ16 check 26mils pad

VSENSE-TRACE
ROUTED
DIFFERENTIALLY
PARALLEL TO VSENSE+

TABLE	
$V_{OUT} = 0.75 \times (1 + R_{FB1}/R_{FB2})$	
RFB1	RFB2
R625	R626
11.8K	14.7K for 1.35V
13.3K	13.3K for 1.50V
12.1K	14.7K for 1.367V
13.3K	13.0K for 1.517V



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Taipei Hsien 221, Taiwan, R.O.C.

Title **DC/DC VCC0R675B(TPS51200)**

Size A4	Document Number Mystique-1	Rev -2
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Date: Tuesday, November 12, 2013 Sheet 69 of 79

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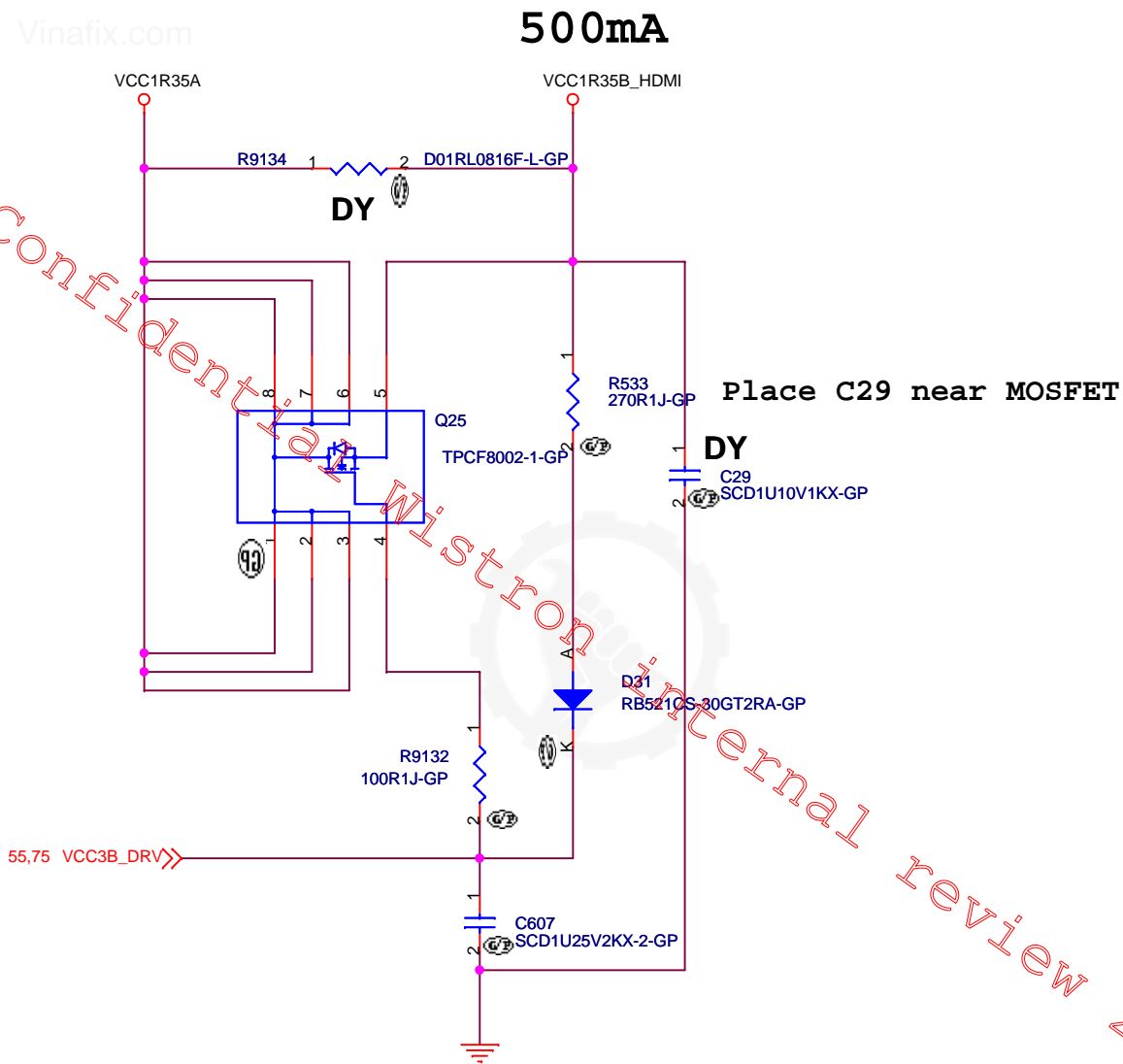
Wistron Corporation

21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,
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Title			BLANK	
Size	Document Number		Rev	
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Title			
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Size	Document Number		Rev
A4	Mystique-1		-2
Date:	Tuesday, November 12, 2013		Sheet 71 of 79

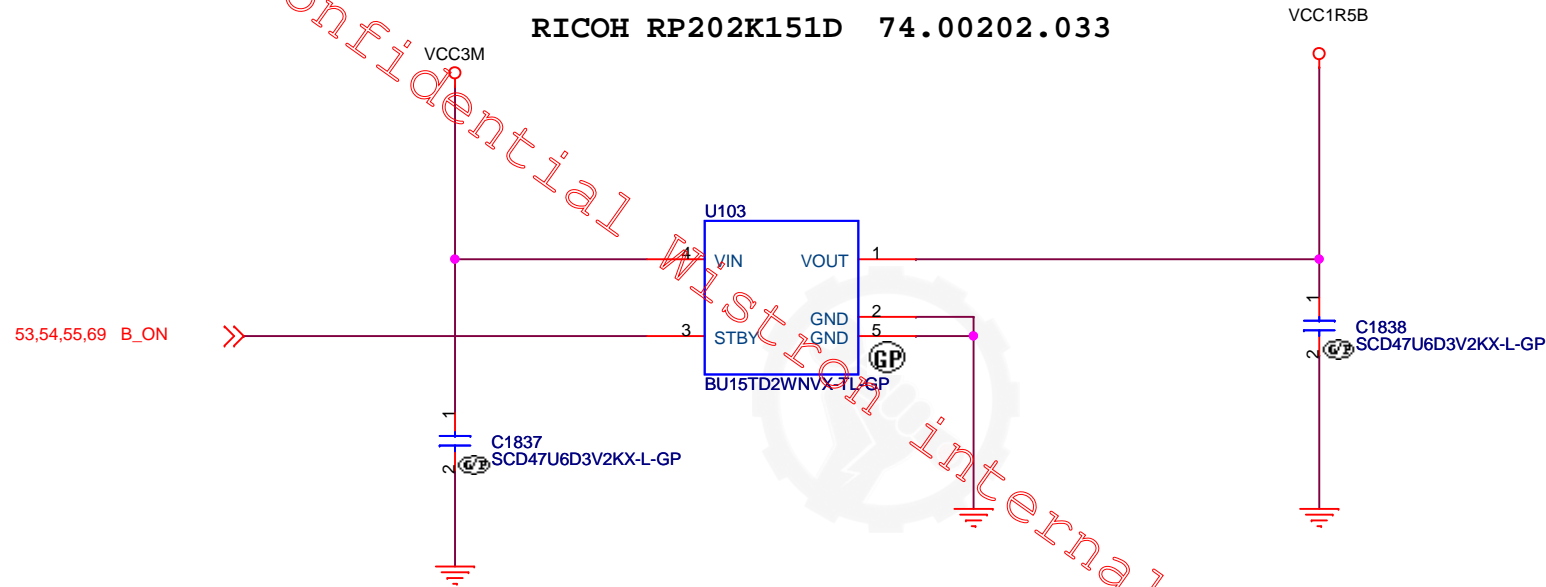
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TABLE

ROHM BU15TD2WNVX 74.00152.033
RICOH RP202K151D 74.00202.033

10mA



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Taipei Hsien 221, Taiwan, R.O.C.

Title

DC/DC VCC1R5B(BU15TD2WNVX)

Size
A4

Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

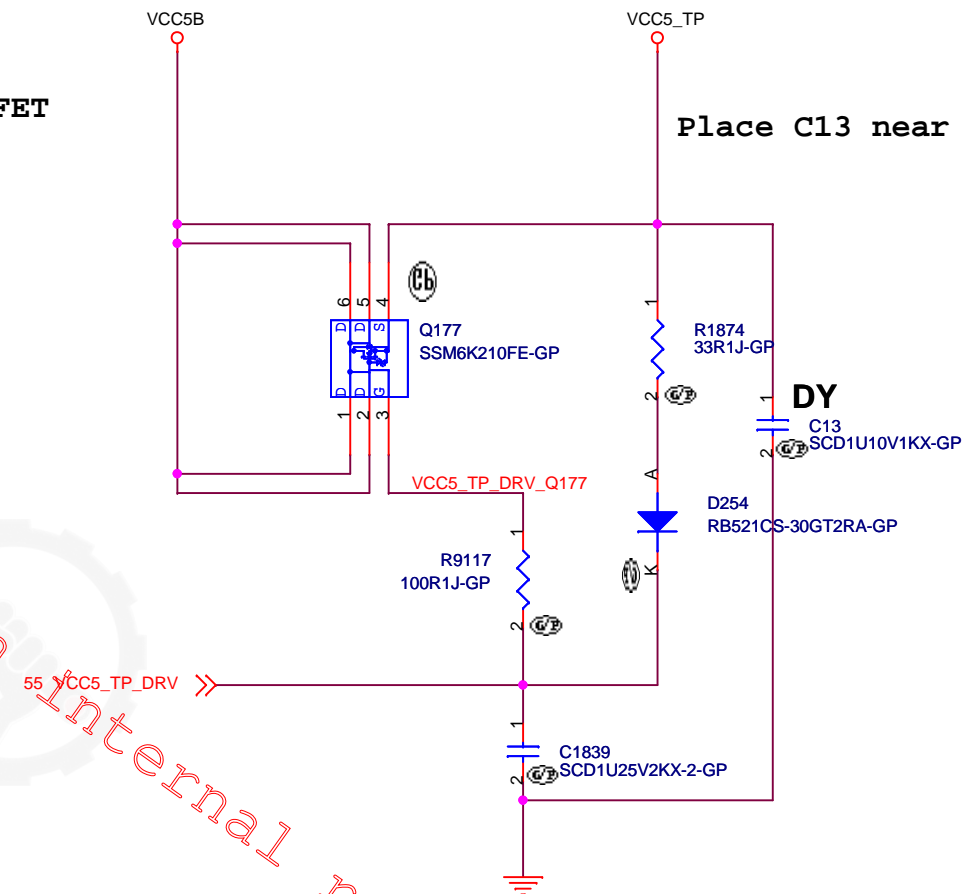
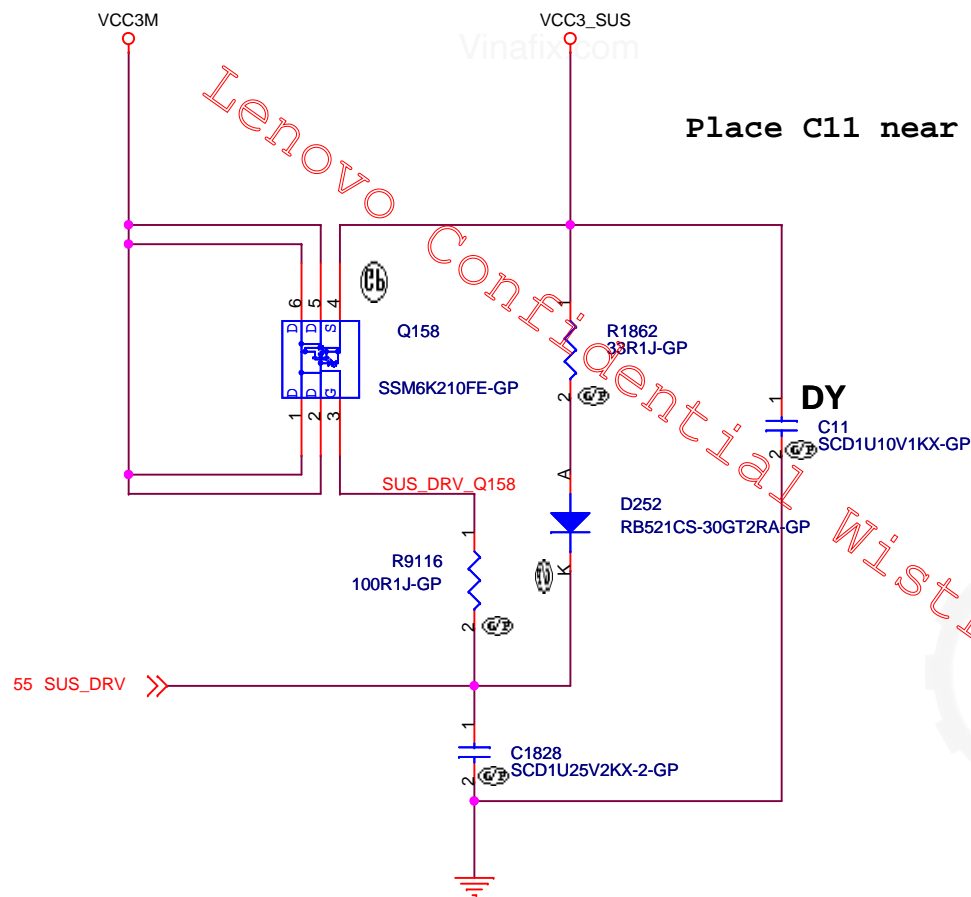
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0.3A

15mA

Place C11 near MOSFET

Place C13 near MOSFET



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Taipei Hsien 221, Taiwan, R.O.C.

Title **LOAD SW PCH SUS**

Size
A4

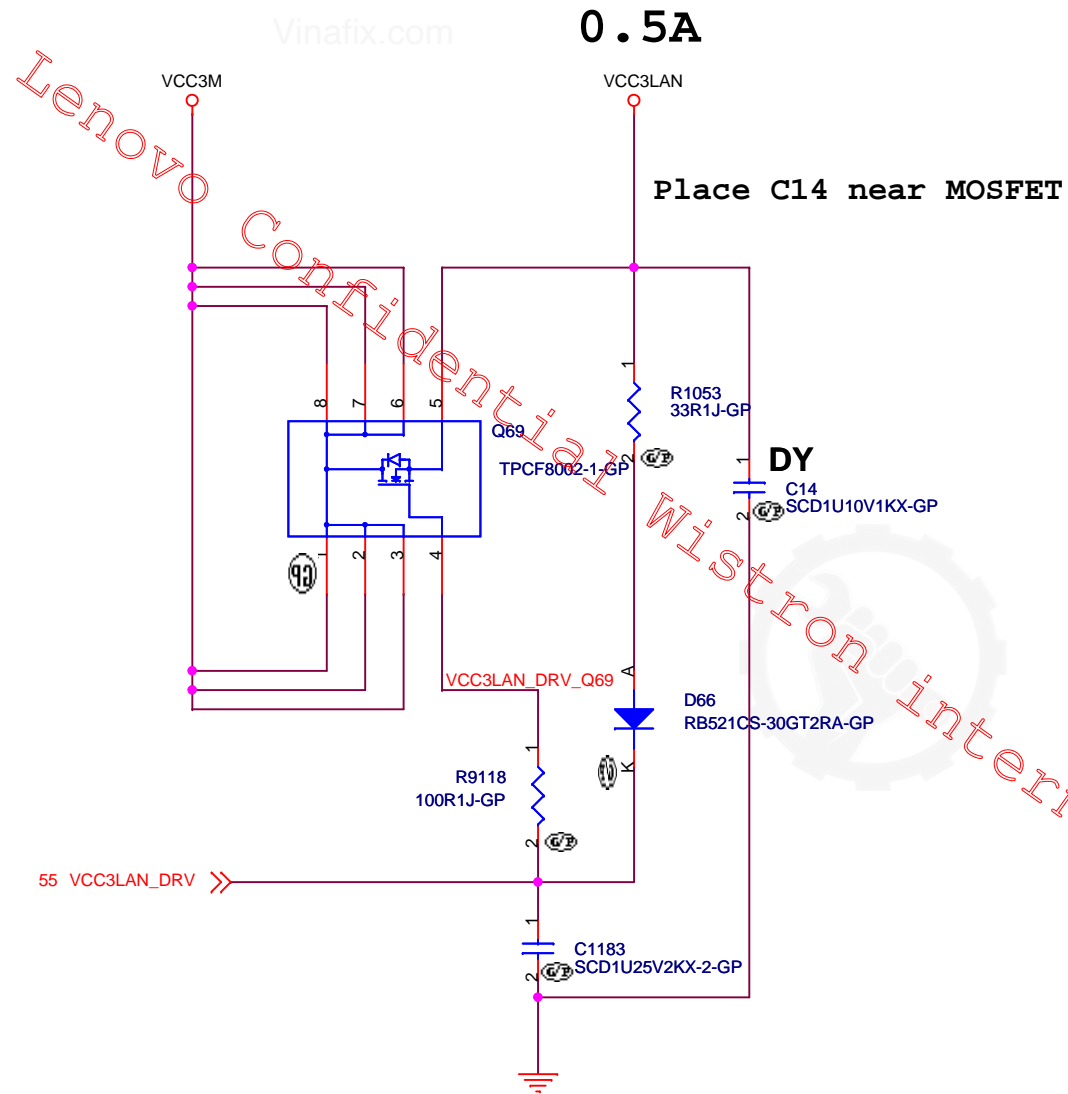
Document Number

Mystique-1

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Title

LOAD SW LAN

Size
A4

Document Number

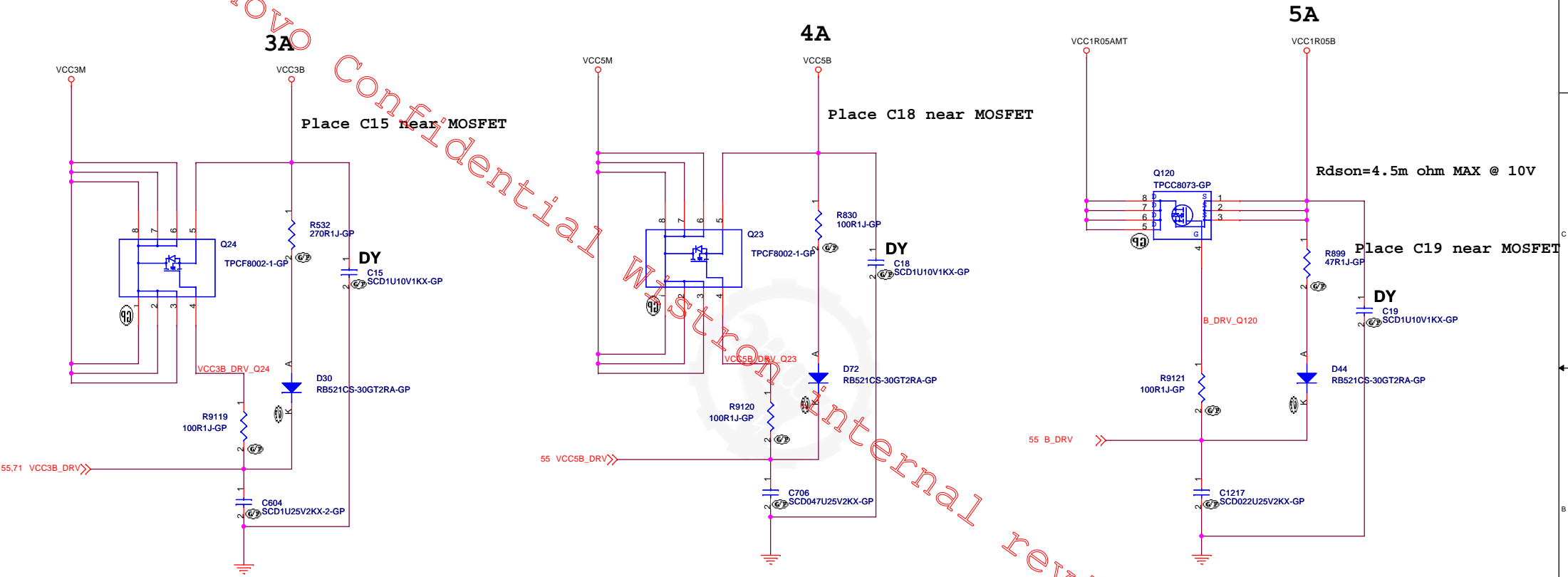
Mystique-1

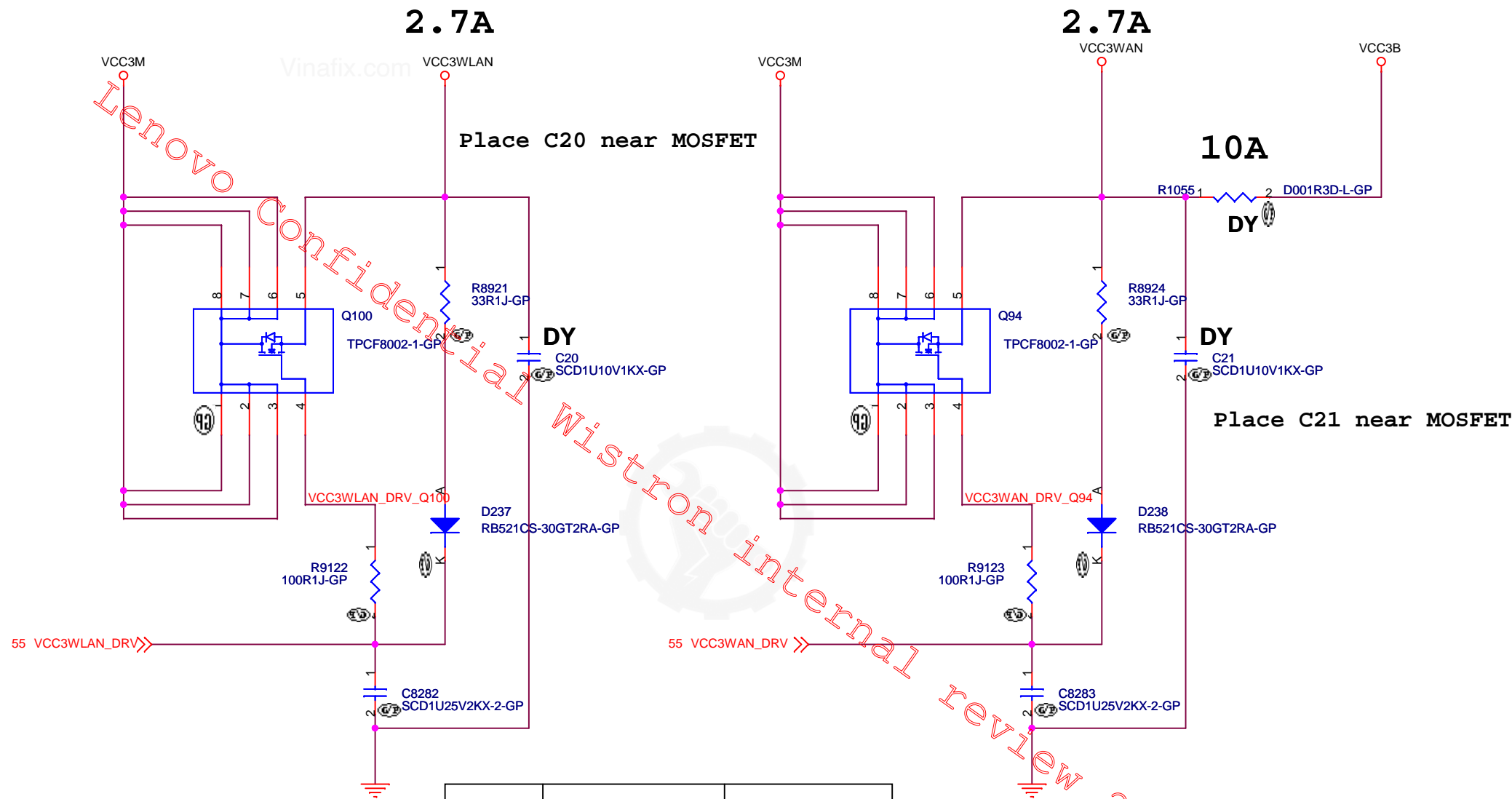
Rev
-2

Date: Tuesday, November 12, 2013

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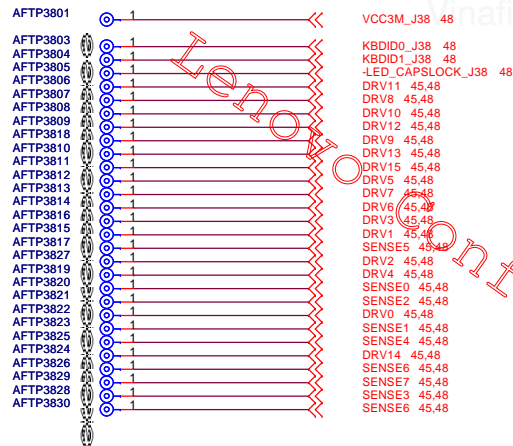
AOAC	YES	NO
R1055	NO-ASM	ASM
Q94	ASM	NO-ASM
R8924	ASM	NO-ASM
R8926	ASM	NO-ASM
C8283	ASM	NO-ASM
D238	ASM	NO-ASM

LOGIC

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<p>緯創資通 Wistron Corporation 21F, 88, Sec. 4, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</p>		
<p>Title LOAD SW WWAN & WLAN</p>		
Size A4	Document Number Mystique-1	Rev -2
<p>Date: Tuesday, November 12, 2013 Sheet 76 of 79</p>		

Near J38(Keyboard connector)



Near J37(Track point connector)



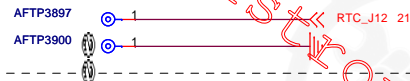
Near S3



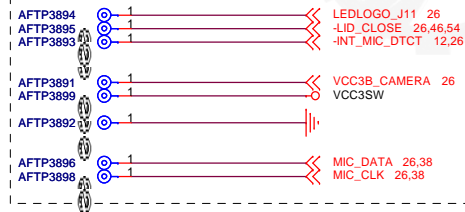
Near J15(FAN Connector)



Near J12(RTC)



Near J11(LED)



Near J21 (Clickpad + NFC)



For Power Switch

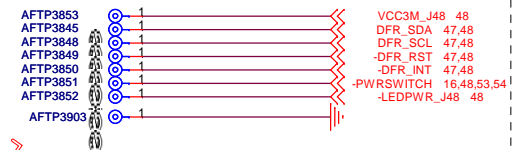
Top side



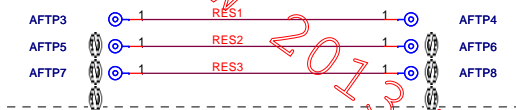
Bottom side



For J48 (DFR)



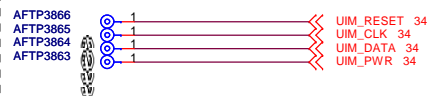
For PCB resistance measurement when temperature is vary.



Near J4 (Speaker)



Near J25(SIM)



MFG request

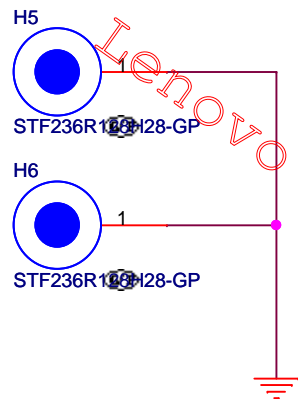
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Taipei Hsien 221, Taiwan, R.O.C.

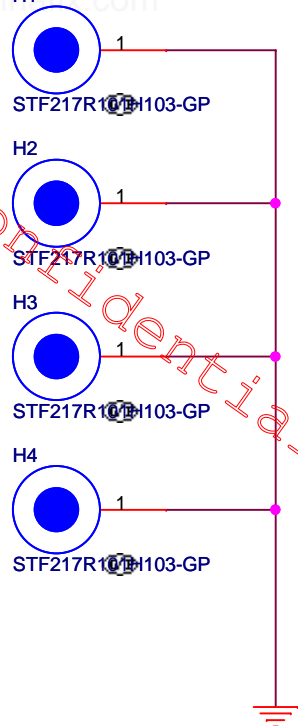
Title TEST POINT

Size A3 Document Number Mystique-1 Rev -2
Date: Tuesday, November 12, 2013 Sheet 77 of 79

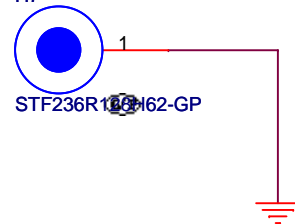
34.4LY01.001
NGFF



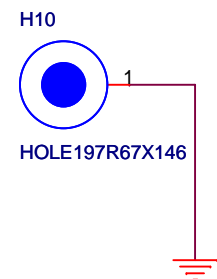
34.4LY02.001
CPU BOSS



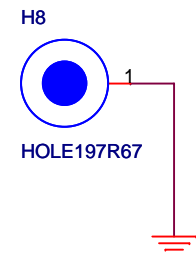
34.4LY03.001
NGFF SSD



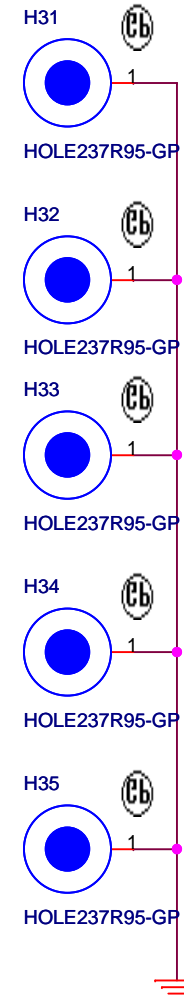
KN1.PAD.01



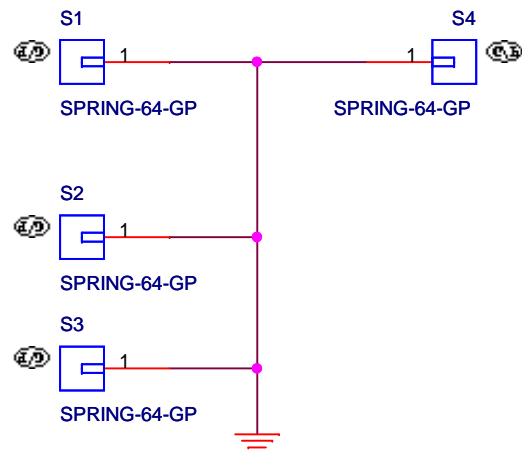
NL1.PAD.01



ZZ.00PAD.921



34.4H602.001
mount antenna cable



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Title **HOLES/GND/PADS/TEMP**

Size A Document Number

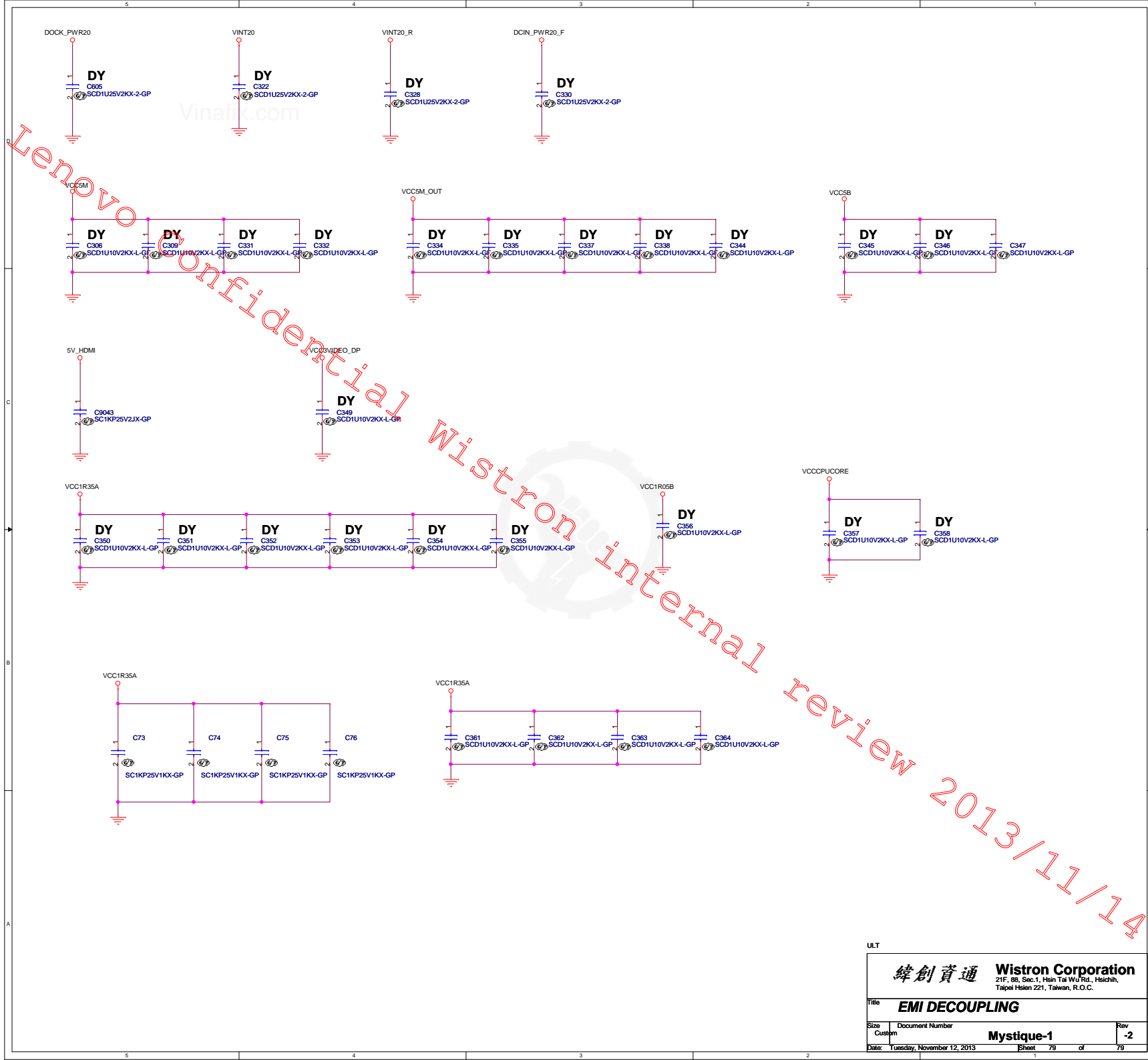
Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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