

| | |
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| | |
| | |
| | |

MS-7610

Version 0A

CPU:

Intel Conroe (95W Dual core)

System Chipset:

Intel G41 - MCH (North Bridge)

Intel ICH7 (South Bridge)

On Board Chipset:

BIOS -- SPI

HD -- ALC889

LPC Super I/O -- F71889G

LAN-- REALTEK RTL8111D Co-lay RTL8103E

CLOCK -- RTM875-605

Main Memory:

DDR II *2 (Max 4GB)

Expansion Slots:

PCI2.3 SLOT * 3

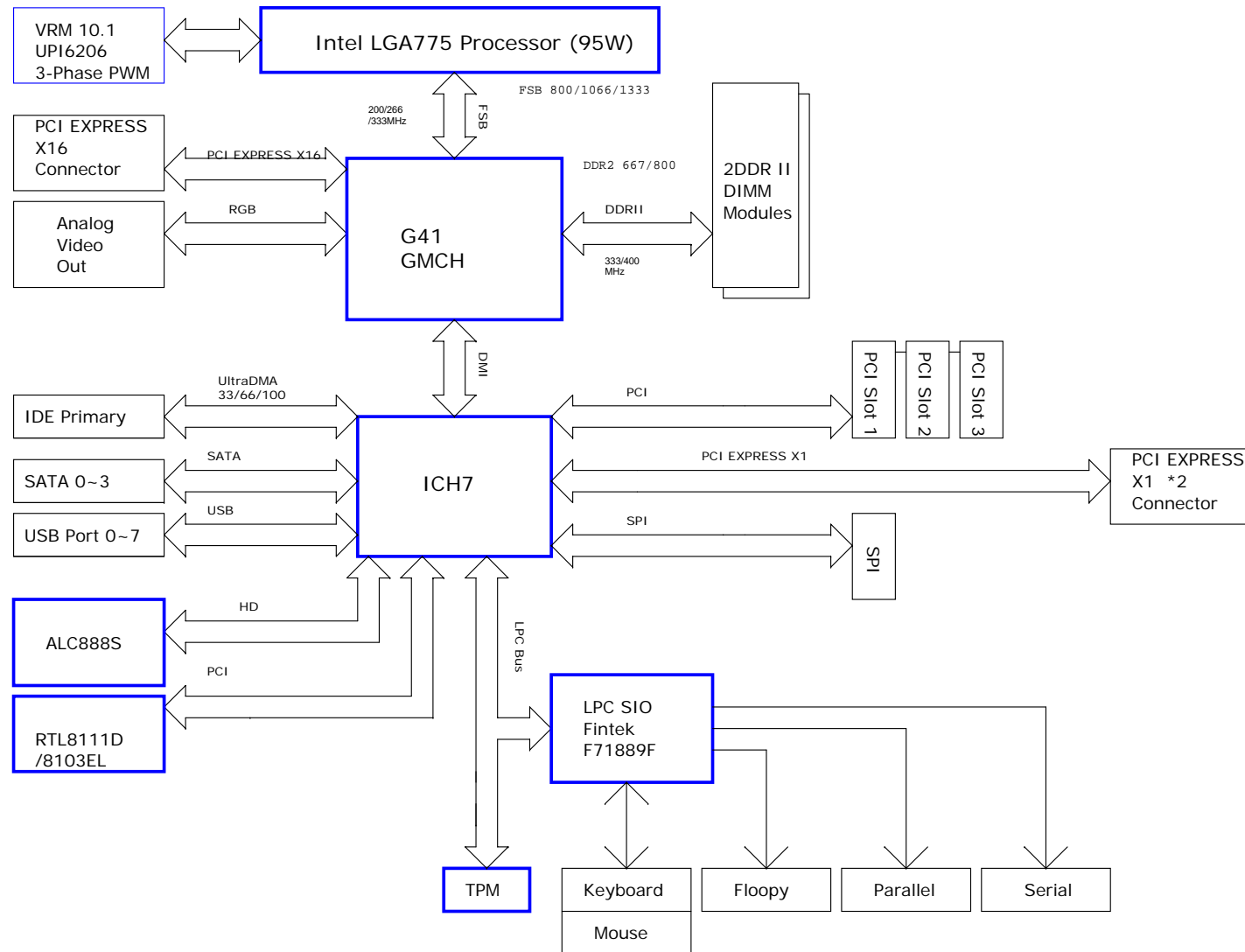
PCI EXPRESS X1 SLOT *2

PCI EXPRESS X16 SLOT *1

UPI PWM:

Controller: 3 PHASES + APS

Block Diagram

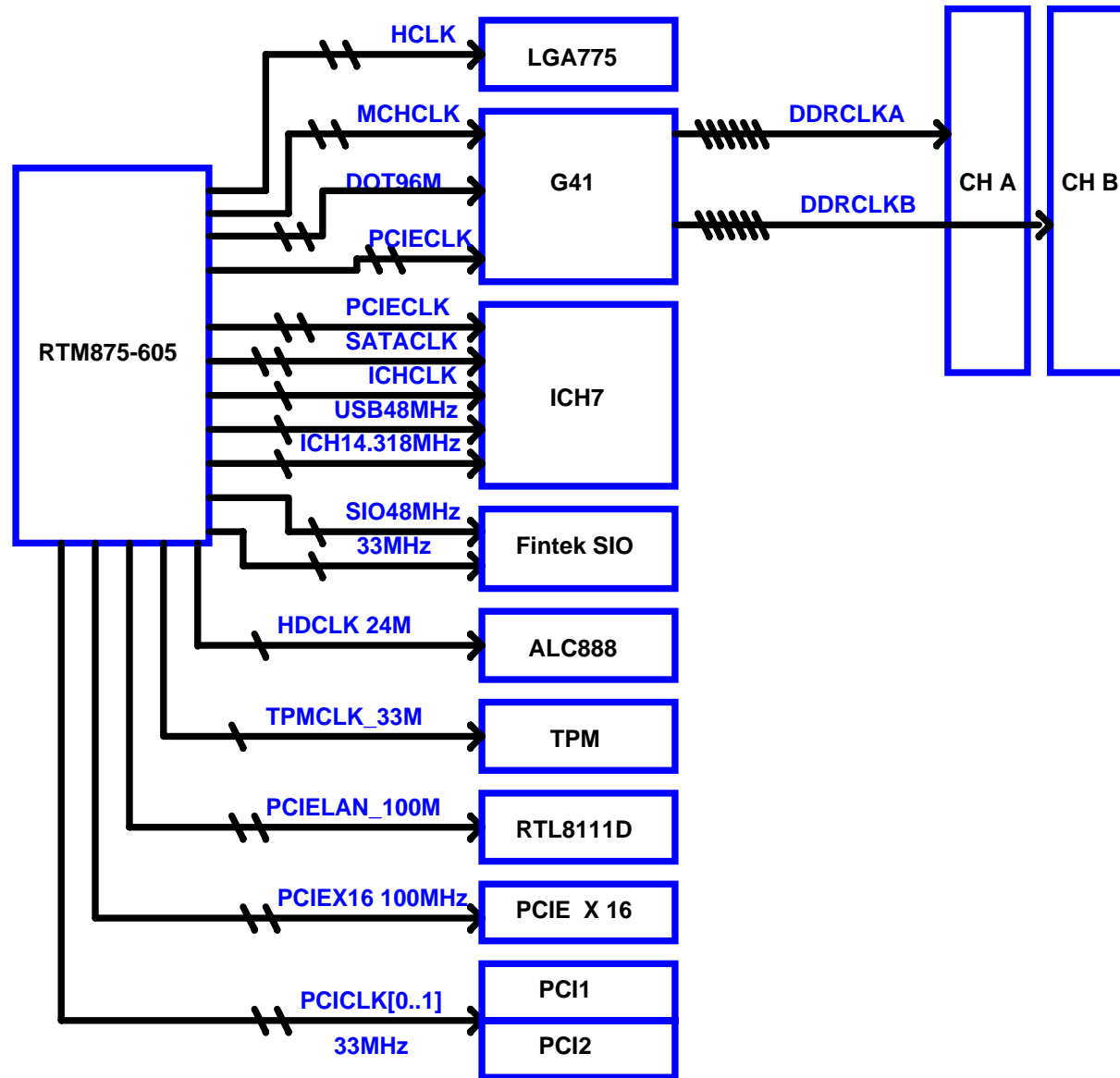


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| | | |
|------------------------------|--|------------|
| Size Custom | Document Description BLOCK DIAGRAM | Rev 1.0 |
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CLOCK MAP



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| Size | Document Description | Rev |
|------------------------------|----------------------|-----|
| Custom | CLOCK MAP | 1.0 |
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| |
|------------------------|
| Processor (95W) |
| 1.15-1.5000V Core-70A |
| 1.2V FSB Vtt-5.8A |
| VCCPLL |
| VCC-IOPLL & VCCA |

| |
|------------------------------|
| G41 MCH 1.1V core 22A |
| 1.2V FSB Vtt-0.9A |
| 1.8V DDR2 I/O-4.4A(S0,S1) |
| 1.8V DDR2 I/O-25mA(S3) |
| 0.9V DDR2 VREF-2mA |
| 0.9V DDR2 SB_VREF-10uA |
| DDR2 Resister Comp V-36mA |
| DDR2 Resis Comp SB_V-10uA |
| 1.1V Core-13.8A(Integrated) |
| 1.1V Core-8.9A(Discrete) |
| 1.5V PCI Express&DMI-0.68A |
| 1.1V PCIE&DMI PLL-41mA |
| 1.5V HOST PLL-45mA |
| 1.5V VCCA_DPLLA&B-55mA |
| 1.5V MPLL-66mA |
| 1.1V Vcc-core 1.16A |
| 1.1V VCC_CL-3A |

| |
|---------------------------|
| ICH7 |
| 1.2V VCC_CPU-14mA |
| 1.05V Core-0.86A |
| VCC1_5 SATA/USB/PLL 1.65A |
| VCC1_5B*-0.646A |
| 5VRef-6mA |
| 5VrefSus-10mA |
| +3.3V-0.33A |
| RTC-6uA(G3) |
| 3.3V VccSus*-52mA |
| VccSus1_05V-See Note 1 |
| VccUSBPLL-10mA |
| VccDMIPLL-41mA |
| VccSATAIPLL-50mA |

Battery

| |
|--------------------------|
| UPI6206 Regulator |
| VCCP |
| 1.15-1.5000V |

| |
|----------------------|
| VTT Regulator |
| V_FSB_VTT |
| 1.2V |

| |
|-------------------------|
| uP6103 Regulator |
| VCC_DDR |
| 1.8V |

| |
|-----------------------|
| V1.5 Regulator |
| V_1P5_CORE |
| 1.5V |

| |
|-----------------------|
| 1.1V Regulator |
| V_1P1_Core |
| 1.1V |

| |
|------------------------|
| 1.05V Regulator |
| V_1P05_CORE |
| 1.05V |

| |
|-------------------------|
| uP7706 Regulator |
| 3VSB |
| 3.3V |

| |
|-------------------------|
| uP7501 Regulator |
| 5VDIMM |
| 5V |

| |
|------------------------|
| W83310DS Regula |
| VTT_DDR |
| 0.9V |

| |
|-------------------------------------|
| DDR2 DIMM conn(4) & term |
| 0.9V SM Vtt-1.2A(S0) |
| 1.8V Vdd/vddq-4.7A(S0,S1) |

| |
|-------------------------|
| PCIE X16 slot(1) |
| +12V-5.5A |
| +3.3Vaux-375mA(wake) |
| +3.3Vaux-20mA(no wake) |
| +3.3V-3.0A |

| |
|------------------------|
| PCIE X1 slot(0) |
| +12V-0.5A |
| +3.3Vaux-375mA(wake) |
| +3.3Vaux-20mA(no wake) |
| +3.3V-3.0A |

| |
|-------------------------|
| PCI slot slot(2) |
| +3.3Vaux-375mA(wake) |
| +3.3Vaux-20mA(no wake) |
| +3.3V-5.6A |
| +5.0V-5.0A |
| +12V-0.5A |
| -12V-0.1A |

| |
|---------------|
| USB |
| +5V-4A(S0,S1) |

| |
|------------------|
| PS2 |
| +5V-345mA(S0,S1) |

| |
|---------------|
| CLKGEN |
| +3.3V-560mA |

| |
|------------|
| LAN |
| 3VSB- |

| |
|------------|
| SIO |
| +3.3V |
| 3VSB- |

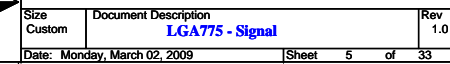
| |
|----------------|
| SPI ROM |
|----------------|

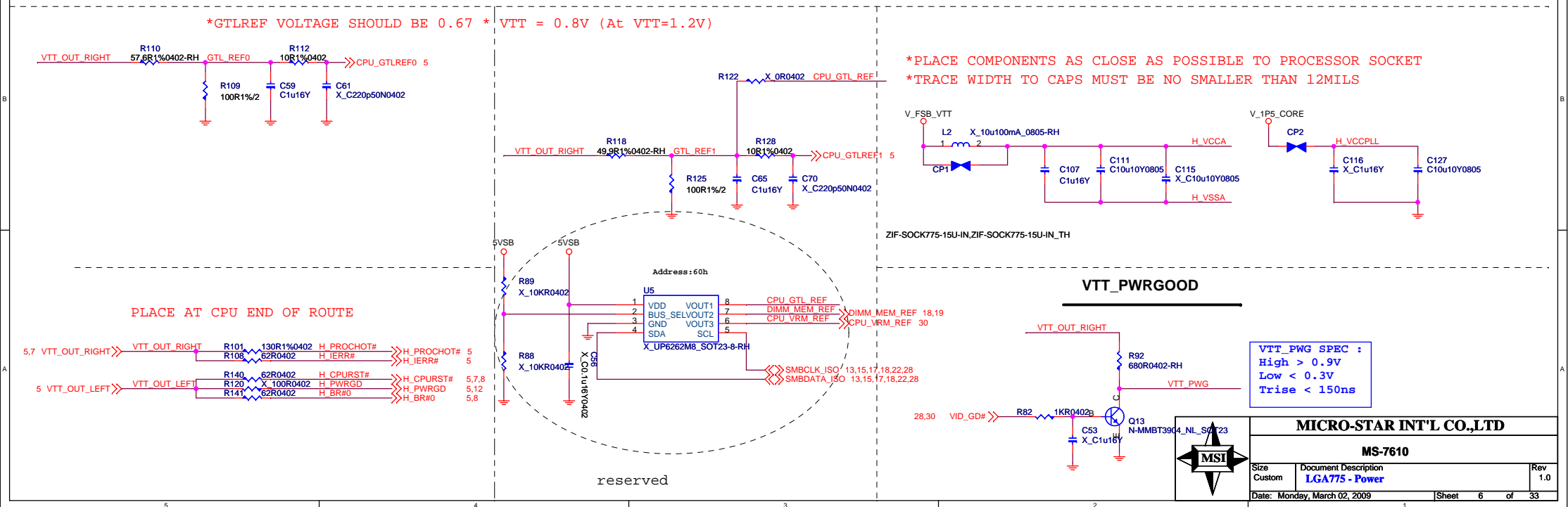
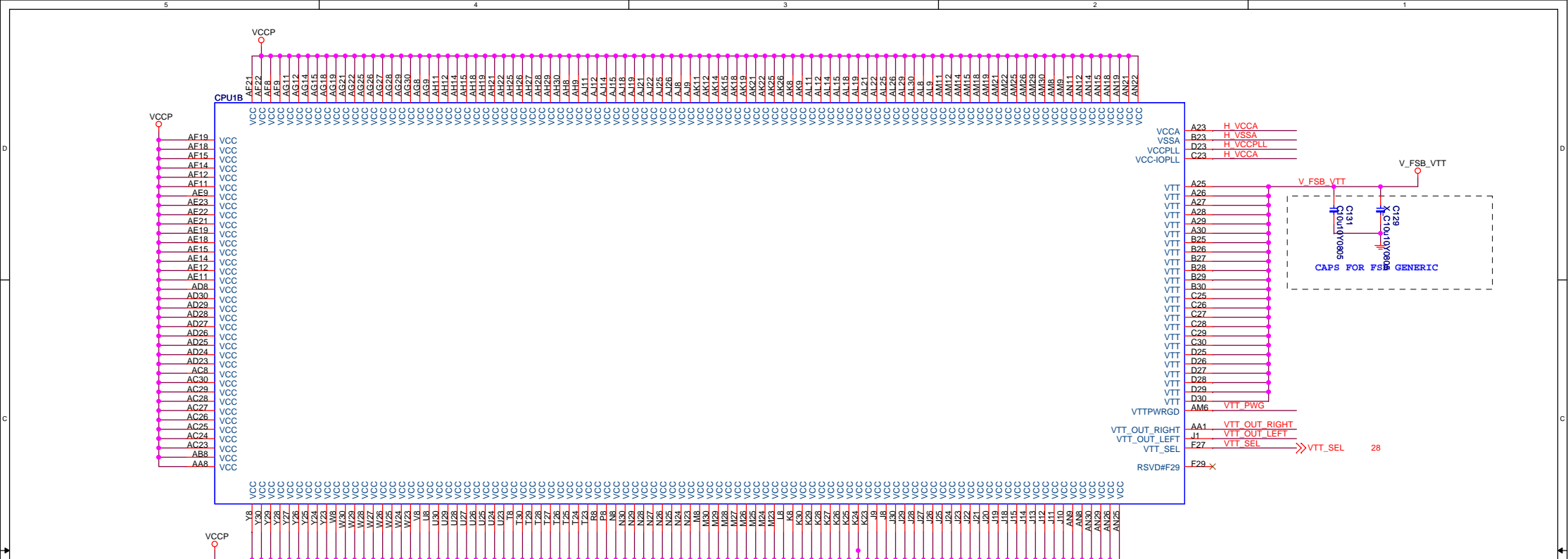
| |
|--------------------|
| Audio Codec |
|--------------------|

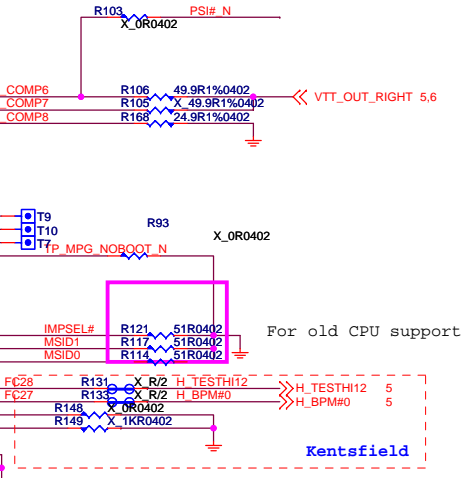
| |
|-------------|
| 1394 |
|-------------|

| |
|----------------|
| +12V |
| ATX 2x2 |

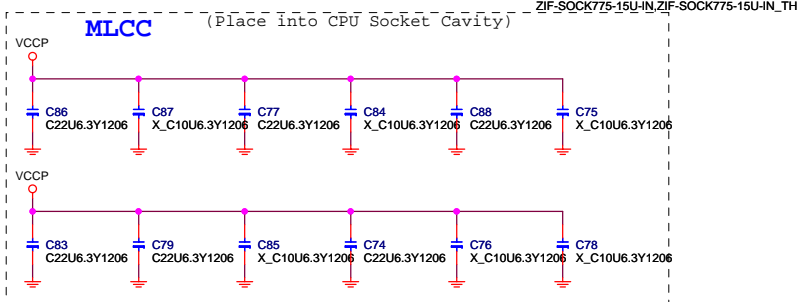
| | | | |
|------------------|------------|--------------|--------------|
| +12V | +5V | +3.3V | +5VSB |
| ATX POWER | | | |

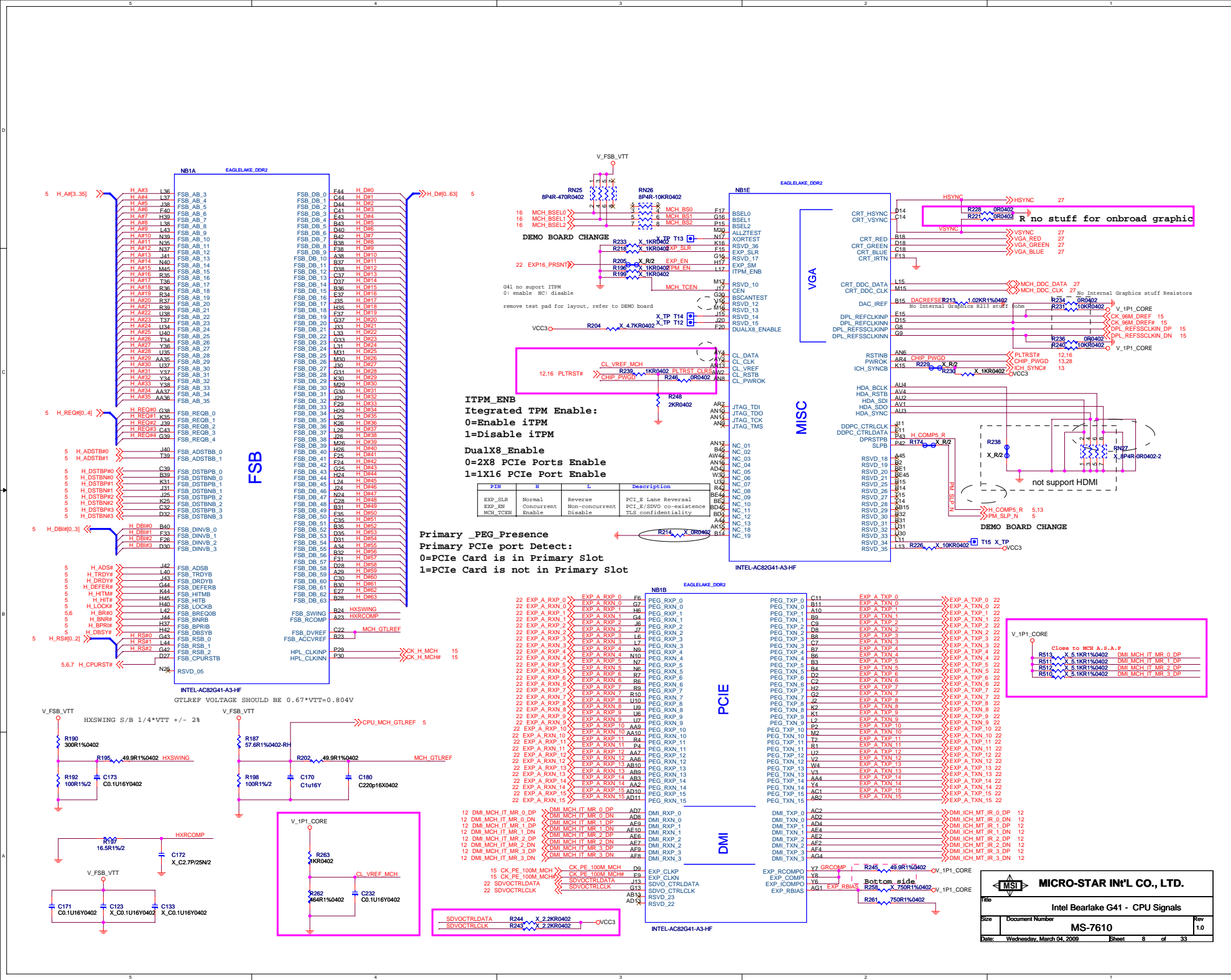


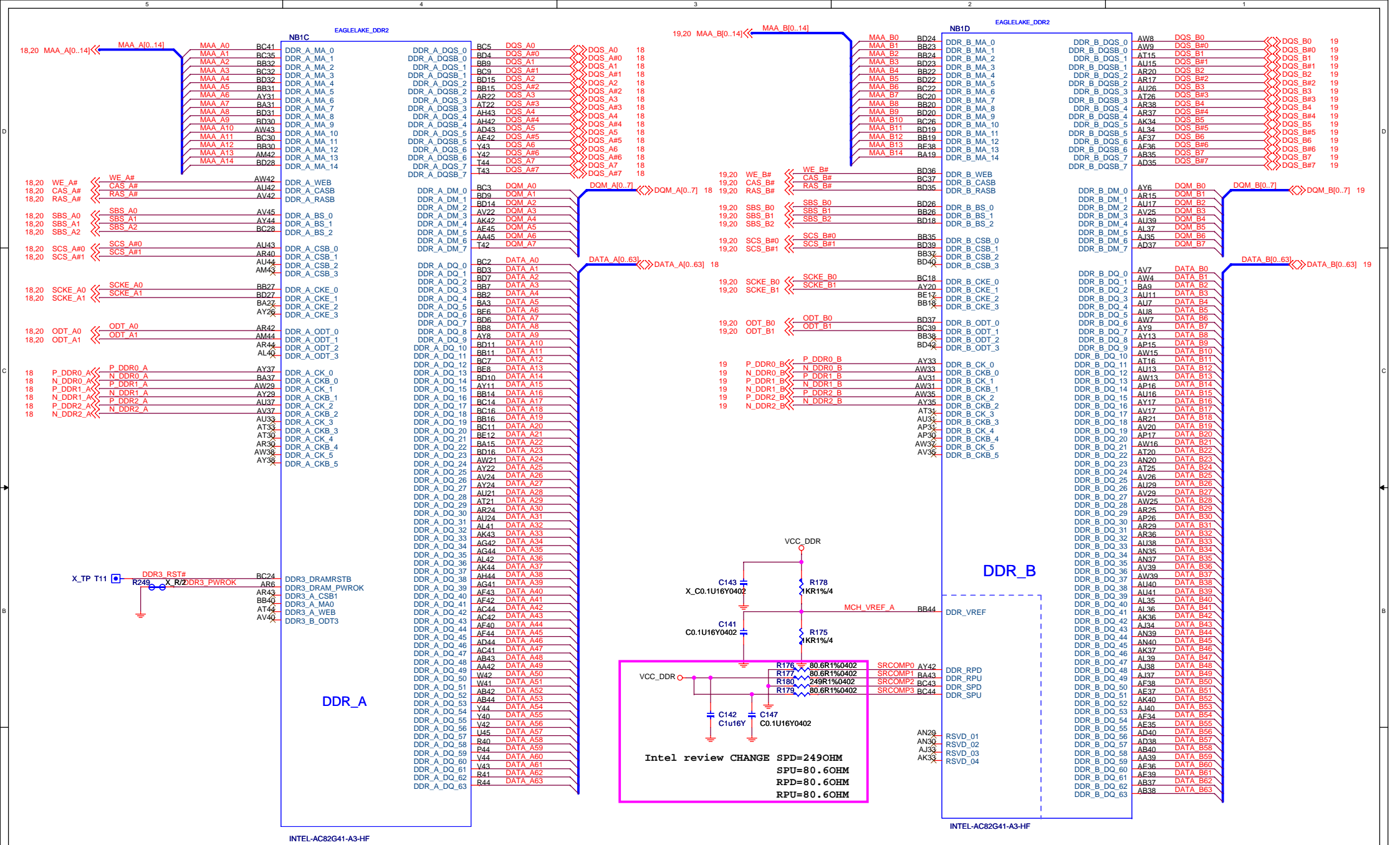


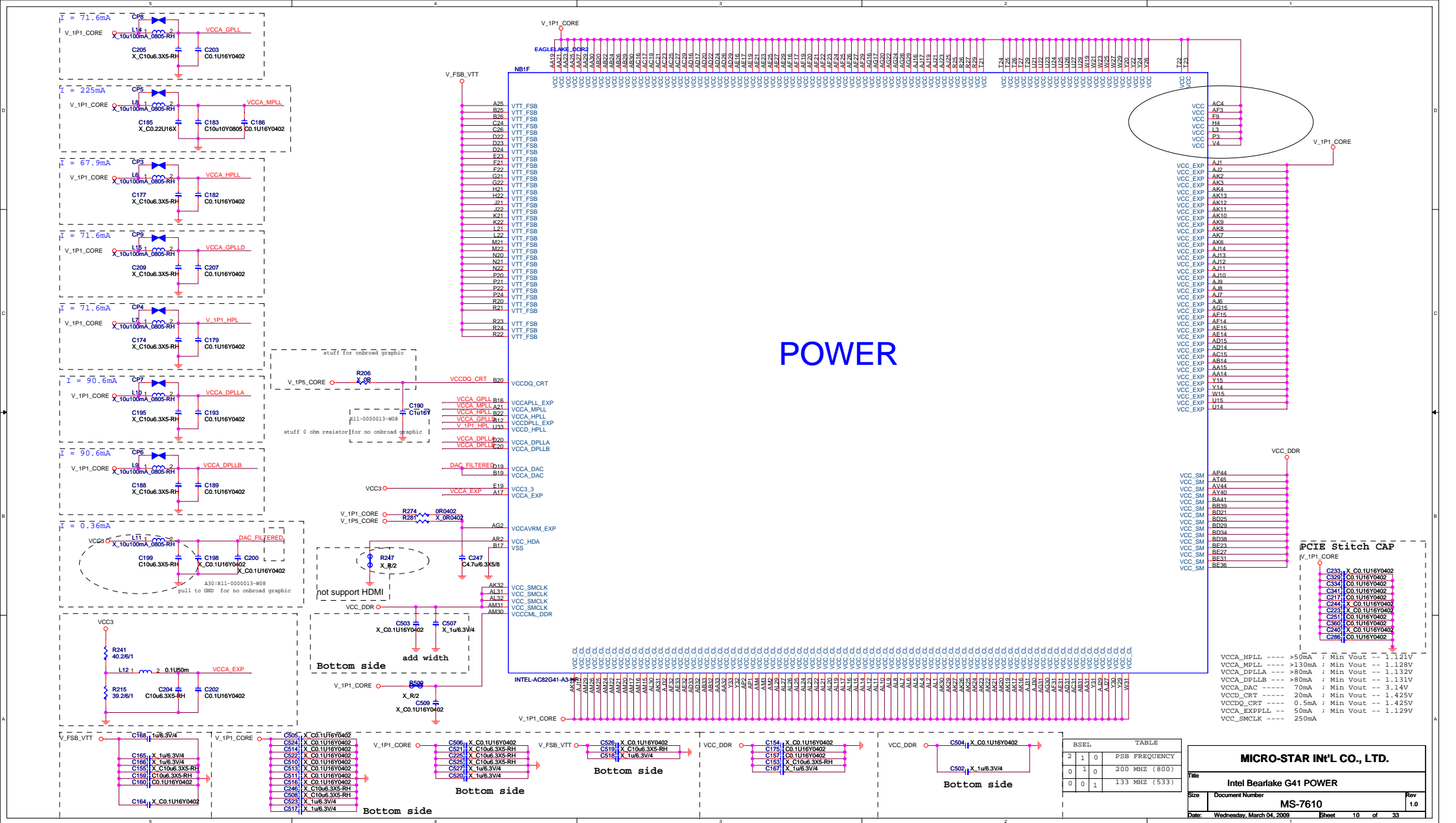


| | MSID1 | MSID0 |
|--------------|-------|-------|
| 05 Per FMB | 0 | 0 |
| 05 Value FMB | 0 | 1 |









POWER

| TABLE | | |
|---------------|---|---|
| BSEL | | |
| 2 | 1 | 0 |
| FSB FREQUENCY | | |
| 0 | 1 | 0 |
| 200 MHz (800) | | |
| 0 | 0 | 1 |
| 133 MHz (533) | | |

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Intel Bearlake G41 POWER

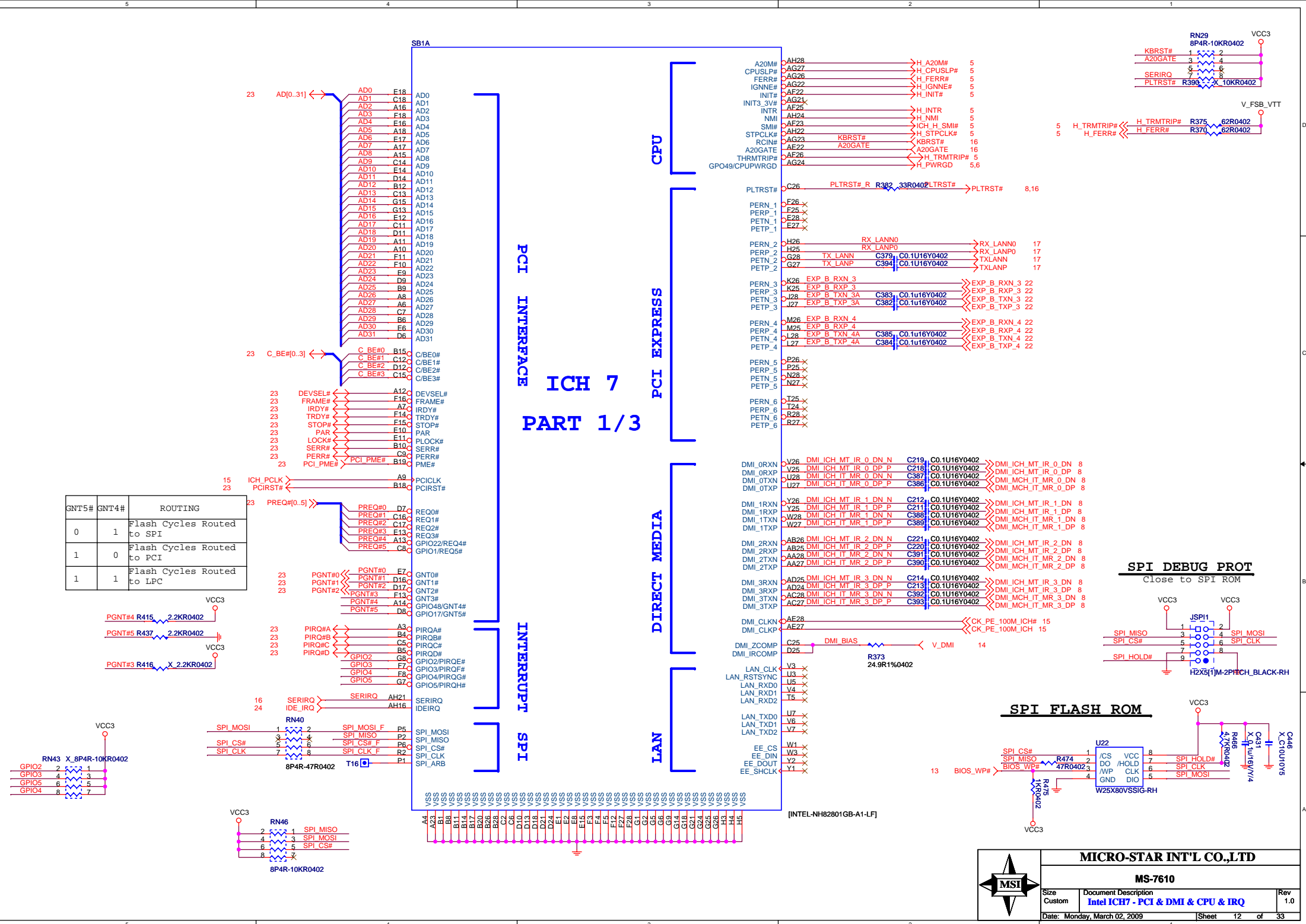
MS-7610

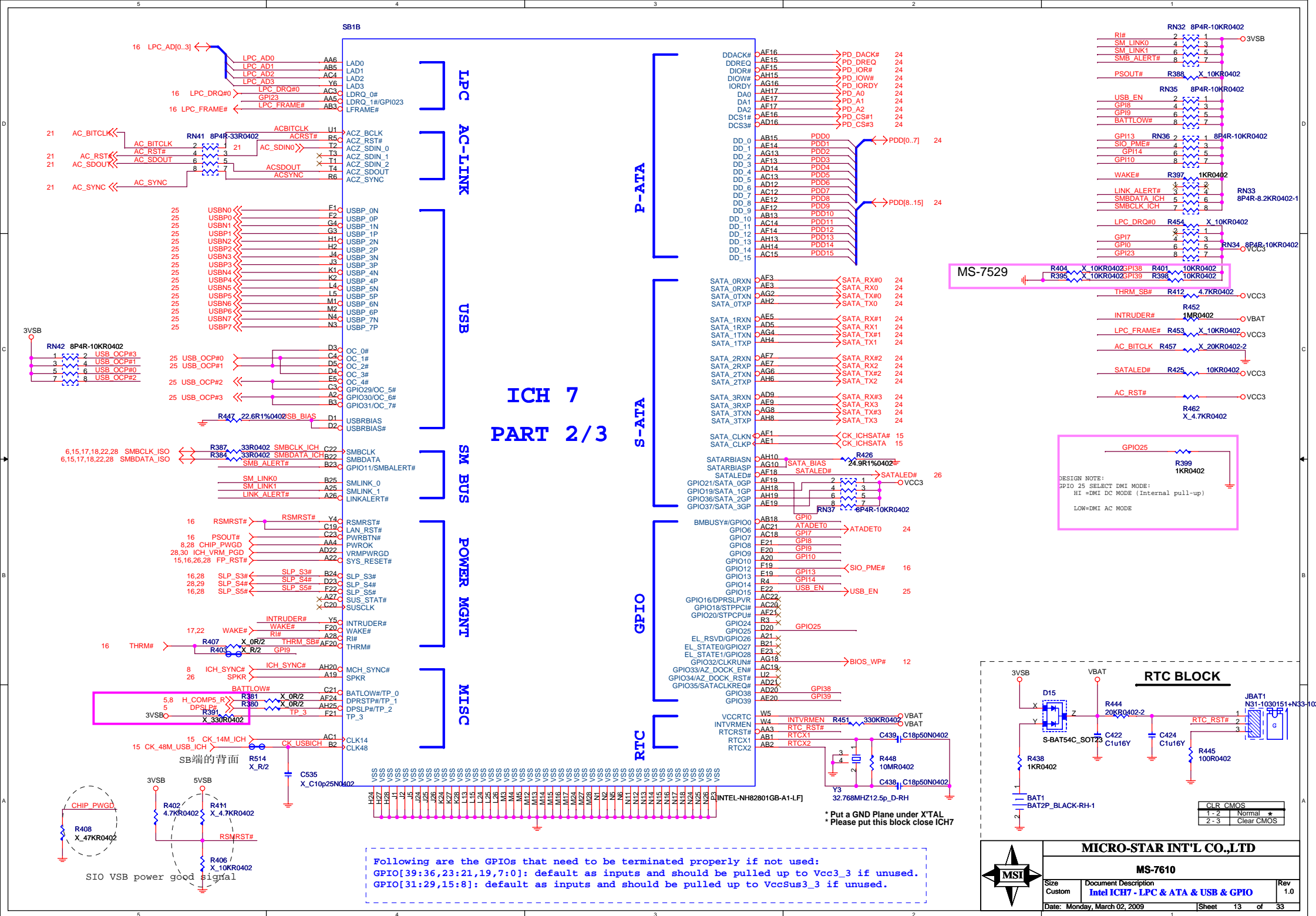
Rev 1.0

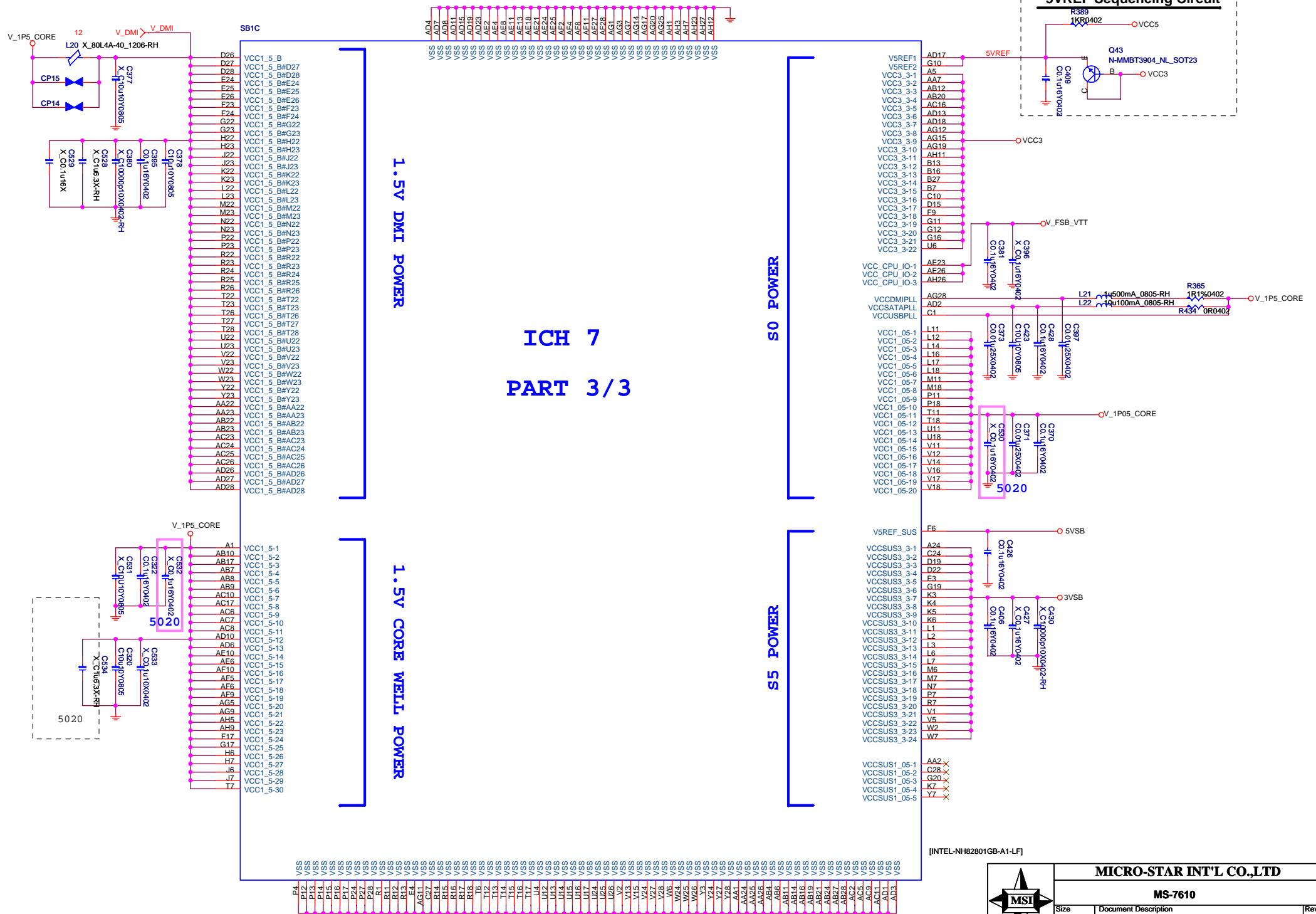
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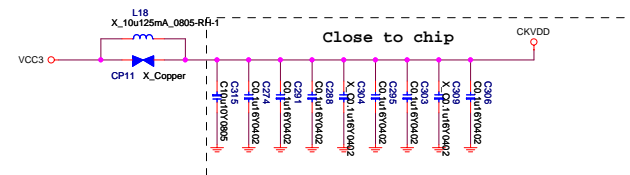
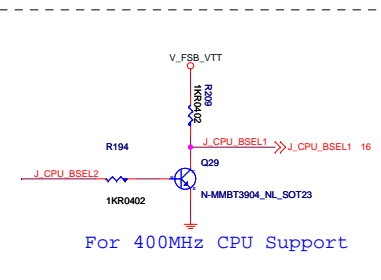
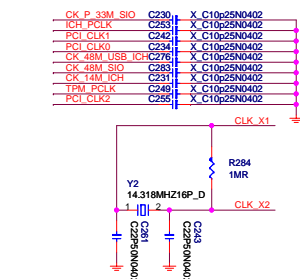
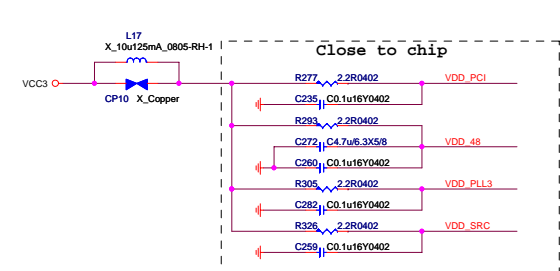
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GND



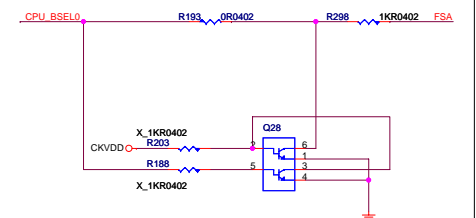
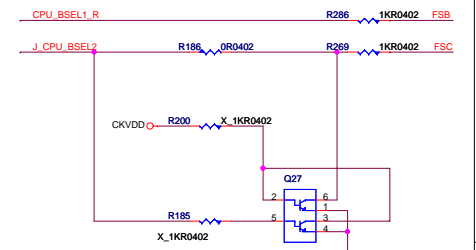
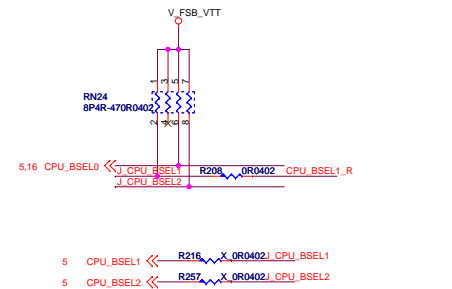
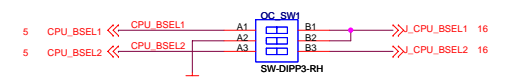
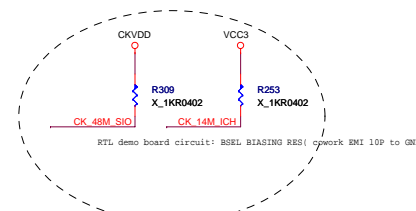




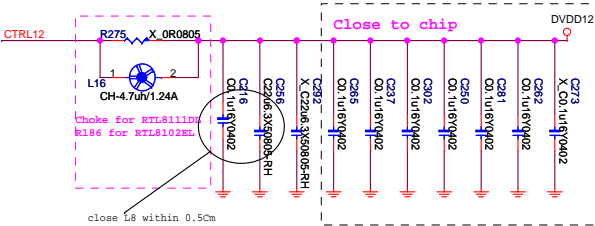
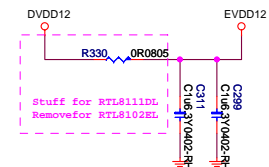
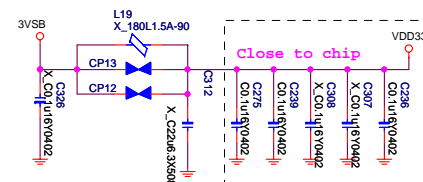
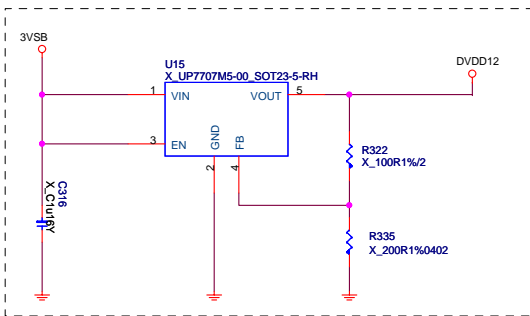






| | 0 | 1 |
|---------|-------------------------------|--------------------|
| TIME_OC | Normal Run | No Overclocking |
| SRC5_EN | Pin29/30 is PCI STOP/CPU STOP | Pin29/30 is SRC 5 |
| 27M_SEL | Pin17/18 is SRC 1 | Pin17/18 is 27MHz |
| ITP_EN | Pin38/39 is SRC 8 | Pin38/39 is CPUITP |

| Default | 200-->266 | 200-->333 | 200-->400 |
|---------|-----------|-----------|-----------|
| 1:ON | 1:ON | 1:ON | 1:OFF/ON |
| 2:OFF | 2:ON | 2:ON | 2:OFF |
| 3:ON | 3:ON | 3:OFF | 3:OFF |
| | | 266-->333 | 266-->400 |
| | | 1:ON | 1:OFF |
| | | 2:OFF | 2:OFF |
| | | 3:OFF | 3:OFF |
| | | | 333-->400 |
| | | | 1:OFF |
| | | | 2:OFF |
| | | | 3:OFF |

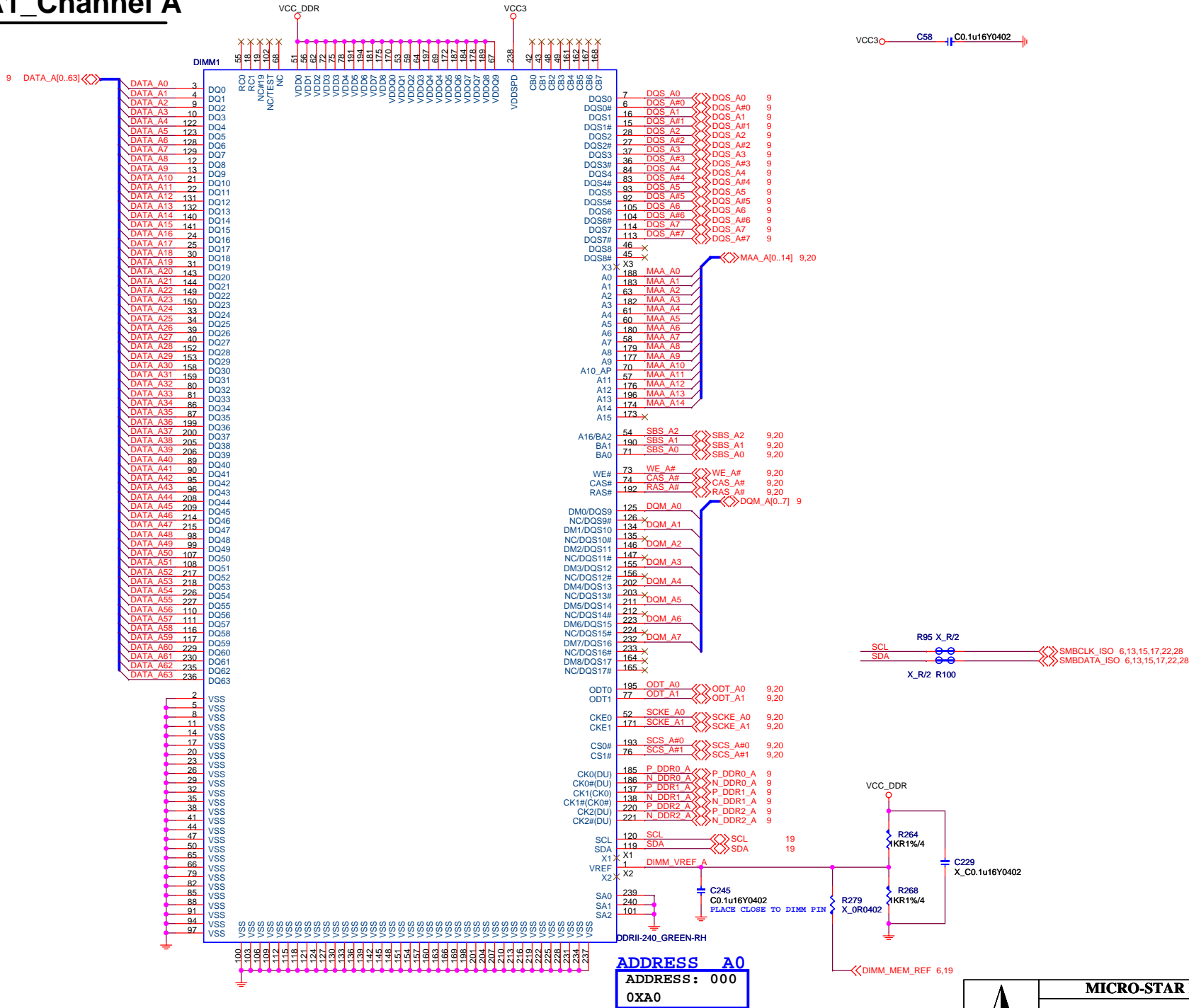


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| Size Custom | Document Description CLK-RTM 875T-605 | Rev 1.0 |
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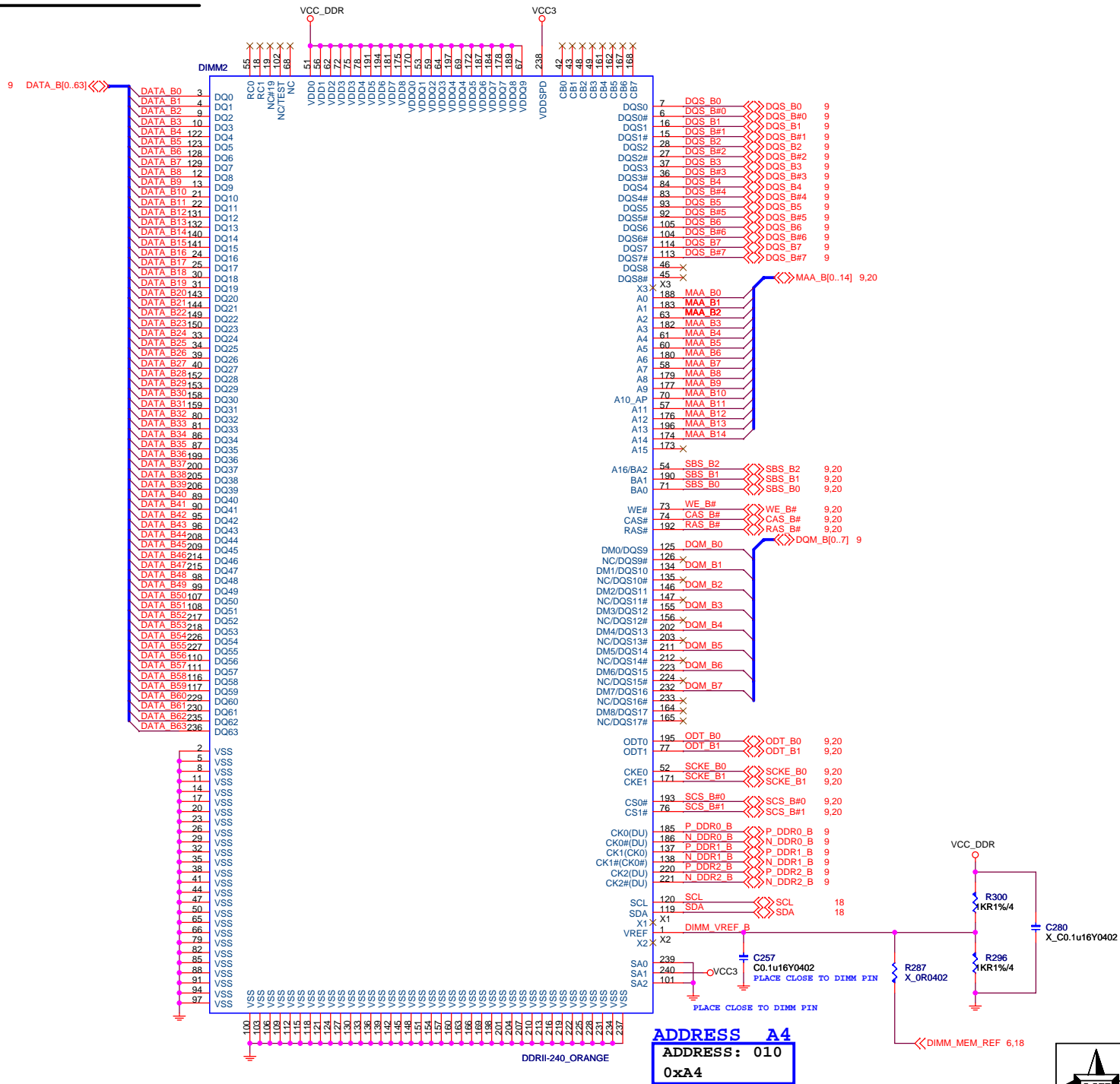
| Giga-Lan | | 10/100-Lan | |
|------------------------|--|------------------------|--|
| N58-22F0731-F02 | | N58-22F0771-F02 | |
| Link | Yellow | Link | Yellow |
| Active | Blinking | Active | Blinking |
| 1000 | Orange | 100 | Green |
| 100 | | 10 | None |
| 10 | None | | |
| 19 |  | 19 |  |
| 20 | Yellow | 20 | Yellow |
| 21 | Orange | 21 | |
| 22 |  | 22 |  |
| | Green | | Green |

DDRII DIMM_A1_Channel A



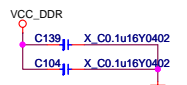
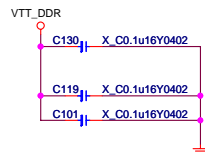
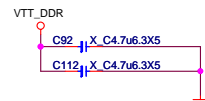
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|------------------------------|----------------------|-----|
| MS-7610 | | |
| Size | Document Description | Rev |
| Custom | DDR II DIMM A | 1.0 |
| Date: Monday, March 02, 2009 | | |
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DDR II DIMM_B1_Channel B

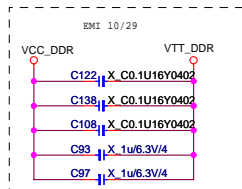
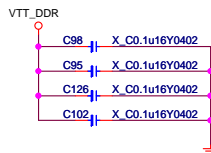
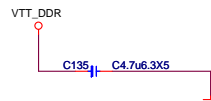


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|------------------------------|----------------------|-----|
| MS-7610 | | |
| Size | Document Description | Rev |
| Custom | DDR II DIMM B | 1.0 |
| Date: Friday, March 06, 2009 | | |
| Sheet 19 of 33 | | |

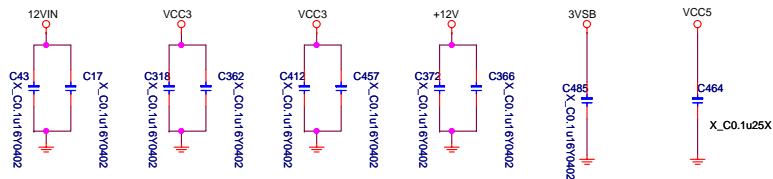
CHANNEL A V_SM_VTT DECOUPLING CAPS



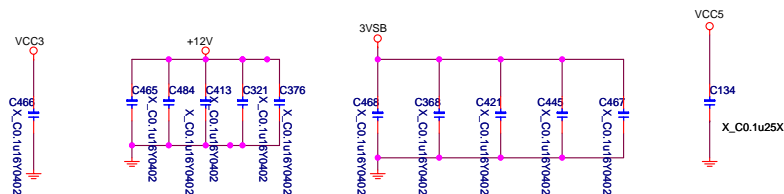
CHANNEL B V_SM_VTT DECOUPLING CAPS



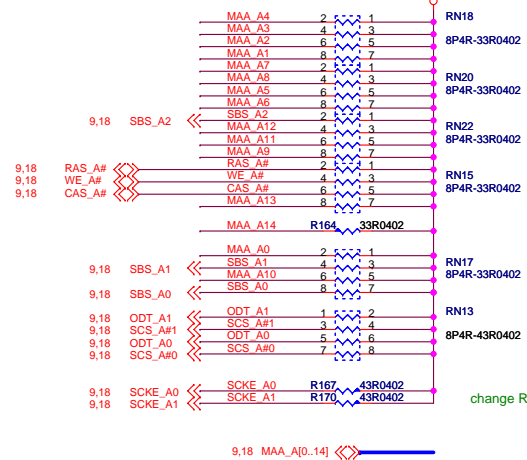
EMI solution(2009/01/14)



EMI solution(2009/01/20)

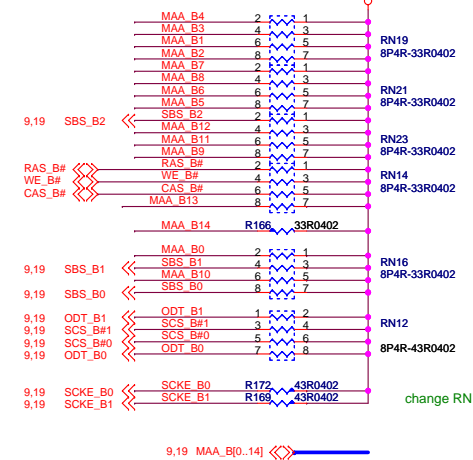


VTT_DDR



change RN

VTT_DDR



change RN

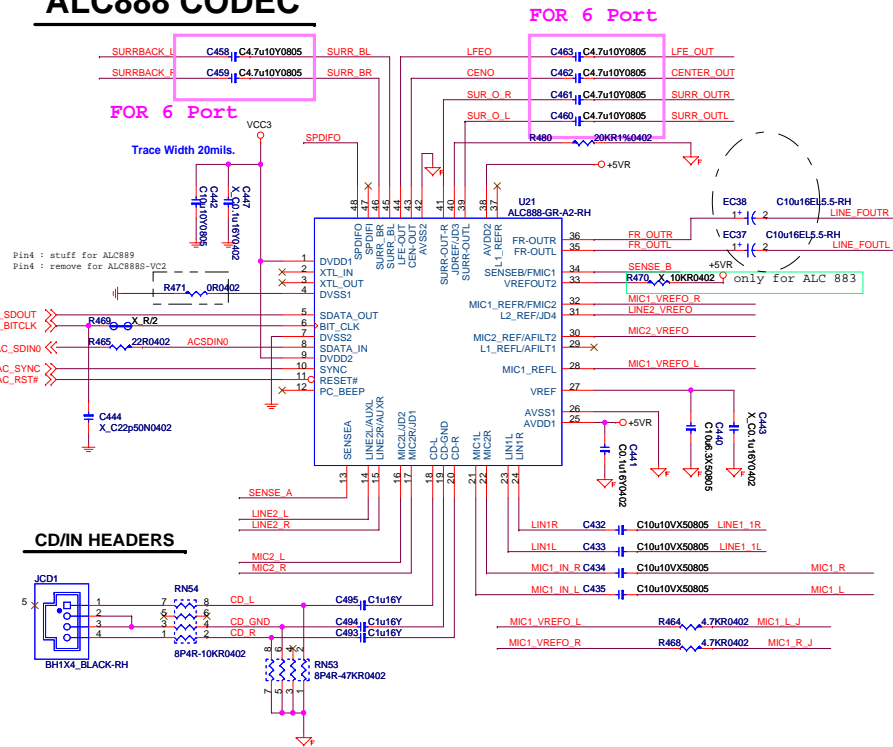


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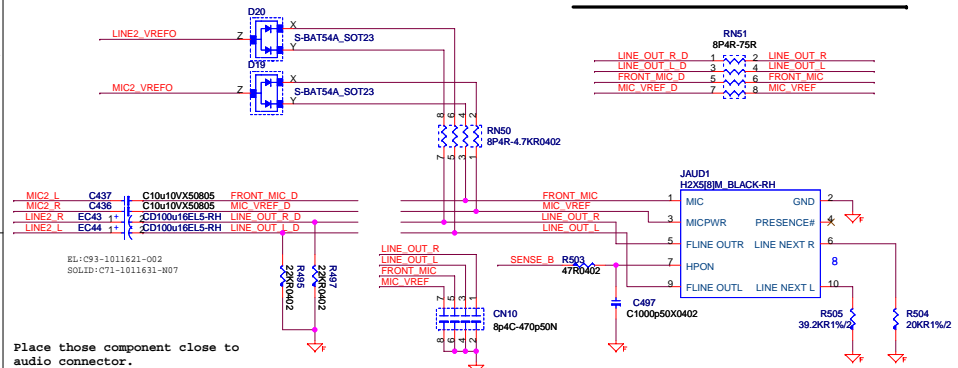
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|--------|------------------------|----------------|
| Custom | DDR II VTT DECOUPLING | 1.0 |
| Date: | Monday, March 02, 2009 | Sheet 20 of 33 |

ALC888 CODEC

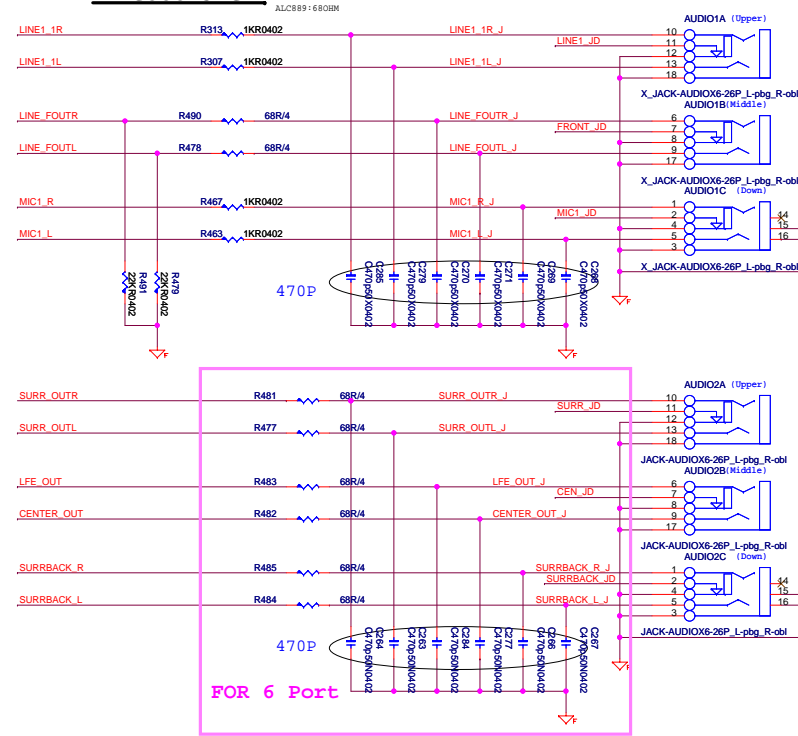


Azalia Front Audio Connector



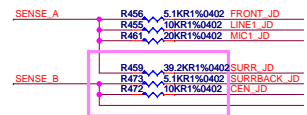
Place those component close to audio connector.

3 hole : line1/Mic1 change to 75 ohm
ALC888 JACK

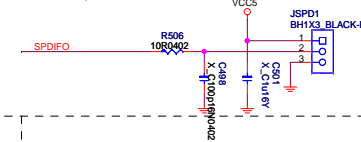


| PIN | ALC888 | ALC888S VC2 |
|-----|-----------------|-----------------|
| 2 | GPIO0/DMIC_CLK | SFDIF02 |
| 3 | GPIO1/DMIC_DATA | GPIO0/DMIC_CLK |
| 4 | DVSS | GPIO1/DMIC_DATA |
| 33 | NC | SENSE C |
| | | |

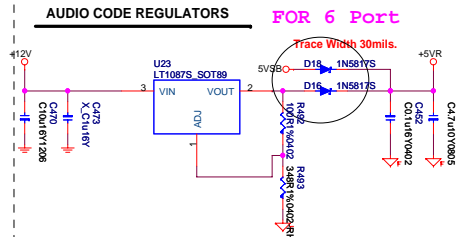
ALC883 JACK DETECT



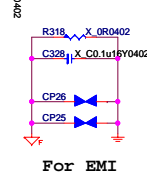
SPDIF OUT



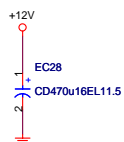
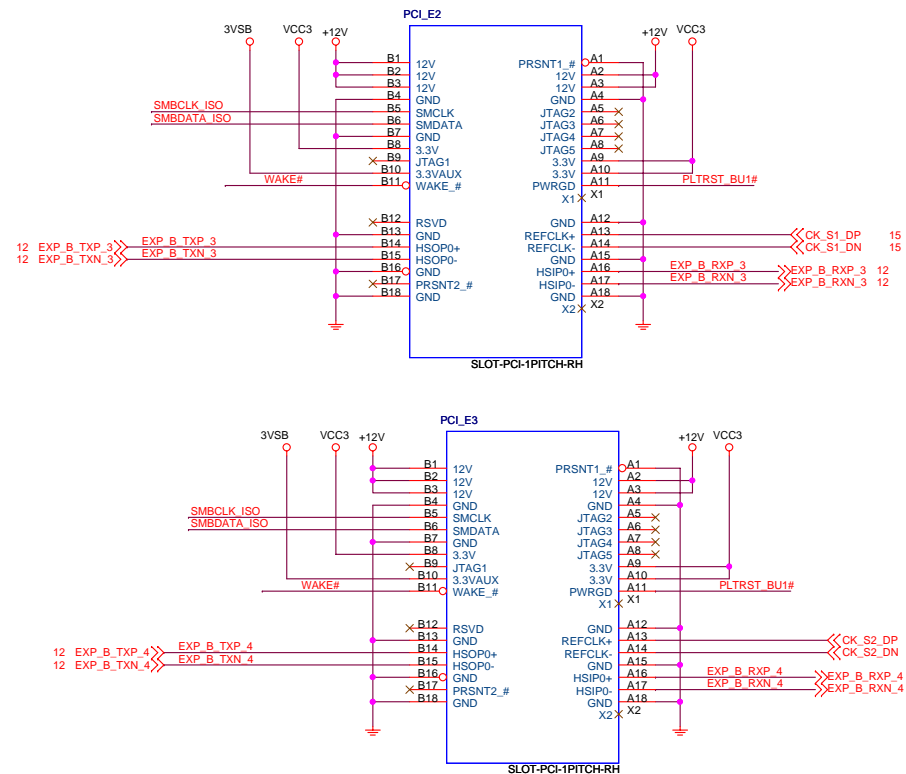
AUDIO CODE REGULATORS



FOR 6 Port



PCIE X1 PORT



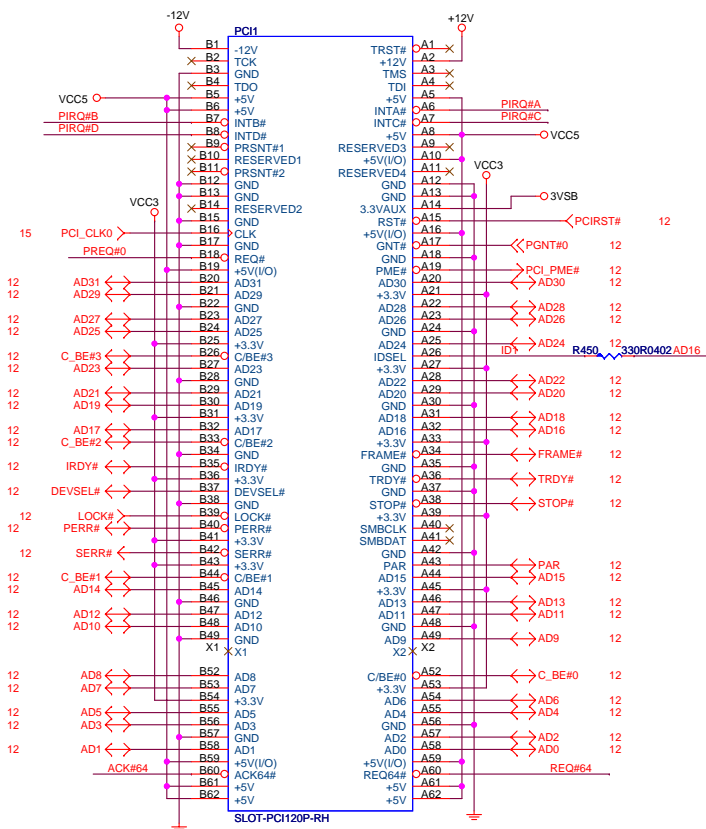
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| | |
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| Document Description | PCI EXPRESS |
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|-----|-----|
| Rev | 1.0 |
|-----|-----|

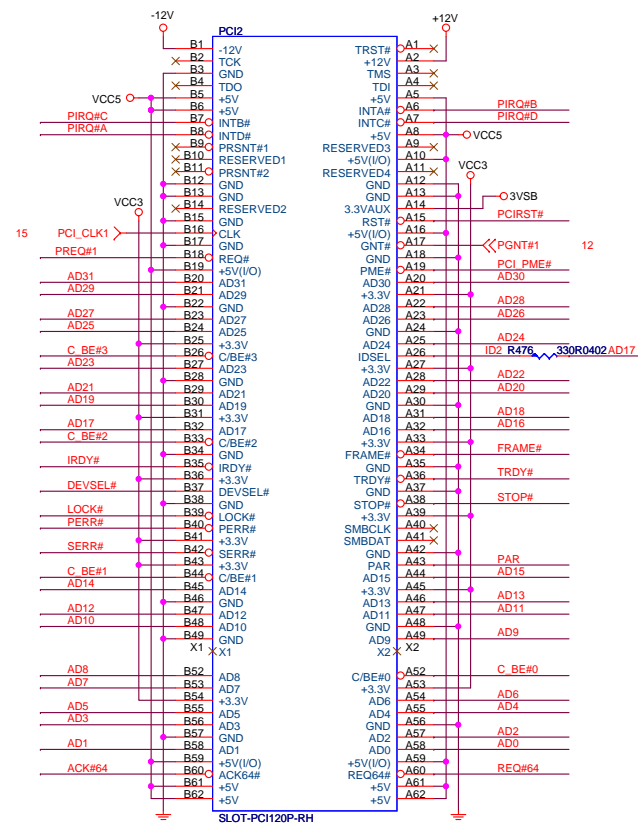
| | |
|------------------------------|----------------|
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|------------------------------|----------------|

PCI SLOT 1 (PCI VER: 2.2 COMPLY)



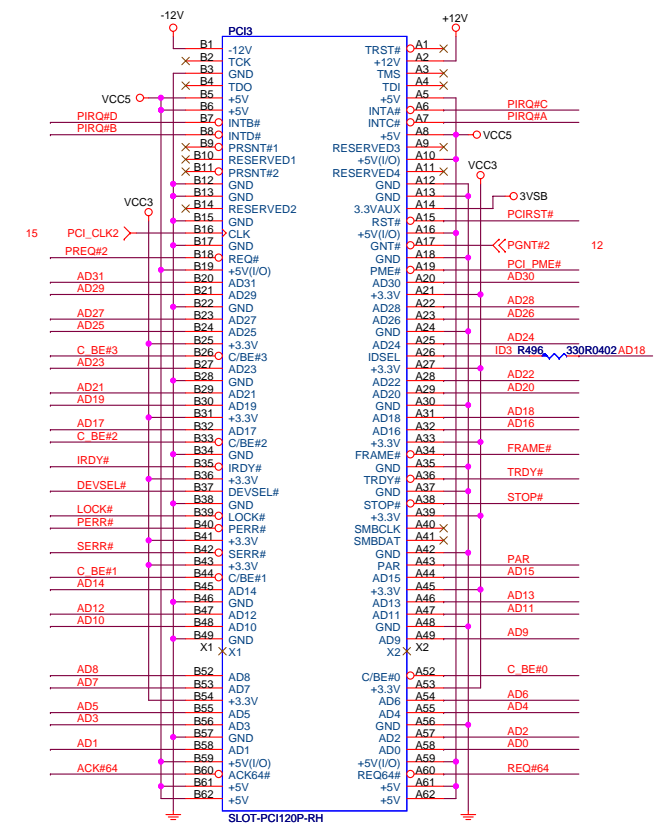
IDSEL = AD16
MASTER = PREQ#0
PIRQ#A

PCI SLOT 2 (PCI VER: 2.2 COMPLY)



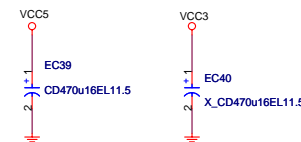
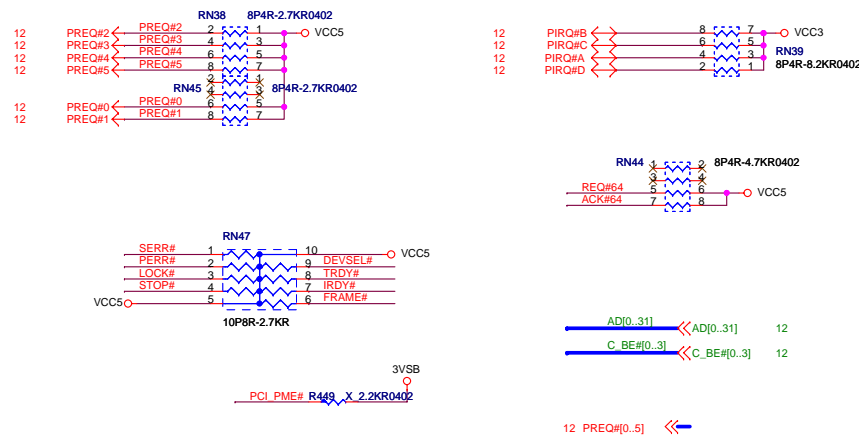
IDSEL = AD17
MASTER = PREQ#1
PIRQ#B

PCI SLOT3 (PCI VER: 2.2 COMPLY)



IDSEL = AD18
MASTER = PREQ#2
PIRQ#C

PCI PULL-UP / DOWN RESISTORS

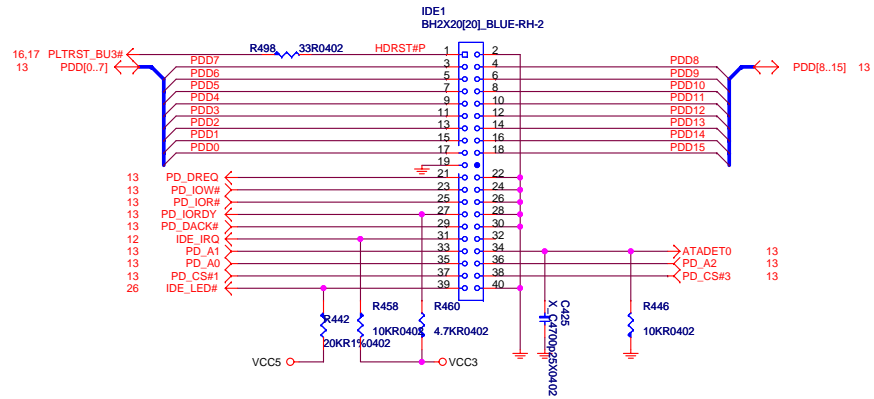


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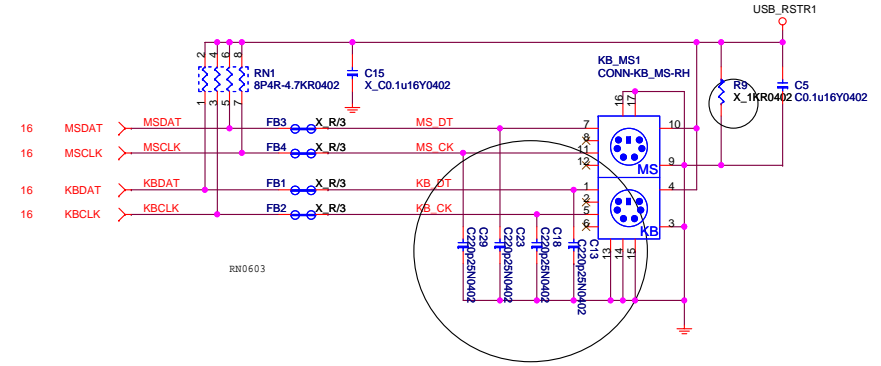
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|------------------------------|----------------------|-----|
| Custom | PCI Slot 1 & 2 | 1.0 |
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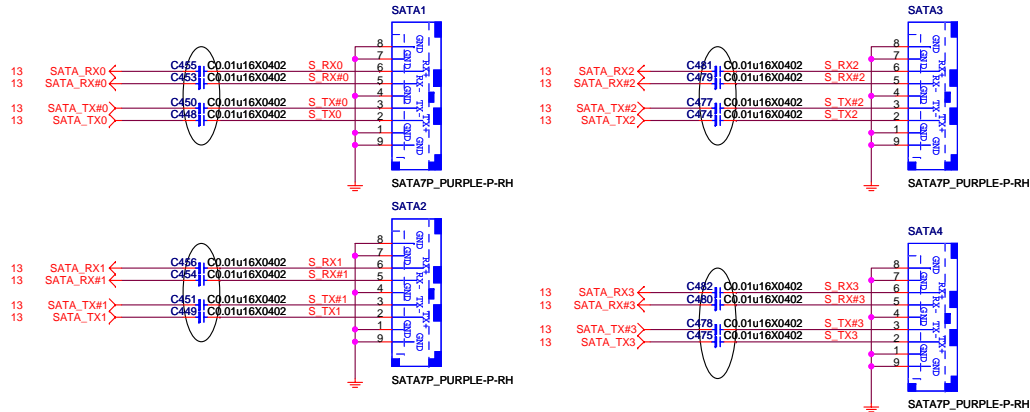
ATA 33/66/100 IDE Connectors



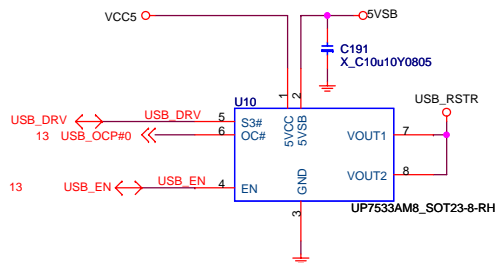
PS2 KEYBOARD & MOUSE CONNECTOR



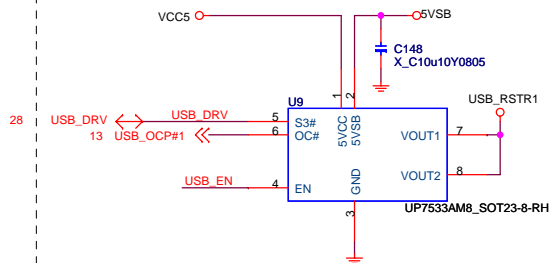
SERIAL ATA CONNECTOR BLOCK



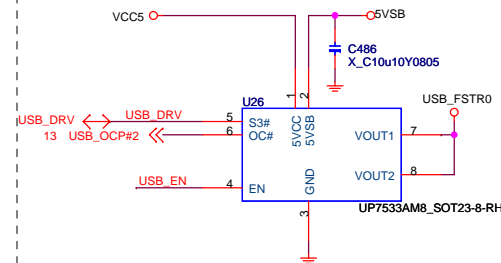
POWER CIRCUIT FOR USB PORT 0,1



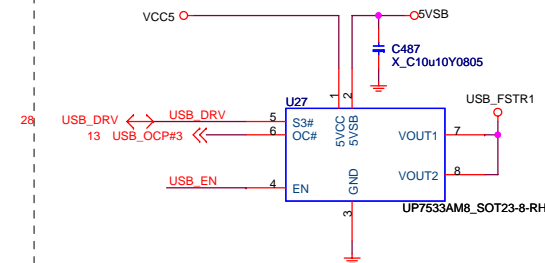
POWER CIRCUIT FOR USB PORT 2,3



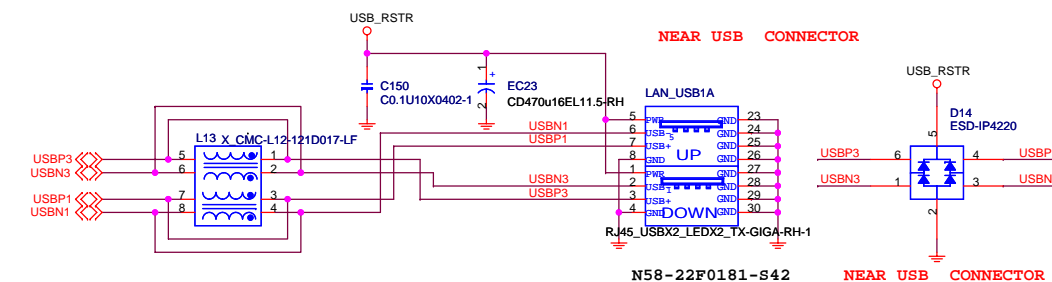
POWER CIRCUIT FOR USB PORT 4,5



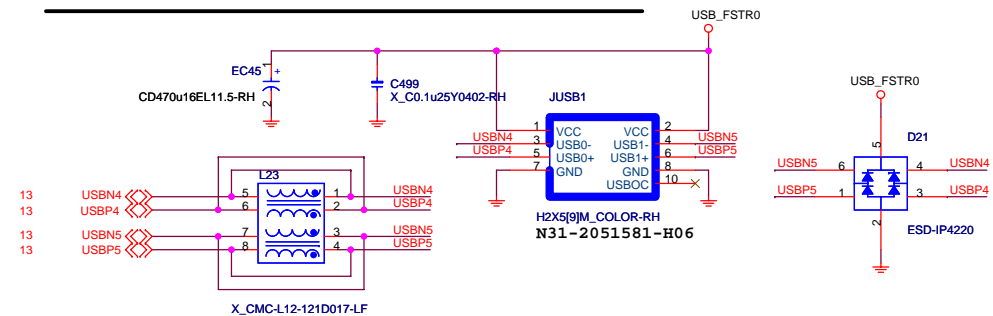
POWER CIRCUIT FOR USB PORT 6,7



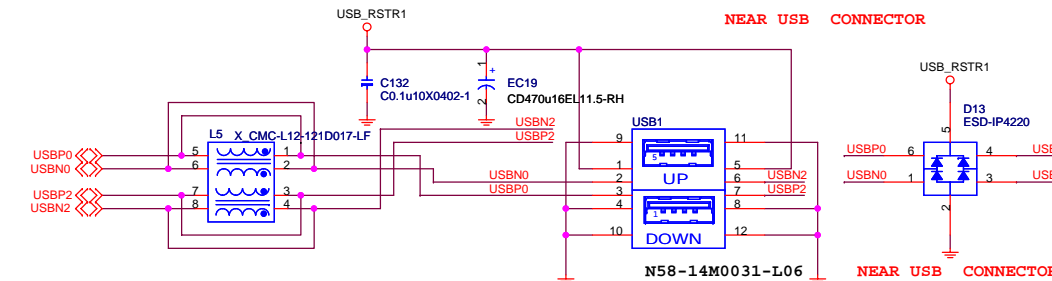
REAR PANEL USB CONNECTOR FOR USB PORT 0,1



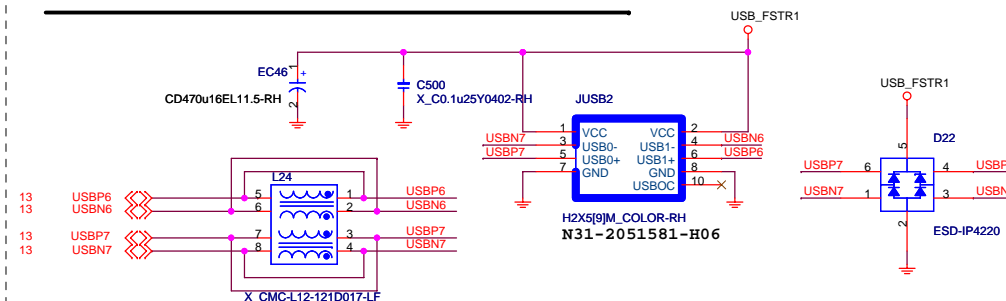
FRONT PANEL USB CONNECTOR FOR USB PORT 4,5



REAR PANEL USB CONNECTOR FOR USB PORT 2,3



FRONT PANEL USB CONNECTOR FOR USB PORT 6,7

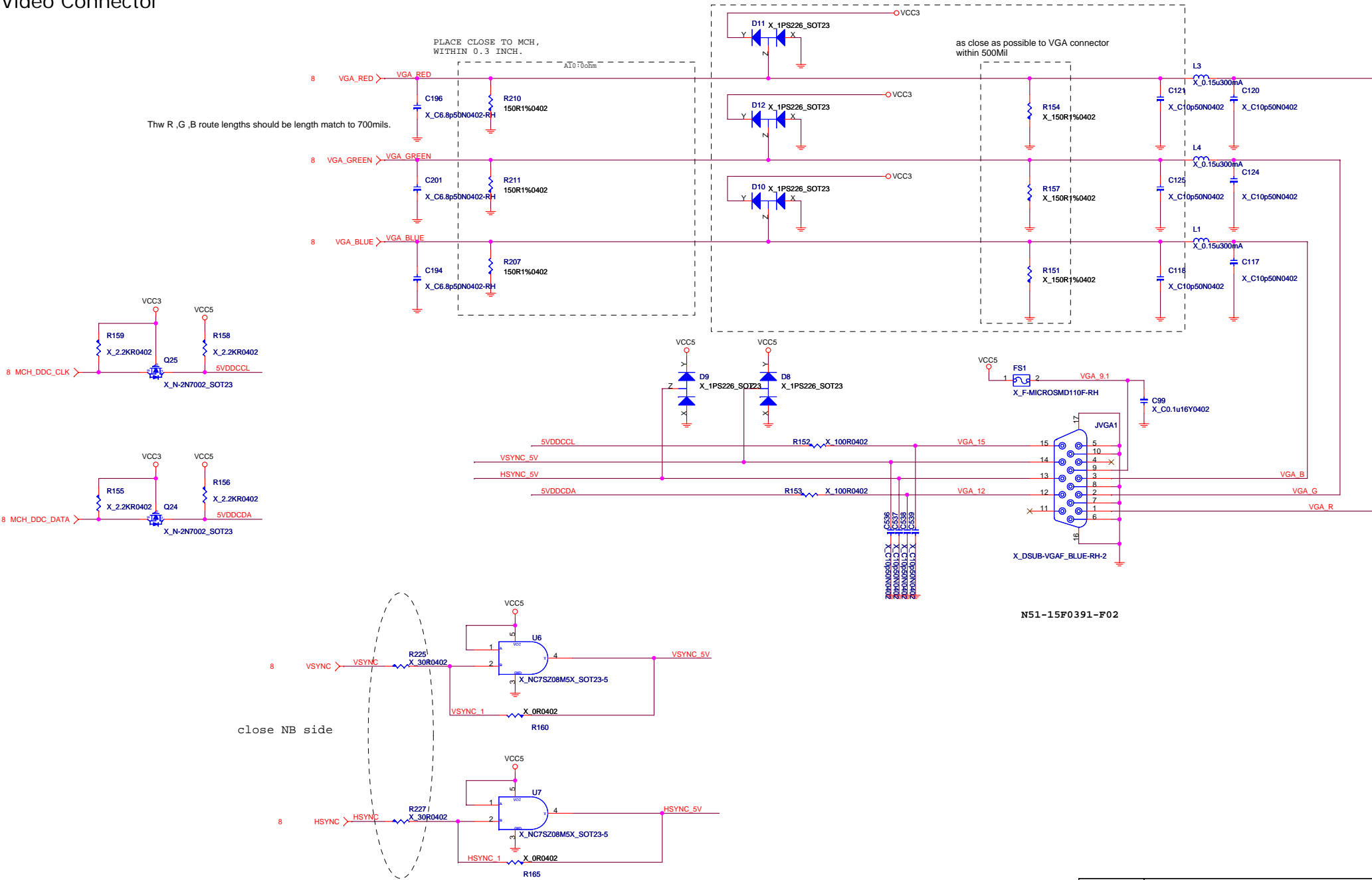


MICRO-STAR INT'L CO.,LTD

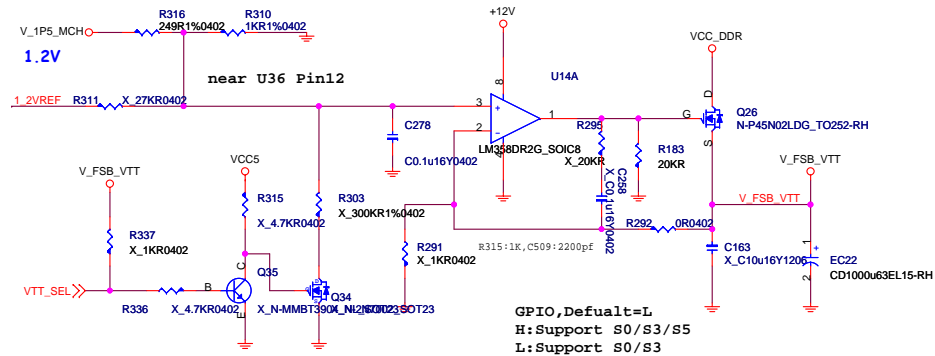
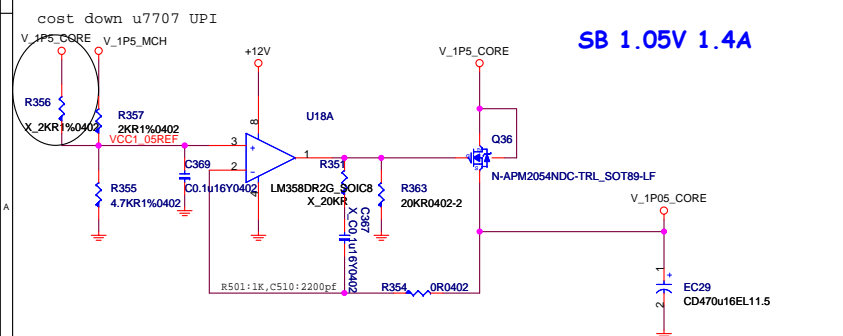
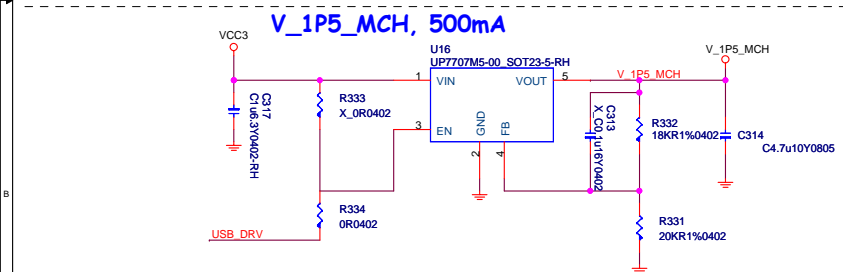
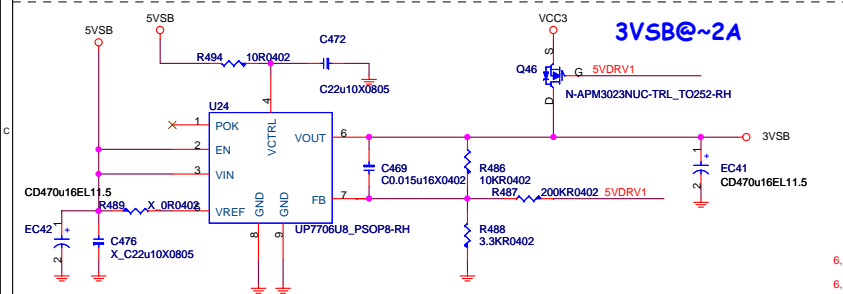
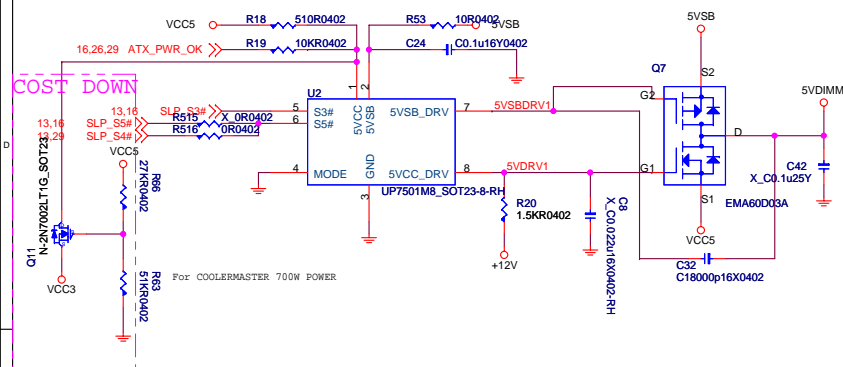
MS-7610

| Size | Document Description | Rev |
|------------------------------|----------------------|-----|
| Custom | USB CONNECTORS | 1.0 |
| Date: Monday, March 02, 2009 | Sheet 25 of 33 | |

Video Connector

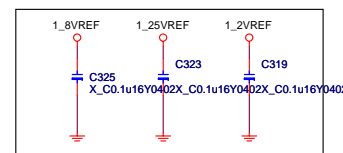
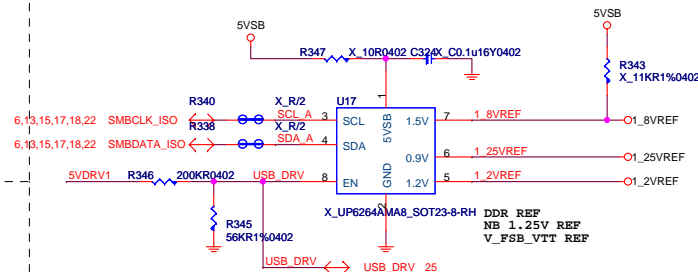


5VDIMM FOR DDR

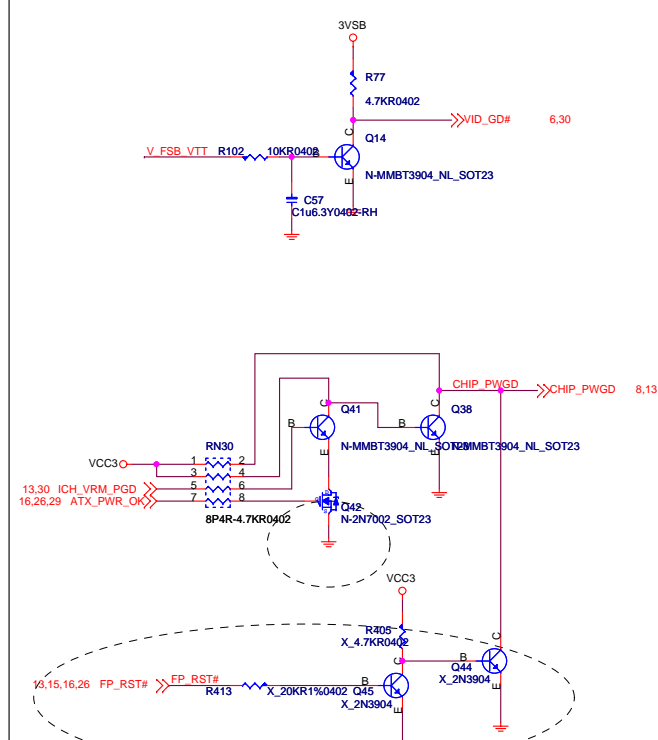


| | | |
|--------------------|----------------|--|
| VTT_SEL = L | V_FSB_VTT=1.1V | For future KENTSFIELD processor (FSB1333, Quad-Core) |
| VTT_SEL = H | V_FSB_VTT=1.2V | For normal processors. |

Reference Voltage



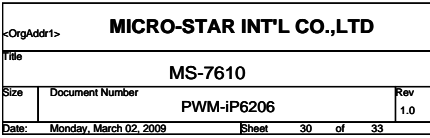
PLACE NEAR PIN OUT



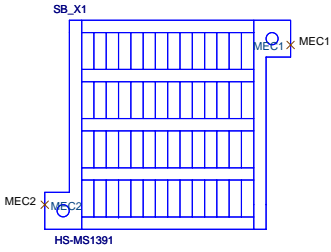
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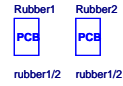
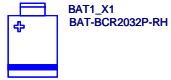
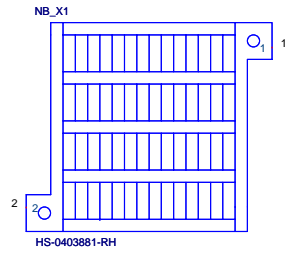
| | | |
|------------------------------|--|----------------|
| Size Custom | Document Description ACPI controller UPI | Rev 1.0 |
| Date: Monday, March 02, 2009 | | Sheet 28 of 33 |



ICH7 HEATSINK

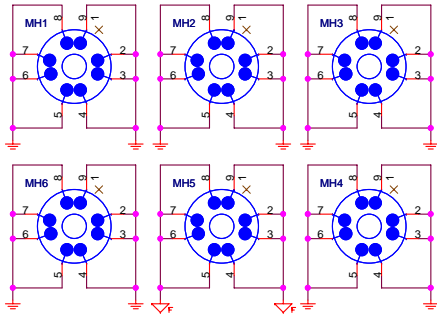


MCH HEATSINK

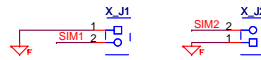


P80-076100A-G37

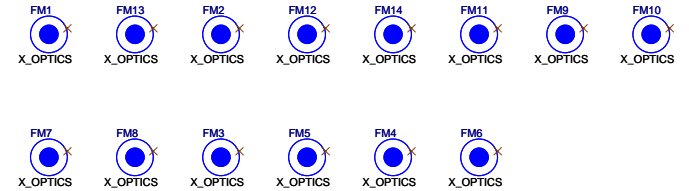
Mounting Holes



Simulation



Optics Orientation Holes



| ICH7 | | | | | | | | | |
|---|---------------|------|--------|----------|----|-----|----------|---------|---------------------------------------|
| GPIO | Alt Func | PIN | I/O/NC | POWER | PU | SMI | TOL | DEFAULT | SIGNAL NAME |
| GPIO0 | Unmultiplexed | AB18 | I/O | CORE | N | Y | 3.3V | GPI | GPIO0(pull high) |
| GPIO1 | REQ5# | C8 | I/O | CORE | N | Y | 5V | GPI | PREQ#5 |
| GPIO2 | PIRQE# | G8 | I/OD | CORE | N | Y | 5V | GPI | GPIO2(pull high) |
| GPIO3 | PIRQF# | F7 | I/OD | CORE | N | Y | 5V | GPI | GPIO3(pull high) |
| GPIO4 | PIRQG# | F8 | I/OD | CORE | N | Y | 5V | GPI | GPIO4(pull high) |
| GPIO5 | PIRQH# | G7 | I/OD | CORE | N | Y | 5V | GPI | GPIO5(pull high) |
| GPIO6 | Unmultiplexed | AC21 | I/O | CORE | N | Y | 3.3V | GPI | ATADET0 |
| GPIO7 | Unmultiplexed | AC18 | I/O | CORE | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO8 | Unmultiplexed | E21 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO9 | Unmultiplexed | E20 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO10 | Unmultiplexed | A20 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO11 | SMBALERT# | B23 | I/O | Resume | N | Y | 3.3V | Native | STRAPPED HI |
| GPIO12 | Unmultiplexed | F19 | I/O | Resume | N | Y | 3.3V | GPI | SIO_PME# |
| GPIO13 | Unmultiplexed | E19 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO14 | Unmultiplexed | R4 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO15 | Unmultiplexed | E22 | I/O | Resume | N | Y | 3.3V | GPI | STRAPPED HI |
| GPIO16 | Unmultiplexed | AC22 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO17 | GNT5# | D8 | I/O | CORE | N | N | 3.3V | GPO | STRAPPED L |
| GPIO18 | Unmultiplexed | AC20 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO19 | SATA_1GP | AH18 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO20 | Unmultiplexed | AF21 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO21 | SATA_0GP | AF19 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO22 | REQ4# | A13 | I/O | CORE | N | N | 3.3V | Native | STRAPPED HI |
| GPIO23 | LDRQ_1# | AA5 | I/O | CORE | N | N | 3.3V | Native | STRAPPED HI |
| GPIO24 | Unmultiplexed | R3 | I/O | Resume | N | N | 3.3V | GPO | NC |
| GPIO25 | Unmultiplexed | D20 | I/O | Resume | Y | N | 3.3V | GPO | GPIO25(high 7507,low 7398) |
| GPIO26 | Unmultiplexed | A21 | I/O | Resume | N | N | 3.3V | GPO | USB_EN |
| GPIO27 | Unmultiplexed | B21 | I/O | Resume | N | N | 3.3V | GPO | NC |
| GPIO28 | Unmultiplexed | E23 | I/O | Resume | N | N | 3.3V | GPO | NC |
| GPIO29 | OC5# | C3 | I/O | Resume | N | N | 3.3V | GPI | USB_OCP#2 |
| GPIO30 | OC6# | A2 | I/O | Resume | N | N | 3.3V | GPI | USB_OCP#3 |
| GPIO31 | OC7# | B3 | I/O | Resume | N | N | 3.3V | GPI | USB_OCP#3 |
| GPIO32 | Unmultiplexed | AG18 | I/O | CORE | N | N | 3.3V | GPO | BIOS_WP#(fill with 1) |
| GPIO33 | Unmultiplexed | AC19 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO34 | Unmultiplexed | U2 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO35 | SATACLKREQ# | AD21 | I/O | CORE | N | N | 3.3V | GPO | NC |
| GPIO36 | SATA2GP | AH19 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO37 | SATA3GP | AE19 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO38 | Unmultiplexed | AD20 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO39 | Unmultiplexed | AE20 | I/O | CORE | N | N | 3.3V | GPI | STRAPPED HI |
| GPIO48 | GNT4# | A14 | I/O | CORE | N | N | 3.3V | Native | STRAPPED HI |
| GPIO49 | CPUPWRGD | AG24 | I/O | V_CPU_IO | N | N | V_CPU_IO | Native | H_PWRGD |
| Following are the GPIOs that need to be terminated properly if not used: GPIO[39:36,23:21,19,7:0]: default as inputs and should be pulled up to Vcc3_3 if unused. GPIO[31:29,15:8]: default as inputs and should be pulled up to VccSus3_3 if unused. | | | | | | | | | |

| SIO Fintek71882FG(CONTINUE) | | | | | |
|-----------------------------|--------------------------|-----|--------------|--------------|-------|
| GPIO | Alt Func | PIN | Usage | Input/Output | NOTES |
| GPIO0 | VIDOUT0 | 49 | MCH_BSEL0 | O12 | |
| GPIO1 | VIDOUT1 | 50 | MCH_BSEL1 | O12 | |
| GPIO2 | VIDOUT2 | 51 | MCH_BSEL2 | O12 | |
| GPIO3 | VIDOUT3 | 52 | NC | O12 | |
| GPIO4 | VIDOUT4 | 53 | NC | O12 | |
| GPIO5 | VIDOUT5/SIC | 54 | NC | I/OD12t | |
| GPIO6 | SLOT0CC# | 55 | GPO | I/OD12t | |
| GPIO7 | Turbo1#/WDTRST# | 56 | WDTRST# | OD12-5v | |
| GPIO15 | LED_VSB/ALERT# | 64 | LED_VSB | OD12 | |
| GPIO16 | LED_VCC/Turbo2# | 65 | LED_VCC | OD12 | |
| GPIO20 | PCIRST1# | 74 | PCIRST1# | OD12 | |
| GPIO21 | PCIRST2# | 75 | PCIRST2# | O12 | |
| GPIO22 | PCIRST3# | 76 | PCIRST3# | O12 | |
| GPIO23 | RSTCON# | 77 | RSTCON# | OD12 | |
| GPIO24 | ATXPG_IN | 78 | ATXPG_IN | AIN | |
| GPIO32 | PWROK | 84 | PWROK | OD12 | |
| GPIO26 | PWSIN# | 80 | PWSIN# | INts5v | |
| GPIO27 | PWSOUT# | 80 | PWSOUT# | OD12 | |
| GPIO30 | S3# | 82 | | INts5v | |
| GPIO31 | PSON# | 83 | PSON# | OD12-5v | |
| GPIO33 | RSMRST# | 85 | RSMRST# | OD12 | |
| GPIO40 | FANIN3 | 25 | FANIN3 | INts5v | |
| GPIO41 | FAN_CTL3 | 26 | FAN_CTL3(NC) | OD12-5v | |
| GPIO25 | PME# | 79 | PME# | OD12-5v | |
| GPIO10 | SPI_SLK/FANIN4 | 59 | GPIO10(NC) | I/OD12t | |
| GPIO11 | SPI_CS0#/FANCTL4 | 60 | GPIO11(NC) | I/OD12t | |
| GPIO12 | SPI_MISO/FANCTL1_1 | 61 | GPIO12(NC) | I/OD12t | |
| GPIO13 | SPI_MOSI/BEEP | 62 | BEEP(NC) | OD24 | |
| GPIO14 | FWH_DIS/WDTRST#/SPI_CS1# | 63 | GPIO14 | I/OD12t | |
| GPIO42 | IRTX | 27 | IRTX | O12 | |
| GPIO43 | IRRX | 28 | IRRX | INts | |
| GPIO17 | | 66 | NC | I/OD12t | |

PCI Config.

| DEVICES | | MCP1 INT | PIN REQ#/GNT# | IDSEL | CLOCK |
|---------|--------|----------|------------------|-------|----------|
| PCI1 | PIRQ#A | | PREQ#0 PGNT#0 | AD16 | PCI_CLK0 |
| | PIRQ#B | | | | |
| | PIRQ#C | | | | |
| | PIRQ#D | | | | |
| PCI2 | PIRQ#B | | PREQ#1 PGNT#1 | AD17 | PCI_CLK1 |
| | PIRQ#C | | | | |
| | PIRQ#D | | | | |
| | PIRQ#A | | | | |

| JCH1 | Chassis Intrusion |
|-------|-------------------|
| Open | Normal |
| (1-2) | Chassis Open |

DDRII DIMM Config.

| DEVICE | ADDRESS | CLOCK |
|--------|---------|-------------------|
| DIMM A | A0H | P_DDR0_A/N_DDR0_A |
| | | P_DDR1_A/N_DDR1_A |
| | | P_DDR2_A/N_DDR2_A |
| | | P_DDR0_B/N_DDR0_B |
| DIMM B | A4H | P_DDR1_B/N_DDR1_B |
| | | P_DDR2_B/N_DDR2_B |
| | | |
| | | |

JUMPER SETTING

| | | |
|-------|-------------|------------|
| JBAT1 | (1-2)NORMAL | (2-3)CLEAR |
|-------|-------------|------------|

| | | |
|------------------------------|-------------------------|---------|
| File BIOS Request Form | | |
| Size C | Document Number MS-7610 | Rev 1.0 |
| Date: Monday, March 02, 2009 | Sheet 32 of 33 | |

0A Change list:

- 1. Add pcie x1 *2,add one pci slot
- 2.change 1P1_core power mode
- 3.add APS function
- 4.modify Audio ALC888S circuit for realtek suggestion
- 5.add OC_SW1
- 6.add C14 C56 C61 C505 C506 C504 C370 for power supply with mini current(use resistor)
- 7.Update TO252 footprint to DPAKSGD
- 8.Add PLTRST#_R,EXP_RBIAS,SCL_A,SDA_A,SDA_LAN net name
- 9.add Q34 Q37 R315
- 10.For power:add C536 C509 C510 C511 R504 R315 R501 R591 R500 R503 R502 EC47

1.0 Change list:

- 1. switch CPU_BSEL0 to J_CPU_BSEL2
- 2.load power solution
- 3.add R515 R516 and update new solution for 5VDIMM
- 4.delete CN7 add C536-539
- 5. add R517 R518, follow msi-newheader_0216,change JPWR1 JPWR2 JCD1 KB_MS1
- 6.change EC20 footprint

| | | | |
|---------|------------------------|-------|----------|
| Title | | | |
| History | | | |
| Size | Document Number | | Rev |
| Custom | MS-7610 | | 1.0 |
| Date: | Monday, March 02, 2009 | Sheet | 33 of 33 |