



### EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

### USB

USB 0	USB Conn
USB 1	USB Conn
USB 2	NA
USB 3	NA
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera

### PCIE

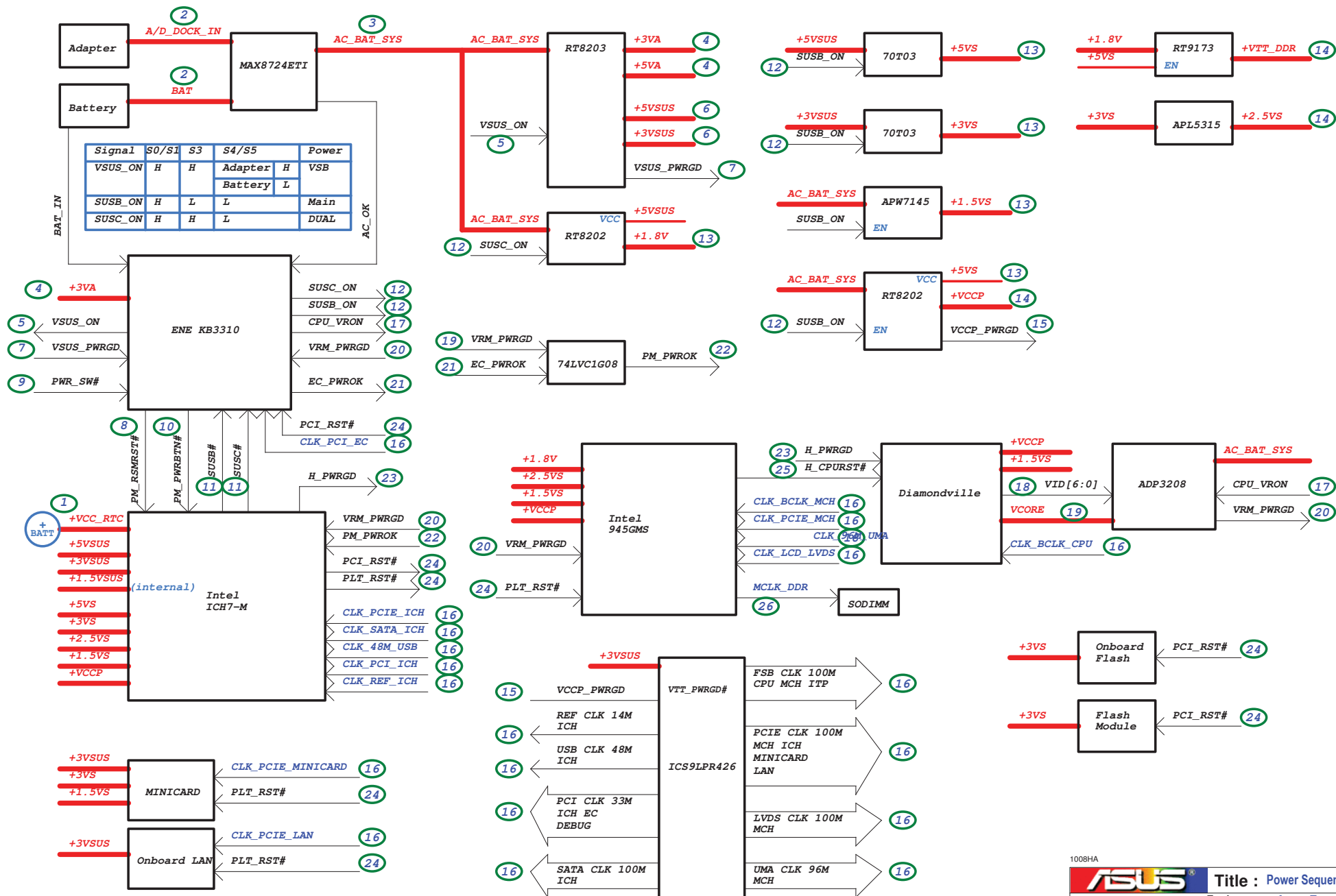
PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

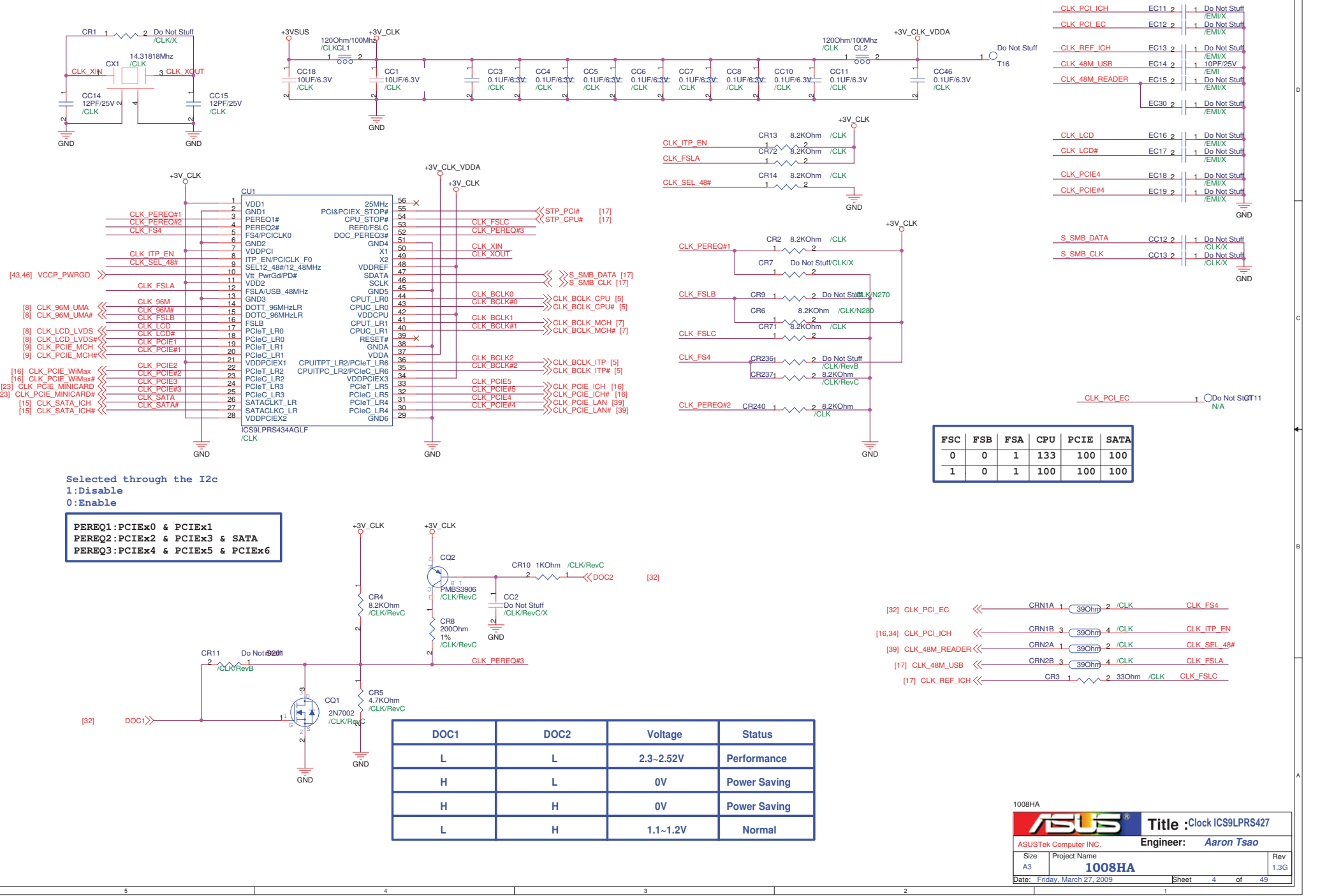
### Azalia

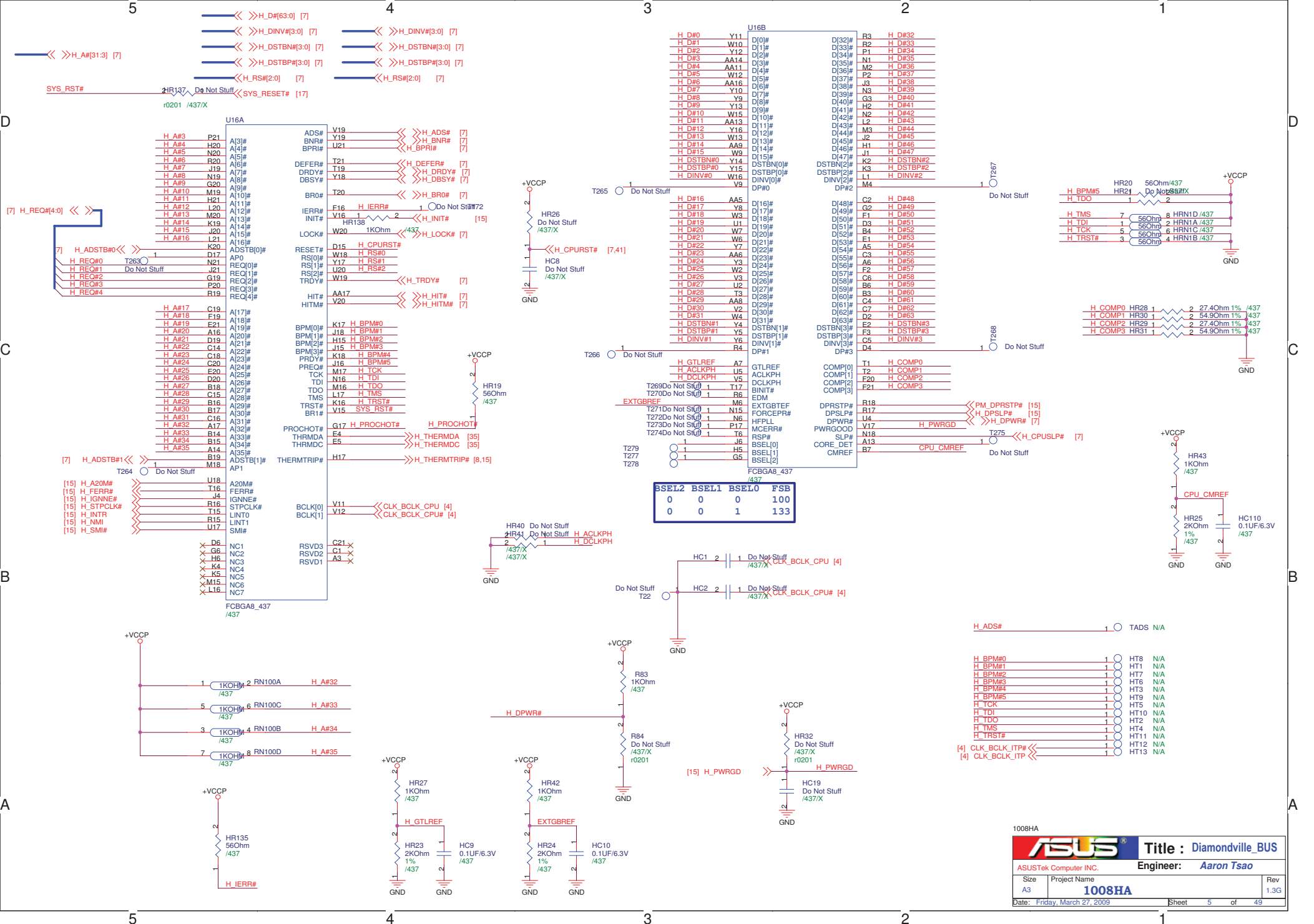
ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

1008HA

		Title : System Setting	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 2 of 49	





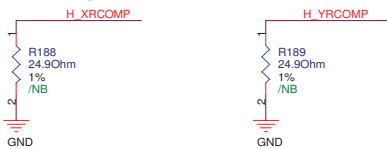




Power :  
+VCCP

### RCOMP

For Calibrating the FSB I/O Buffer



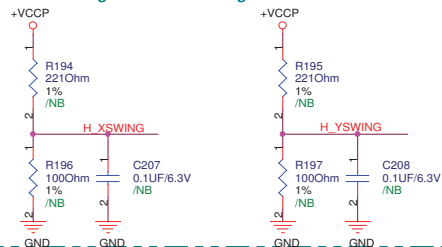
### SCOMP

For Slew Rate Compensation on the FSB

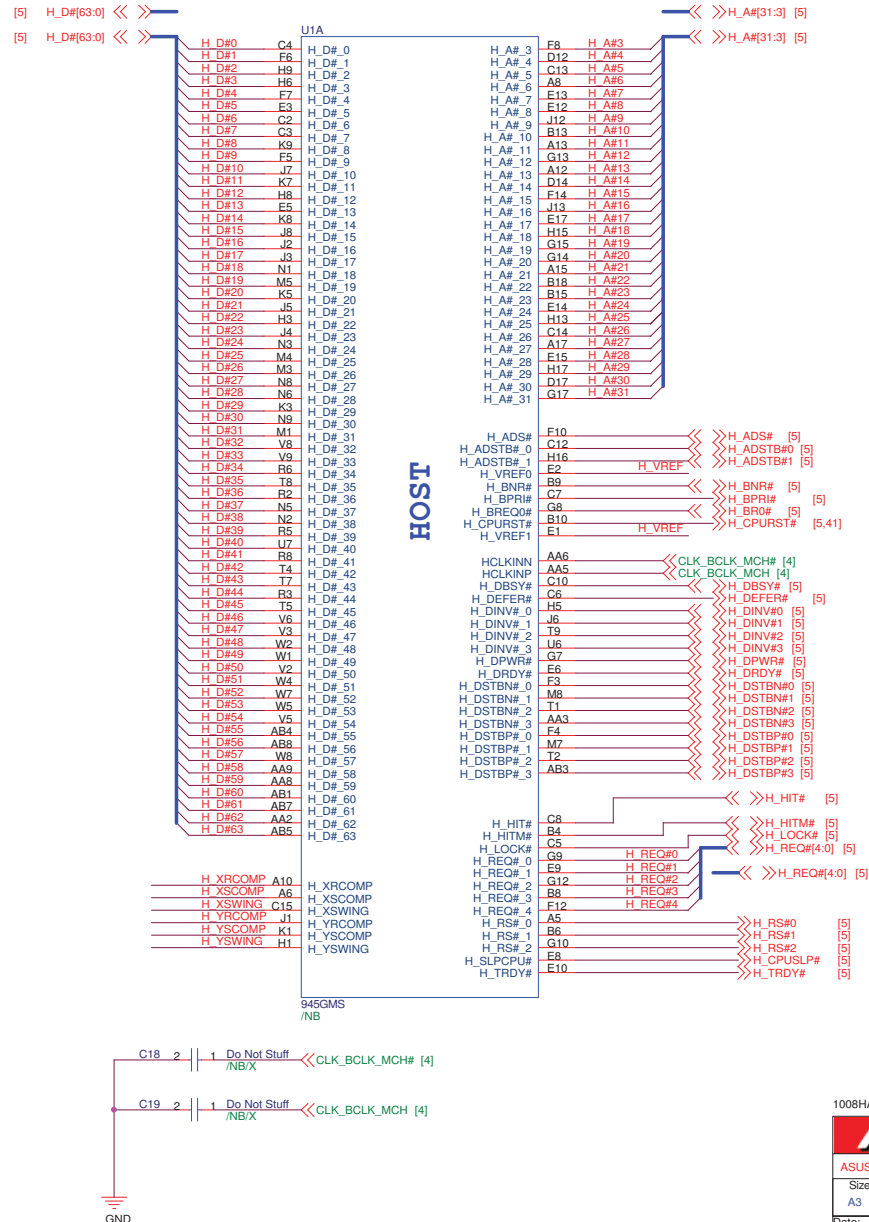


### Voltage Swing

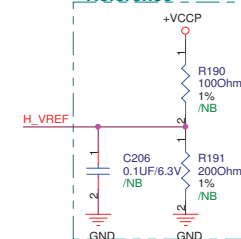
For Providing a Reference Voltage to The FSB RCOMP circuits



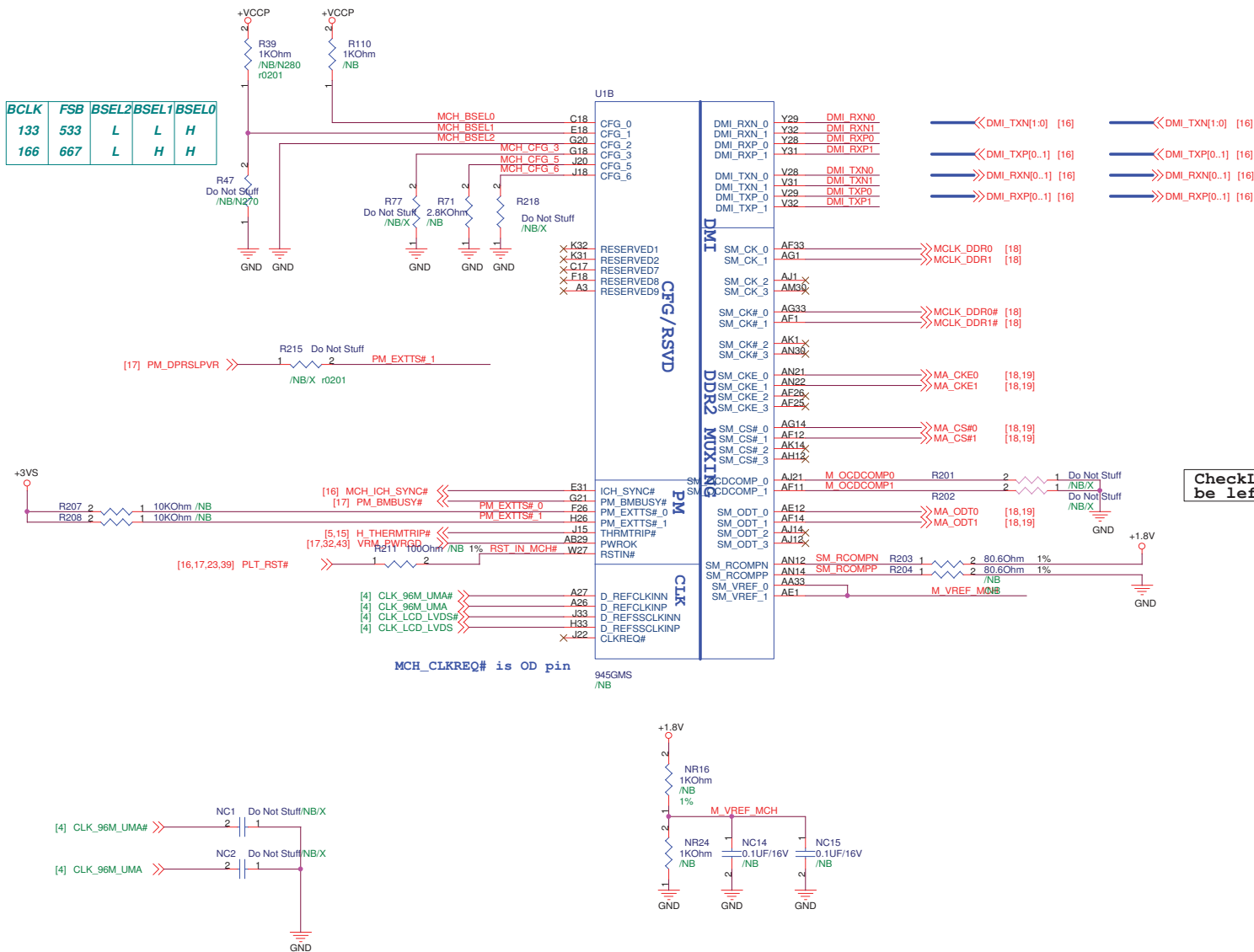
Signal voltage level =  
0.3125\*VCCP  
Trace should be 10 mil wide  
with 20 mil spacing



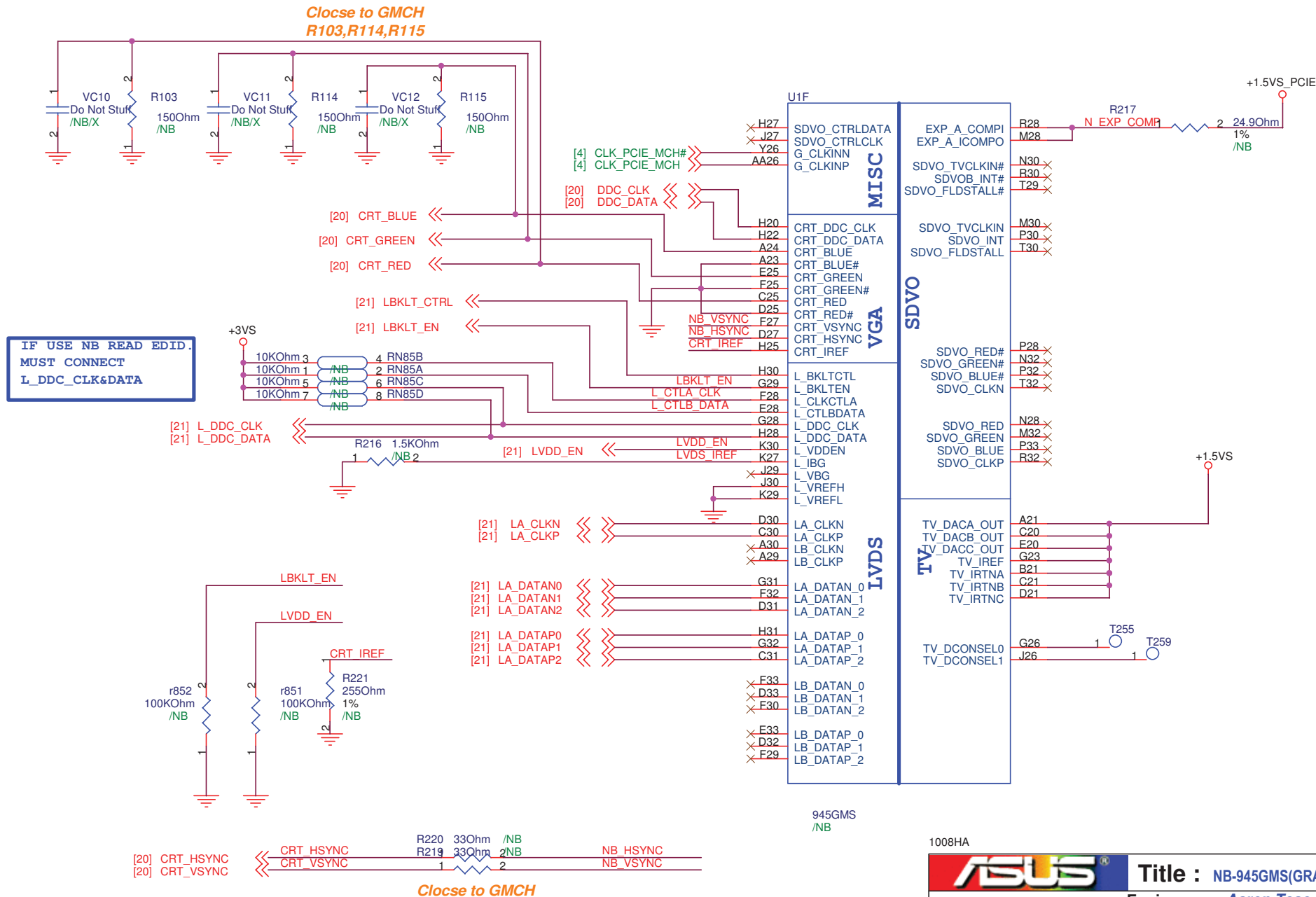
### AGTL+ I/O Voltage Reference

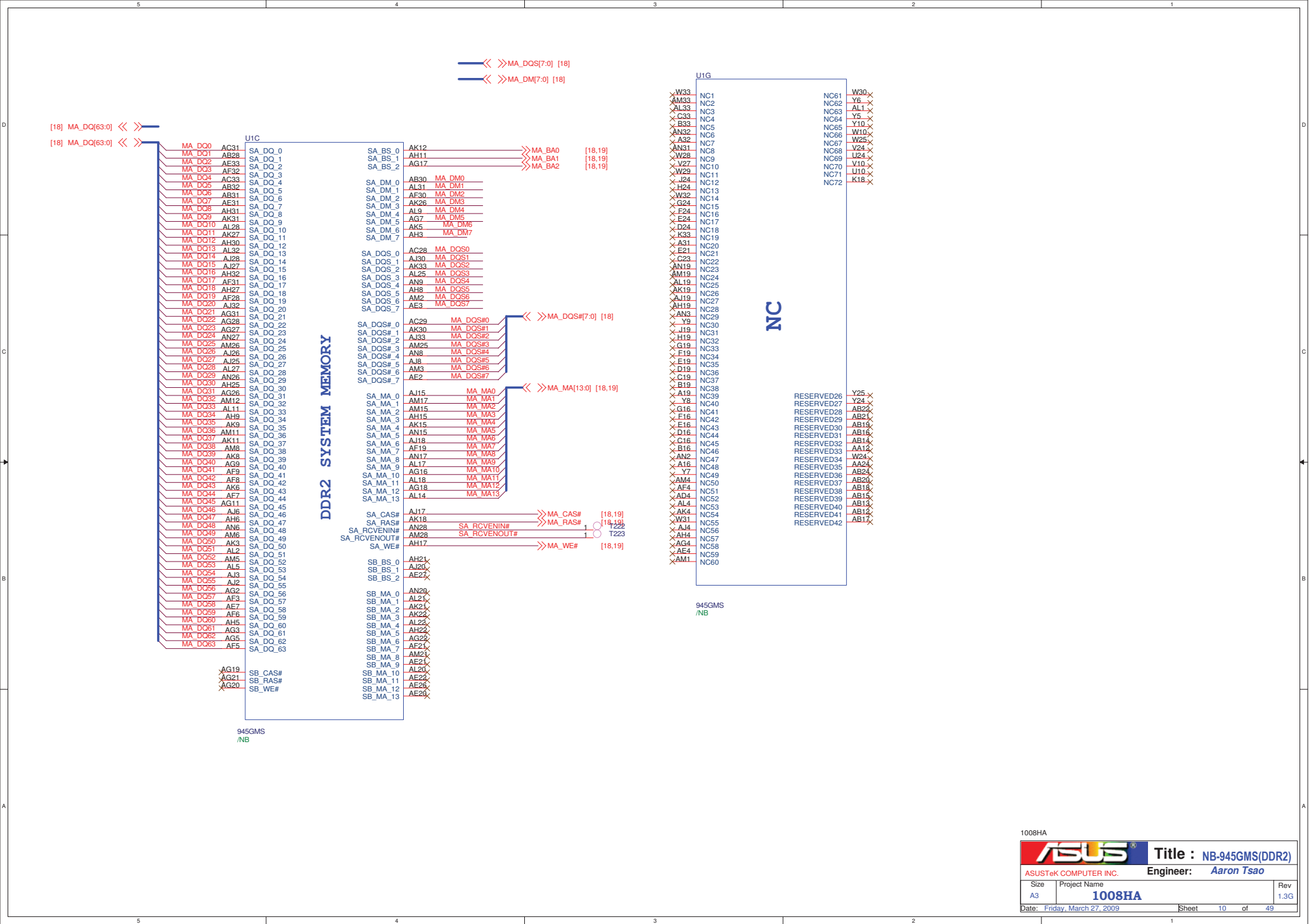


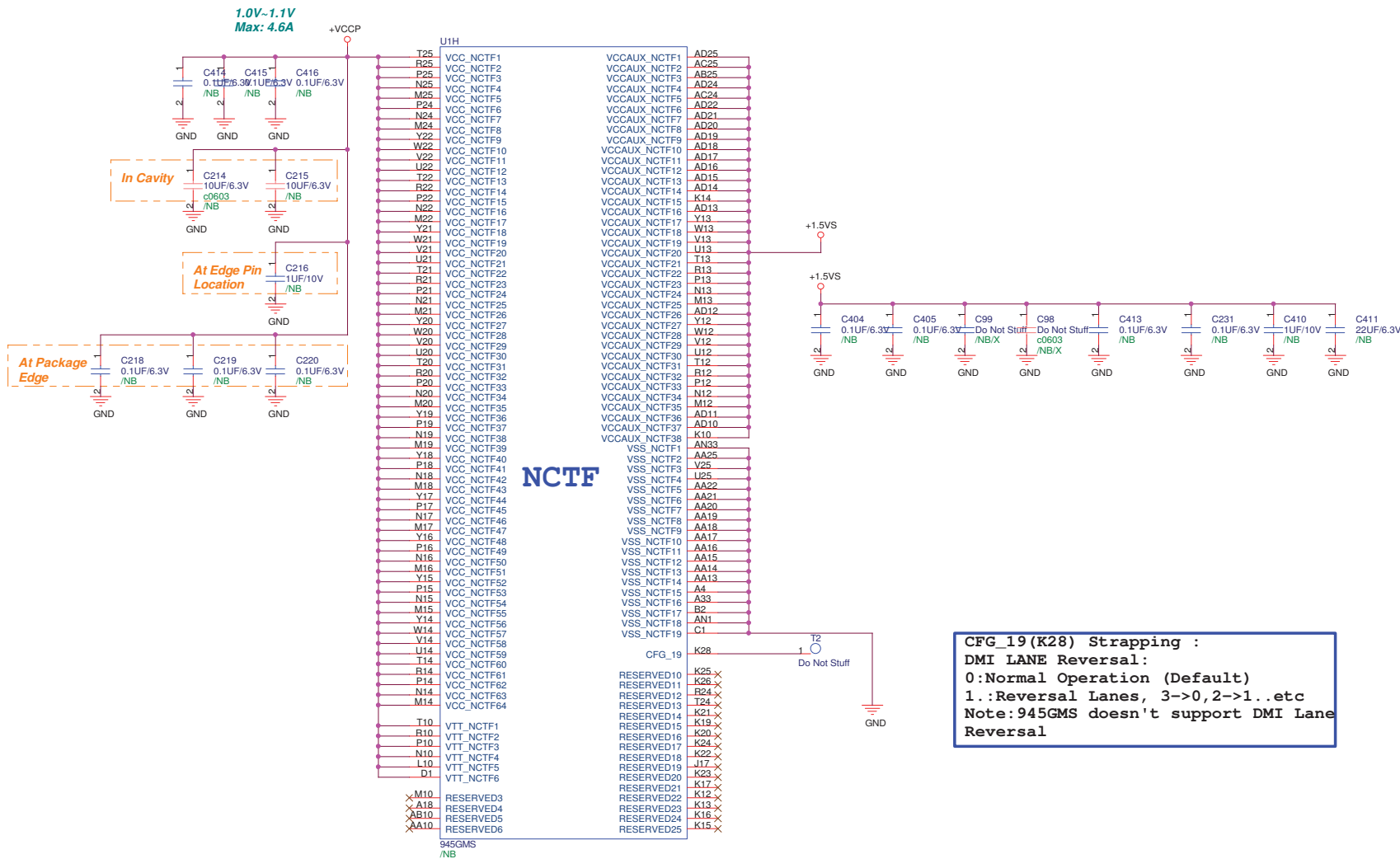
Layout Note:  
0.1uF should be placed 100mils or  
less from GMCH pin.







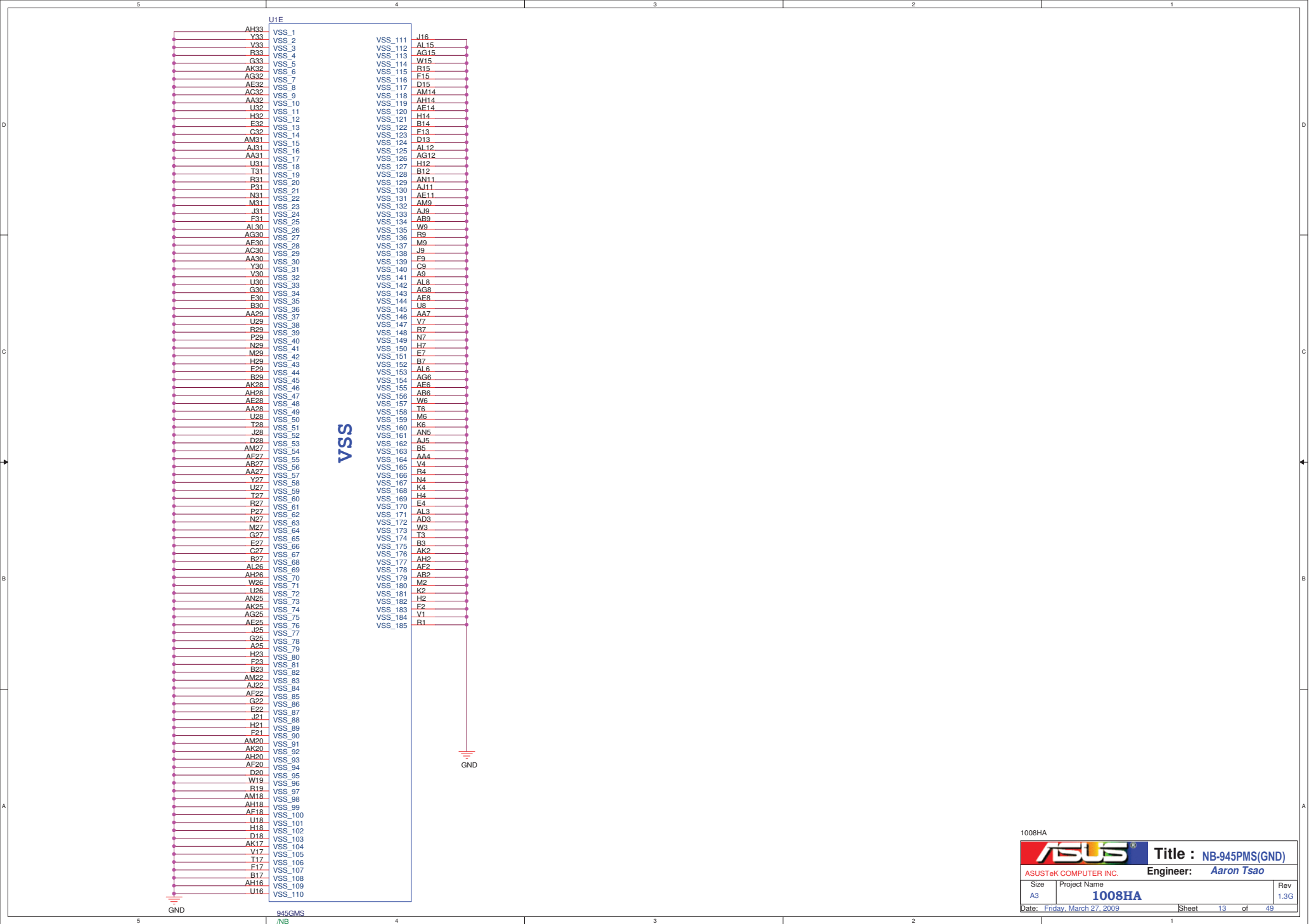




1008HA

<b>ASUS</b>		Title : NB-945GMS(PWR)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 11 of 49	

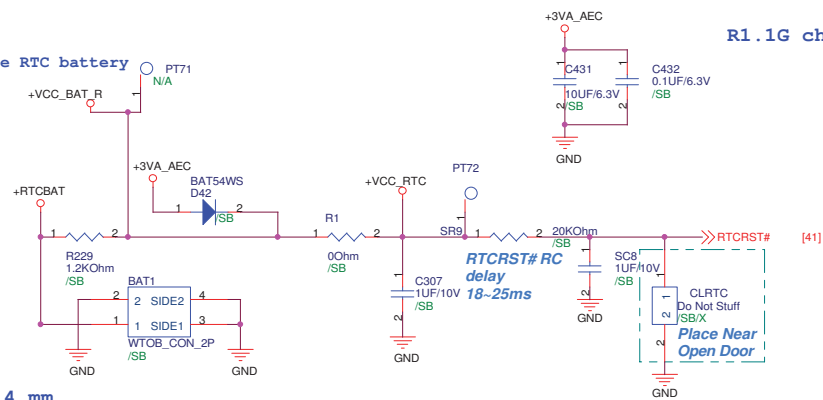




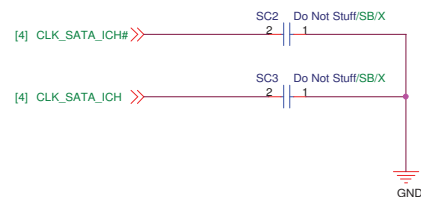
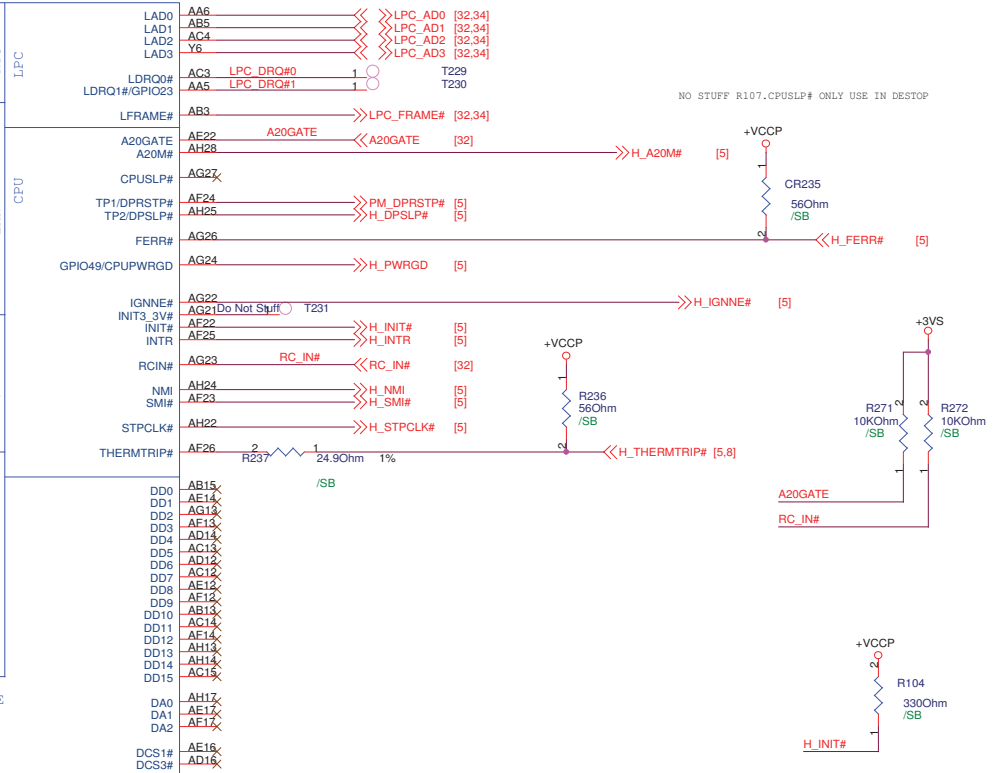
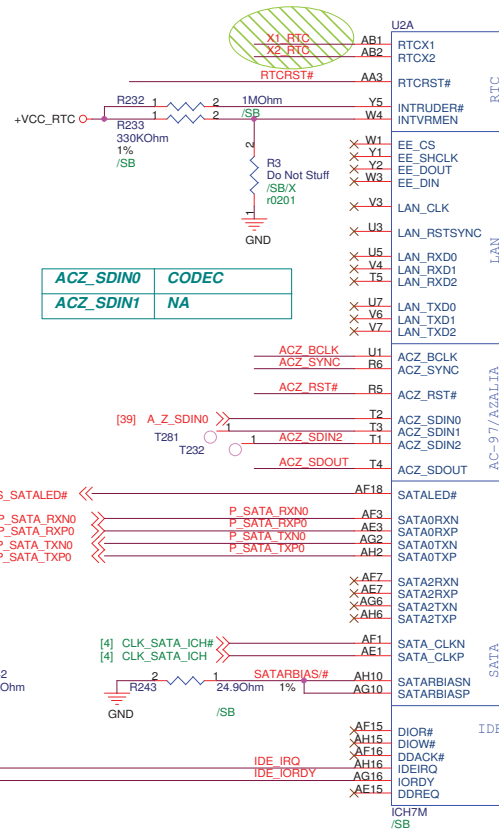
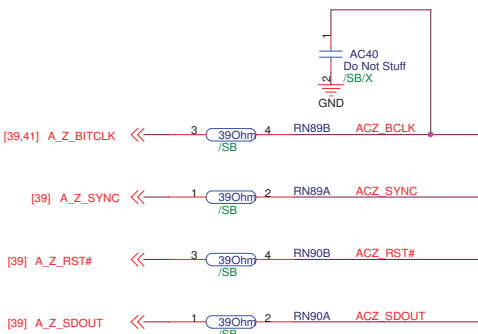
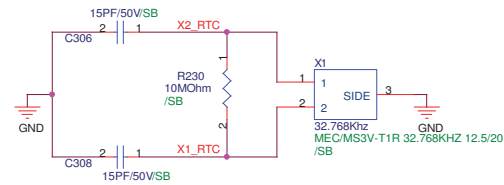


Change to  
rechargeable RTC battery

R1.1G change +3VA net to +3VA\_AEC



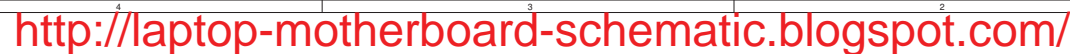
Height : 3.4 mm

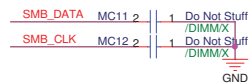












<< >>MA\_DQ[63:0] [10]  
 << >>MA\_DQS[7:0] [10]  
 << >>MA\_DQS#[7:0] [10]  
 << >>MA\_DM[7:0] [10]  
 << >>MA\_MA[13:0] [10,19]  
 << >>MA\_BA[2:0] [10,19]

STD Type

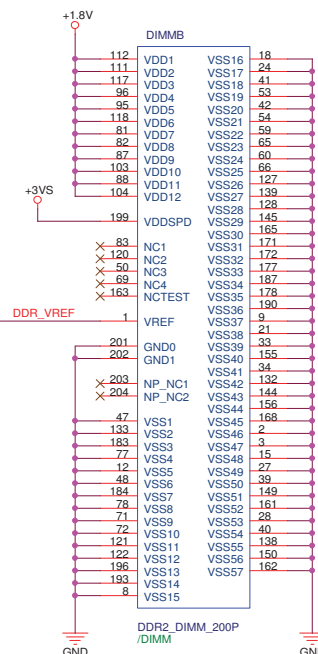
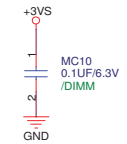
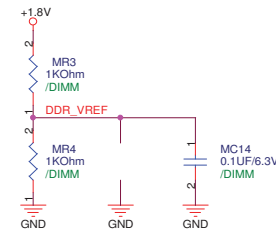
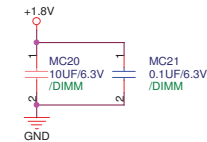
DDR2 Conn. Height=4.0mm

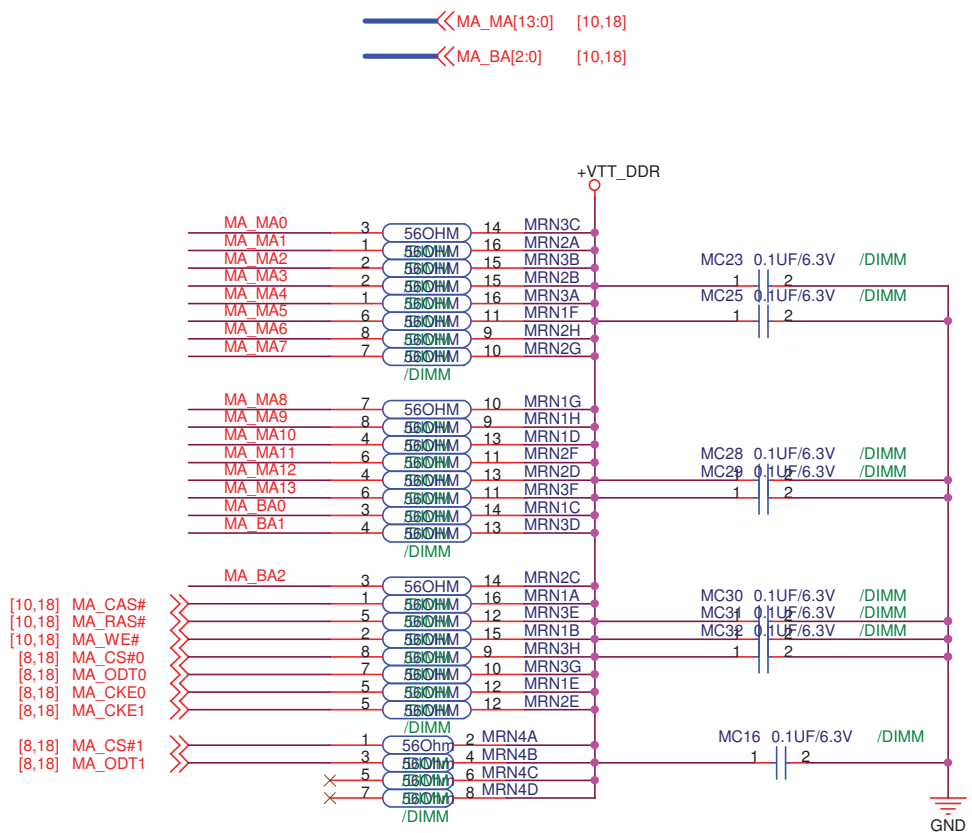
GROUP1  
GROUP2  
SWAP

MA_MA0	102	A0	DQ0	5	MA_DQ0
MA_MA1	101	A1	DQ1	7	MA_DQ1
MA_MA2	100	A2	DQ2	17	MA_DQ2
MA_MA3	98	A3	DQ3	19	MA_DQ3
MA_MA4	98	A4	DQ4	4	MA_DQ4
MA_MA5	97	A5	DQ5	6	MA_DQ5
MA_MA6	94	A6	DQ6	14	MA_DQ6
MA_MA7	92	A7	DQ7	16	MA_DQ7
MA_MA8	91	A8	DQ8	23	MA_DQ8
MA_MA9	91	A9	DQ9	25	MA_DQ9
MA_MA10	105	A10/AP	DQ10	35	MA_DQ10
MA_MA11	90	A11	DQ11	37	MA_DQ11
MA_MA12	89	A12	DQ12	20	MA_DQ12
MA_MA13	116	A13	DQ13	22	MA_DQ13
	86	A14	DQ14	36	MA_DQ14
	84	A15	DQ15	38	MA_DQ15
MA_BA2	85	A16_BA2	DQ16	43	MA_DQ16
MA_BA0	107	BA0	DQ17	45	MA_DQ17
MA_BA1	106	BA1	DQ18	55	MA_DQ18
[8,19] MA_CS#0	110	CS#	DQ19	57	MA_DQ19
[8,19] MA_CS#1	115	S1#	DQ20	44	MA_DQ20
[8] MCLK_DDR0	30	CK0	DQ21	46	MA_DQ21
[8] MCLK_DDR0#	32	CK0#	DQ22	58	MA_DQ22
[8] MCLK_DDR1	164	CK1	DQ23	61	MA_DQ23
[8] MCLK_DDR1#	166	CK1#	DQ24	63	MA_DQ24
[8,19] MA_CKE0	79	CKE0	DQ25	73	MA_DQ25
[8,19] MA_CKE1	80	CKE1	DQ26	75	MA_DQ26
[10,19] MA_CAS#	113	CAS#	DQ27	76	MA_DQ27
[10,19] MA_RAS#	108	RAS#	DQ28	62	MA_DQ28
[10,19] MA_WE#	109	WE#	DQ29	64	MA_DQ29
	198	SA0	DQ30	74	MA_DQ30
[17,23] SMB_CLK	197	SA1	DQ31	76	MA_DQ31
[17,23] SMB_DATA	195	SCL	DQ32	123	MA_DQ32
	196	SDA	DQ33	125	MA_DQ33
	197		DQ34	135	MA_DQ34
	198		DQ35	137	MA_DQ35
[8,19] MA_ODT0	114	ODT0	DQ36	124	MA_DQ36
[8,19] MA_ODT1	119	ODT1	DQ37	126	MA_DQ37
	134		DQ38	134	MA_DQ38
	136		DQ39	141	MA_DQ39
MA_DM0	10	DM0	DQ40	143	MA_DQ40
MA_DM1	26	DM1	DQ41	151	MA_DQ41
MA_DM2	52	DM2	DQ42	153	MA_DQ42
MA_DM3	67	DM3	DQ43	140	MA_DQ43
MA_DM4	130	DM4	DQ44	142	MA_DQ44
MA_DM5	147	DM5	DQ45	152	MA_DQ45
MA_DM6	170	DM6	DQ46	154	MA_DQ46
MA_DM7	185	DM7	DQ47	157	MA_DQ47
MA_DQS0	13	DQS0	DQ48	159	MA_DQ48
MA_DQS1	31	DQS1	DQ49	173	MA_DQ49
MA_DQS2	70	DQS2	DQ50	175	MA_DQ50
MA_DQS3	131	DQS3	DQ51	158	MA_DQ51
MA_DQS4	148	DQS4	DQ52	160	MA_DQ52
MA_DQS5	169	DQS5	DQ53	174	MA_DQ53
MA_DQS6	188	DQS6	DQ54	176	MA_DQ54
MA_DQS7	11	DQS7	DQ55	179	MA_DQ55
MA_DQS8	29	DQS8	DQ56	181	MA_DQ56
MA_DQS9	49	DQS9	DQ57	189	MA_DQ57
MA_DQS10	68	DQS10	DQ58	191	MA_DQ58
MA_DQS11	129	DQS11	DQ59	180	MA_DQ59
MA_DQS12	146	DQS12	DQ60	182	MA_DQ60
MA_DQS13	167	DQS13	DQ61	192	MA_DQ61
MA_DQS14	186	DQS14	DQ62	194	MA_DQ62
MA_DQS15		DQS15	DQ63		MA_DQ63

DDR2\_DIMM\_200P /DIMM

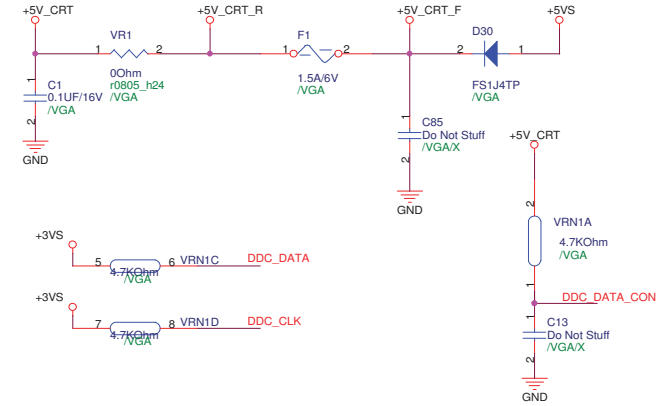
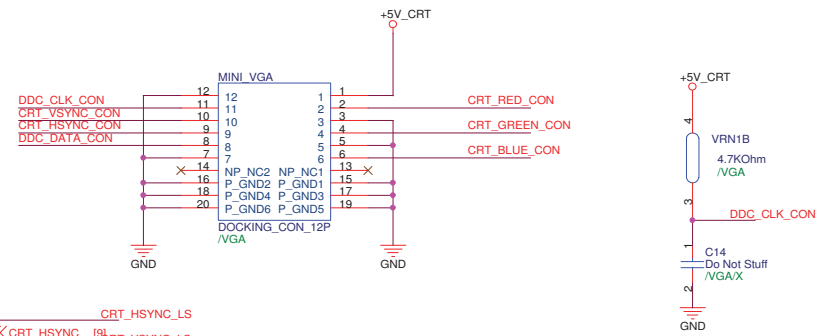
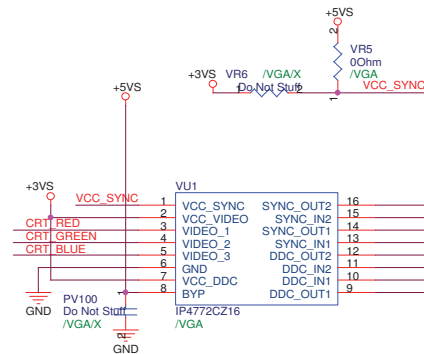
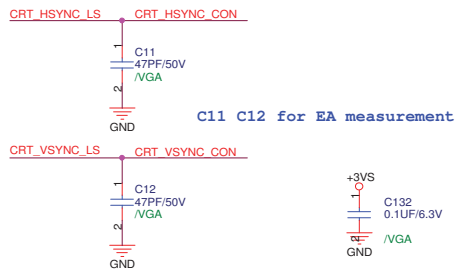
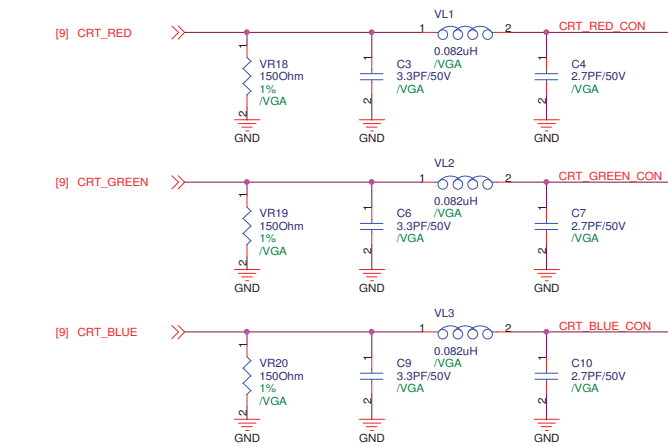
R1.1G MC3 MC4 change to 0603 1uF





1008HA

<b>ASUS</b>		<b>Title : DDR2_Termination</b>	
ASUSTek Computer INC.		Engineer: <b>Aaron Tsao</b>	
Size A4	Project Name <b>1008HA</b>		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 19 of 49	

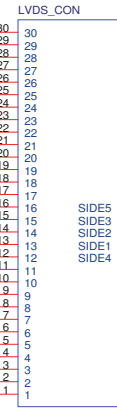


[17] BL\_EN << >> BL\_EN

[9] LVDD\_EN >>

[9] LBKLT\_CTRL >>

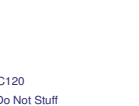
[9] L\_DDC\_DATA <<  
[9] L\_DDC\_CLK <<



WTOB\_CON\_30P /LVDS



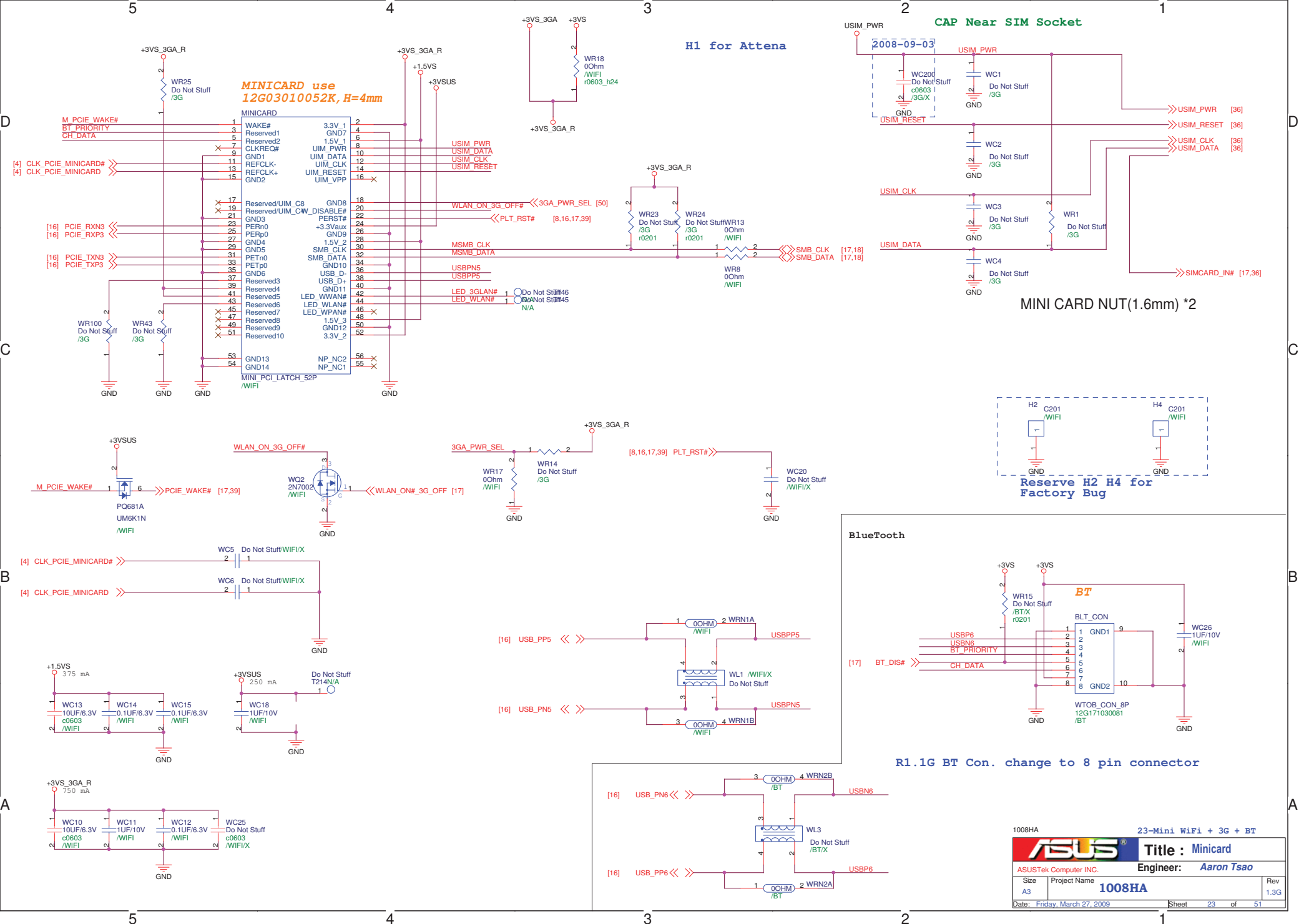
Do Not Stuff /LVDS/X

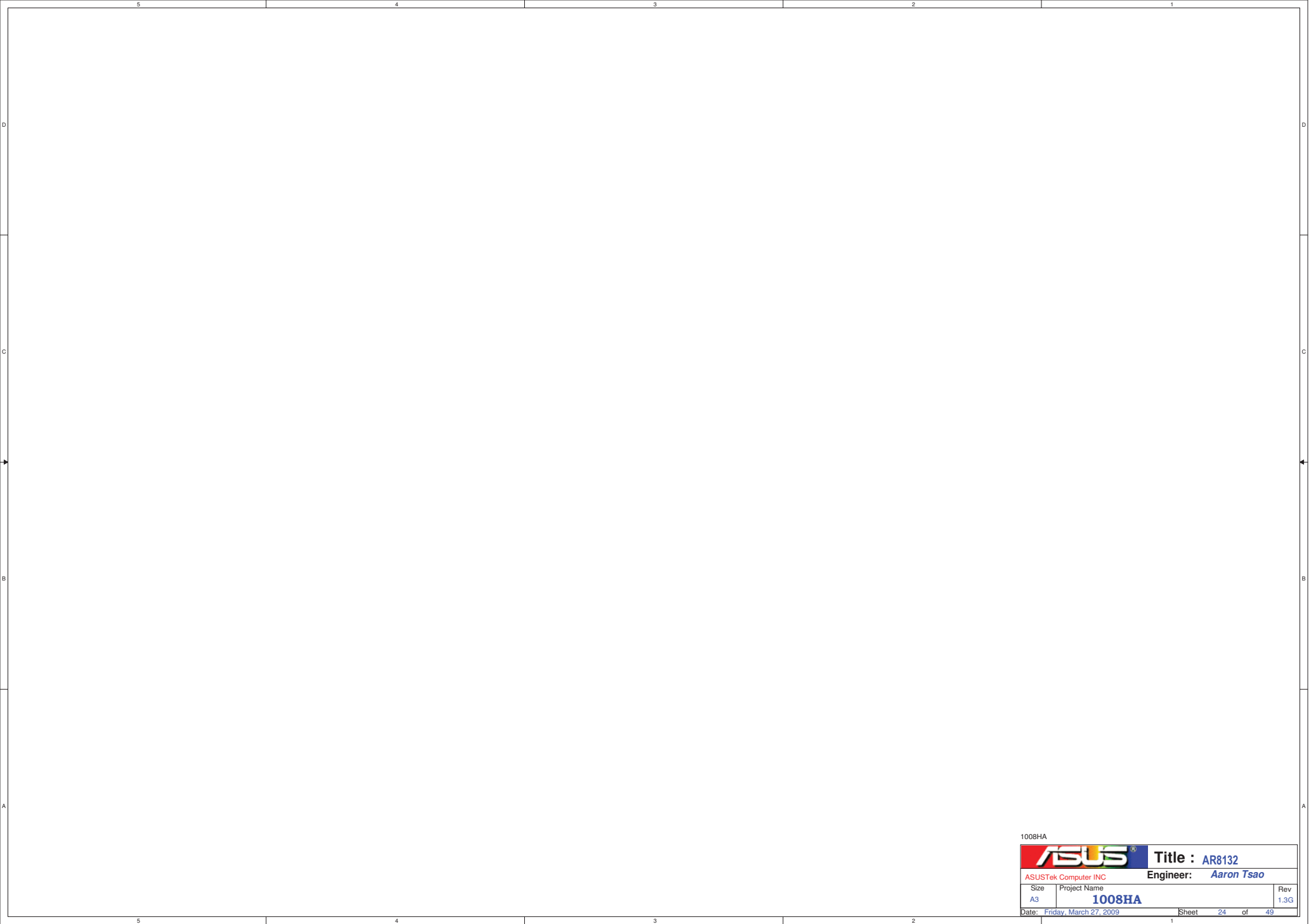


Change to 06G051007011 for cost issue

1008HA		ASUS®		Title : LVDS Conn_LID	
ASUSTek Computer INC.		Engineer: Aaron Tsao			
Size	Project Name			Rev	
A3	1008HA			1.3G	
Date: Friday, March 27, 2009	Sheet 21 of 49				

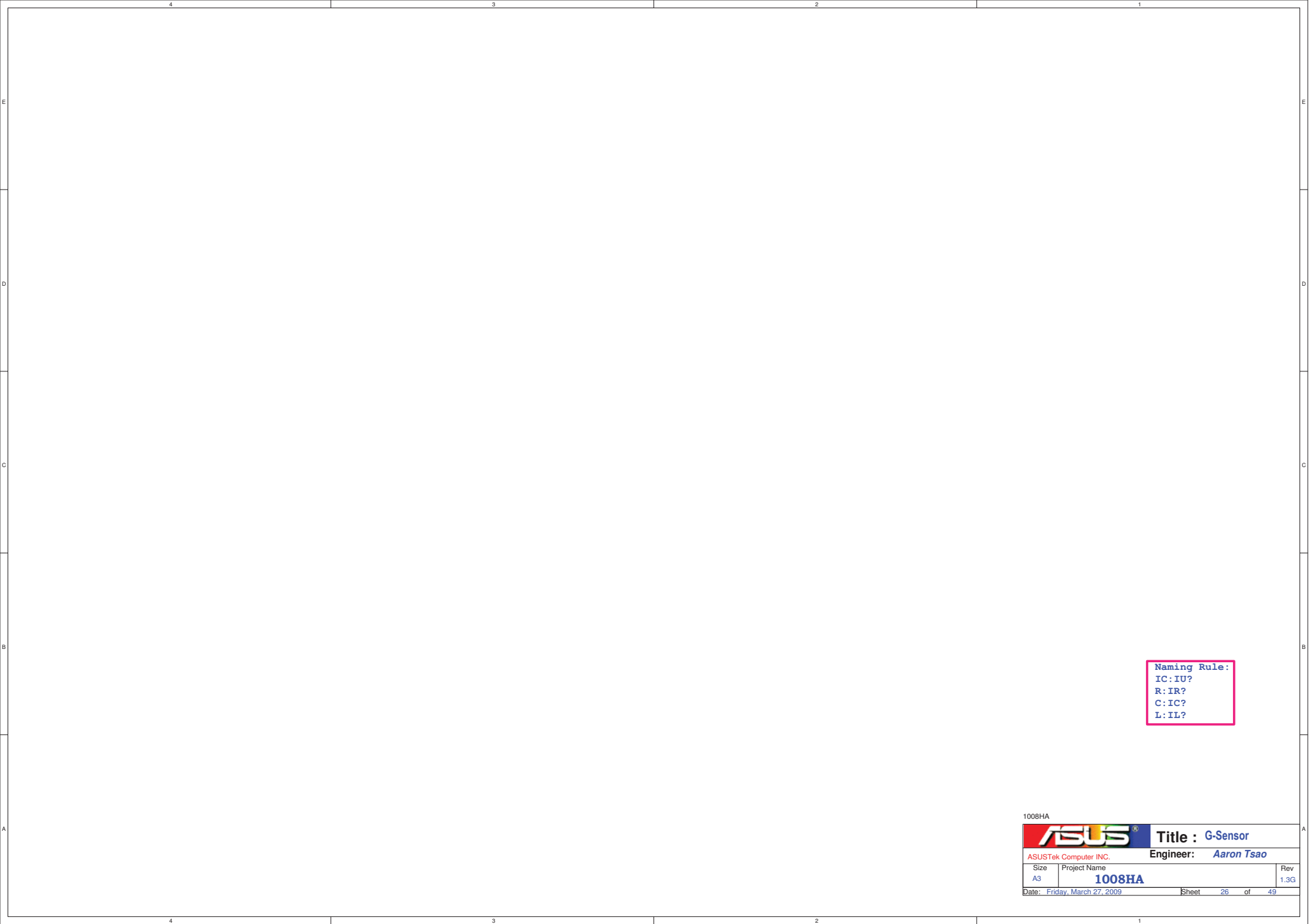




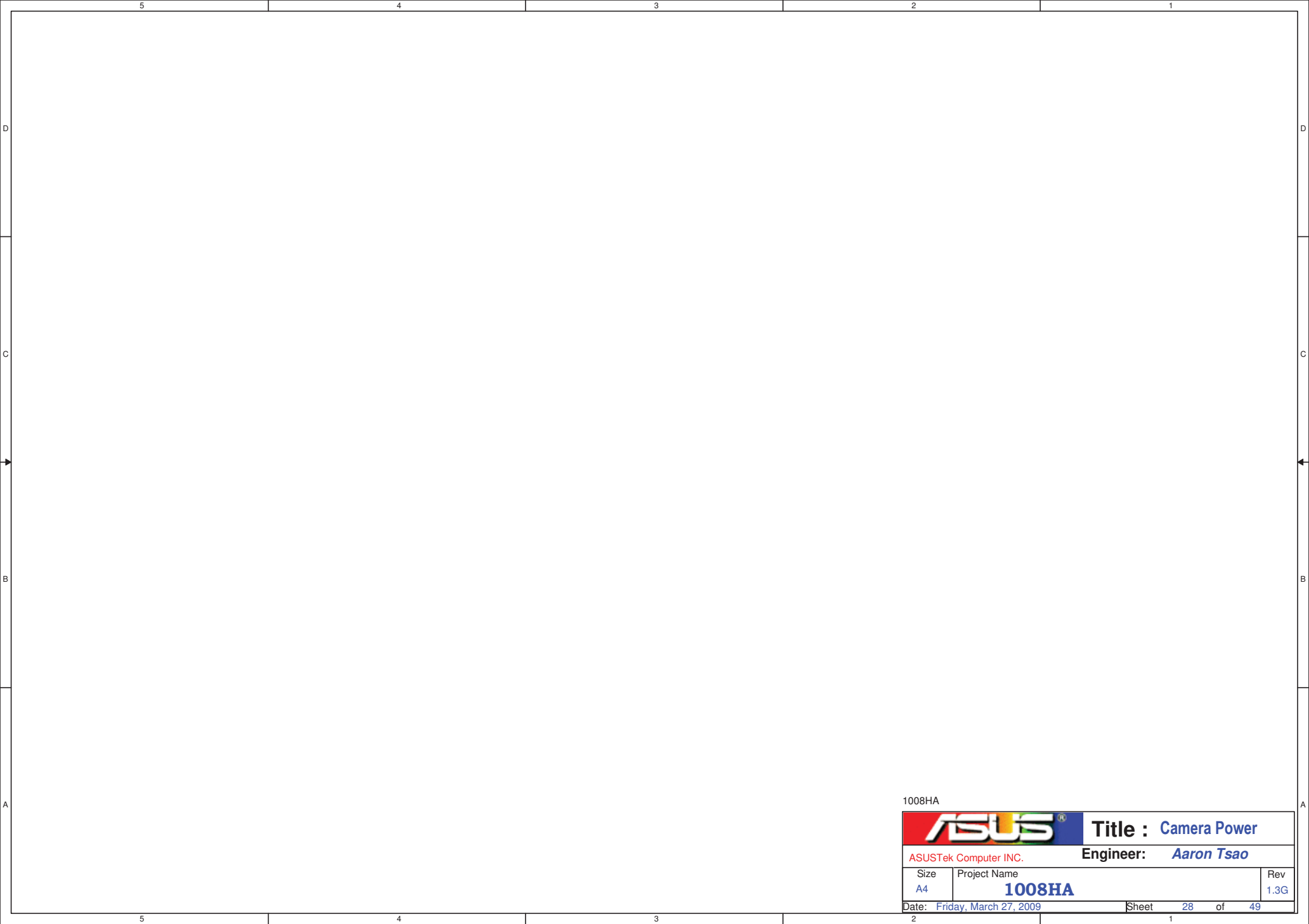













1008HA


		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A4	Project Name 1008HA		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 28 of 49	





1.1G add PWR LED and Charge LED  
DMIC Cable length should be less 30cm  
Change R291 R292 R293 to 510 Ohm

1008HA



Title : **ALC269-2**

ASUSTek Computer Inc.

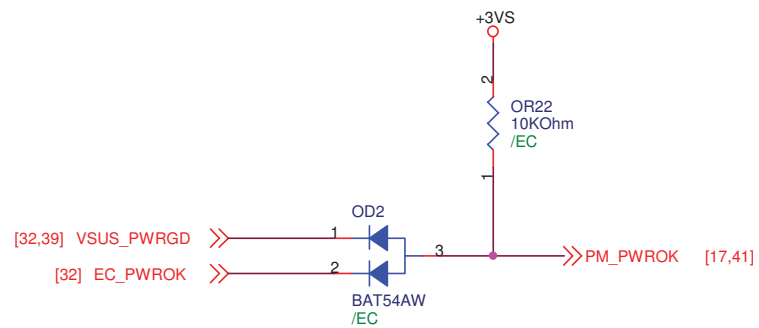
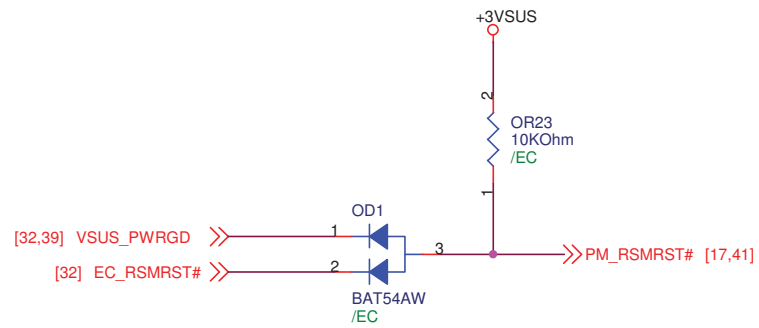
Engineer: **Aaron Tsao**

Size	Project Name	Rev
A3	<b>1008HA</b>	1.3G

Date: Friday, March 27, 2009 Sheet 31 of 49





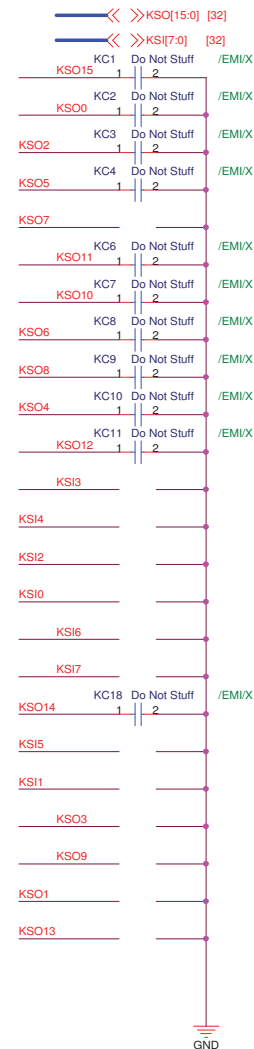


1008HA

ASUS		Title : EC	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A4	Project Name 1008HA		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 33 of 49	





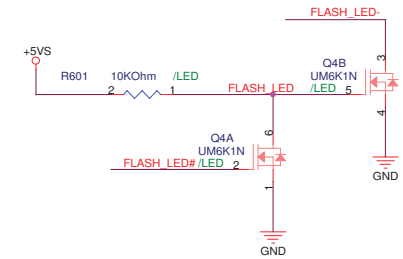
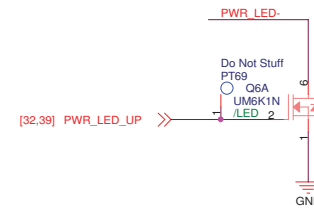
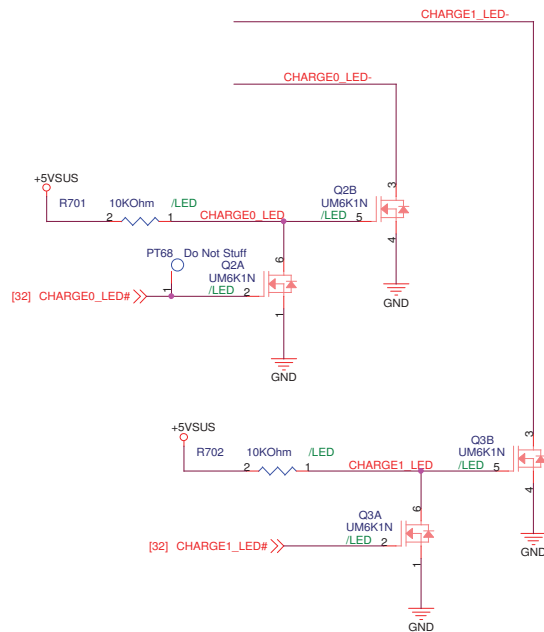


1.1G change to EVERLIGHT

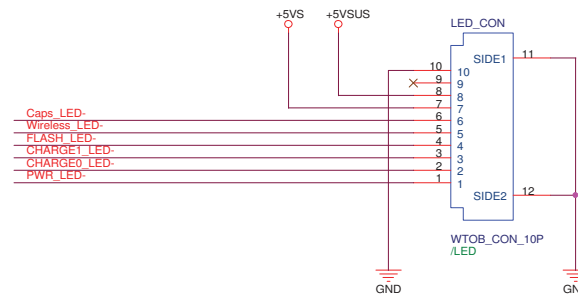
for CHARGE LED  
Height : 0.55mm

for POWER LED  
White

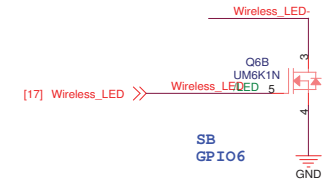
for FLASH LED  
White



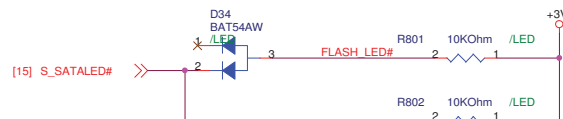
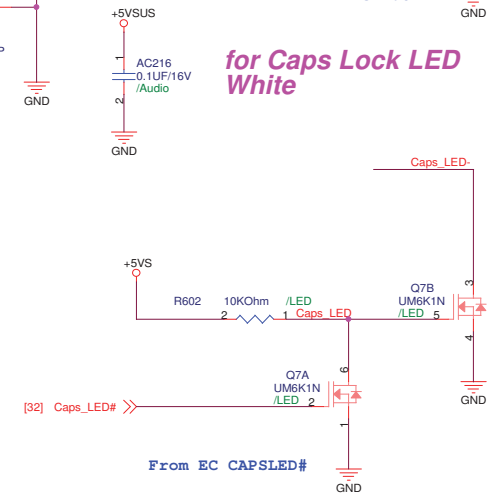
Change LED resistor to 510 Ohm, about 4mA



for WIFI/BlueTooth LED  
White



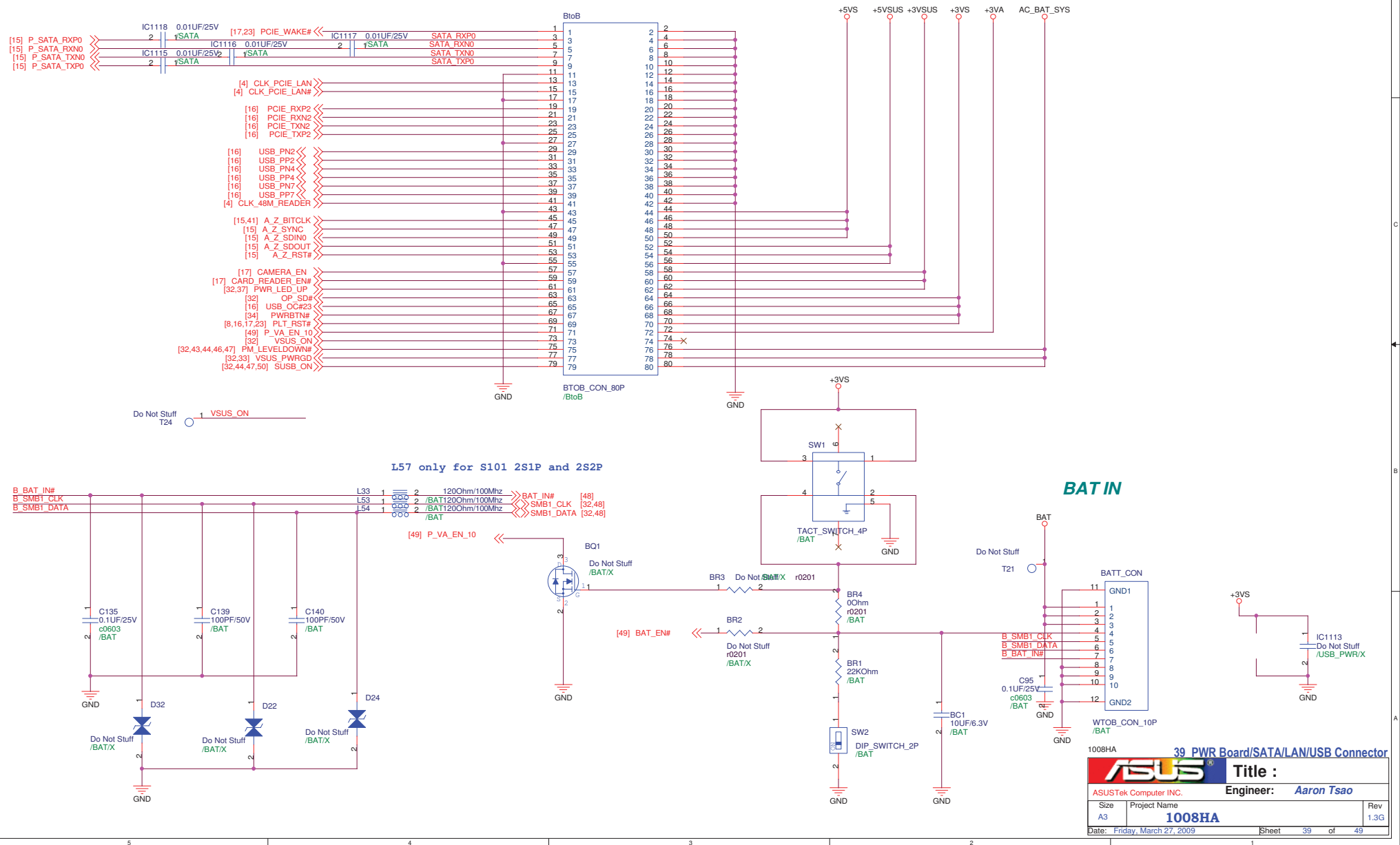
for Caps Lock LED  
White

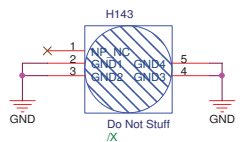
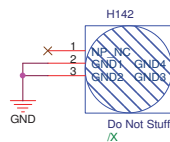
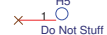
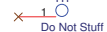
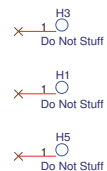
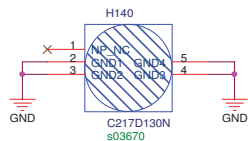
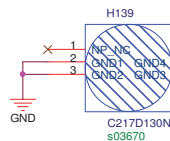
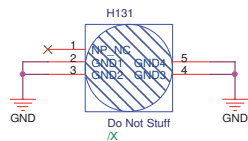


1008HA			
		Title : LED	
ASUSTek Computer INC.		Engineer: <u>Kenneth Hung</u>	
Size A3	Project Name <b>1008HA</b>		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 37 of 50	

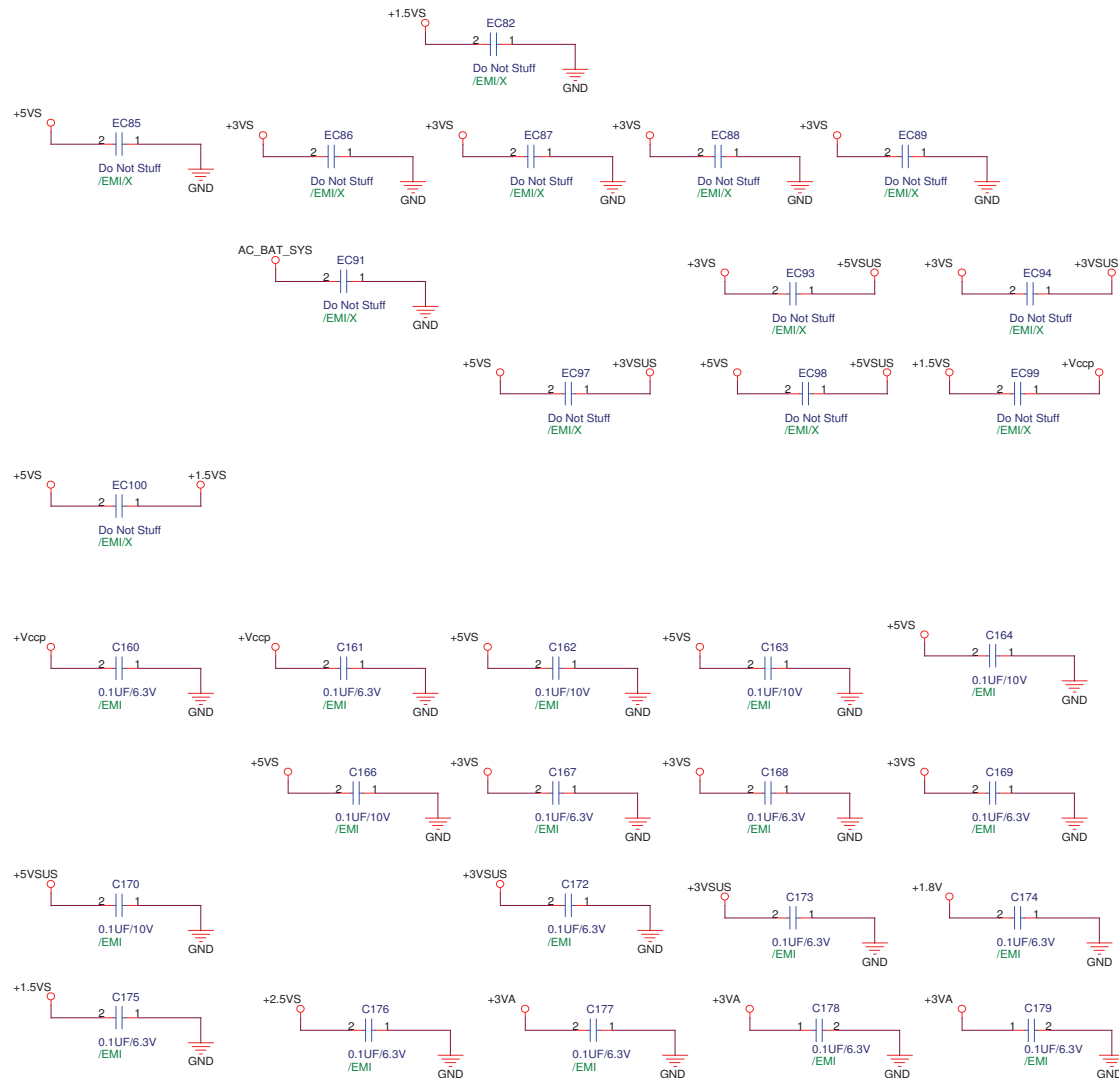


# PWR Board/SATA/LAN/USB Connector





R1.1G Change



1008HA			
		Title : Srew Hole	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA		Rev 1.3
Date: Friday, March 27, 2009		Sheet	40 of 49



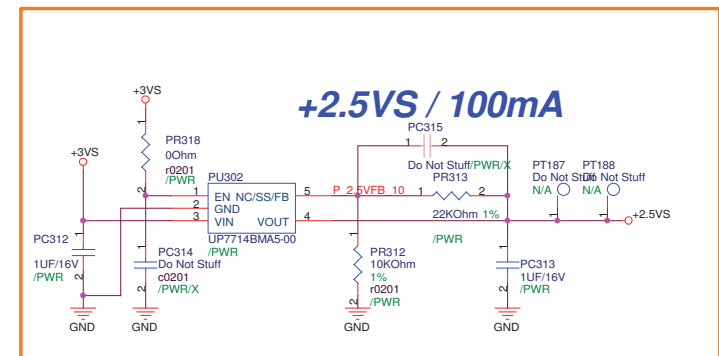
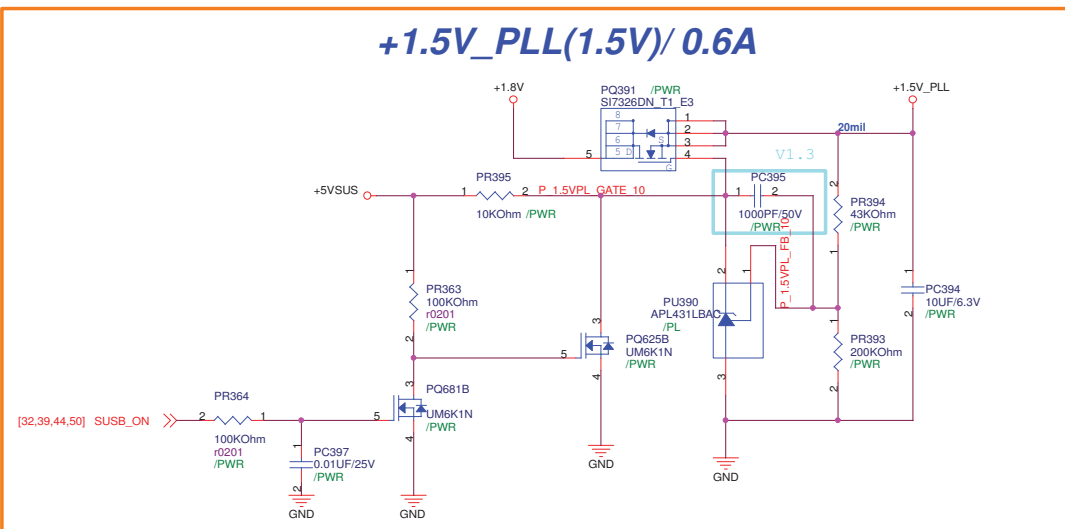
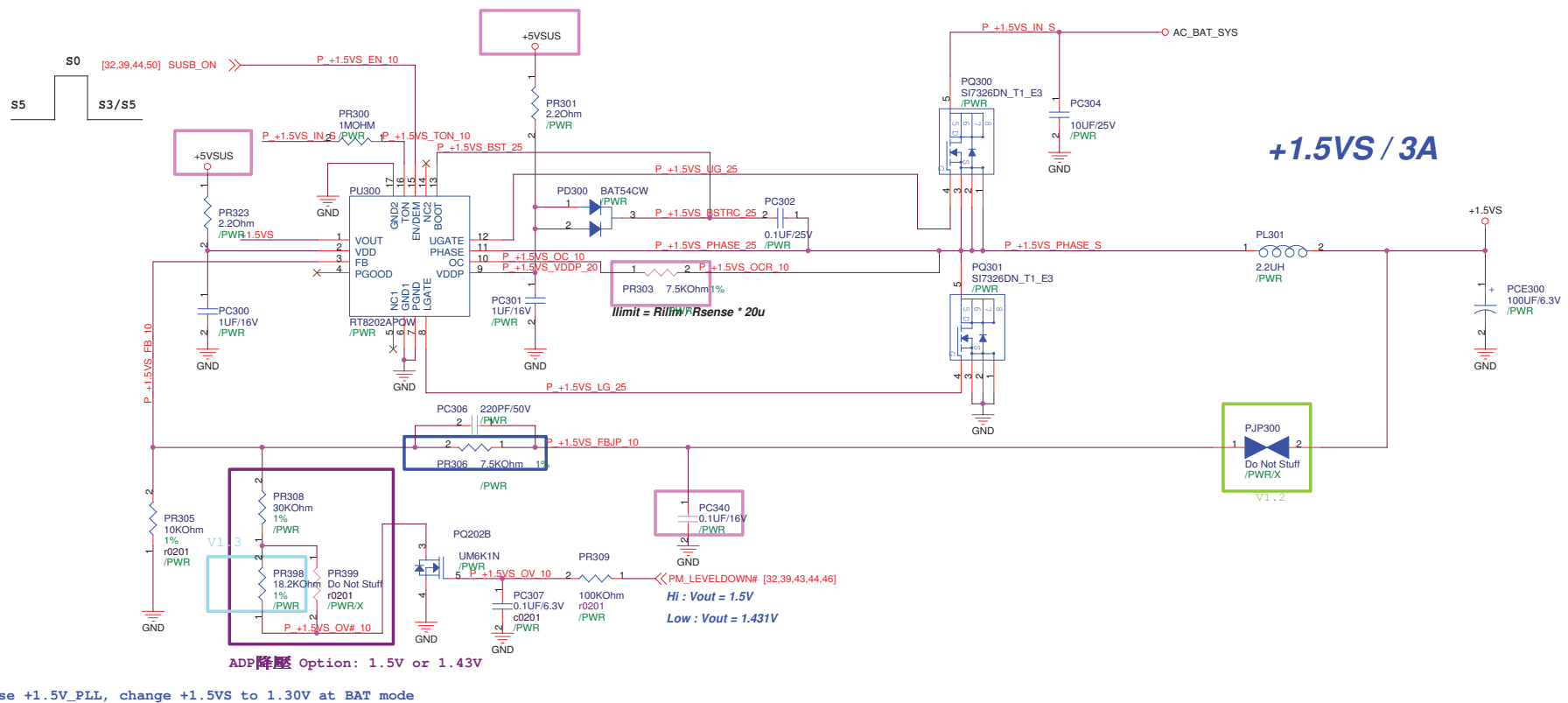




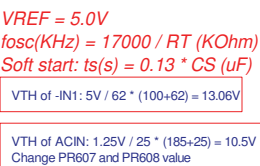








1008HA				
		Title : +1.5VS & +2.5VS		
ASUSTek Computer INC.		Engineer: Aaron Tsao		
Size A3	Project Name <b>1008HA</b>			Rev 1.3G
Date: Friday, March 27, 2009		Sheet 47 of 51		



BAT\_ID = 1, 2 Cells; Vadj2 = 0.998V  
=> Icharge = 1.477A

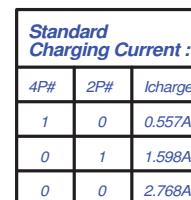
BAT\_ID = 0, 4/6 Cells; Vadj2 = 1.648V  
=> Icharge = 2.517A

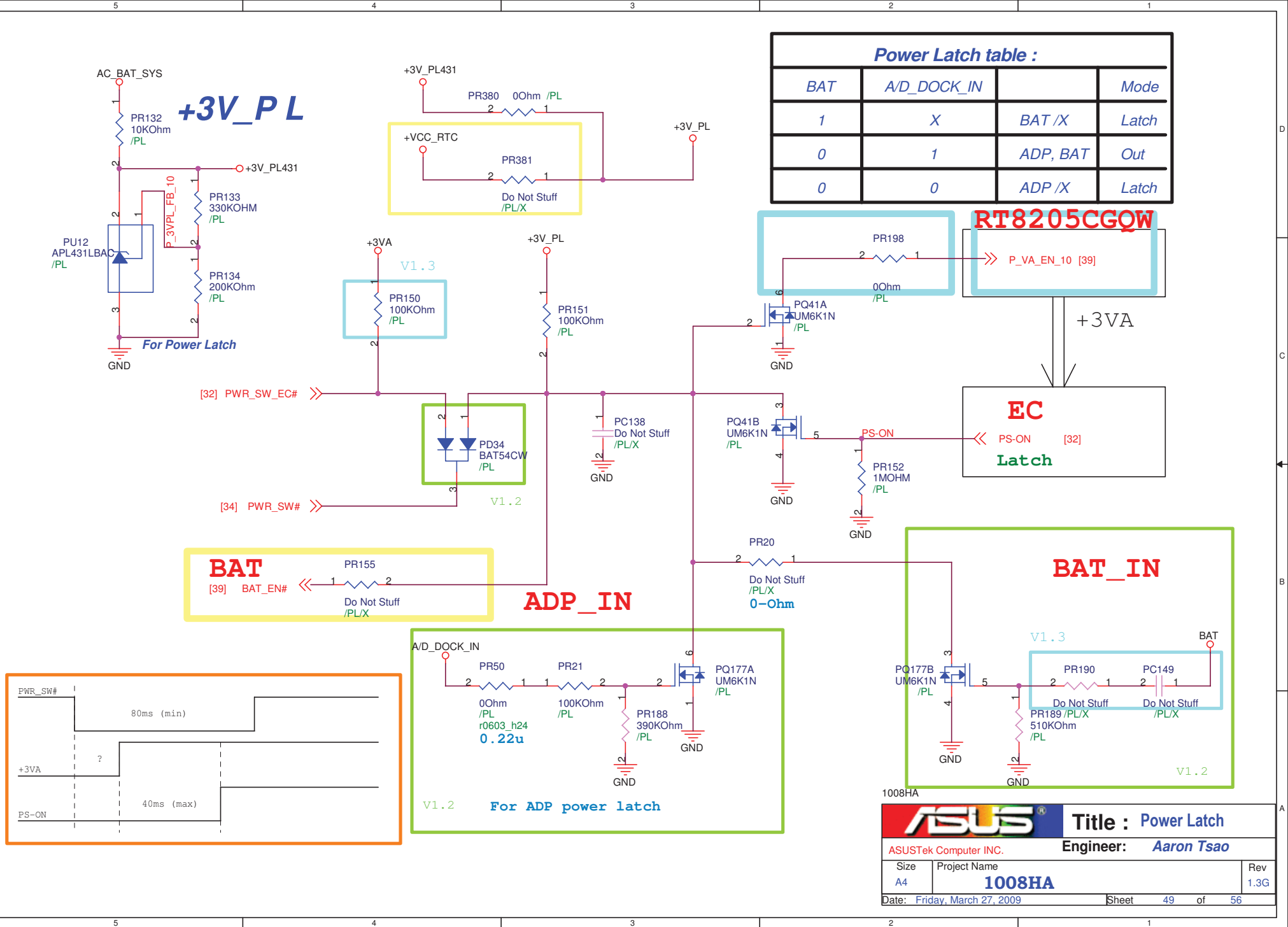
Precharging current = 150mA  
V<sub>adj2</sub> = 168.75mV

$$I_{\text{limit}} = 1.9\text{A}; 36.1\text{W} (19\text{V}/40\text{W})$$

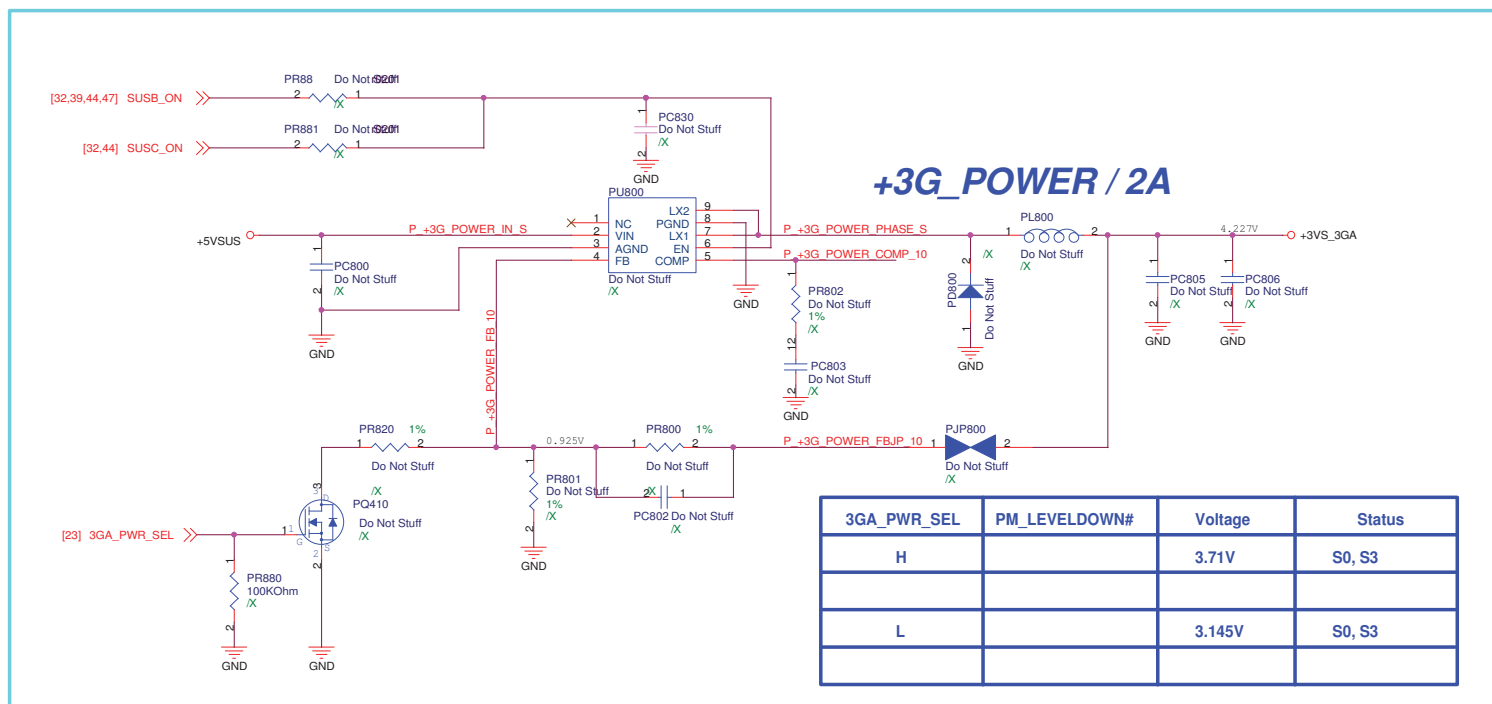
Adaptor > 16V, System Powered by Adaptor  
Adaptor < 16V, System Powered by Battery

$V_{adj3} > 4.1V \implies V_{bat} = 4.2V / \text{cell}$   
 $2.2V > V_{adj3} > 1.1V \implies V_{bat} = 2 \cdot V_{adj3} / \text{cell}$

$$I_{chg} = (V_{adj2} - 0.075) / (25 * R_s)$$
$$I_{\text{limit current}} = (V_{\text{adj1}} - 0.075) / (25 * R_s)$$


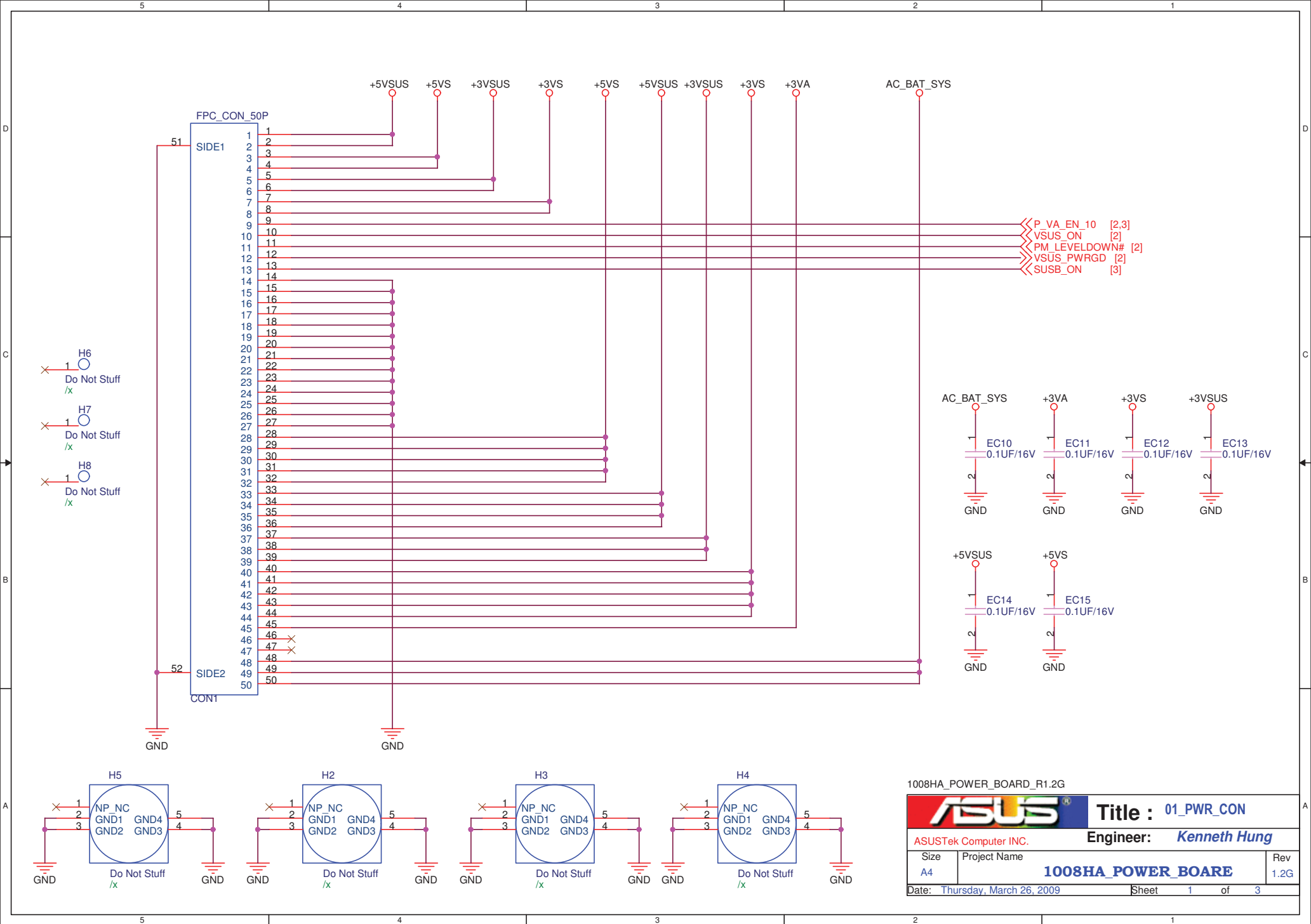




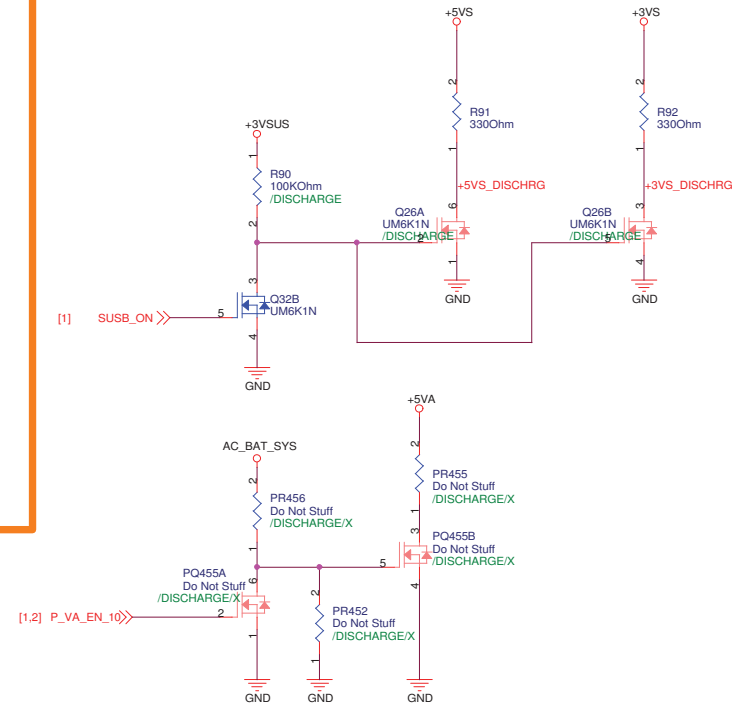
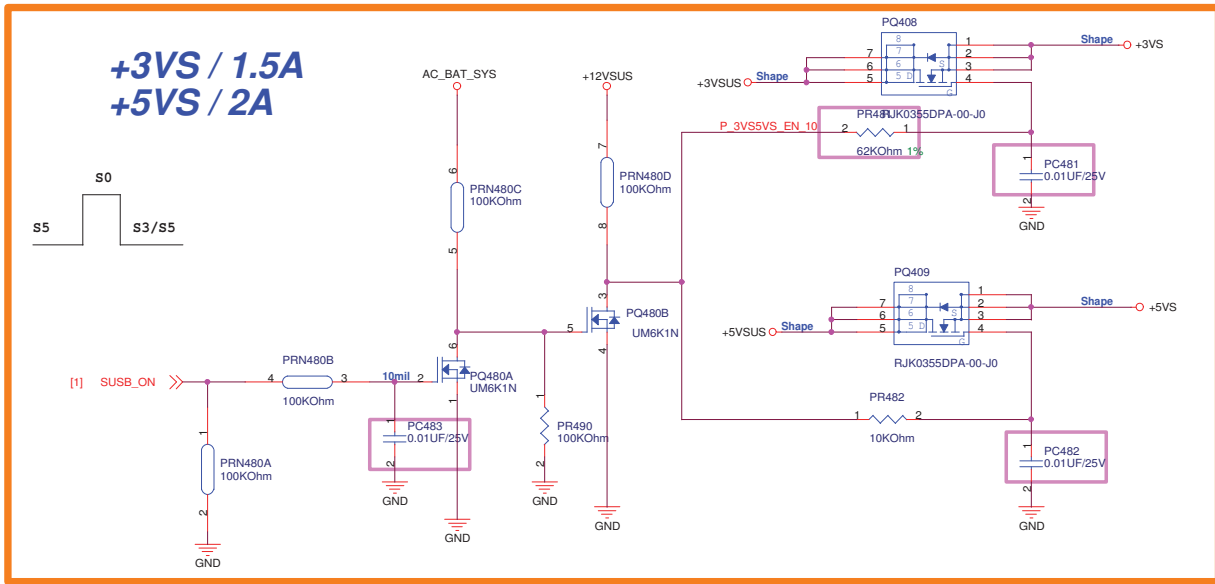


1008HA

<b>ASUS</b>		Title : History	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 50	of 51








1008HA\_POWER\_BOARD\_R1.2G

<b>ASUS</b>		<b>Title : 03_+32S &amp; +52S</b>	
ASUSTek Computer INC.		Engineer: <b>POWER TEAM</b>	
Size A3	Project Name <b>1008HA_POWER_BOARE</b>	Rev 1.2G	
Date: Thursday, March 26, 2009		Sheet 3 of 3	

Rev0.7	Initial release	2008/10/22
Rev0.71	Remove hotkey switch(R2, C427, SW6, OR25)	2008/10/22
	Remove White LED (LED4, R86, Q17, SB_GPIO7)	
	Add 40 pin FPC connector	
	Change netname from BT_LED to Wireless_LED	
	Change NMOS from 2N7002 to UM6K1N(Q25, 23, Q18, Q15, Q58, Q19, Q16, Q19)	
	Change 3G power name to +3VS_3GA	
	Add dual-core support (not ready)	
Rev0.72	Change the part for LAN connector	2008/10/23
	Change the SPEAKER connector (same as FAN)	
Rev0.73	Remove CC36-40, CC45, CC24-25, CC16-17 for layout space	2008/10/27
	Remove HC32, 33 for Layout space	
	Remove HC14-16, HC125 (stuff HC126) for Layout space	
	Remove C422, 212, 232 (stuff C223) for Layout space	
	Remove C436, 292, 305 for Layout space	
	Change U47 to /X and sutff R265	
	Remove MC5, 6, 7, 9, 15, 17, 22, 24, 26, 27, 33 for Layout space	
	Remove WIFI & 3G RSV component	

1008HA

		Title : History	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	51 of 49