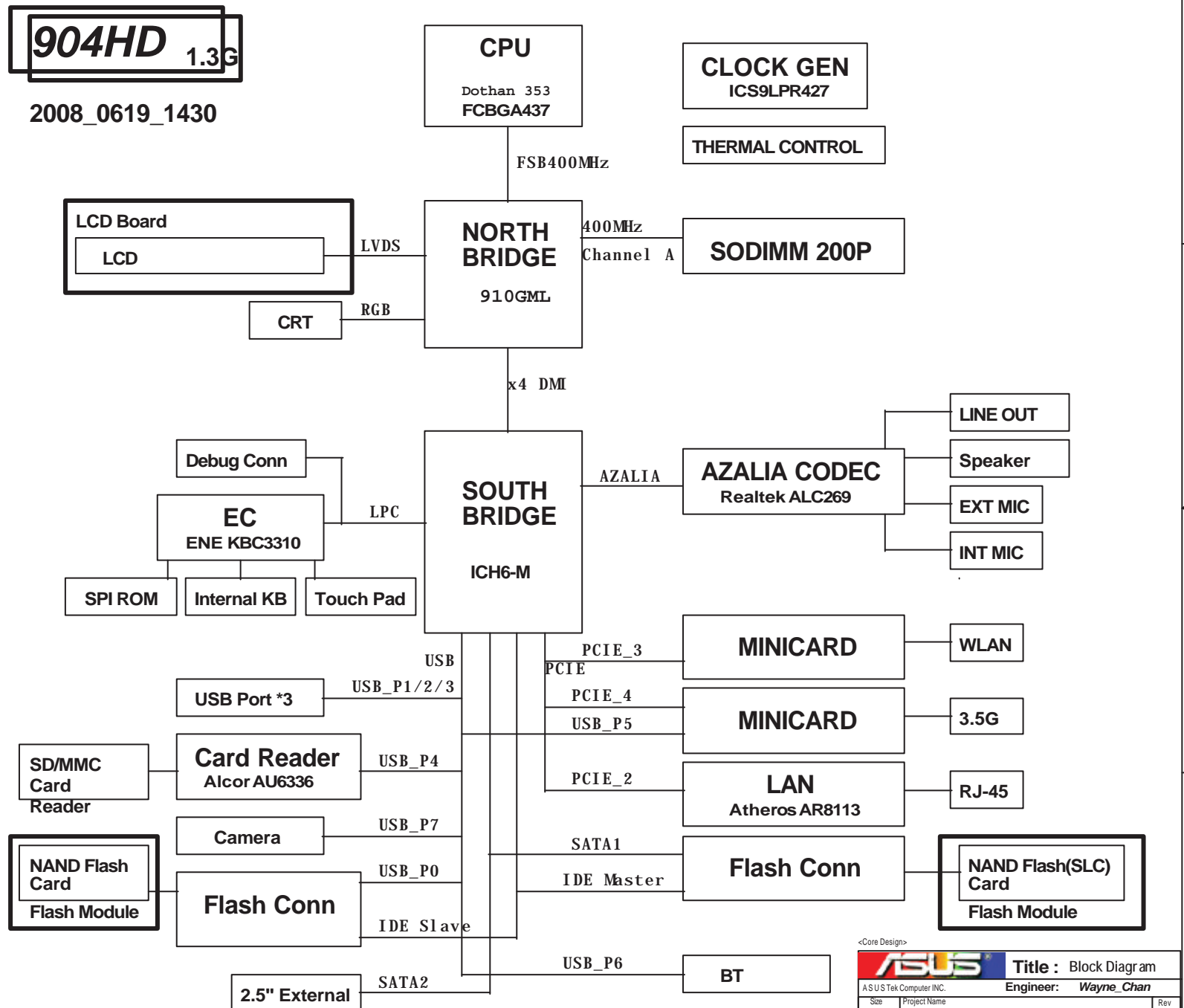


01_Block Diagram
 02_System Setting
 03_Power Sequence
 04_EC Pin Define
 05_History
 06_*
 07_Clock Gen_ICS9LPR427
 08_Dothan_HOST
 09_Dothan_PWR_GND
 10_NB-910GML_HOST_DMI
 11_NB-910GML_DRAM
 12_NB-910GML_VGA_LVDS_TV
 13_NB-910GML_PWR
 14_NB-910GML_GND
 15_SB-ICH6-M_Azalia_GPIO_PCI_LAN
 16_SB-ICH6-M_USB_PCIE_DMI_IDE_SATA
 17_SB-ICH6-M_PWR_GND
 18_DDR2 SODIMM
 19_DDR2 Termination
 20_Onboard VGA
 21_LCD Conn_LID
 22_PCIE 3.5G & Ext. Antenna
 23_Mini WIFI+ BT
 24_LAN_Atheros AR8113
 25_MDC_RJ45
 26_Flash Conn
 27_SATA HDD
 28_USB Port
 29_Camera Power
 30_Card Reader_AU6336C52
 31_Codec_ALC269
 32_Audio_AMP_Jack
 33_EC_ENE KB3310
 34_EC_UART controller
 35_Switch_SPI ROM_Debug Conn
 36_Thermal Sensor_FAN
 37_KB_Touch Pad
 38_LED
 39_Discharge
 40_PWR Jack
 41_Screw Hole
 42_EMI
 43_POWER FLOW
 44_Vcore
 45_Power System
 46_Power_+1.8V & VTTDDR
 47_Power_VCCP
 48_Power_+1.5VS & +2.5VS
 49_Power_Charger



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<Core Design>		Title : Block Diagram	
ASUS		Engineer: Wayne Chan	
Size	Project Name	Rev	
A3	904HD	1.3G	
Date: Tuesday, July 01, 2008	Sheet	1	of 49

EEE PC 1000HD PCB version

GPI29	GPI31	PCB version
0	0	1.0G
0	1	
1	0	
1	1	

USB

USB0	Flash Conn
USB1	USB Conn
USB2	USB Conn
USB3	USB Conn
USB4	Card Reader
USB5	Minicard
USB6	BT
USB7	Camera


PCIE

PCIE1	NC
PCIE2	LAN
PCIE3	Minicard
PCIE4	Minicard

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

<Core Design>

		Title : System Setting	
ASUS Tek Computer INC.		Engineer: Wayne_Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008		Sheet	2 of 49

EC KB3310 GPIO SETTING

Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	HOTKEY_SW0#	I	Internal pullhigh
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	HOTKEY_SW1#	I	Internal pullhigh
15	GPIO08	EXTSM#	OD	10K pull high to +3VSB
16	GPIO0A	LID_EC#	I	LidOff, 1-LidOn. Internal PullUp
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	HOTKEY_SW2#	I	Internal pullhigh
20	GPIO0E/SC#	EXT_SC#	O	10K pull high to +3VSB
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BATSEL_4P#	I	battery charging current setting
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debugport
31	GPIO17/E51_RX	E51_RX	I	RS232 debugport
32	GPIO18	PWR_SW#	I	power button, internal pullup
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	CHG_LED_GREEN#	O	Green LED for charging
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pullhigh
56	GPIO31/KSI1	KSI1	I	Internal pullhigh
57	GPIO32/KSI2	KSI2	I	Internal pullhigh
58	GPIO33/KSI3	KSI3	I	Internal pullhigh
59	GPIO34/KSI4	KSI4	I	Internal pullhigh
60	GPIO35/KSI5	KSI5	I	Internal pullhigh
61	GPIO36/KSI6	KSI6	I	Internal pullhigh
62	GPIO37/KSI7	KSI7	I	Internal pullhigh
63	GPI38/AD0	BAT_I_CHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configuration
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/OD	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPI_SO	I	
120	WR#/SPIDO	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSERVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery LowLow
73	GPIO40	AC_OK	I	AC Adaptor PlugIn
74	GPIO41	EC_RSMRST#	O	10K pull down to GND
75	GP42	BAT_IN	I	
76	GP43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/OD	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/OD	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/OD	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/OD	10K pull high to +3V
81	GPIO48/KSO16	NC	I	for KB type detection
82	GPIO49/KSO17	NC	I	for KB type detection
83	GPIO4A/PSCLK1	NC	O	LCD_SCL
84	GPIO4B/PSDAT1	NC	O	LCD_SDA
85	GPIO4C/PSCLK2	NC	O	LCD_CSB
86	GPIO4D/PSDAT2	NC	O	LCD_VSYNC
87	GPIO4E/PSCLK3	TP_CLK	I/OD	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/OD	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCRLED	O	
95	GPIO56	HOTKEY_SW3#	I	Internal pullhigh
97	GPXOA00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPXOA01/SDICLK	SUSC_ON	O	
99	GPXOA02/SDIDO	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	EC_PWROK	O	
103	GPXOA06	PM_LEVELDOWN#	O	
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P, 3P
110	GPXID1	CPU_LEVELDOWN#	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	PM_SUSB#	I	100K pull down to GND
115	GPXID4	PM_SUSC#	I	100K pull down to GND
116	GPXID5	VRM_PWRGD	I	Pull high to +3V
117	GPXID6	VSUS_PWRGD	I	
118	GPXID7	BATSEL_LIFe	O	
121	GPIO57	INTERNET#	I	Internal pullhigh
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	Internal pullhigh

1.0G From 1000H 2008.3.31.2030 circuit

- 1.Change CPU to Dothan
- 2.Change NB to 910GML
- 3.Change SB to ICH6-M
- 4.VCORE control change to ISL6218CRZ

1.1G

- 1.Change Project name to 1000D
- 2.Support LiFe Battery
- 3.Add speaker connect

1.2G

- 1.Change Project name to 1000HD
- 2.Modify page 45.
- 3.Add and reserved CE1 CE5 CE6 for USB port
- 4.Modify schematic of LED
- 5.Remove Capacitor of Microphone from clock and data signal
- 6.Add page33 Hotkey de-bounced related schematic
- 7.Add page23 PERST#pull down 1M ohm

1.3G


- 1.Change net BAT_TS pull up to +3VA_AEC
- 2.Change SD1.2 pull up to +3VA_AEC
- 3.Add and reserved R287,R288,R289,R290 for USB power
- 4.Add PR670,PR671
- 5.Add PR483,PR42, PR230, PR231,remove PJP400

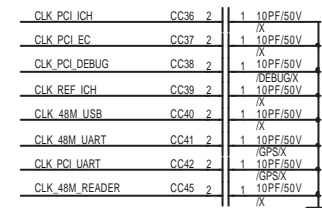
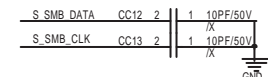
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		Title : History	
ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD		Rev 1.3G
Date: Tuesday, July 01, 2008		Sheet	5 of 49

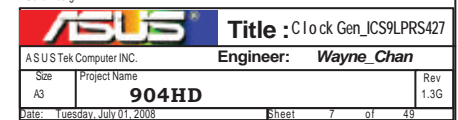


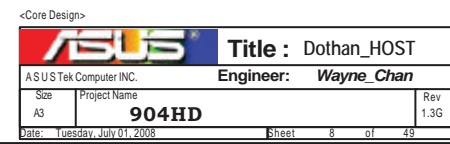
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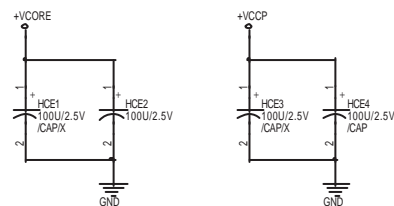
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ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD		Rev 1.3G
Date: Tuesday, July 01, 2008		Sheet	6 of 49



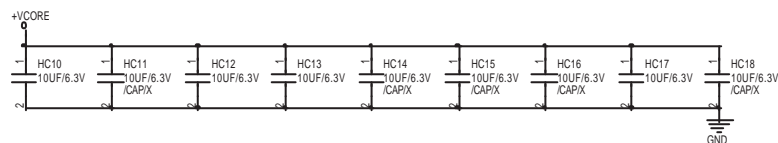
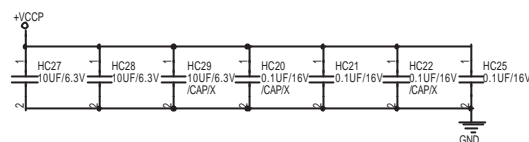
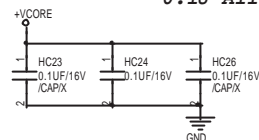
The schematic diagram illustrates the CLK pin connections for the CR13-240 components. The +3V_CLK supply is connected to a network of resistors and components. On the left, CLK_PEREQ#1, CLK_PEREQ#3, CLK_FSLB, CLK_FSLC, CLK_FS4, and CLK_PEREQ#2 are connected to the network. On the right, CLK_ITP_EN, CLK_FSLA, and CLK_SEL_48# are connected. The network includes resistors of 10KOhm, 8.2KOhm, and 1KOhm, and components CR6, CR7, CR236, CR237, CR239, and CR240. A GND connection is also shown.

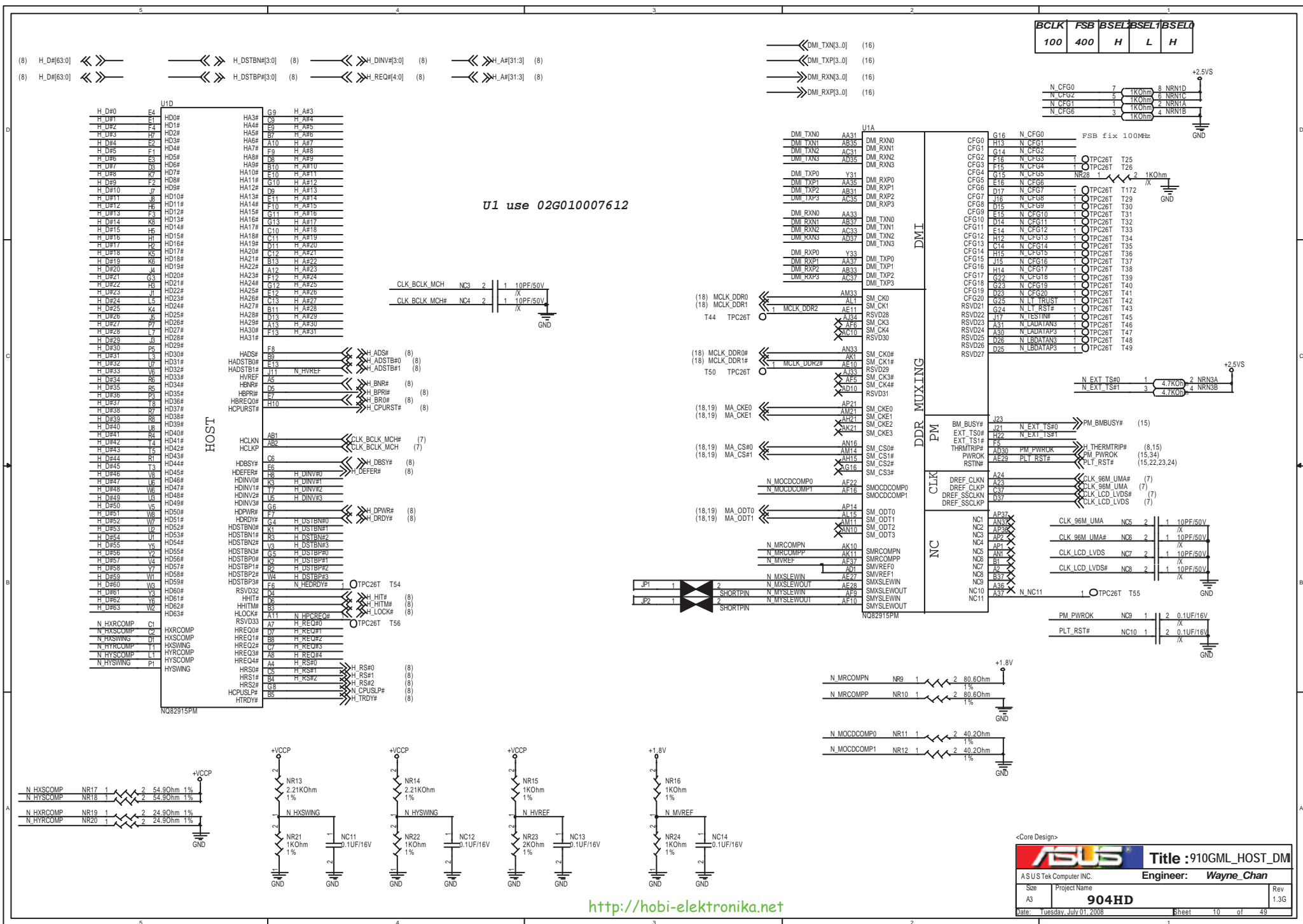


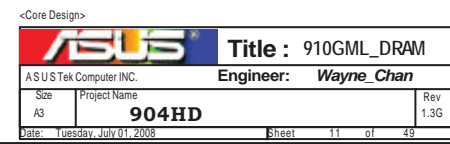


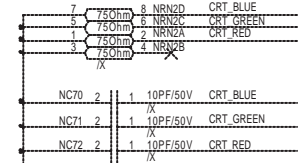
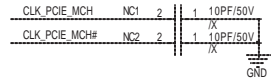


0.1U All X7R

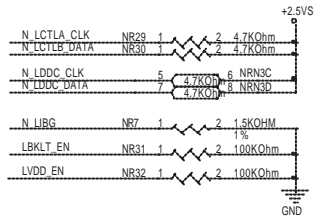






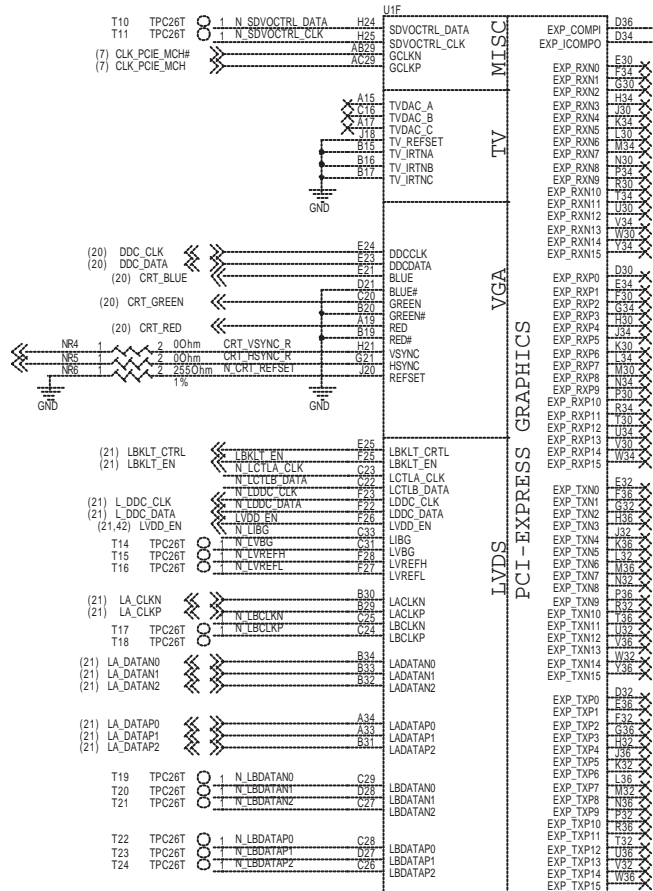


(20) CRT_VSYNC
(20) CRT_HSYNC

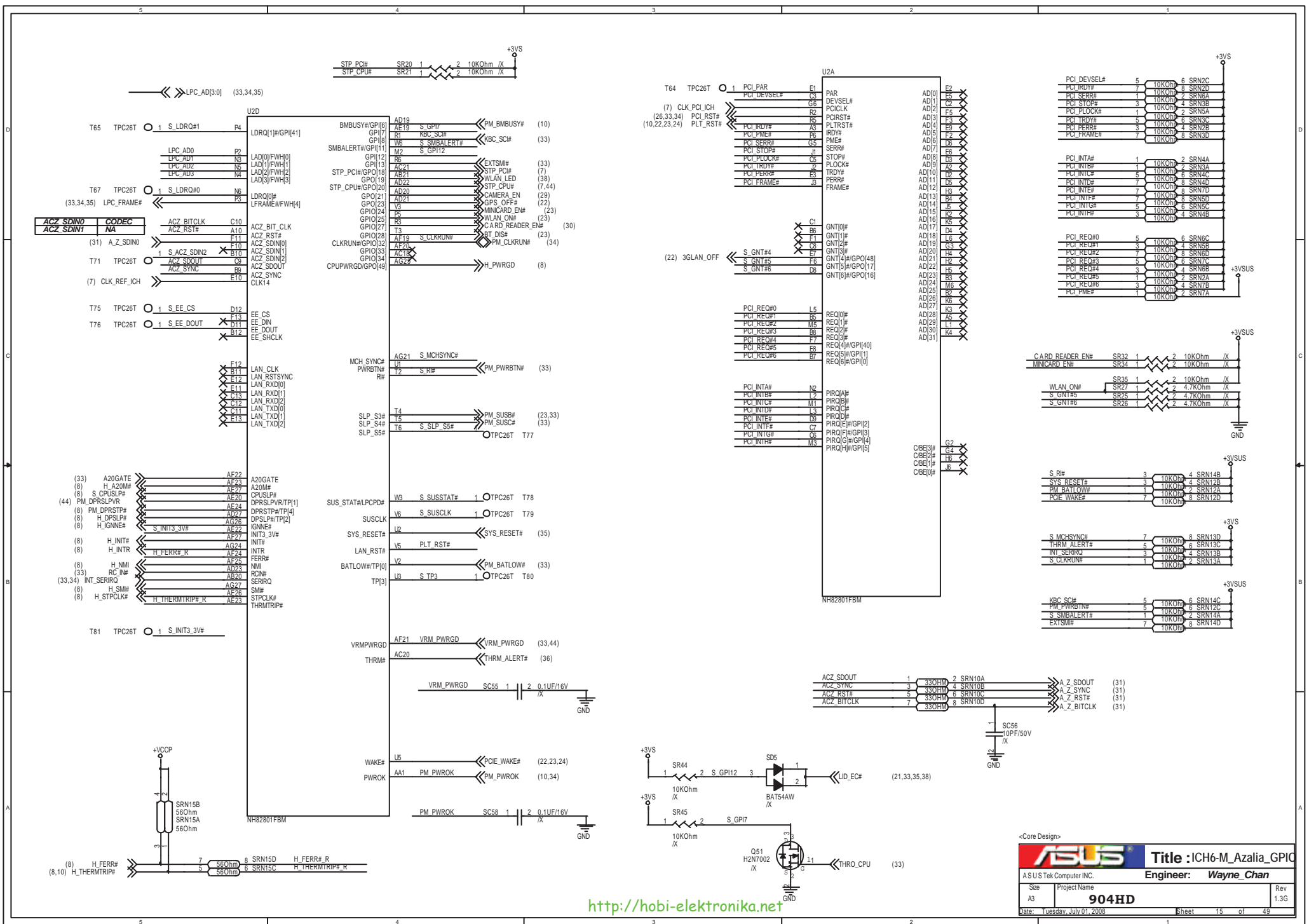


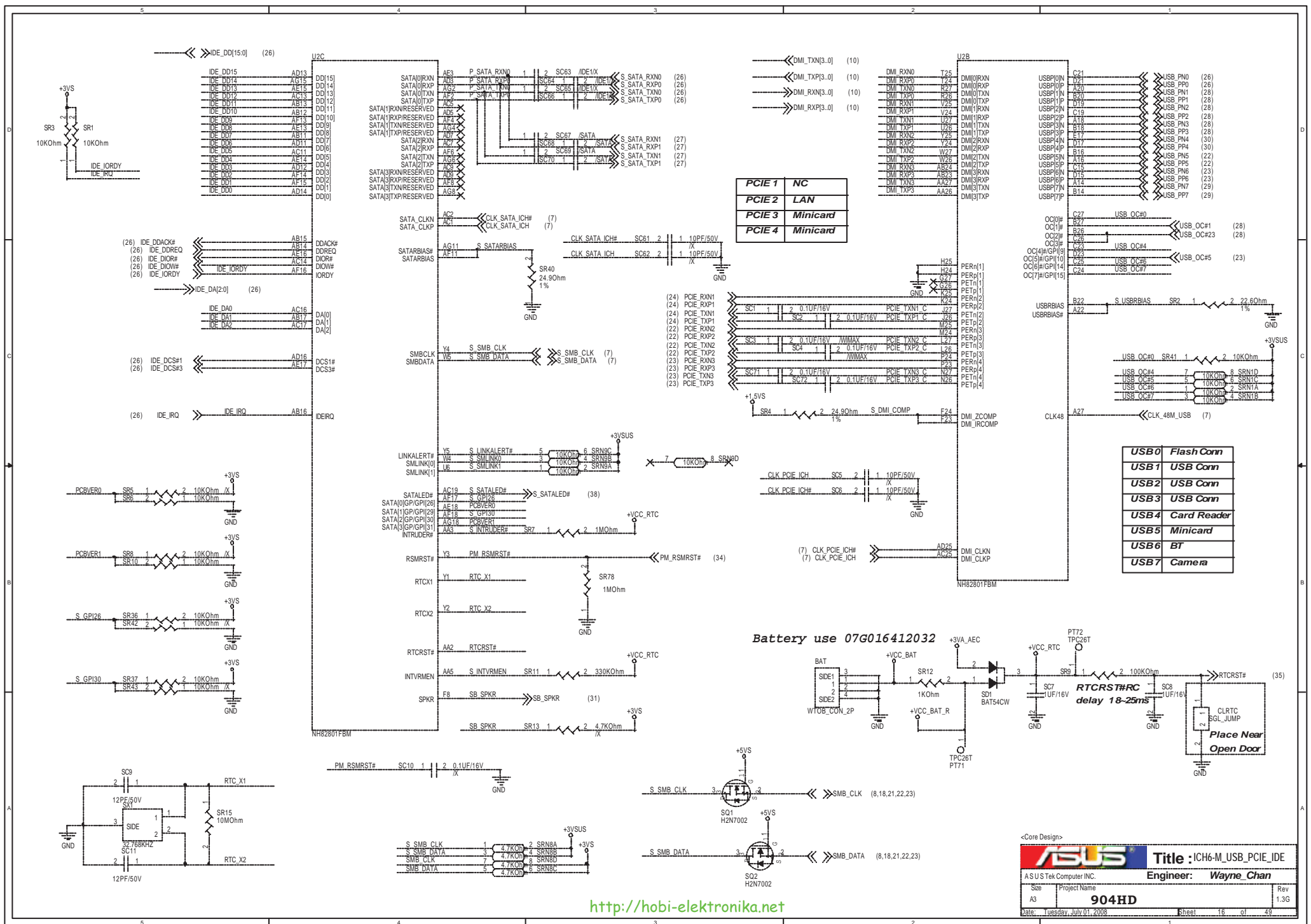
SDVO SMBus have internal pull down
SDVOCTRL_DATA Int PD
0 : No SDVO device
1 : SDVO device present

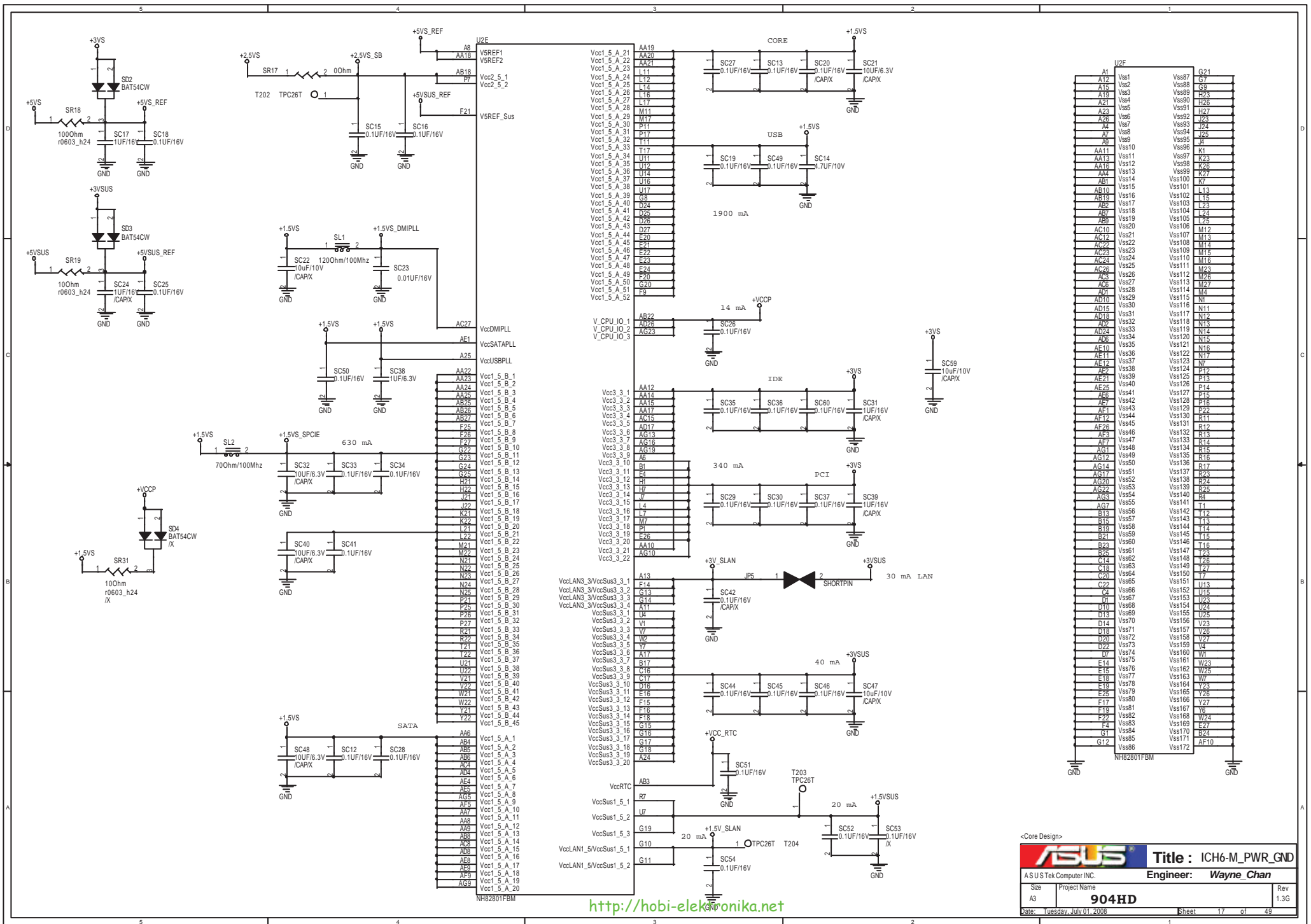
U1 use 02G010007612



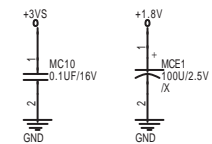
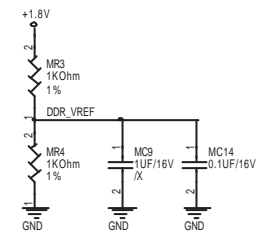
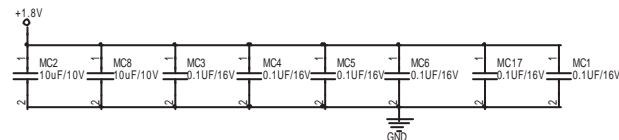
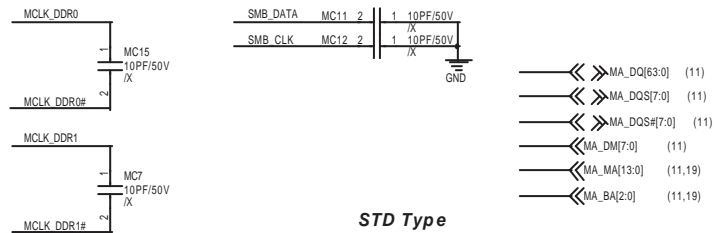
PCI-E signals can be left NC, if unused.







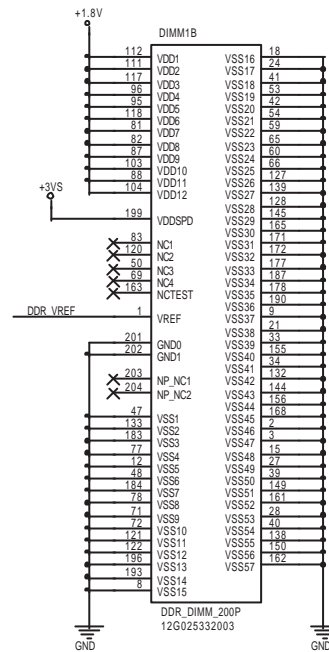
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DIMM1A			
MA_MA0	102	A0	DQ0
MA_MA1	101	A1	DQ1
MA_MA2	100	A2	DQ2
MA_MA3	99	A3	DQ3
MA_MA4	98	A4	DQ4
MA_MA5	97	A5	DQ5
MA_MA6	94	A6	DQ6
MA_MA7	92	A7	DQ7
MA_MA8	93	A8	DQ8
MA_MA9	91	A9	DQ9
MA_MA10	105	A10/AP	DQ10
MA_MA11	90	A11	DQ11
MA_MA12	89	A12	DQ12
MA_MA13	116	A13	DQ13
	86	A14	DQ14
	84	A15	DQ15
MA_BA2	85	A16, BA2	DQ16
			DQ17
MA_BA0	107	BA0	DQ18
MA_BA1	106	BA1	DQ19
	110	DQ#	DQ20
	115	S1#	DQ21
	30	CK0	DQ22
	32	CK0#	DQ23
	164	CK1	DQ24
	166	CK1#	DQ25
	79	CKE0	DQ26
	80	CKE1	DQ27
	113	CAS#	DQ28
	108	RAS#	DQ29
	109	WE#	DQ30
	139	SA0	DQ31
	200	SA1	DQ32
	197	SCL	DQ33
	195	SDA	DQ34
		DQ35	DQ35
	114	DQ36	DQ36
	119	ODT0	DQ37
		ODT1	DQ38
MA_DM0	10	DM0	DQ39
MA_DM1	26	DM1	DQ40
MA_DM2	52	DM2	DQ41
MA_DM3	67	DM3	DQ42
MA_DM4	130	DM4	DQ43
MA_DM5	147	DM5	DQ44
MA_DM6	170	DM6	DQ45
MA_DM7	185	DM7	DQ46
			DQ47
MA_DS0	13	DQ50	DQ48
MA_DS1	31	DQ51	DQ49
MA_DS2	51	DQ52	DQ50
MA_DS3	70	DQ53	DQ51
MA_DS4	131	DQ54	DQ52
MA_DS5	148	DQ55	DQ53
MA_DS6	169	DQ56	DQ54
MA_DS7	188	DQ57	DQ55
MA_DS#0	11	DQ58#0	DQ56
MA_DS#1	29	DQ58#1	DQ57
MA_DS#2	49	DQ58#2	DQ58
MA_DS#3	68	DQ58#3	DQ59
MA_DS#4	129	DQ58#4	DQ60
MA_DS#5	146	DQ58#5	DQ61
MA_DS#6	167	DQ58#6	DQ62
MA_DS#7	186	DQ58#7	DQ63

DDR_DIMM_200P
12G025332003

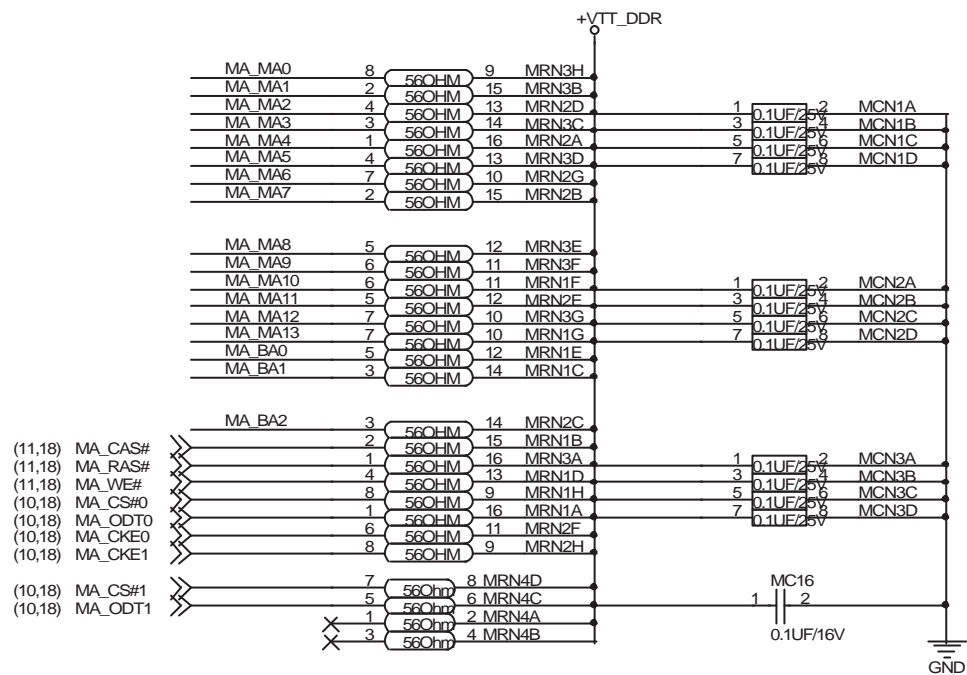
GROUP1
GROUP2
SWAP



<Core Design>

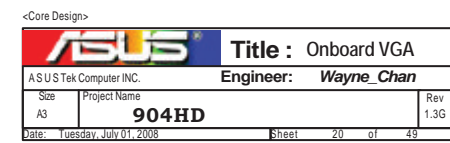
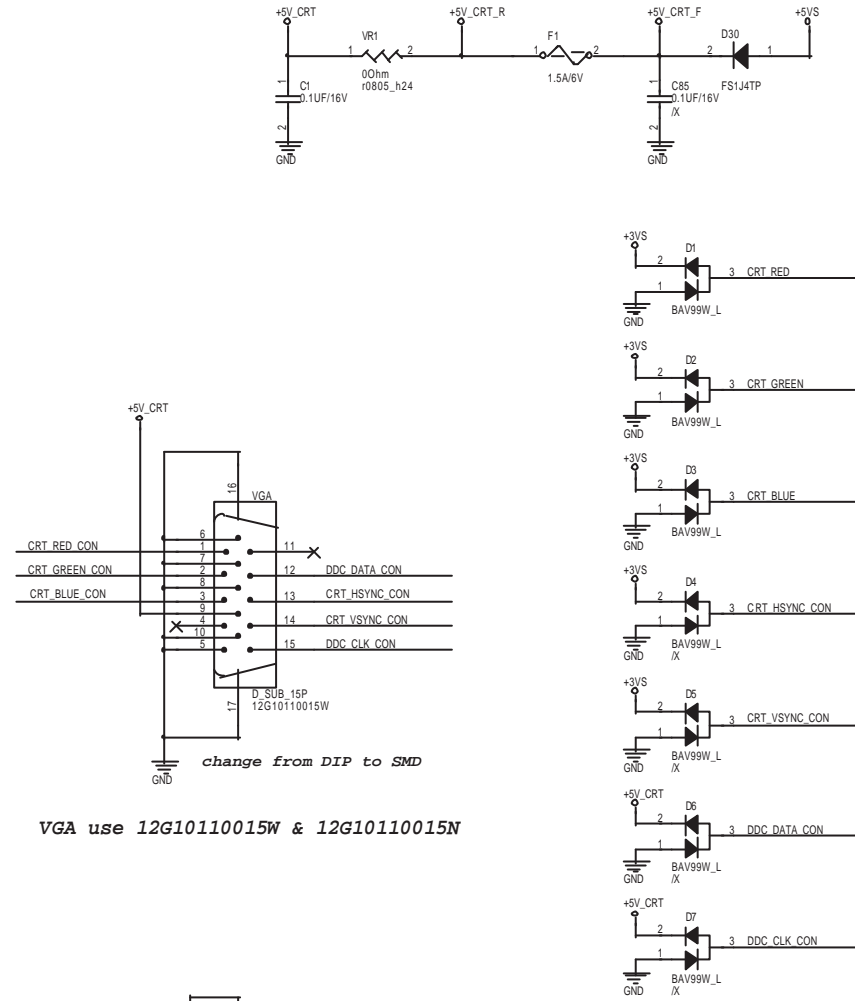
ASUS		Title : DDR2 SODIMM	
ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008	Sheet	18	of 49

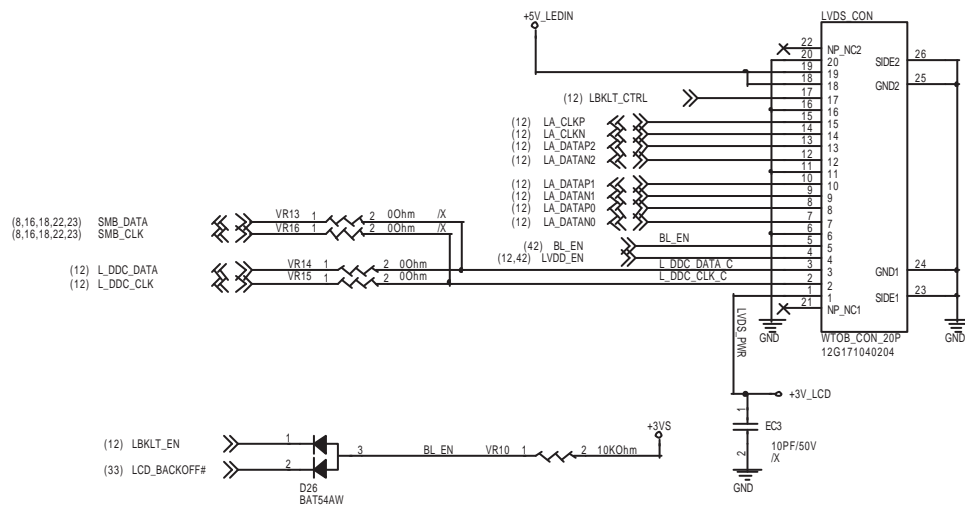
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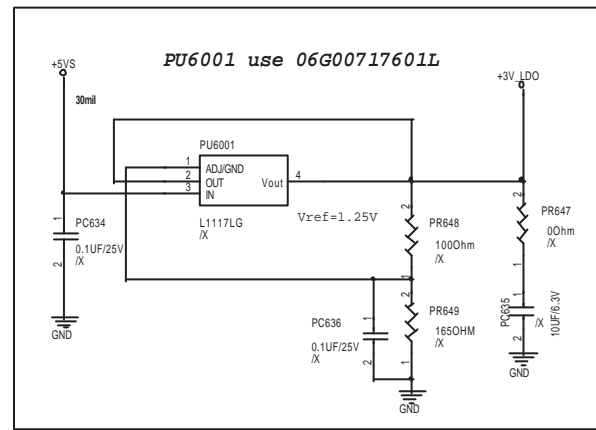
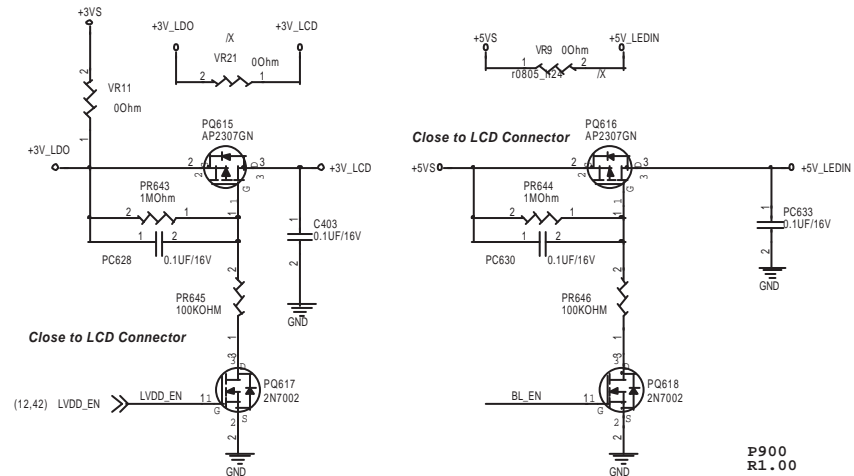
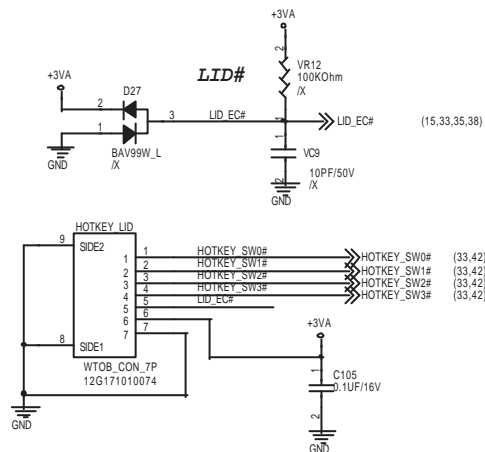
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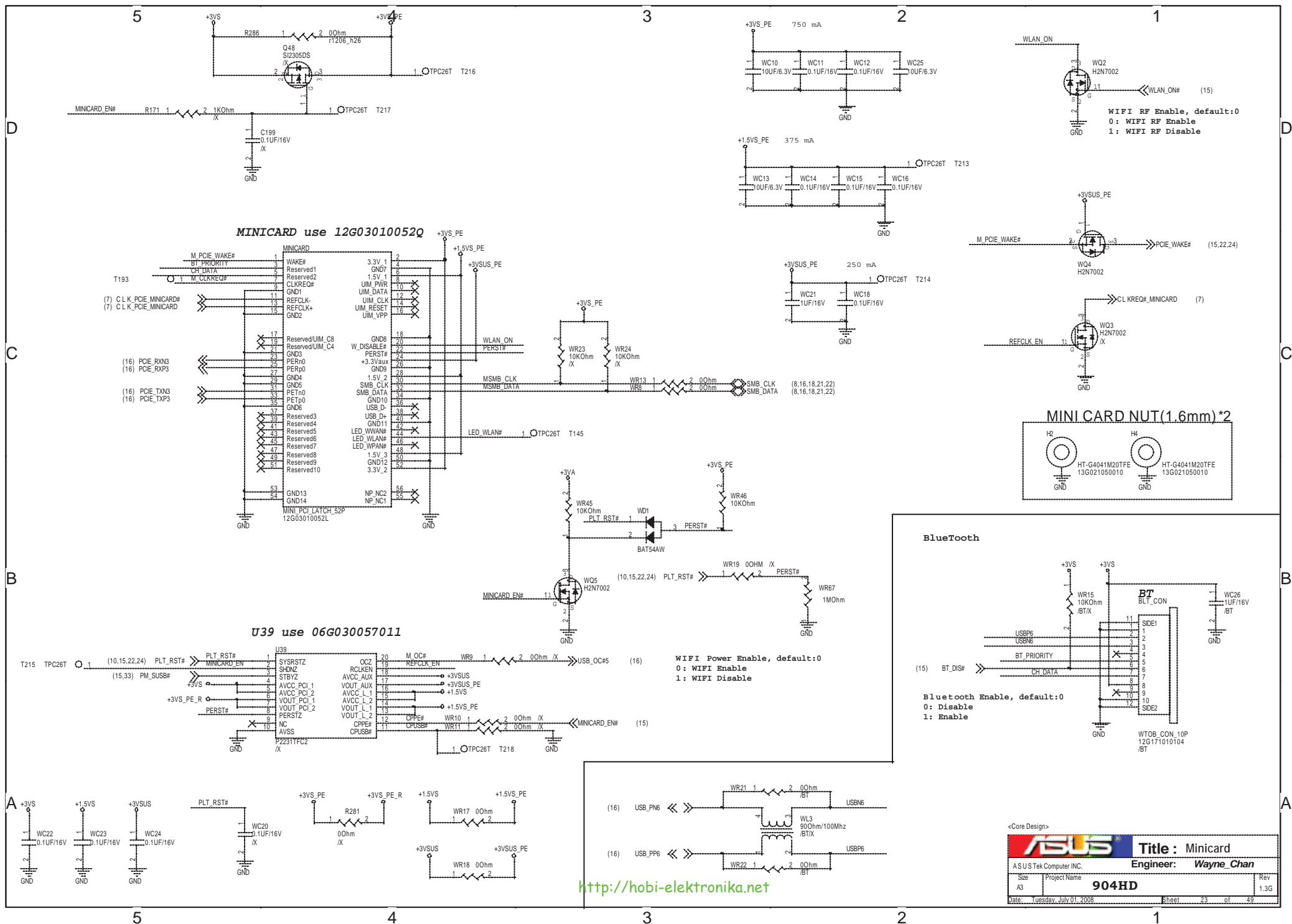
		Title : DDR2_Termination	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A4	Project Name 904HD		Rev 1.3G
Date: Tuesday, July 01, 2008		Sheet 19 of 49	





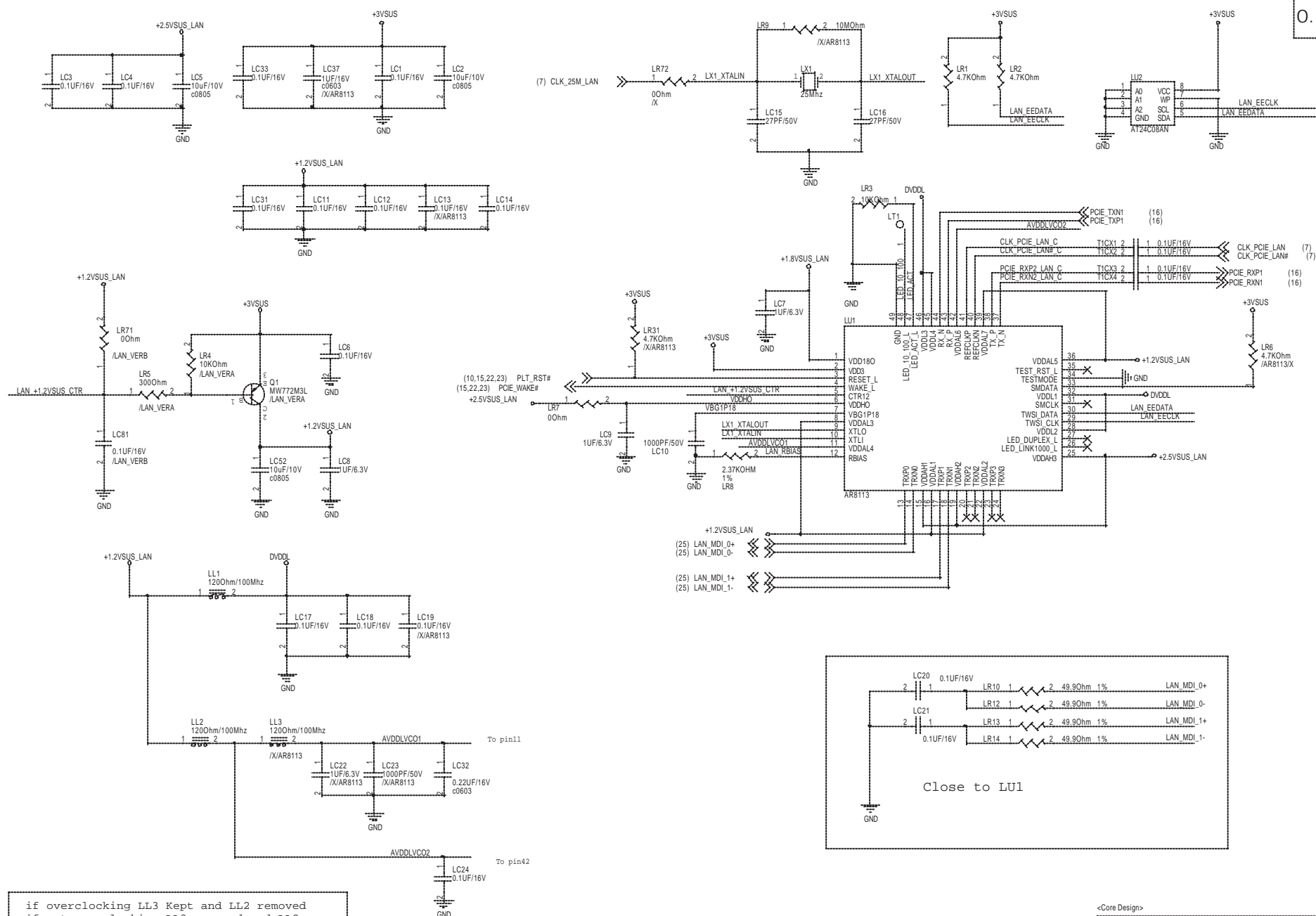
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L_DDC_DATA_C	EC2	2	1	10PF/50V
LA_CLKP	VC1	2	1	10PF/50V
LA_CLKN	VC2	2	1	10PF/50V
LA_DATAP2	VC3	2	1	10PF/50V
LA_DATAN2	VC4	2	1	10PF/50V
LA_DATAP1	VC5	2	1	10PF/50V
LA_DATAN1	VC6	2	1	10PF/50V
LA_DATAP0	VC7	2	1	10PF/50V
LA_DATAN0	VC8	2	1	10PF/50V





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0.1A Beta


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<Core Design>

ASUS

Title : AR8113

ASUS Tek Computer INC.

Engineer: Wayne Chan

Size

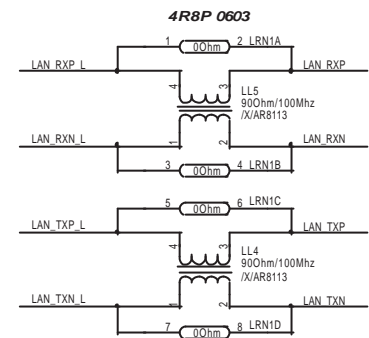
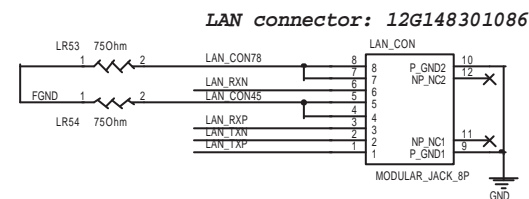
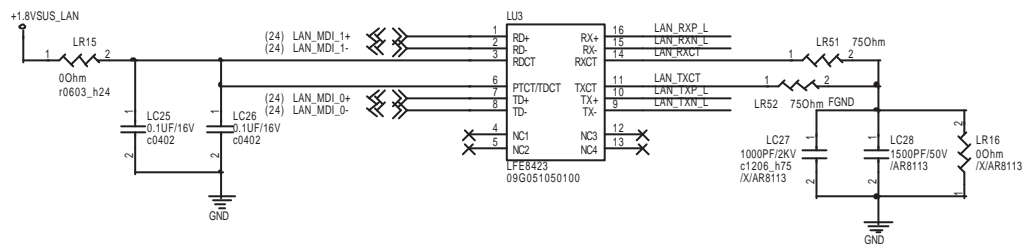
Project Name

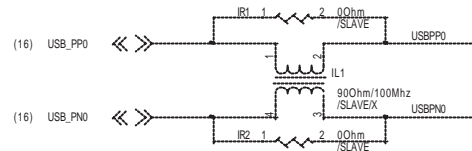
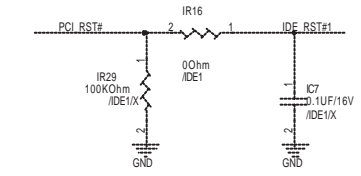
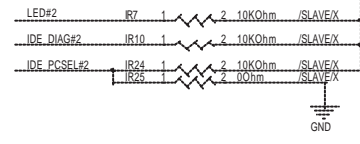
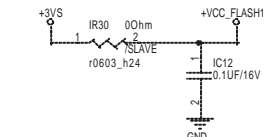
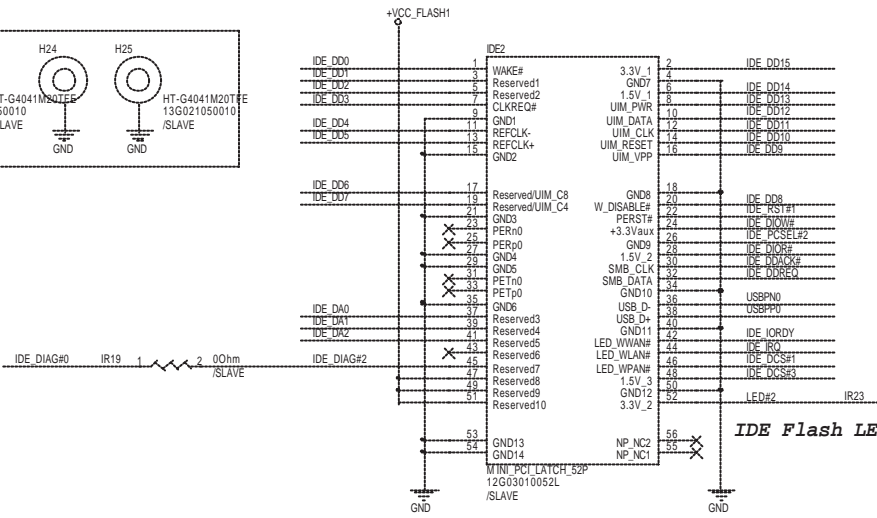
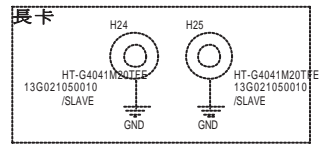
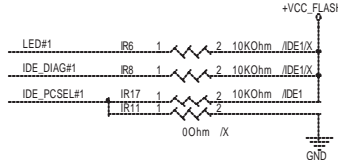
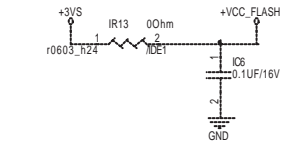
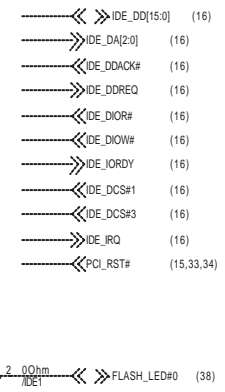
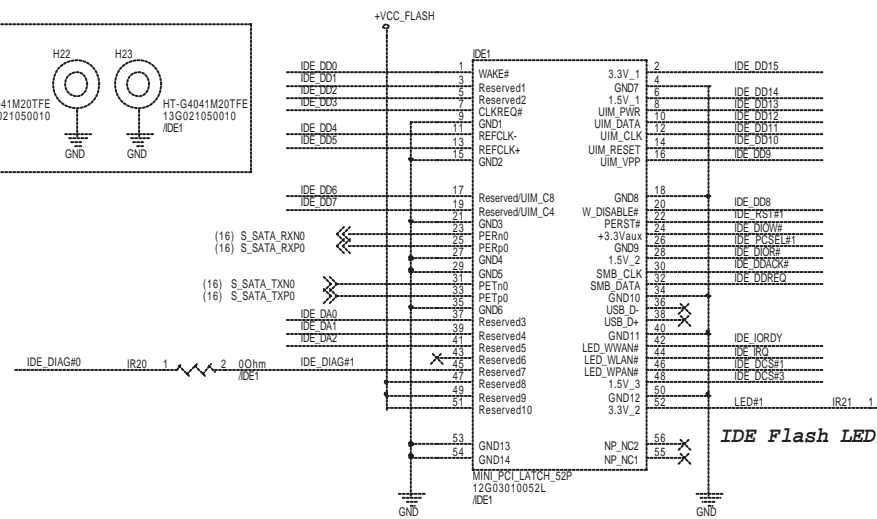
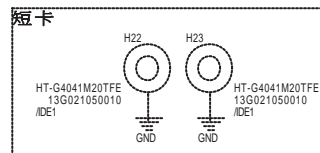
904HD

Rev

Date: Tuesday, July 01, 2008

Sheet 24 of 49

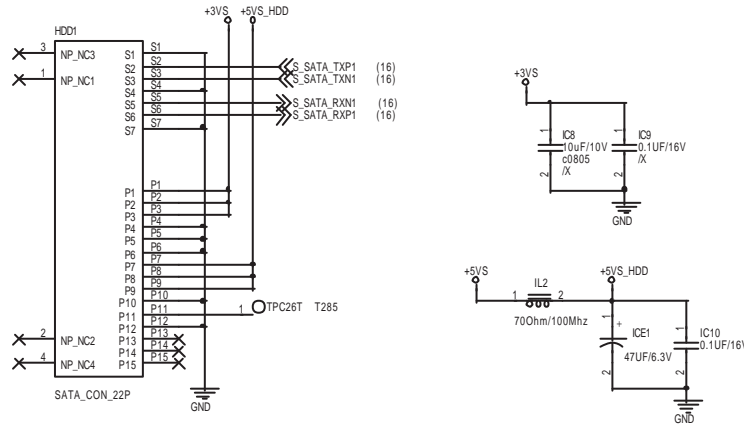




Naming Rule:
 IC: IUP?
 R: IR?
 C: IC?
 L: IL?

Change_ODD to
SATA IF

SATA HDD Connector

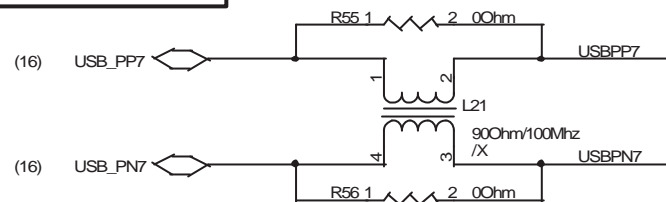
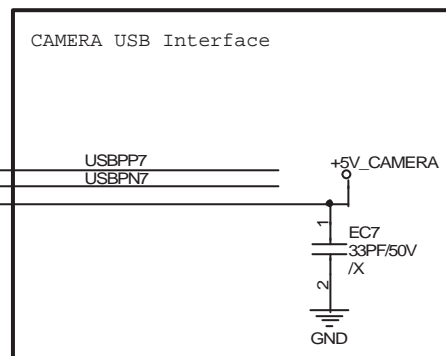
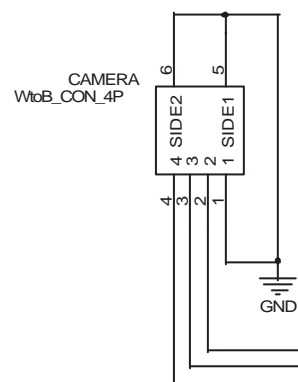
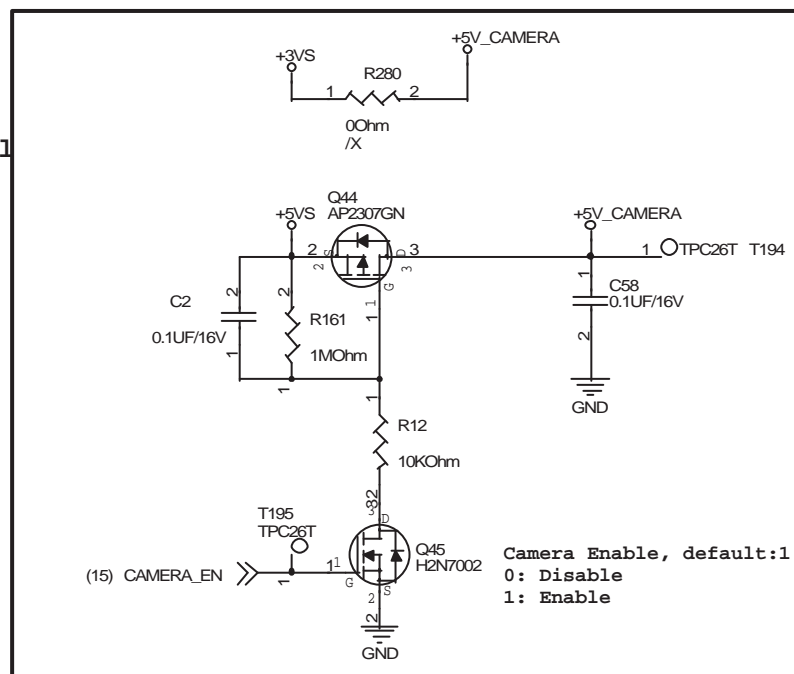


<http://hobi-elektronika.net>

<Core Design>

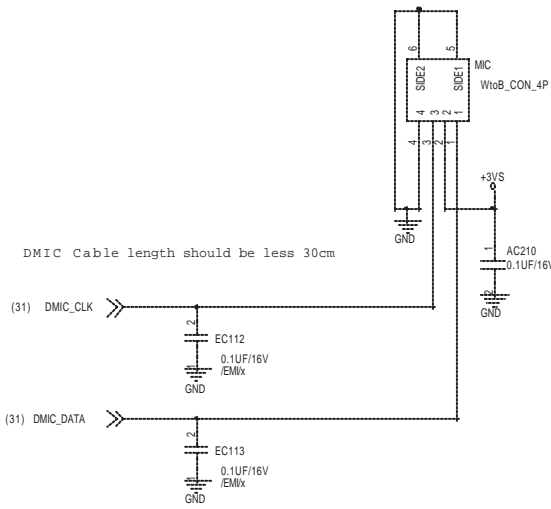
ASUS		Title : HD + Flash Conn	
ASUS Tek Computer INC.		Engineer: Wayne_Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008	Sheet	27	of 49

Power Control

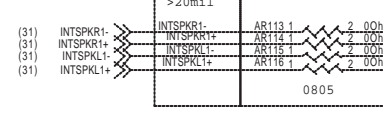


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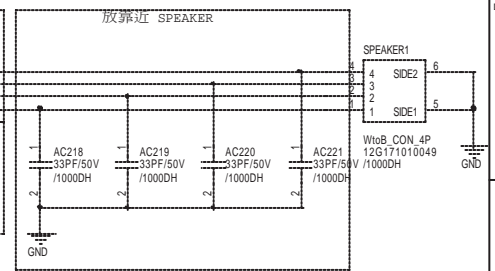
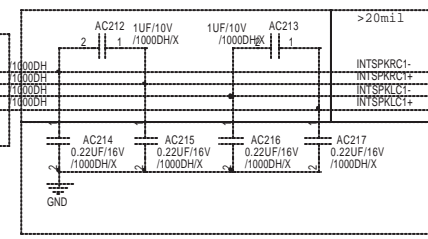
ASUS		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A4	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008		Sheet 29 of 49	



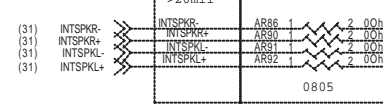
Total length from speakerR+- L+-(pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)



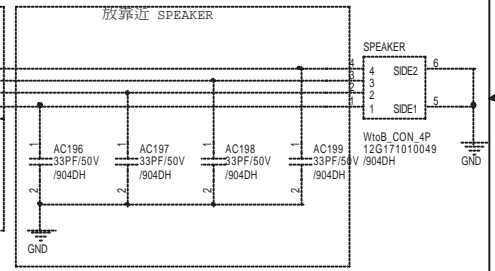
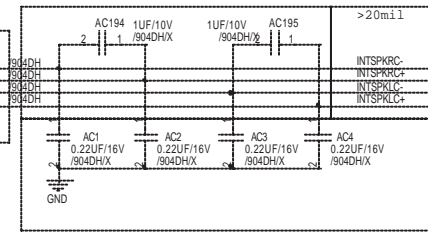
If chioke AR113, AR114, AR115, AR116 are mounted, please mount AC212 AC213 to avoid EMI issue.



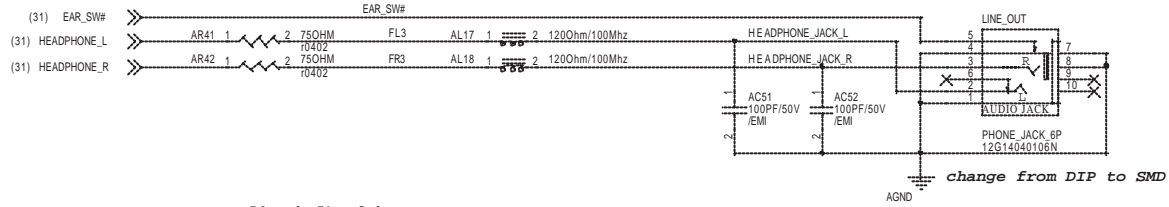
Total length from speakerR+- L+-(pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)



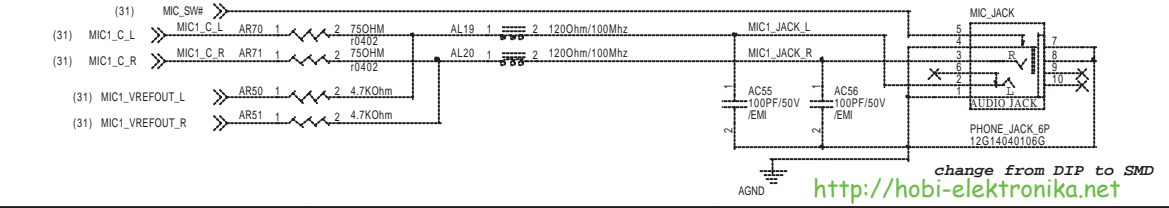
If chioke AR86, AR90, AR91, AR92 are mounted, please mount AC194 AC195 to avoid EMI issue.



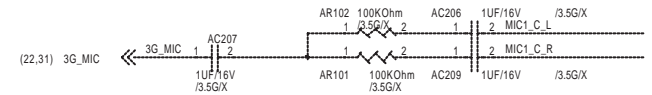
LINE_OUT use 12G140501060

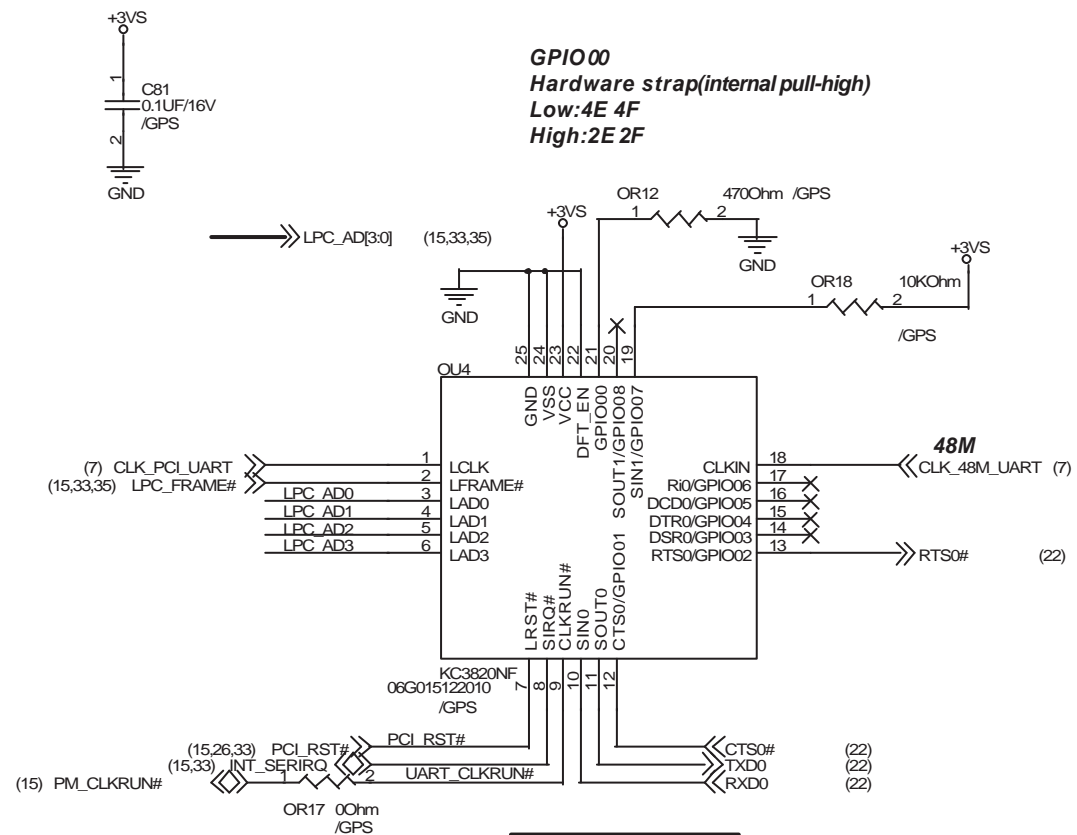
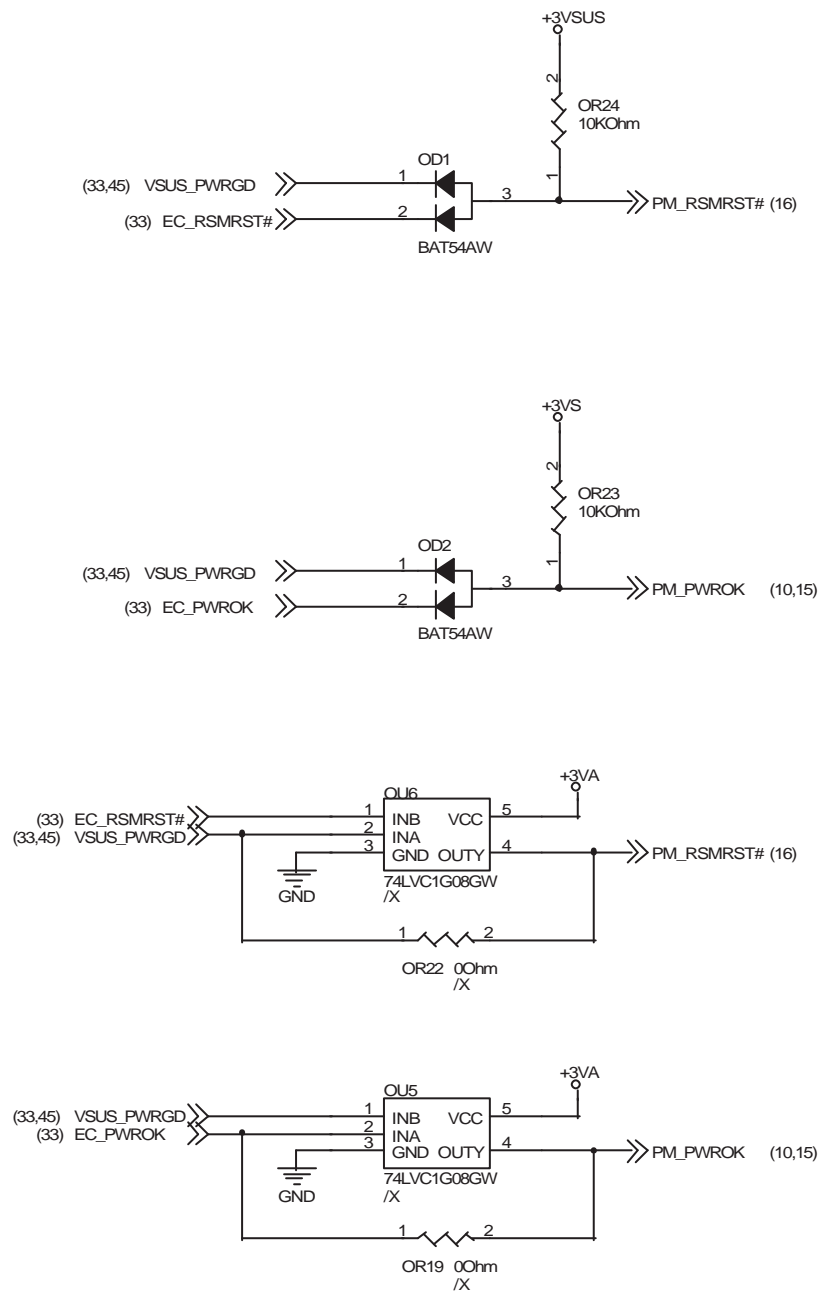


R70 and R71: If don't need retasking function, change to 1K.

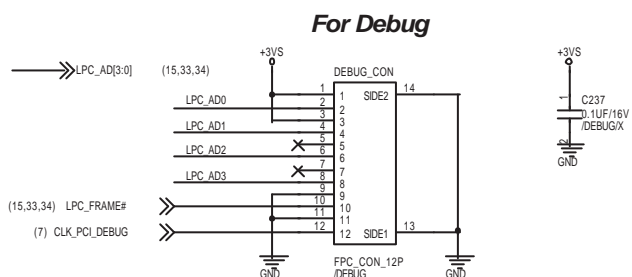
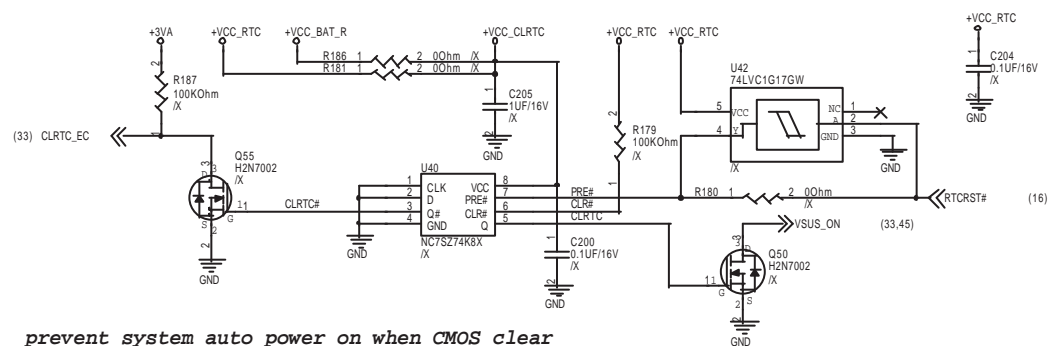
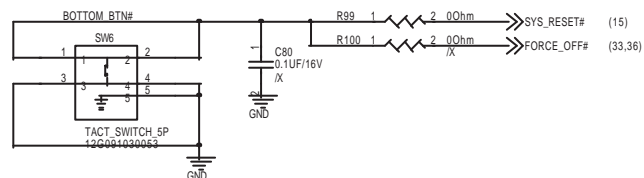
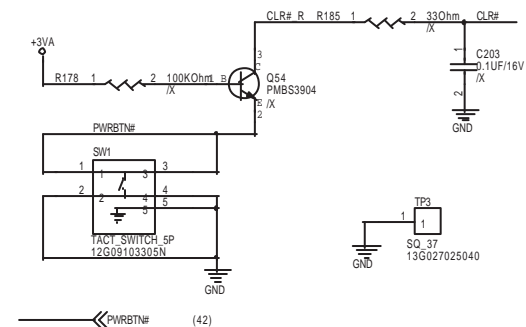
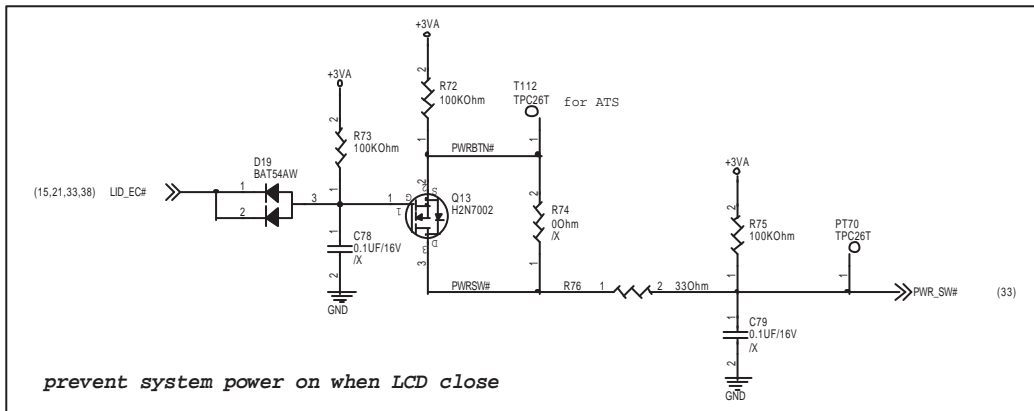


MIC JACK use 12G14040106Y

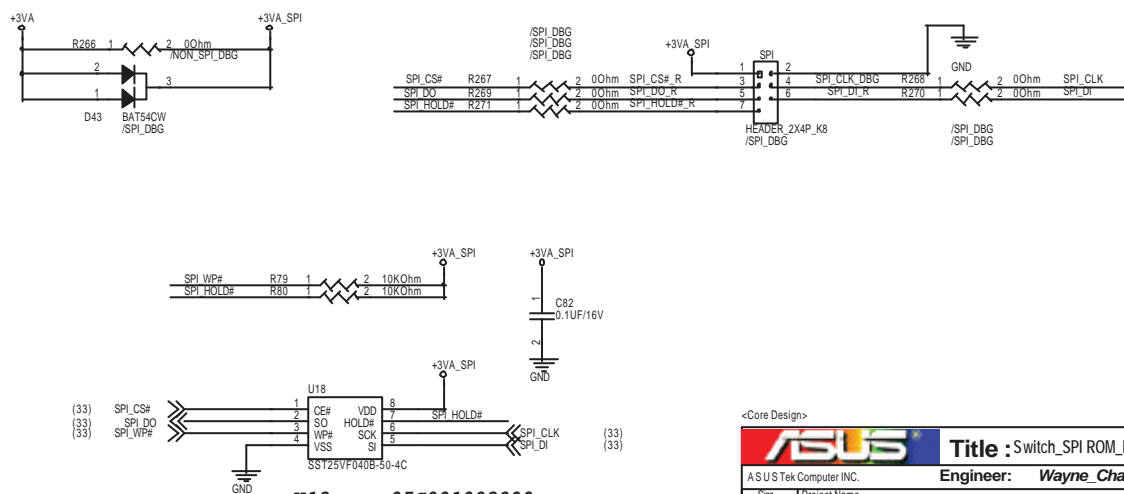




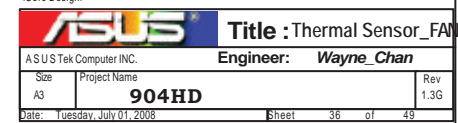
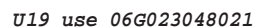
UART Control
IC for using
GPS module due
to no UART on
ENE EC



Debug Card cable use Z96 Touch Pad cable, P/N:
14G124110126, 14G124110120, 14G124110121
14G124110124, 14G124110125

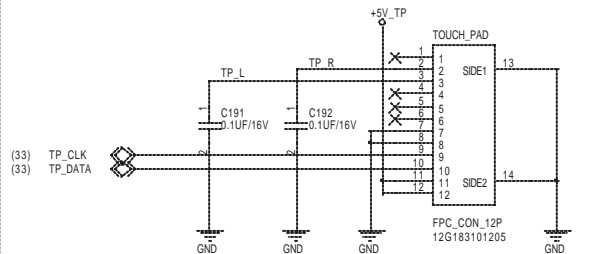


<http://hobi-elektronika.net>

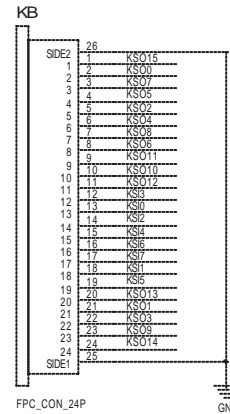


For Touch-Pad

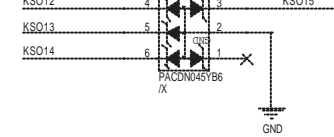
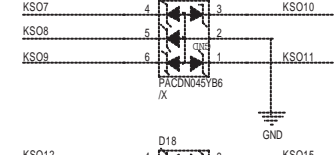
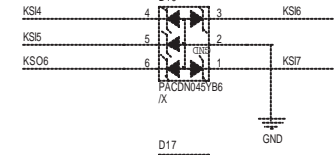
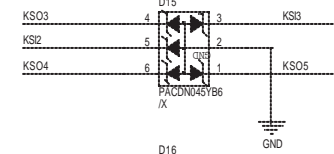
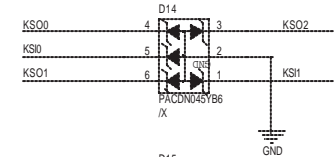
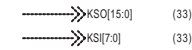
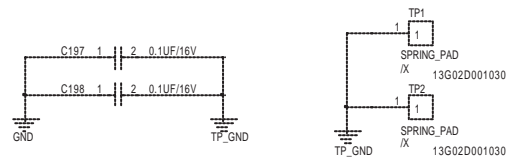
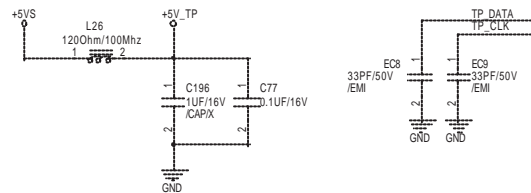
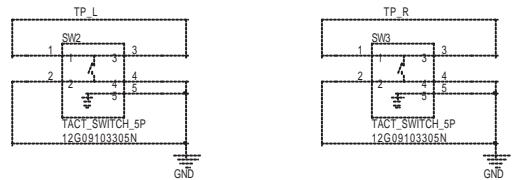
P900 R1.0G



For Keyboard Connector

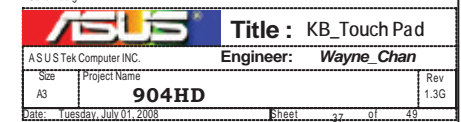


SW2, SW3 use 12G09103305N

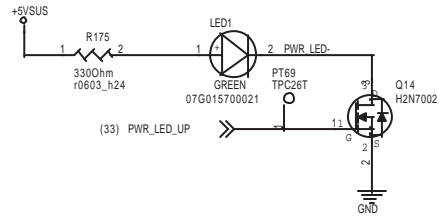


KS0	EC10	1	2	33PF/50V
KS1	EC11	1	2	33PF/50V
KS2	EC12	1	2	33PF/50V
KS3	EC13	1	2	33PF/50V
KS4	EC14	1	2	33PF/50V
KS5	EC15	1	2	33PF/50V
KS6	EC16	1	2	33PF/50V
KS7	EC17	1	2	33PF/50V
KS00	EC18	1	2	33PF/50V
KS01	EC19	1	2	33PF/50V
KS02	EC20	1	2	33PF/50V
KS03	EC21	1	2	33PF/50V
KS04	EC22	1	2	33PF/50V
KS05	EC23	1	2	33PF/50V
KS06	EC24	1	2	33PF/50V
KS07	EC25	1	2	33PF/50V
KS08	EC26	1	2	33PF/50V
KS09	EC27	1	2	33PF/50V
KS010	EC28	1	2	33PF/50V
KS011	EC29	1	2	33PF/50V
KS012	EC30	1	2	33PF/50V
KS013	EC31	1	2	33PF/50V
KS014	EC32	1	2	33PF/50V
KS015	EC33	1	2	33PF/50V

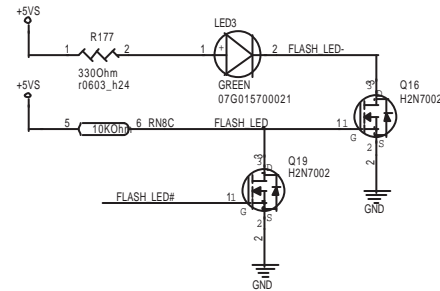
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for POWER LED

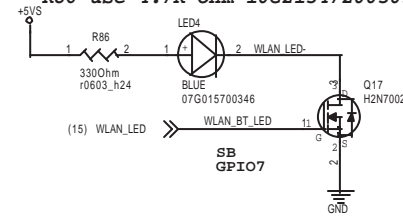


for FLASH LED



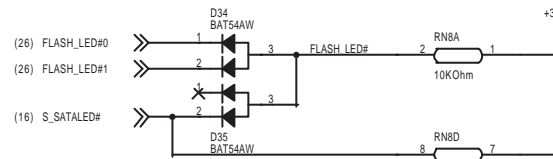
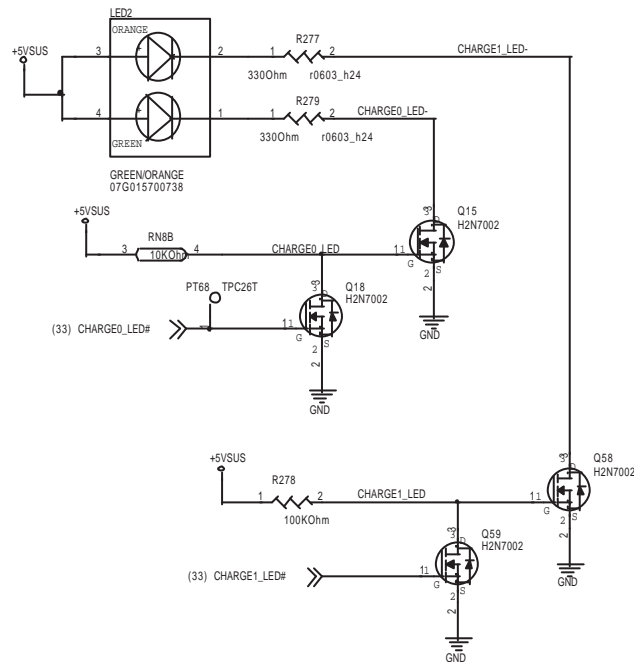
for WLAN/BlueTooth LED

R86 use 4.7K OHm 10G213472003030

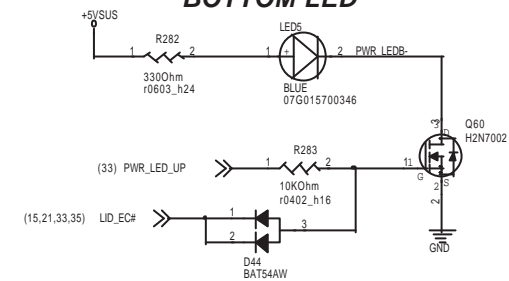


WIFI/BT LED Enable,default:1
0: WIFI and BT are both disabled
1: one of WIFI and BT is Enable or both are Enable

for CHARGE LED



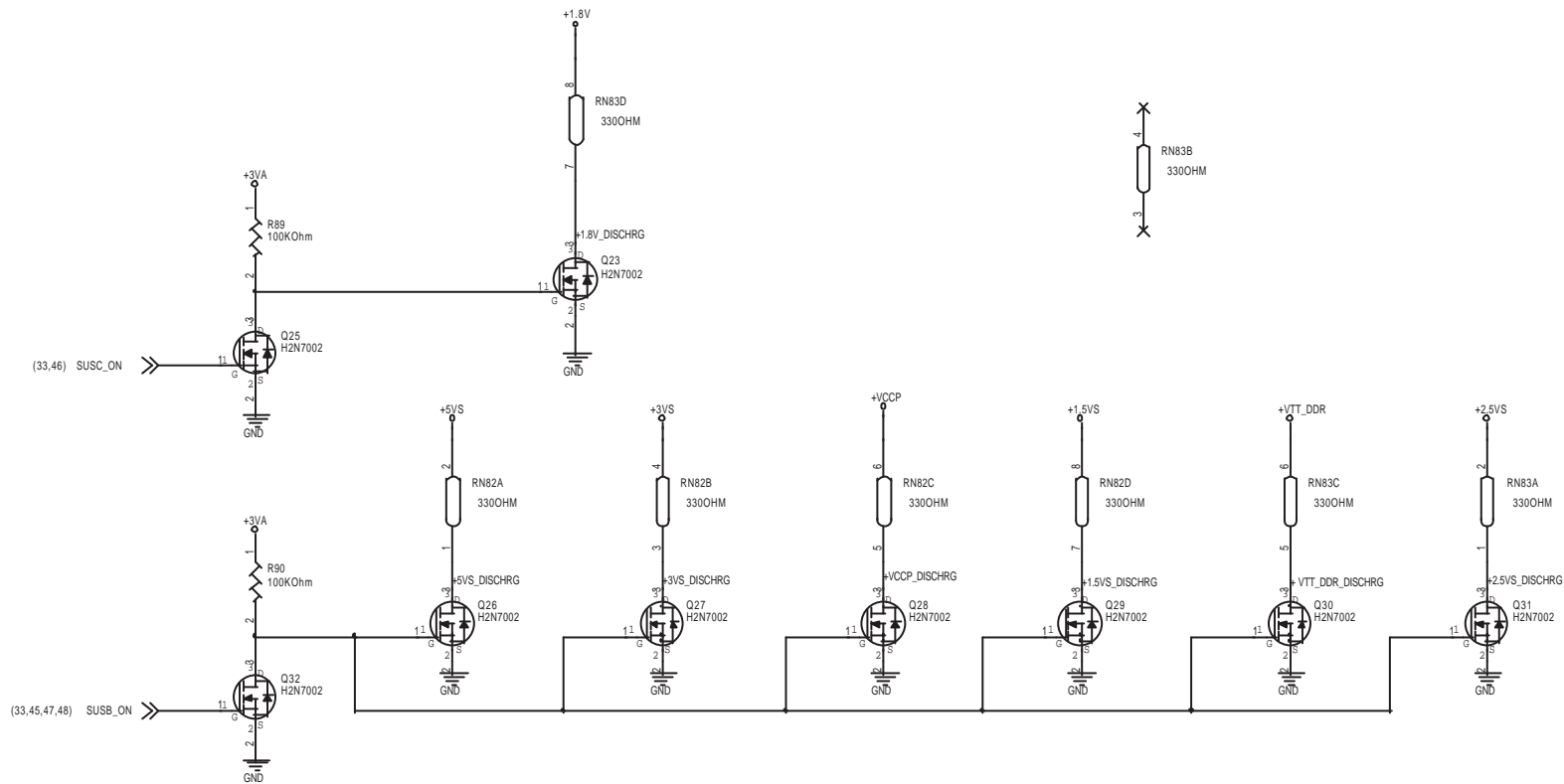
for POWER BOTTOM LED



<http://hobi-elektronika.net>

<Core Design>

ASUS		Title : LED	
ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008	Sheet	38	of 49

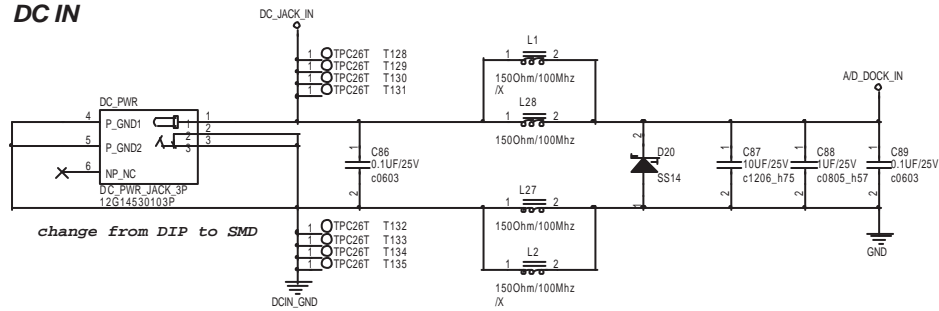


<http://hobi-elektronika.net>

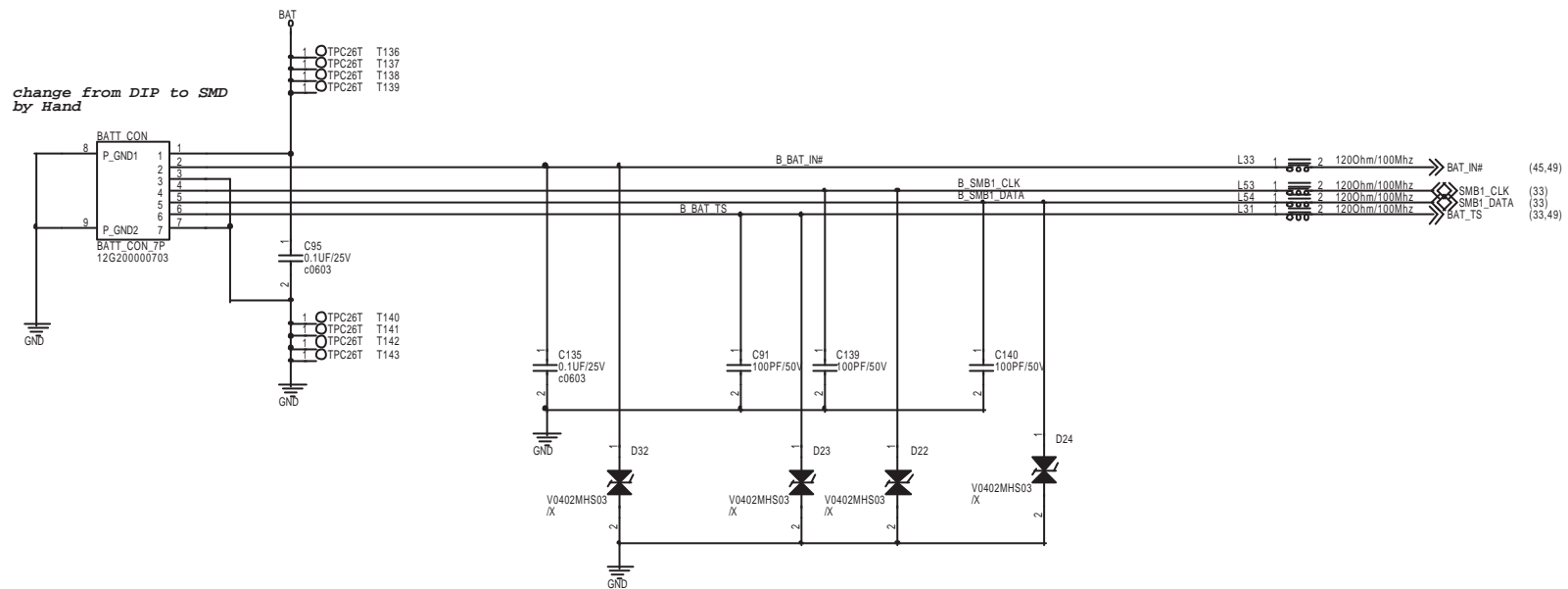
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ASUS		Title : Discharge	
ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size	Project Name	Rev	
A3	904HD	1.3G	
Date: Tuesday, July 01, 2008	Sheet	39	of 49

DC IN

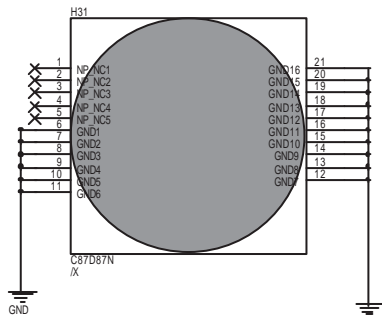
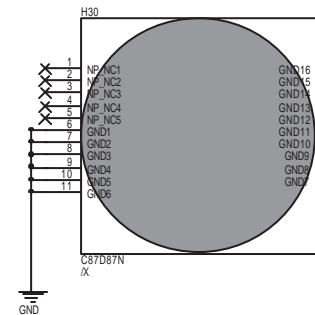
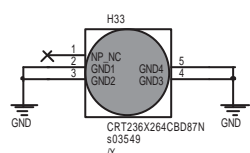
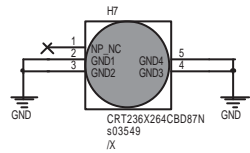
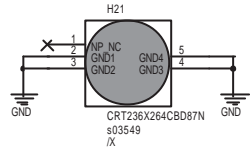
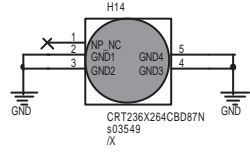
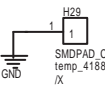
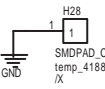
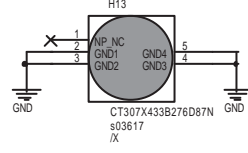
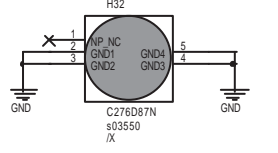
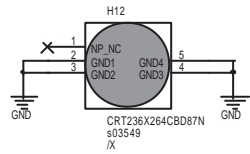
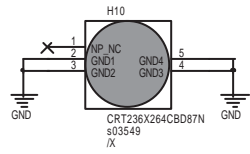
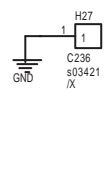
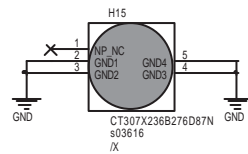
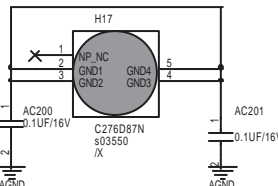
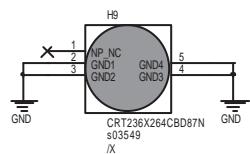
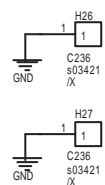
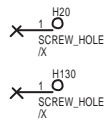
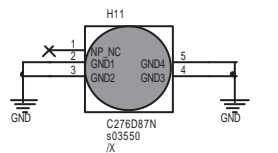
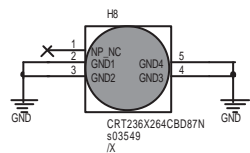


BAT IN



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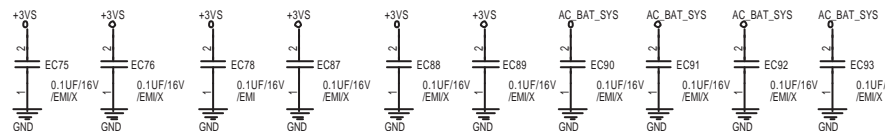
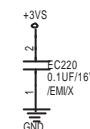
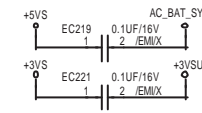
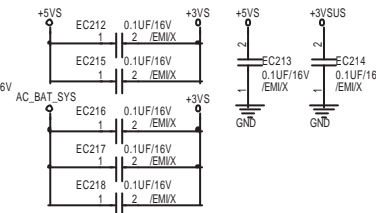
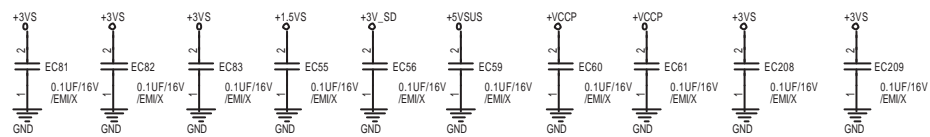
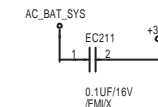
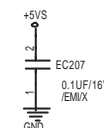
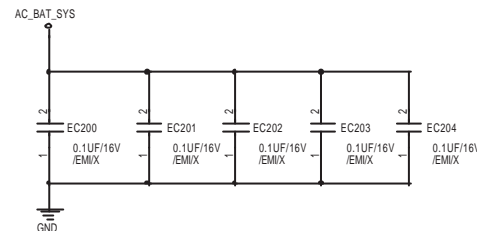
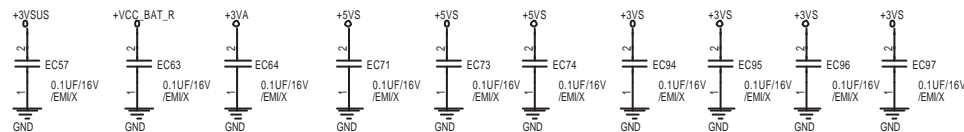
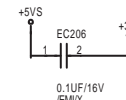
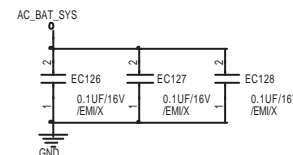
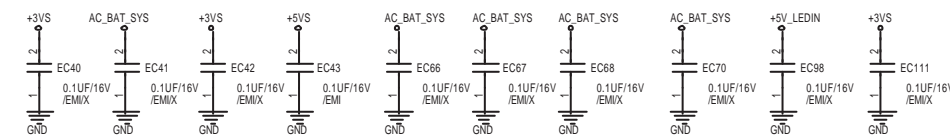
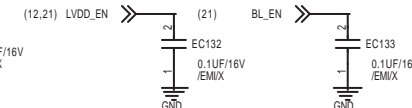
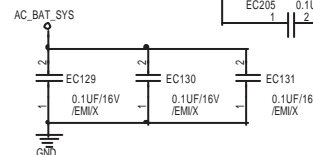
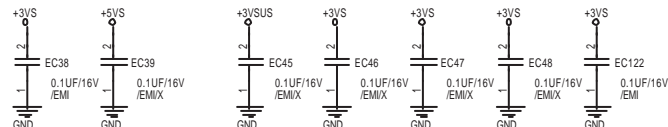
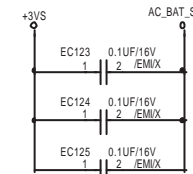
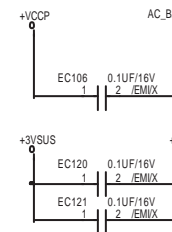
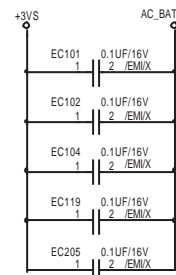
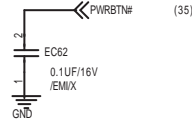
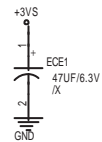
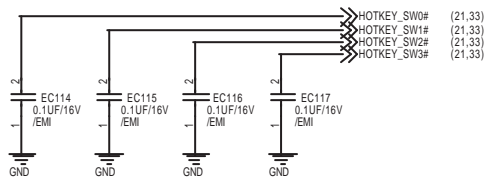
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ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008		Sheet	40 of 49



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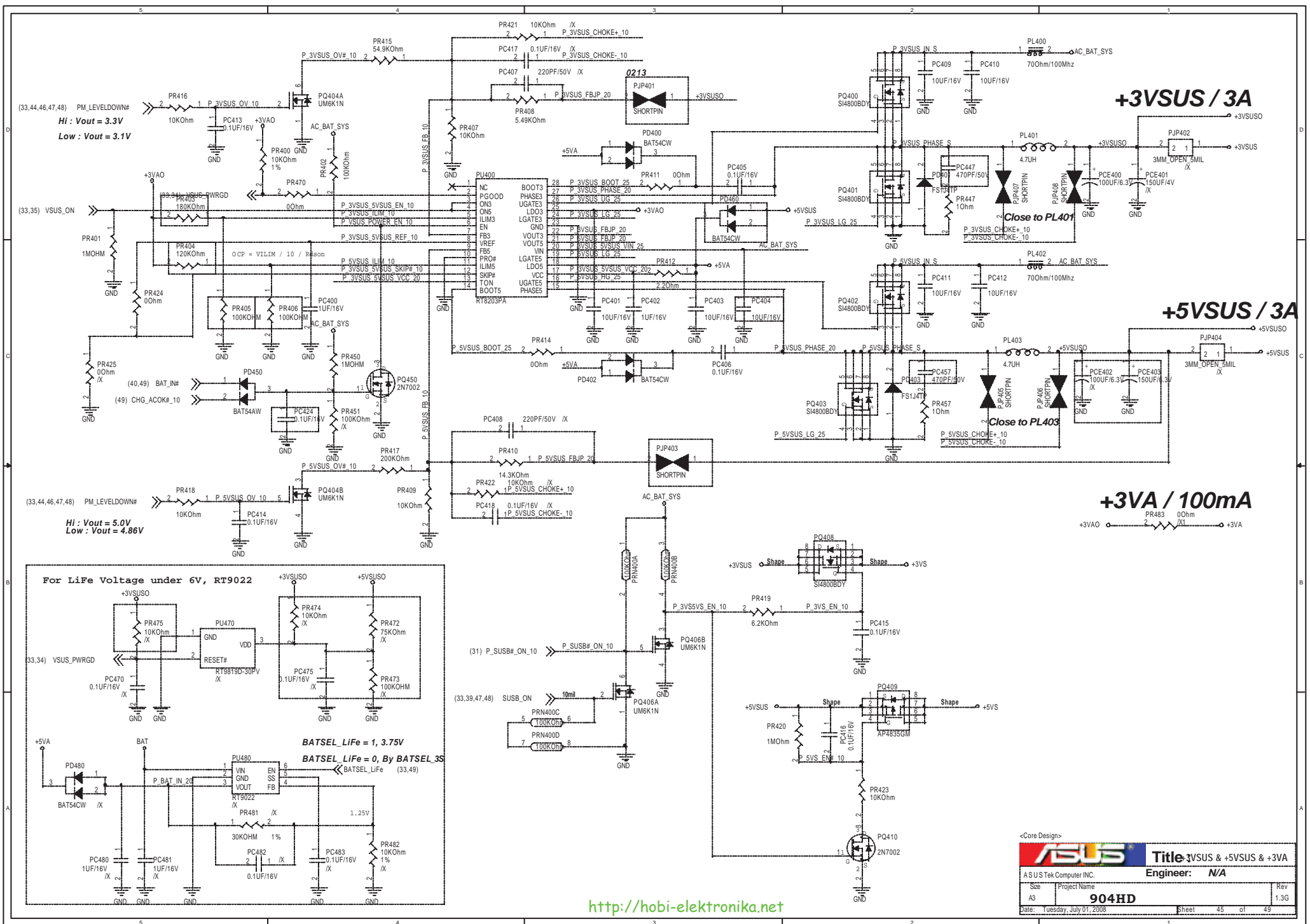
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ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size A3	Project Name 904HD	Rev 1.3G	
Date: Tuesday, July 01, 2008	Sheet	41	of 49

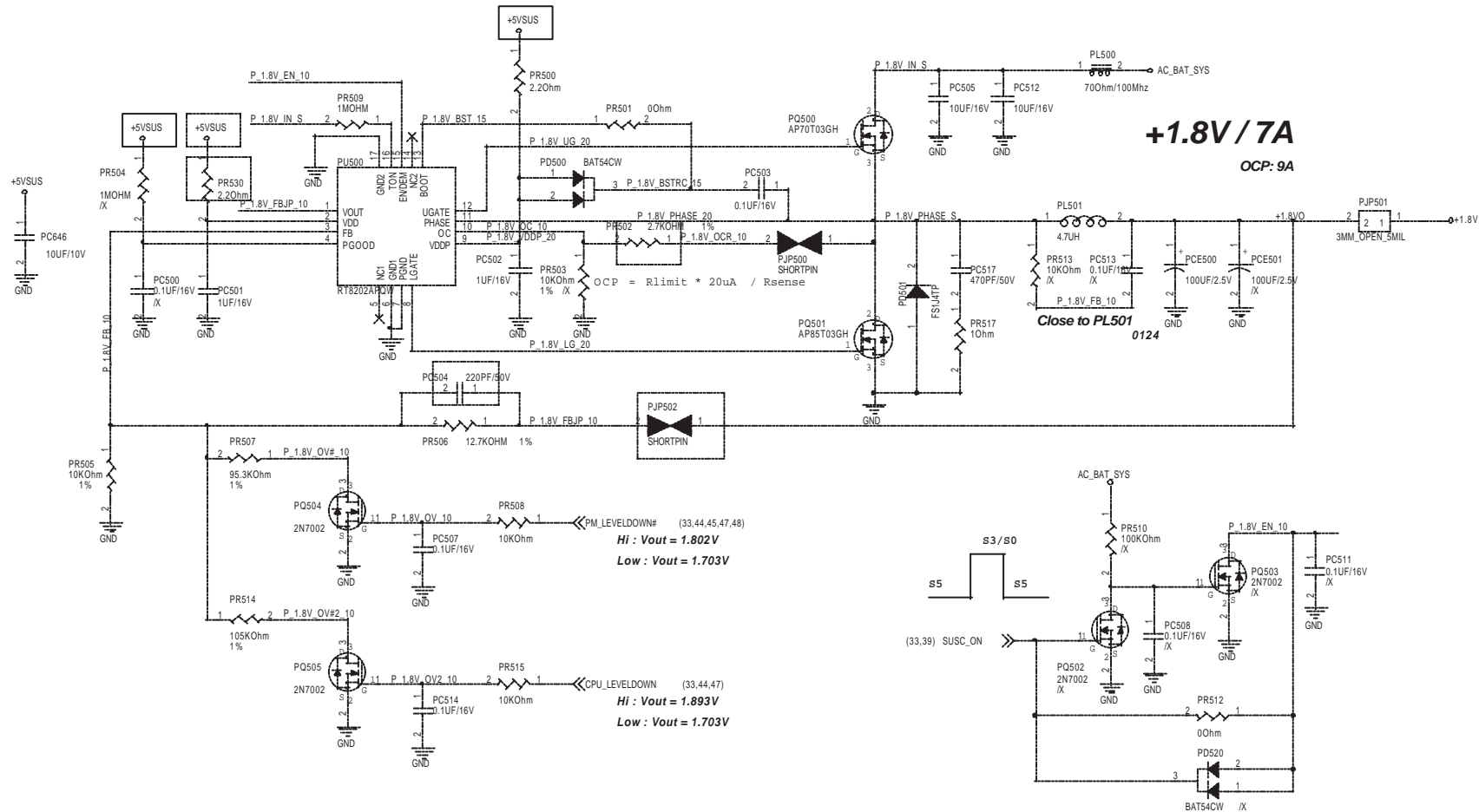


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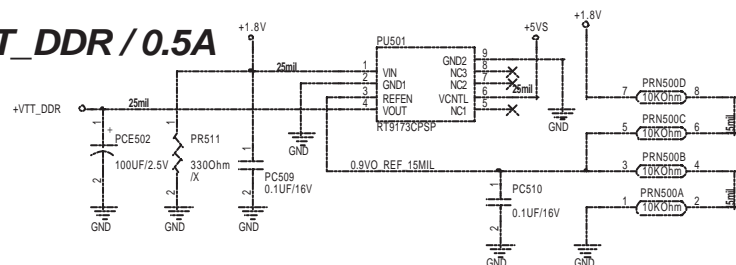
ASUS		Title : EMI	
ASUS Tek Computer INC.		Engineer: Wayne Chan	
Size	Project Name	Rev	
A3	904HD	1.3G	
Date: Tuesday, July 01, 2008		Sheet	42 of 49

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VTT_DDR / 0.5A



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	1.703V	Power Saving
H	L	H	1.802V	Normal
H	H	L	1.893V	Performance
H	L	H	1.802V	Performance

DEFAULT

<Core Design>



Title : +1.8V & VTTDDR

ASUSTek Computer INC.

Engineer: Joy Zhou

Size
A3

Project Name
904HD

Rev
1.3G

Date: Tuesday, July 01, 2008

Sheet 46 of 49

