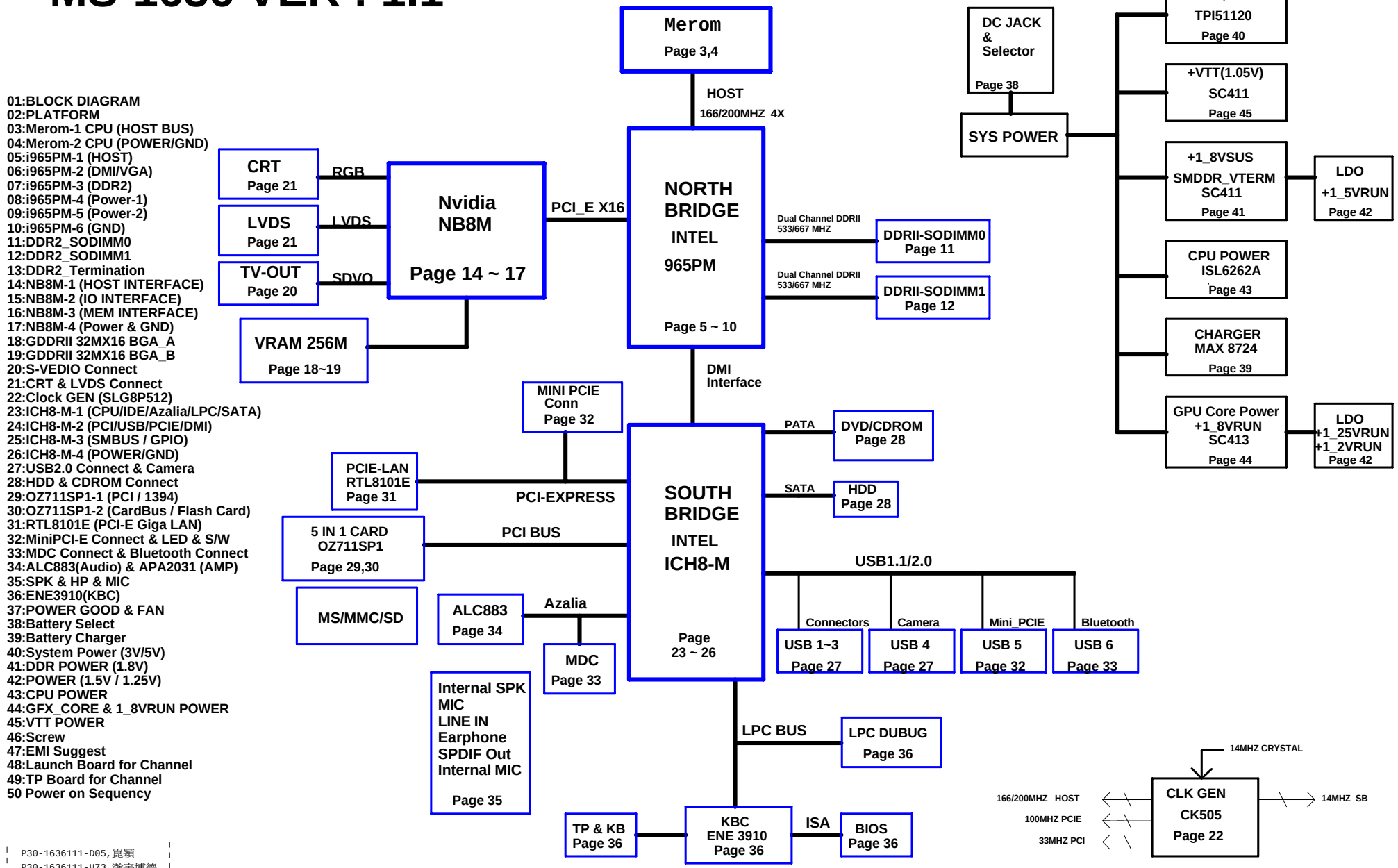
MS-1636 VER : 1.1



01:BLOCK DIAGRAM
02:PLATFORM
03:Merom-1 CPU (HOST BUS)
04:Merom-2 CPU (POWER/GND)
05:i965PM-1 (HOST)
06:i965PM-2 (DMI/VGA)
07:i965PM-3 (DDR2)
08:i965PM-4 (Power-1)
09:i965PM-5 (Power-2)
10:i965PM-6 (GND)
11:DDR2_SODIMM0
12:DDR2_SODIMM1
13:DDR2_Termination
14:NB8M-1 (HOST INTERFACE)
15:NB8M-2 (IO INTERFACE)
16:NB8M-3 (MEM INTERFACE)
17:NB8M-4 (Power & GND)
18:GDDRII 32MX16 BGA_A
19:GDDRII 32MX16 BGA_B
20:S-VEDIO Connect
21:CRT & LVDS Connect
22:Clock GEN (SLG8P512)
23:ICH8-M-1 (CPU/IDE/Azalia/LPC/SATA)
24:ICH8-M-2 (PCI/USB/PCIE/DMI)
25:ICH8-M-3 (SMBUS / GPIO)
26:ICH8-M-4 (POWER/GND)
27:USB2.0 Connect & Camera
28:HDD & CDROM Connect
29:OZ711SP1-1 (PCI / 1394)
30:OZ711SP1-2 (CardBus / Flash Card)
31:RTL8101E (PCI-E Giga LAN)
32:MiniPCI-E Connect & LED & S/W
33:MDC Connect & Bluetooth Connect
34:ALC883(Audio) & APA2031 (AMP)
35:SPK & HP & MIC
36:ENE3910(KBC)
37:POWER GOOD & FAN
38:Battery Select
39:Battery Charger
40:System Power (3V/5V)
41:DDR POWER (1.8V)
42:POWER (1.5V / 1.25V)
43:CPU POWER
44:GFX_CORE & 1_8VRUN POWER
45:VTT POWER
46:Screw
47:EMI Suggest
48:Launch Board for Channel
49:TP Board for Channel
50 Power on Sequency

P30-1636111-D05,嵐穎
P30-1636111-H73,瀚宇博德

MSI CORPORATION

Title: BLOCK DIAGRAM

Size: Custom | Document Number: MS-1636 | Rev: 0B

Date: Wednesday, September 05, 2007 | Sheet 1 of 51

Voltage Rails

Voltage	Description	Control Signal
PWR_SRC	AC ADAPTER OR BATTERY IN	
V_CORE	Core Voltage for Processor	VR_ON
+VTT	1.05 rail for Processor & 945GM I/O	PM_SLP_S3# (RUN_ON)
+1_5VRUN	1.5V switched power rail(off in S3-S5)	RUN_ON(+1_8VSUS-LDO)
+1_25VRUN	1.25V power rail NB PLL and PXE (off in S3-S5)	NV_PWRON(+1_8VRUN-LDO)
+3VRUN	3.3V switched power rail(off in S3-S5)	RUND (RUN_ON)
+5VRUN	5.0V switched power rail(off in S3-S5)	RUND (RUN_ON)
SMDDR_VTERM	0.9V DDR Termination voltage (off in S4-S5)	PM_SLP_S4# (DIMM_ON)
+1_8VDIMM	1.8V power rail DDRII (off in S4-S5)	PM_SLP_S4# (DIMM_ON)
+3VSUS	3.3V power rail (off in S4-S5)	SUS_ON
+5VSUS	5.0V power rail (off in S4-S5)	SUS_ON
+3VALW	3.3V always on power rail	PWR_SRC
+5VALW	5.0V always on power rail	PWR_SRC
+V5_AUDIO	5.0V Power rail Audio codec(off in S3-S5)	RUND
+G73M_CORE	Core Voltage for External Graphics NB8M	VGA-PGOOD
+1_8VRUN	1.8V power rail GDDRIII (off in S3-S5)	NV_DELAY_PWRON
+1_2VRUN_GFX	1.2V power rail NB8M PLL (off in S3-S5)	NV_PWRON(+1_8VRUN-LDO)

POWER STATES

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+V*ALWAYS	+V*SUS	+V*RUN	Clocks
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3(Suspend to RAM)	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4(Suspend to Disk)	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 / Soft OFF	LOW	LOW	LOW	ON	OFF	OFF	OFF

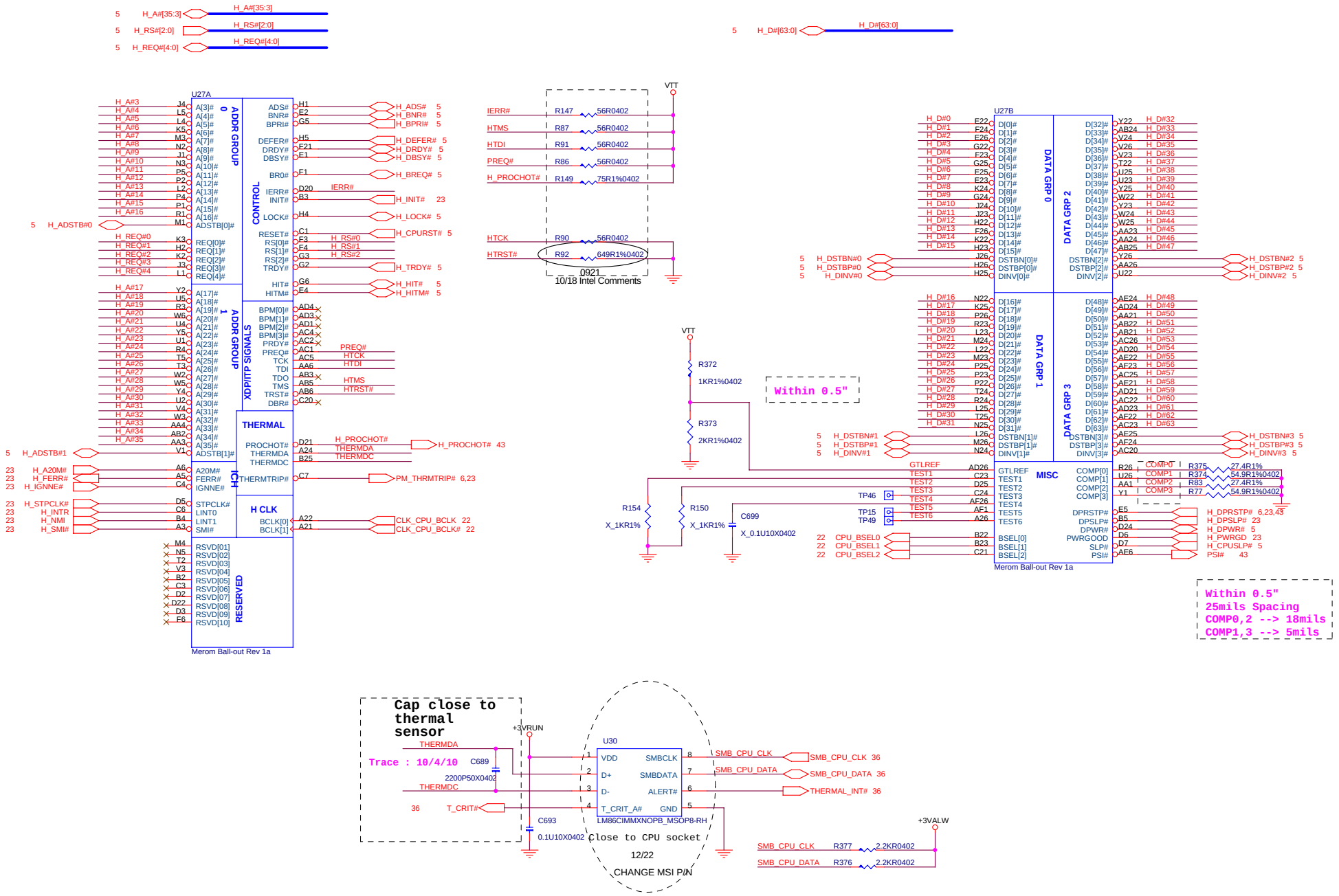
Note : WHEN AC MODE , System turn on then +V*SUS will always keep high

MSI CORPORATION

Title: PLATFORM

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 2 of 51

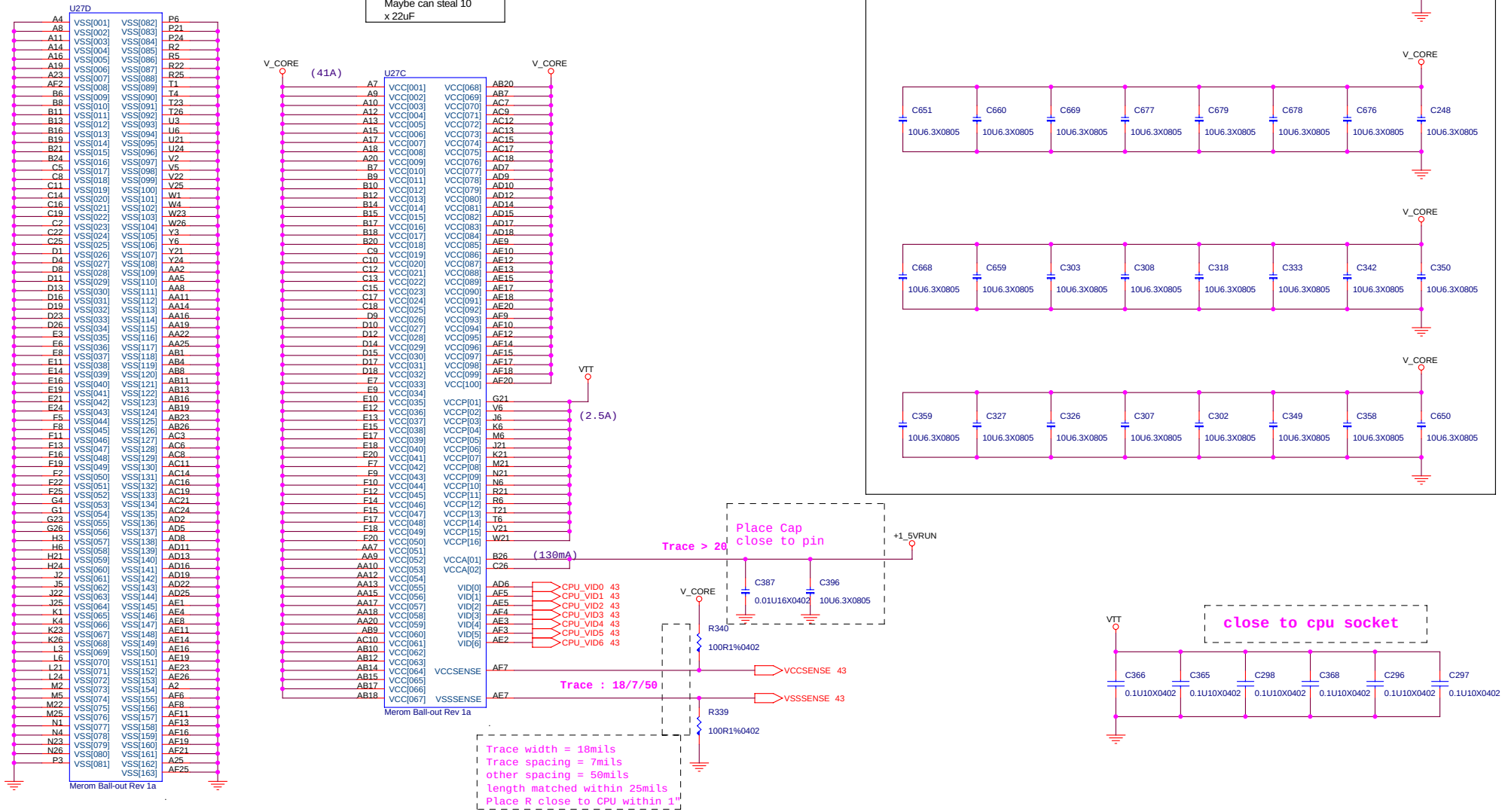


MSI CORPORATION

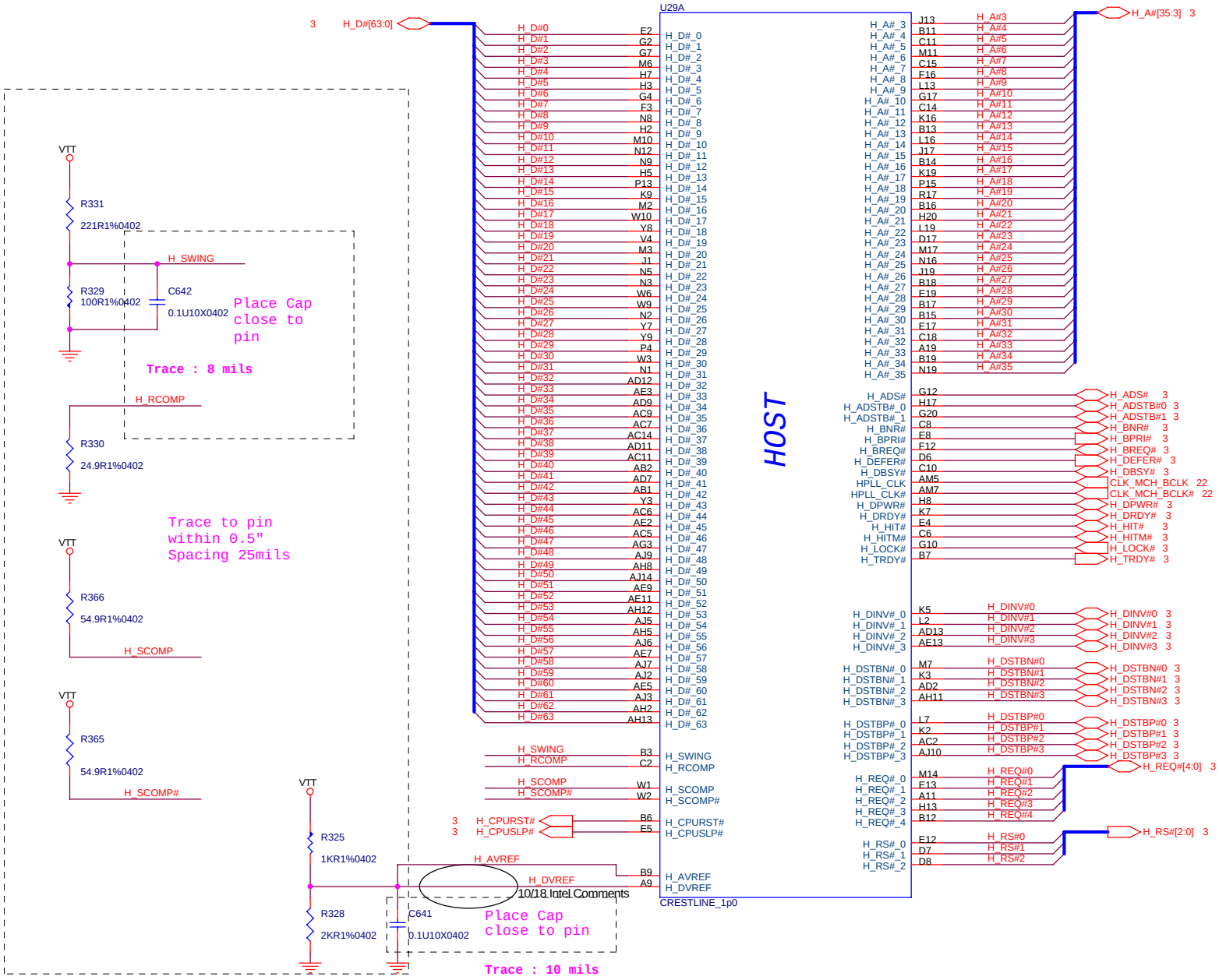
Title: Merom-1 (HOST BUS)

Size: Custom | Document Number: MS-1636 | Rev: 0A

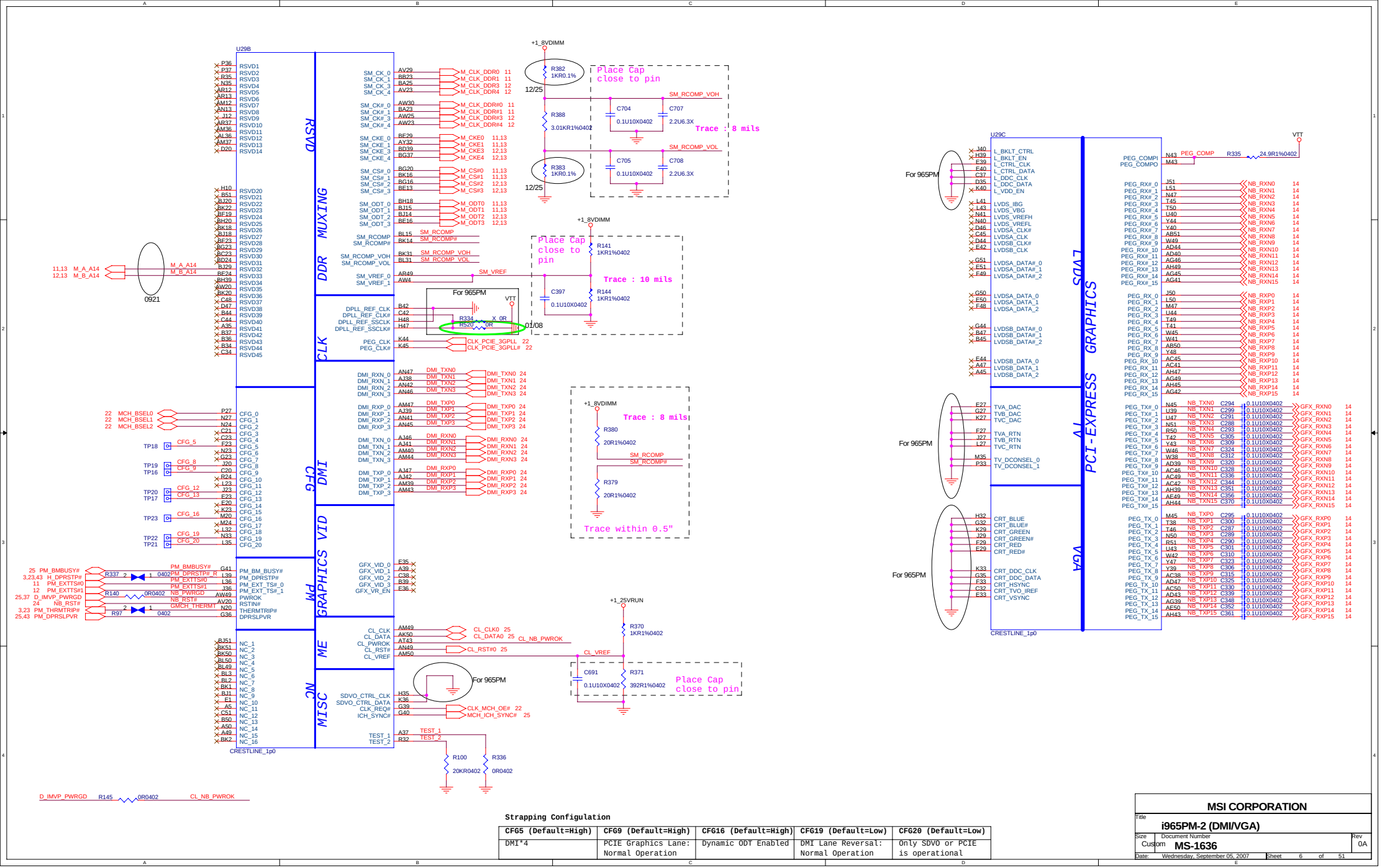
Date: Wednesday, September 05, 2007 | Sheet 3 of 51



MSI CORPORATION			
Title: Merom-2 (POWER/GND)			
Size: Custom	Document Number: MS-1636		Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 4 of 51		



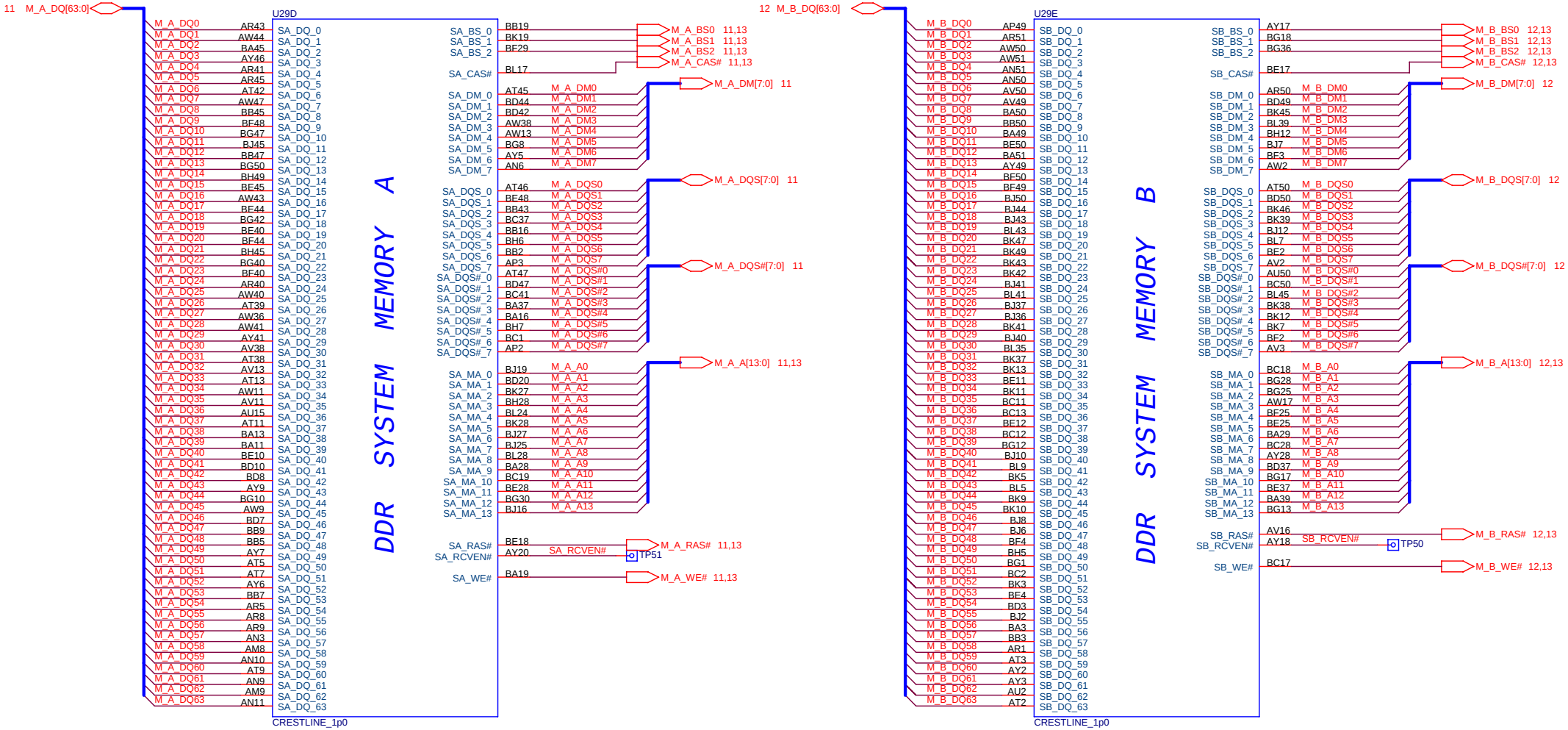
Company	MSI CORPORATION
Title	i965PM-1 (HOST BUS)
Size	Custom
Document Number	MS-1636
Rev	0A
Date:	Wednesday, September 05, 2007
Sheet	5 of 51



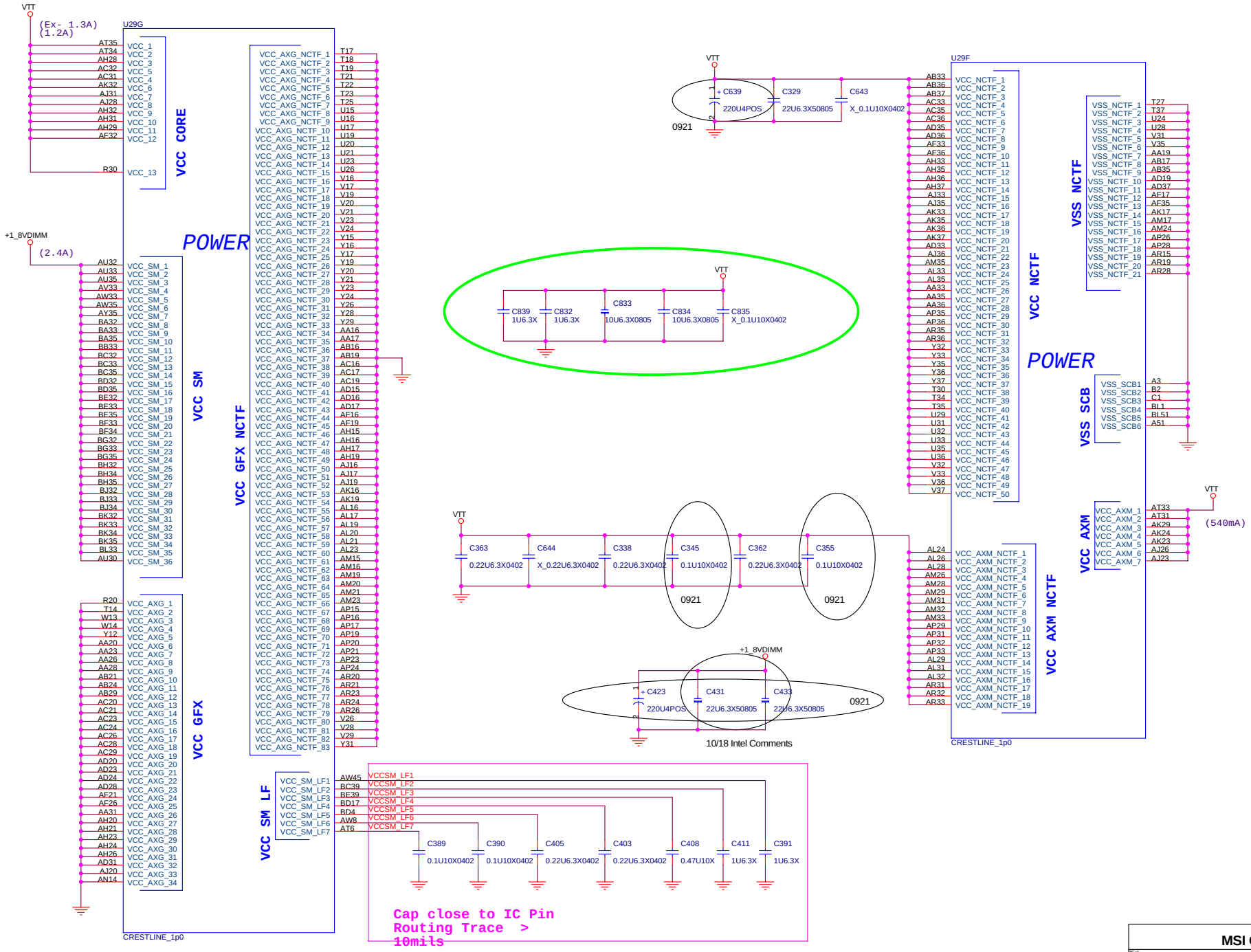
Strapping Configulation

CFG5 (Default=High)	CFG9 (Default=High)	CFG16 (Default=High)	CFG19 (Default=Low)	CFG20 (Default=Low)
DMI*4	PCIE Graphics Lane: Normal Operation	Dynamic ODT Enabled	DMI Lane Reversal: Normal Operation	Only SDVO or PCIE is operational

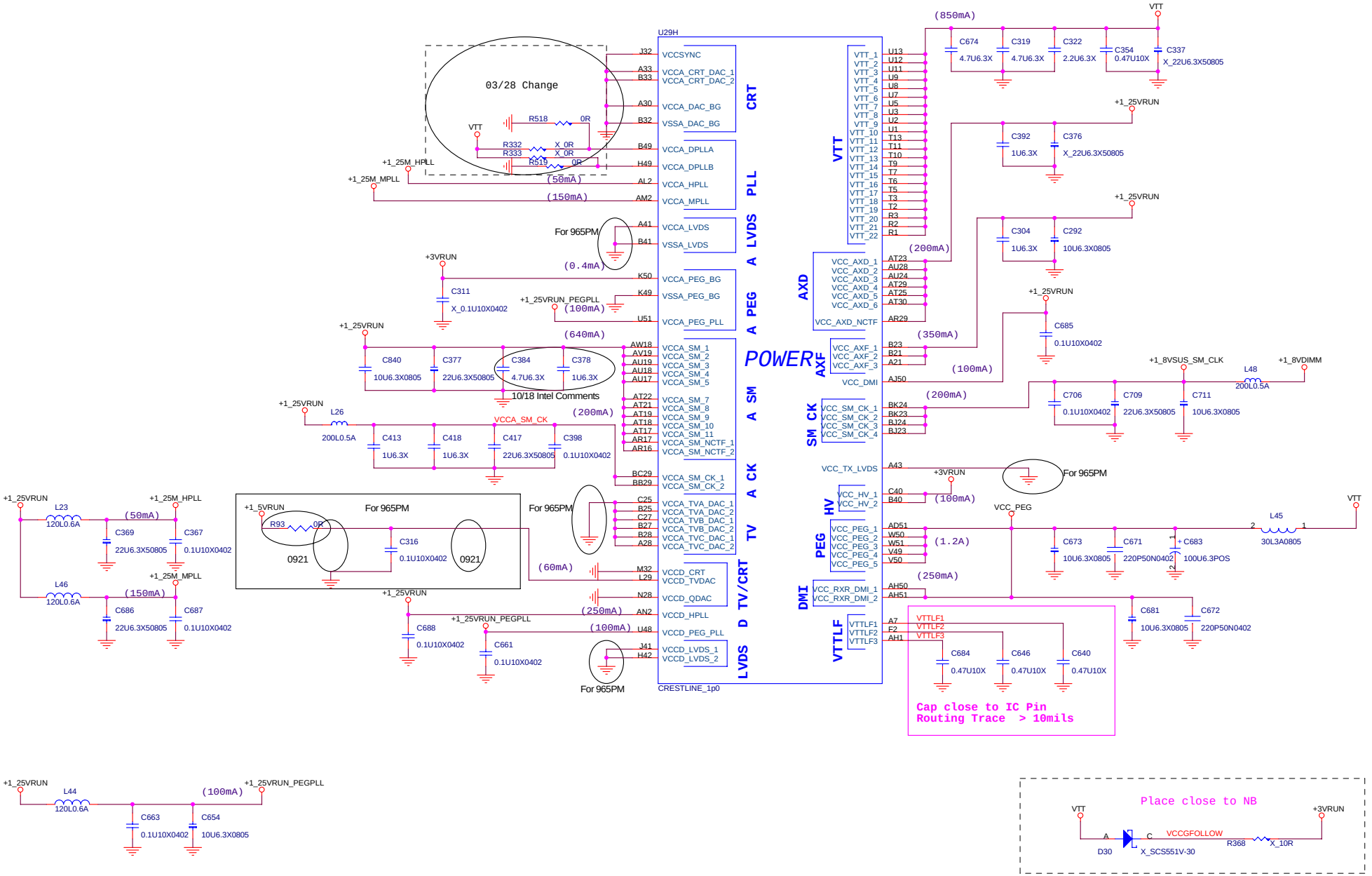
Title	MSI CORPORATION
	i965PM-2 (DMI/VGA)
Size: Custom	Document Number: MS-1636
Rev	0A
Date: Wednesday, September 05, 2007	Sheet 6 of 51



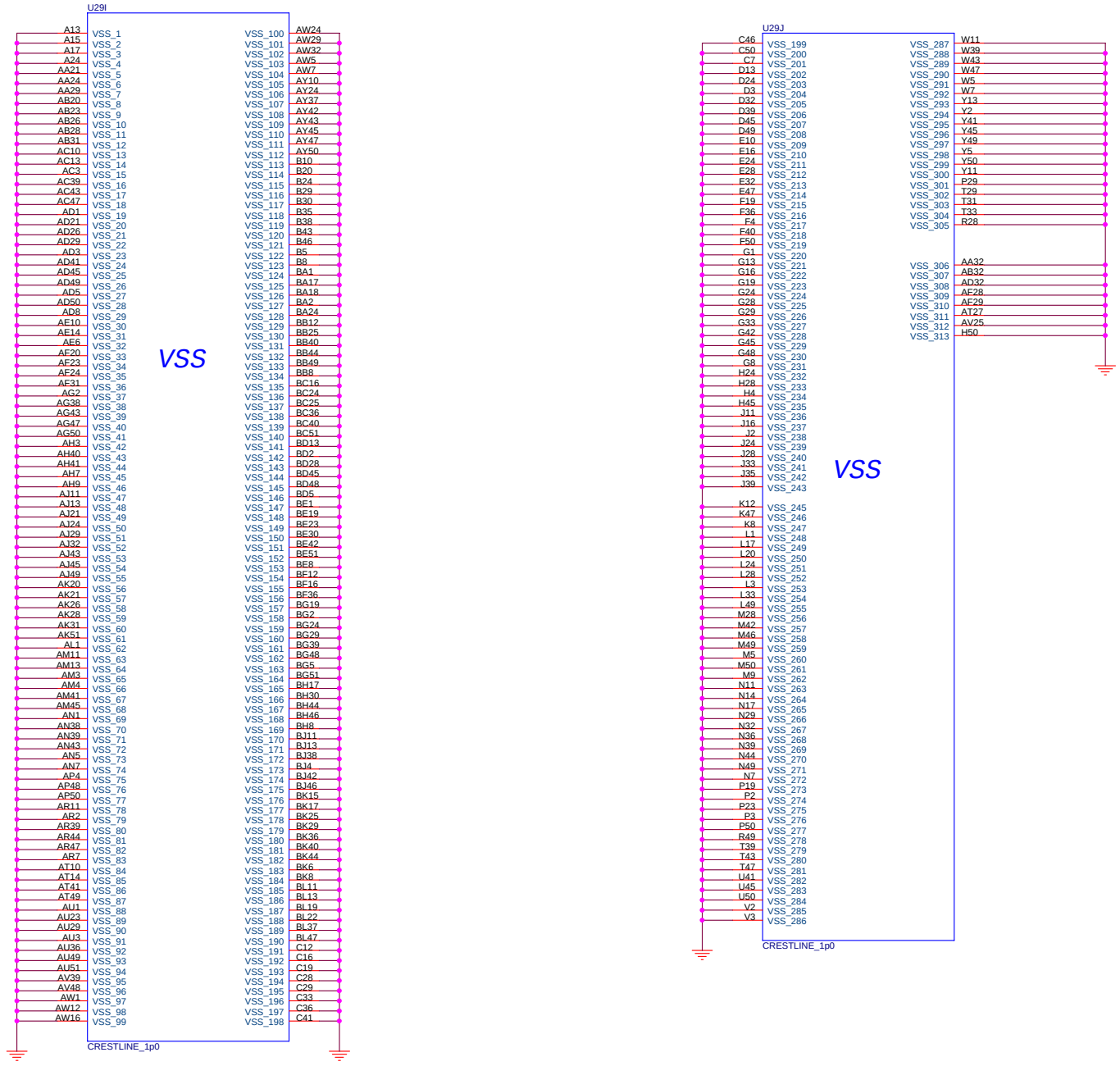
MSI CORPORATION			
Title: i965PM-3 (DDR)			
Size: B	Document Number: MS-1636		Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 7 of 51		



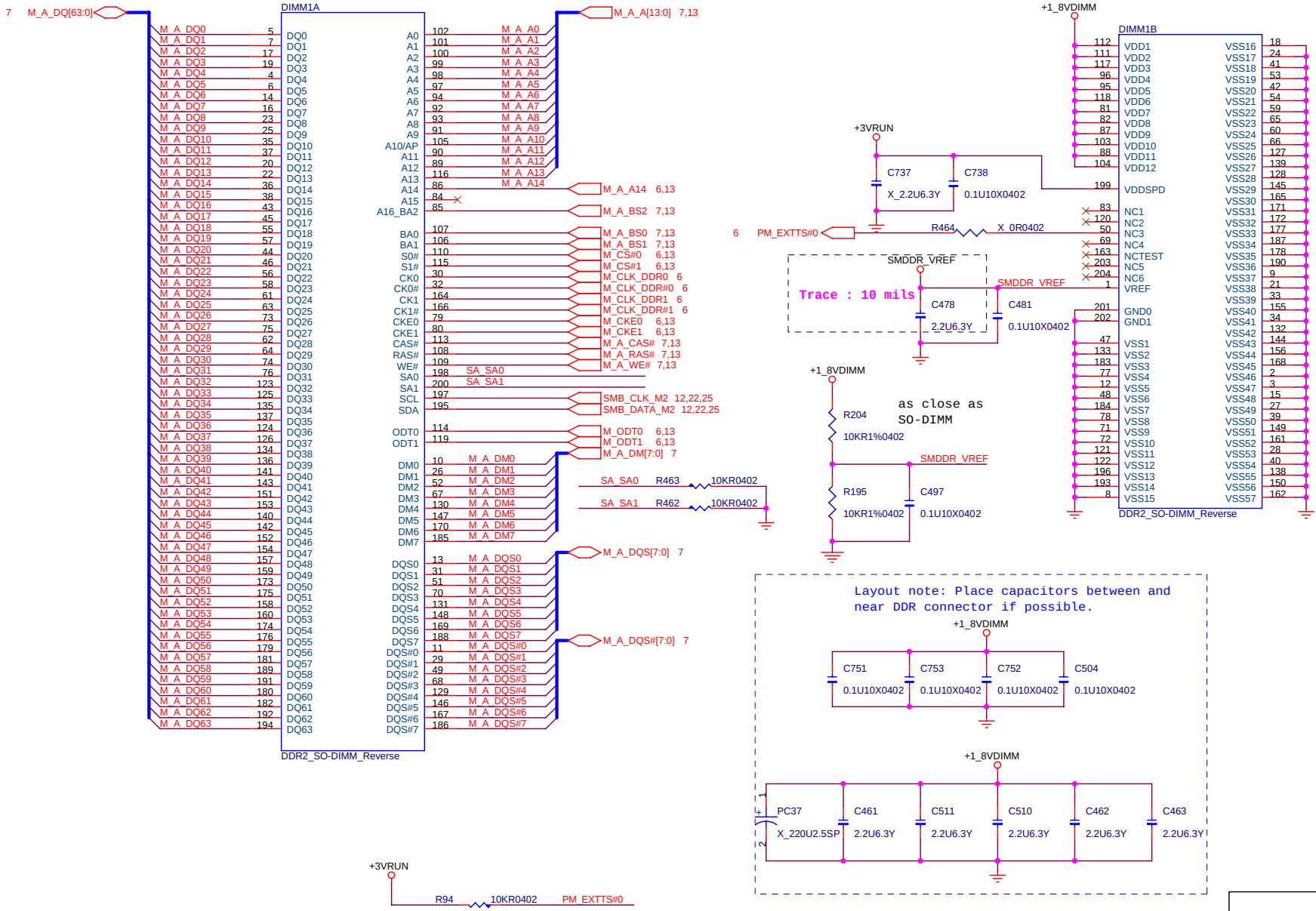
MSI CORPORATION		
Title: i965PM-4 (POWER-1)		
Size: Custom	Document Number: MS-1636	Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 8 of 51	



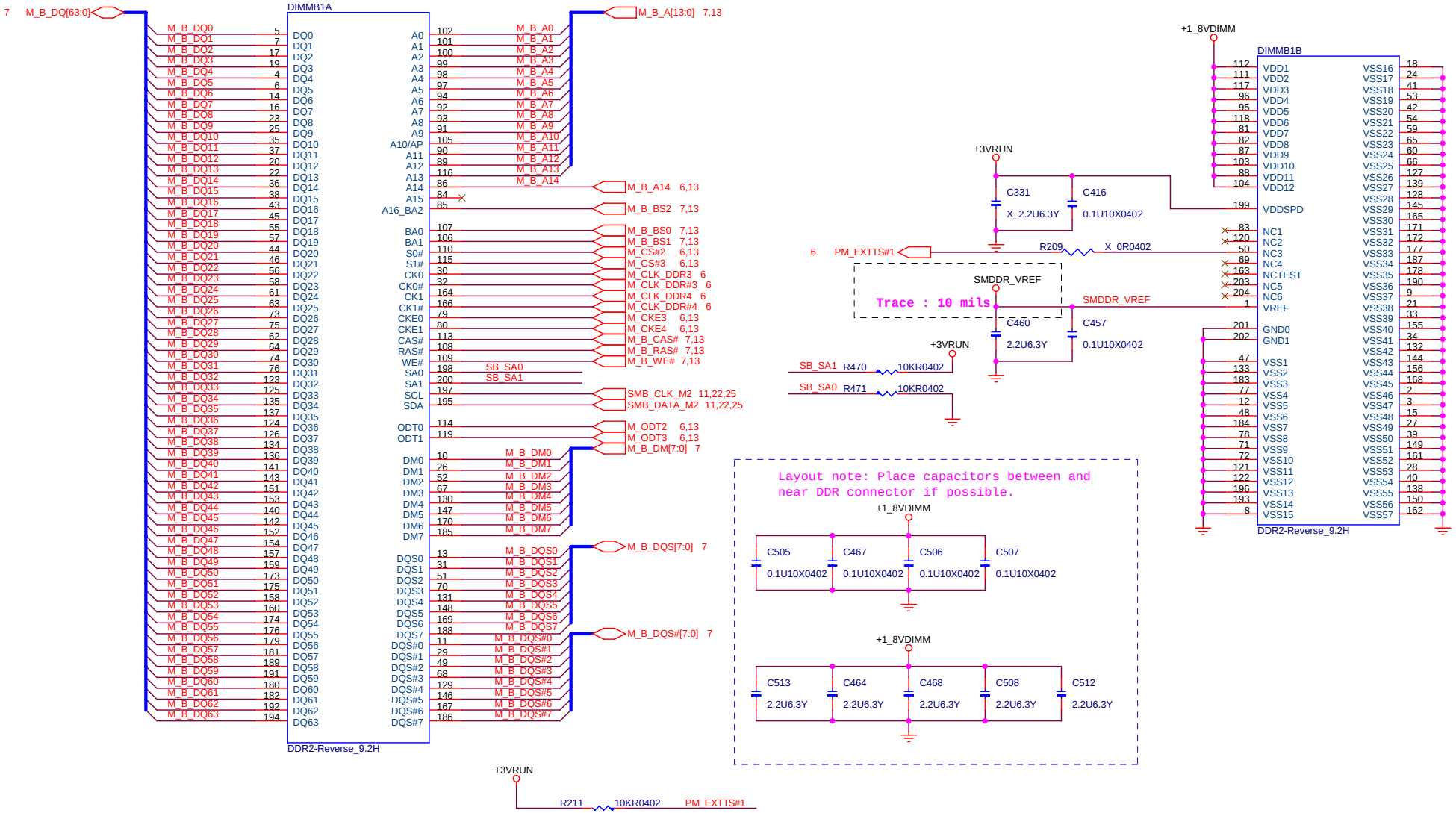
Title	MSI CORPORATION
	i965PM-5 (POWER-2)
Size	Custom
Document Number	MS-1636
Rev	0A
Date:	Wednesday, September 05, 2007
Sheet	9 of 51



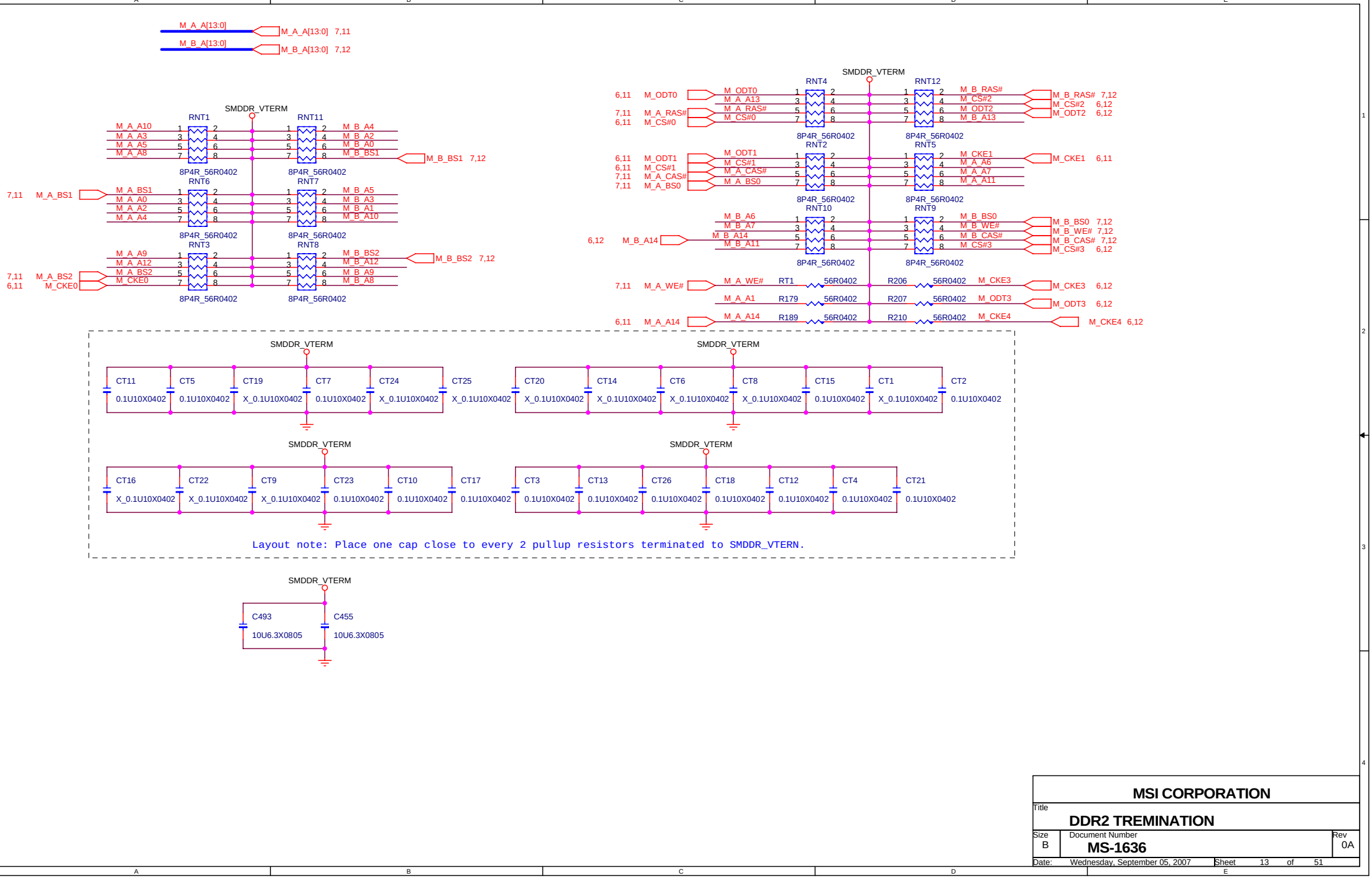
MSI CORPORATION	
Title	**i965PM-6 (VSS)**
Size: Custom	Document Number: **MS-1636**
Rev	0A
Date: Wednesday, September 05, 2007	Sheet 10 of 51



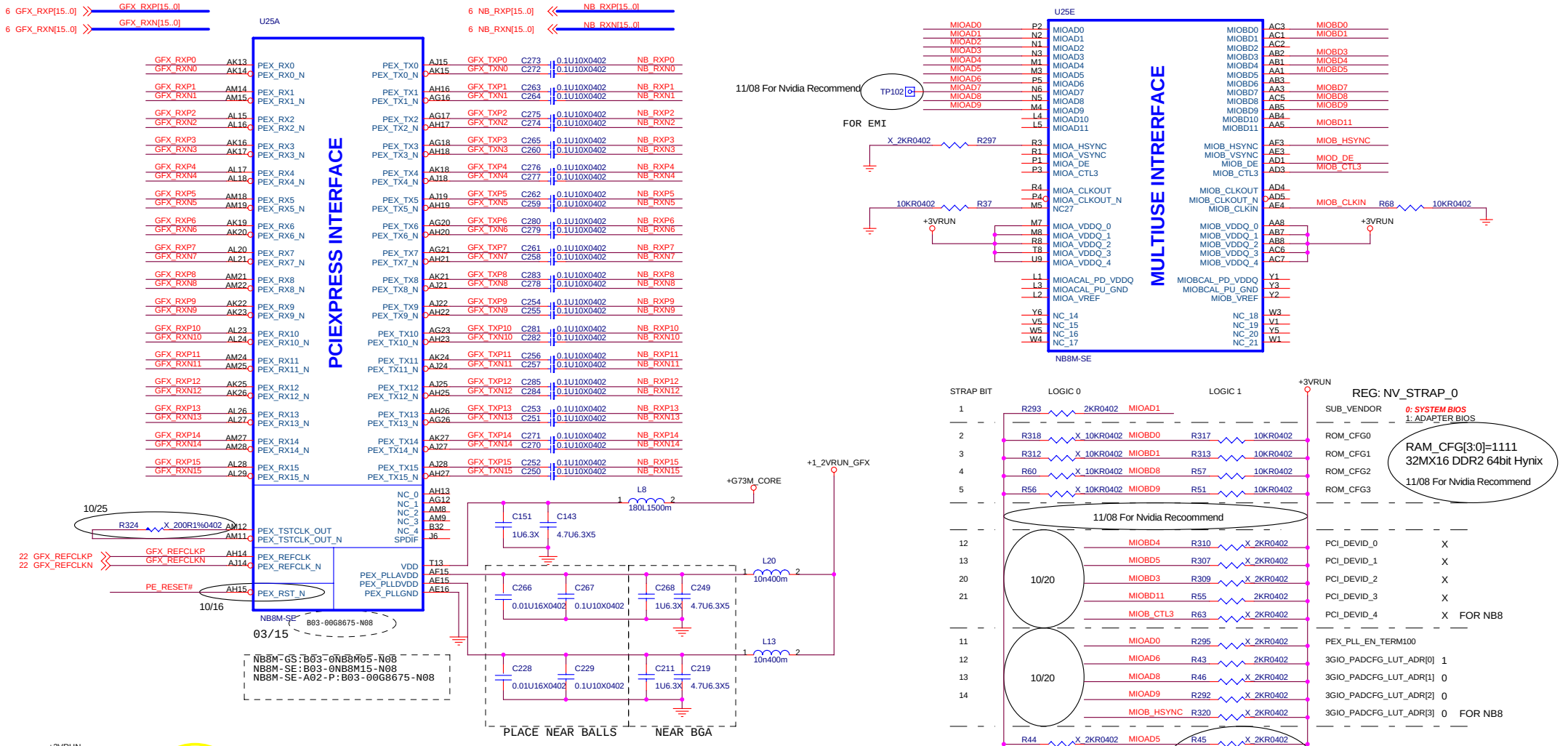
Company	MSI CORPORATION
Title	DDR2 SODIMM 0
Size	B
Document Number	MS-1636
Rev	0A
Date:	Wednesday, September 05, 2007
Sheet	11 of 51



MSI CORPORATION			
Title: DDR2 SODIMM 1			
Size: B	Document Number: MS-1636		Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 12 of 51		



MSI CORPORATION		
Title: DDR2 TREMINATION		
Size: B	Document Number: MS-1636	Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 13 of 51	



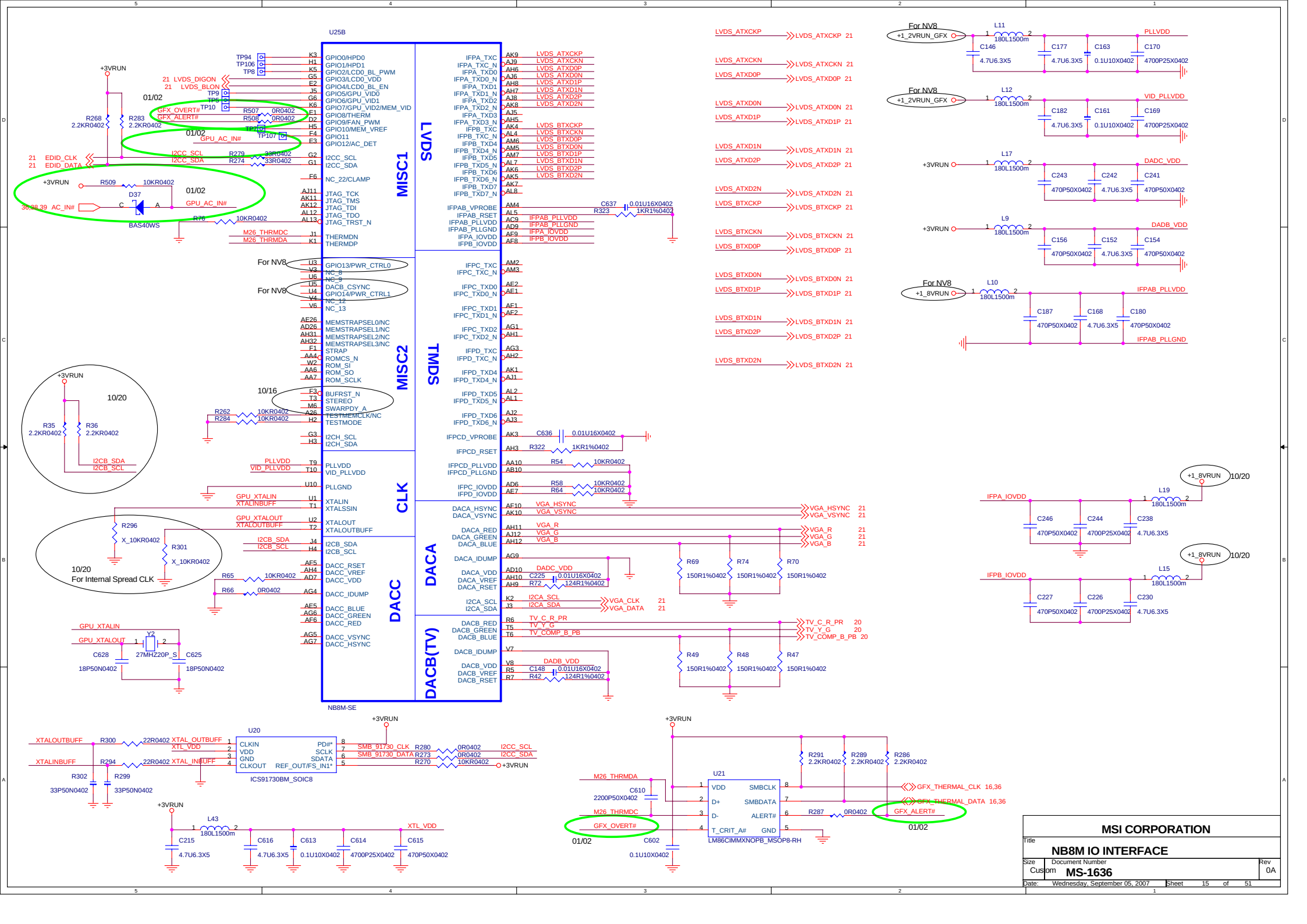
PIN NAME	G8X FUNCTION	DEFAULT	VBIOS ROM NOT PRESENT	NB8
MIOBD2	CRYSTAL	0	NOT REQUIRED	0
MIOAD7	TVMODE0	1	NOT REQUIRED	X
MIOAD10	TVMODE1	0	NOT REQUIRED	0
MIOBD6	TVMODE2	0	NOT REQUIRED	0
MIOBD4	PCI_DEVID_0	0	2K PULL-UP RECOMMENDED	X
MIOBD5	PCI_DEVID_1	0	2K PULL-UP RECOMMENDED	X
MIOBD3	PCI_DEVID_2	0	2K PULL-UP RECOMMENDED	X
MIOBD11	PCI_DEVID_3	0	2K PULL-UP RECOMMENDED	X
MIOB_CTL3	PCI_DEVID_4	0	2K PULL-UP RECOMMENDED	X
MIOAD6	3GIO_PADCFG0	0	2K PULL-UP RECOMMENDED	1
MIOAD8	3GIO_PADCFG1	0	2K PULL-UP RECOMMENDED	0
MIOAD9	3GIO_PADCFG2	0	2K PULL-UP RECOMMENDED	0
MIOB_HSYNC	3GIO_PADCFG3	0	2K PULL-UP RECOMMENDED	0
MIOBD7	PCI_IOBAR	1	NOT REQUIRED	1
MIOB_DE	BAR2_SIZE	0	NOT REQUIRED	0

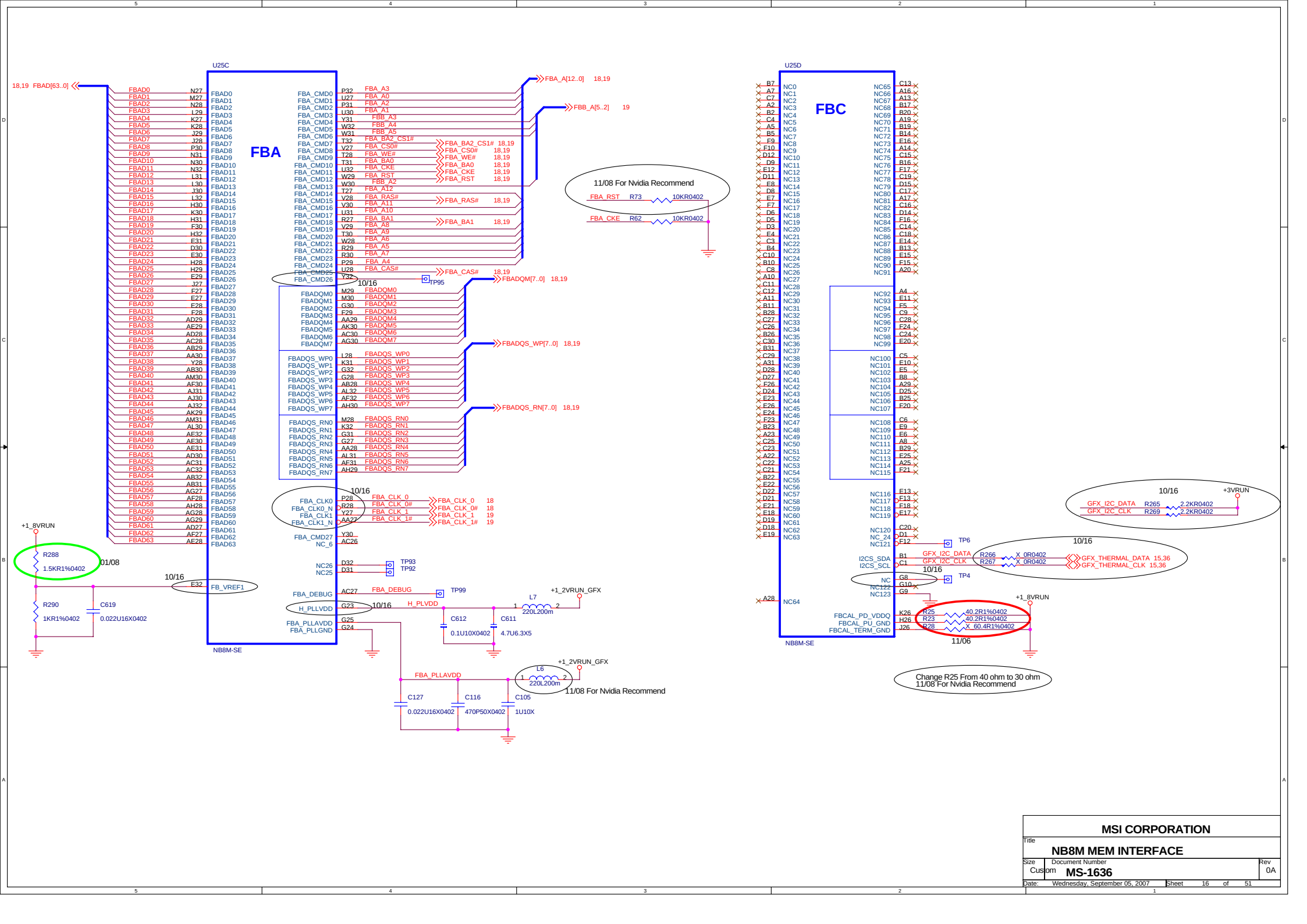
MSI CORPORATION

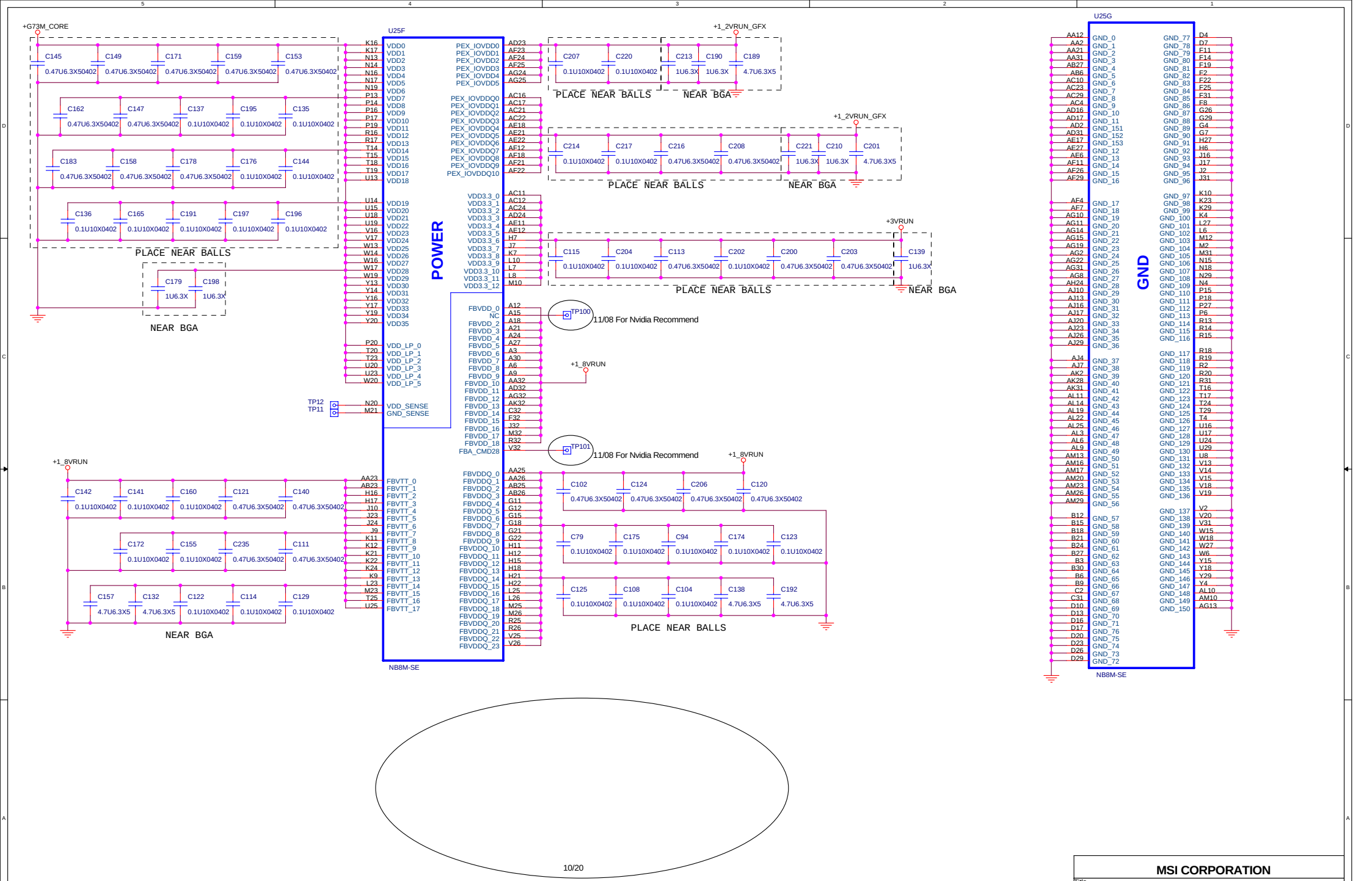
Title: NB8M HOST INTERFACE

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 14 of 51


U25B
MISC1
MISC2
CLK
DACC
LVDS
TMDS
DACA
DACB(TV)
NB8M-SE
U20
ICS91730BM_SOIC8
U21
LM86CIMMXNOPB_MSOP8-RH
MSI CORPORATION
NB8M IO INTERFACE
MS-1636
Custom
0A
Wednesday, September 05, 2007
Sheet 15 of 51


U25C
FBA
NB8M-SE
18,19 FBAD[63..0]
FBAD0 N27
FBAD1 M27
FBAD2 N28
FBAD3 L29
FBAD4 K27
FBAD5 K28
FBAD6 J29
FBAD7 J28
FBAD8 P30
FBAD9 N31
FBAD10 N30
FBAD11 N32
FBAD12 L31
FBAD13 L30
FBAD14 J30
FBAD15 L32
FBAD16 H30
FBAD17 K30
FBAD18 H31
FBAD19 F30
FBAD20 H32
FBAD21 E31
FBAD22 D30
FBAD23 E30
FBAD24 H28
FBAD25 H29
FBAD26 E29
FBAD27 J27
FBAD28 F27
FBAD29 E27
FBAD30 E28
FBAD31 F28
FBAD32 AD29
FBAD33 AE29
FBAD34 AD28
FBAD35 AC28
FBAD36 AB29
FBAD37 AA30
FBAD38 Y28
FBAD39 AB30
FBAD40 AM30
FBAD41 AF30
FBAD42 AJ31
FBAD43 AJ30
FBAD44 AJ32
FBAD45 AK29
FBAD46 AM31
FBAD47 AL30
FBAD48 AE32
FBAD49 AE30
FBAD50 AE31
FBAD51 AD30
FBAD52 AC31
FBAD53 AC32
FBAD54 AB32
FBAD55 AB31
FBAD56 AG27
FBAD57 AF28
FBAD58 AH28
FBAD59 AG28
FBAD60 AG29
FBAD61 AD27
FBAD62 AF27
FBAD63 AE28
FBA_CMD0 P32 FBA_A3
FBA_CMD1 U27 FBA_A0
FBA_CMD2 P31 FBA_A2
FBA_CMD3 U30 FBA_A1
FBA_CMD4 Y31 FBB_A3
FBA_CMD5 W32 FBB_A4
FBA_CMD6 W31 FBB_A5
FBA_CMD7 T32 FBA_BA2_CS1#
FBA_CMD8 V27 FBA_CS0#
FBA_CMD9 T28 FBA_WE#
FBA_CMD10 T31 FBA_BA0
FBA_CMD11 U32 FBA_CKE
FBA_CMD12 W29 FBA_RST
FBA_CMD13 W30 FBB_A2
FBA_CMD14 T27 FBA_A12
FBA_CMD15 V28 FBA_RAS#
FBA_CMD16 V30 FBA_A11
FBA_CMD17 U31 FBA_A10
FBA_CMD18 R27 FBA_BA1
FBA_CMD19 V29 FBA_A8
FBA_CMD20 T30 FBA_A9
FBA_CMD21 W28 FBA_A6
FBA_CMD22 R29 FBA_A5
FBA_CMD23 R30 FBA_A7
FBA_CMD24 P29 FBA_A4
FBA_CMD25 U28 FBA_CAS#
FBA_CMD26 Y32
TP95
10/16
FBA_A[12..0] 18,19
FBB_A[5..2] 19
FBA_BA2_CS1# 18,19
FBA_CS0# 18,19
FBA_WE# 18,19
FBA_BA0 18,19
FBA_CKE 18,19
FBA_RST 18,19
FBA_RAS# 18,19
FBA_BA1 18,19
FBA_CAS# 18,19
FBADQM[7..0] 18,19
FBADQM0 M29 FBADQM0
FBADQM1 M30 FBADQM1
FBADQM2 G30 FBADQM2
FBADQM3 F29 FBADQM3
FBADQM4 AA29 FBADQM4
FBADQM5 AK30 FBADQM5
FBADQM6 AC30 FBADQM6
FBADQM7 AG30 FBADQM7
FBADQS_WP[7..0] 18,19
FBADQS_WP0 L28 FBADQS_WP0
FBADQS_WP1 K31 FBADQS_WP1
FBADQS_WP2 G32 FBADQS_WP2
FBADQS_WP3 G28 FBADQS_WP3
FBADQS_WP4 AB28 FBADQS_WP4
FBADQS_WP5 AL32 FBADQS_WP5
FBADQS_WP6 AF32 FBADQS_WP6
FBADQS_WP7 AH30 FBADQS_WP7
FBADQS_RN[7..0] 18,19
FBADQS_RN0 M28 FBADQS_RN0
FBADQS_RN1 K32 FBADQS_RN1
FBADQS_RN2 G31 FBADQS_RN2
FBADQS_RN3 G27 FBADQS_RN3
FBADQS_RN4 AA28 FBADQS_RN4
FBADQS_RN5 AL31 FBADQS_RN5
FBADQS_RN6 AF31 FBADQS_RN6
FBADQS_RN7 AH29 FBADQS_RN7
10/16
FBA_CLK0 P28 FBA_CLK_0
FBA_CLK0_N R28 FBA_CLK_0#
FBA_CLK1 Y27 FBA_CLK_1
FBA_CLK1_N AA27 FBA_CLK_1#
FBA_CLK_0 18
FBA_CLK_0# 18
FBA_CLK_1 19
FBA_CLK_1# 19
FBA_CMD27 Y30
NC_6 AC26
NC26 D32
NC25 D31
TP93
TP92
FBA_DEBUG AC27 FBA_DEBUG
TP99
H_PLLVDD G23
10/16
H_PLLVDD
L7
220L200m
+1_2VRUN_GFX
C612 0.1U10X0402
C611 4.7U6.3X5
FBA_PLLAVDD G25
FBA_PLLGND G24
FBA_PLLAVDD
L6
220L200m
+1_2VRUN_GFX
C127 0.022U16X0402
C116 470P50X0402
C105 1U10X
11/08 For Nvidia Recommend
11/08 For Nvidia Recommend
FBA_RST R73 10KR0402
FBA_CKE R62 10KR0402
+1_8VRUN
R288 1.5KR1%0402
01/08
10/16
E32 FB_VREF1
R290 1KR1%0402
C619 0.022U16X0402
U25D
FBC
NB8M-SE
NC0 B7
NC1 A7
NC2 C7
NC3 A2
NC4 B2
NC5 C4
NC6 A5
NC7 B5
NC8 F9
NC9 F10
NC10 D12
NC11 D9
NC12 E12
NC13 D11
NC14 E8
NC15 D8
NC16 E7
NC17 F7
NC18 D6
NC19 D5
NC20 D3
NC21 E4
NC22 C3
NC23 B4
NC24 C10
NC25 B10
NC26 C8
NC27 A10
NC28 C11
NC29 C12
NC30 A11
NC31 B11
NC32 B28
NC33 C27
NC34 C26
NC35 B26
NC36 C30
NC37 B31
NC38 C29
NC39 A31
NC40 D28
NC41 D27
NC42 E26
NC43 D24
NC44 E23
NC45 E26
NC46 E24
NC47 F23
NC48 B23
NC49 A23
NC50 C25
NC51 C23
NC52 A22
NC53 C22
NC54 C21
NC55 B22
NC56 E22
NC57 D22
NC58 D21
NC59 E21
NC60 E18
NC61 D19
NC62 D18
NC63 E19
NC64 A28
NC65 C13
NC66 A16
NC67 A13
NC68 B17
NC69 B20
NC70 A19
NC71 B19
NC72 B14
NC73 E16
NC74 A14
NC75 C15
NC76 B16
NC77 E17
NC78 C19
NC79 D15
NC80 C17
NC81 A17
NC82 C16
NC83 D14
NC84 E16
NC85 C14
NC86 C18
NC87 E14
NC88 B13
NC89 E15
NC90 F15
NC91 A20
NC92 A4
NC93 E11
NC94 F5
NC95 C9
NC96 C28
NC97 F24
NC98 C24
NC99 E20
NC100 C5
NC101 E10
NC102 E5
NC103 B8
NC104 A29
NC105 D25
NC106 B25
NC107 E20
NC108 C6
NC109 E9
NC110 E6
NC111 A8
NC112 B29
NC113 E25
NC114 A25
NC115 F21
NC116 E13
NC117 F13
NC118 F18
NC119 E17
NC120 C20
NC_24 D1
NC121 F12
NC122 G10
NC123 G9
TP6
TP4
10/16
I2CS_SDA B1
I2CS_SCL C1
GFX_I2C_DATA
GFX_I2C_CLK
R266 X_0R0402
R267 X_0R0402
GFX_THERMAL_DATA 15,36
GFX_THERMAL_CLK 15,36
10/16
+3VRUN
GFX_I2C_DATA R265 2.2KR0402
GFX_I2C_CLK R269 2.2KR0402
FBCAL_PD_VDDQ K26
FBCAL_PU_GND H26
FBCAL_TERM_GND J26
R25 40.2R1%0402
R23 40.2R1%0402
R28 X_60.4R1%0402
11/06
+1_8VRUN
Change R25 From 40 ohm to 30 ohm
11/08 For Nvidia Recommend
MSI CORPORATION
Title
NB8M MEM INTERFACE
Size Custom
Document Number MS-1636
Rev 0A
Date: Wednesday, September 05, 2007
Sheet 16 of 51

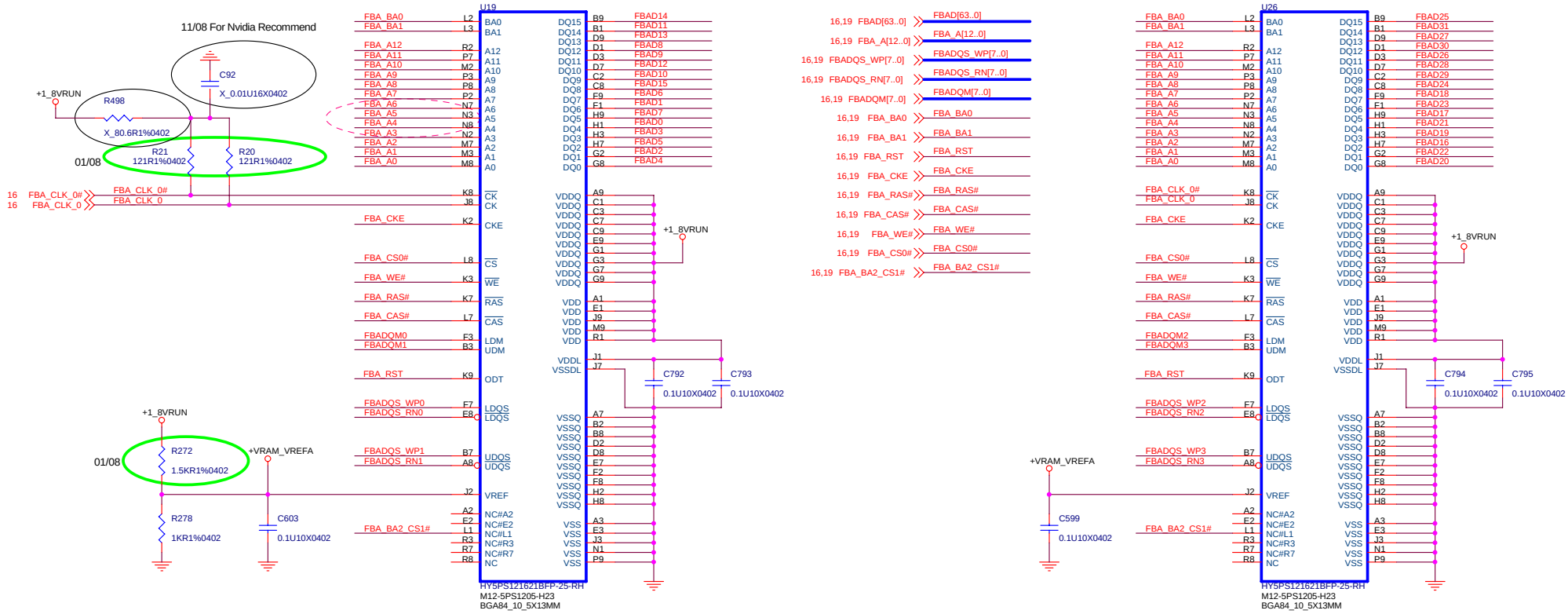


10/20

MSI CORPORATION		
Title: NB8M POWER		
Size: Custom	Document Number: MS-1636	Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 17 of 51	

256 M GDDRII Channels A Rank 0~1

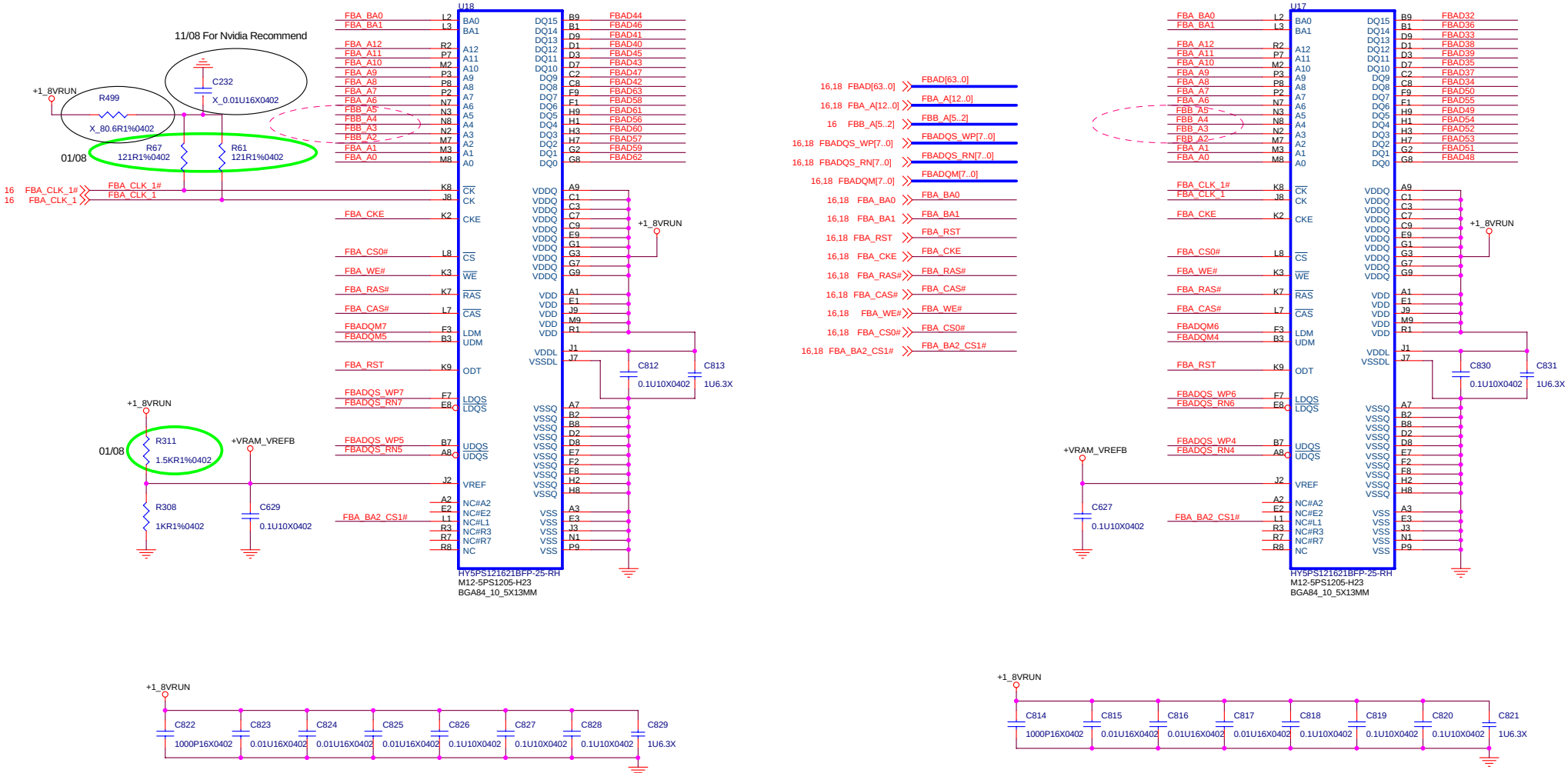
Channel A



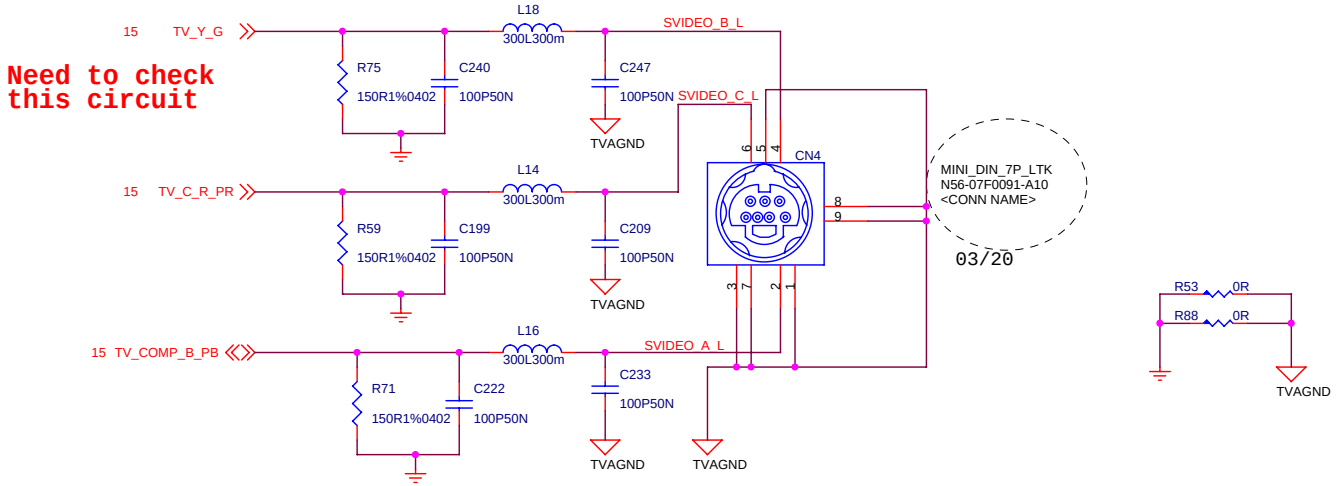
MSI CORPORATION			
Title: GDDRII_32Mx16_BGA_A			
Size: Custom	Document Number: MS-1636		Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 18 of 51		

256 M GDDRII Channel A Rank 2~3

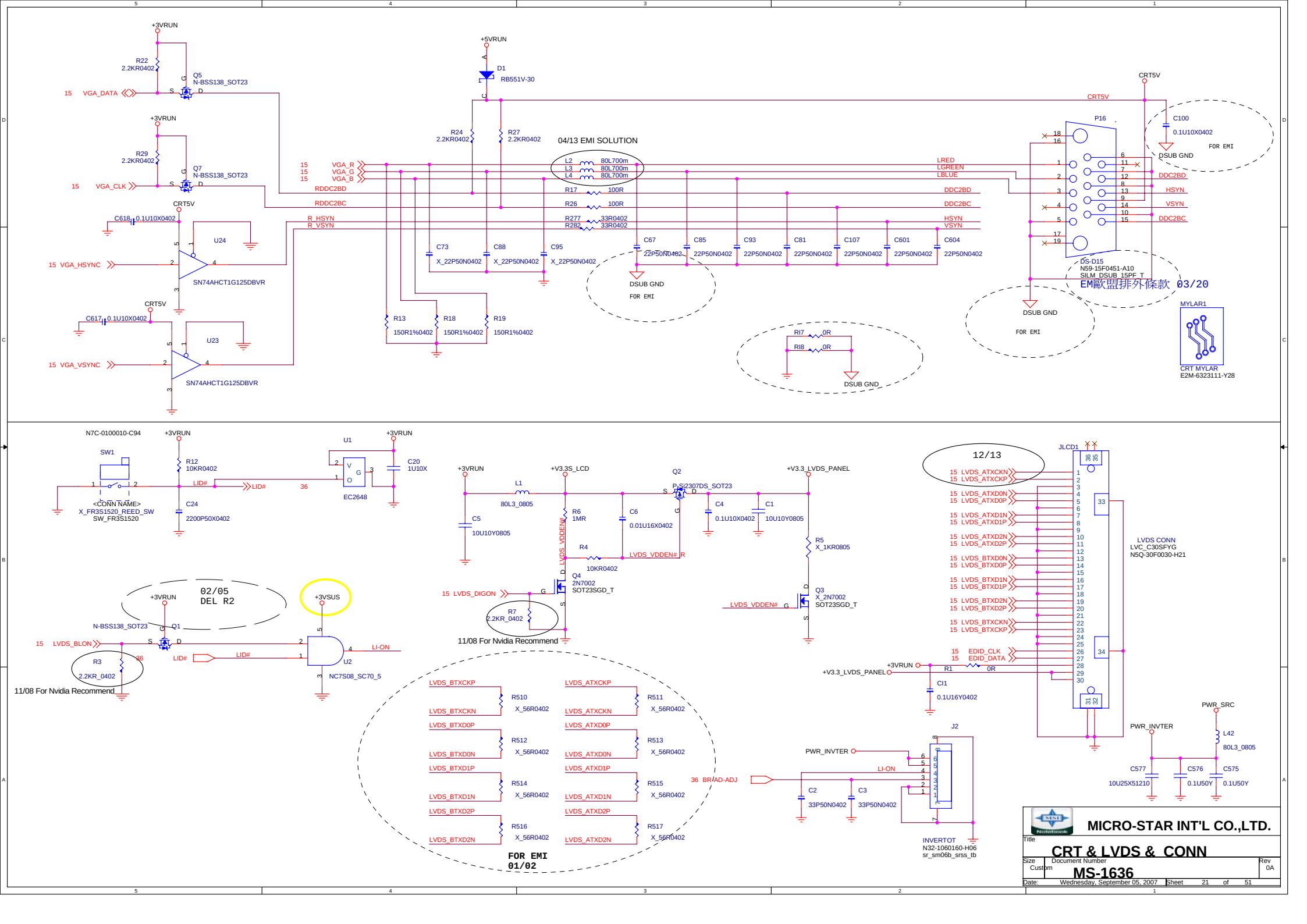
Channel A



MSI CORPORATION		
Title: GDDRII_32Mx16_BGA_B		
Size: Custom	Document Number: MS-1636	Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 19 of 51	



MICRO-STAR INT'L CO.,LTD.			
Title: S-VEDIO			
Size: B	Document Number: MS-1636		Rev: 0A
Date: Wednesday, September 05, 2007	Sheet 20 of 51		

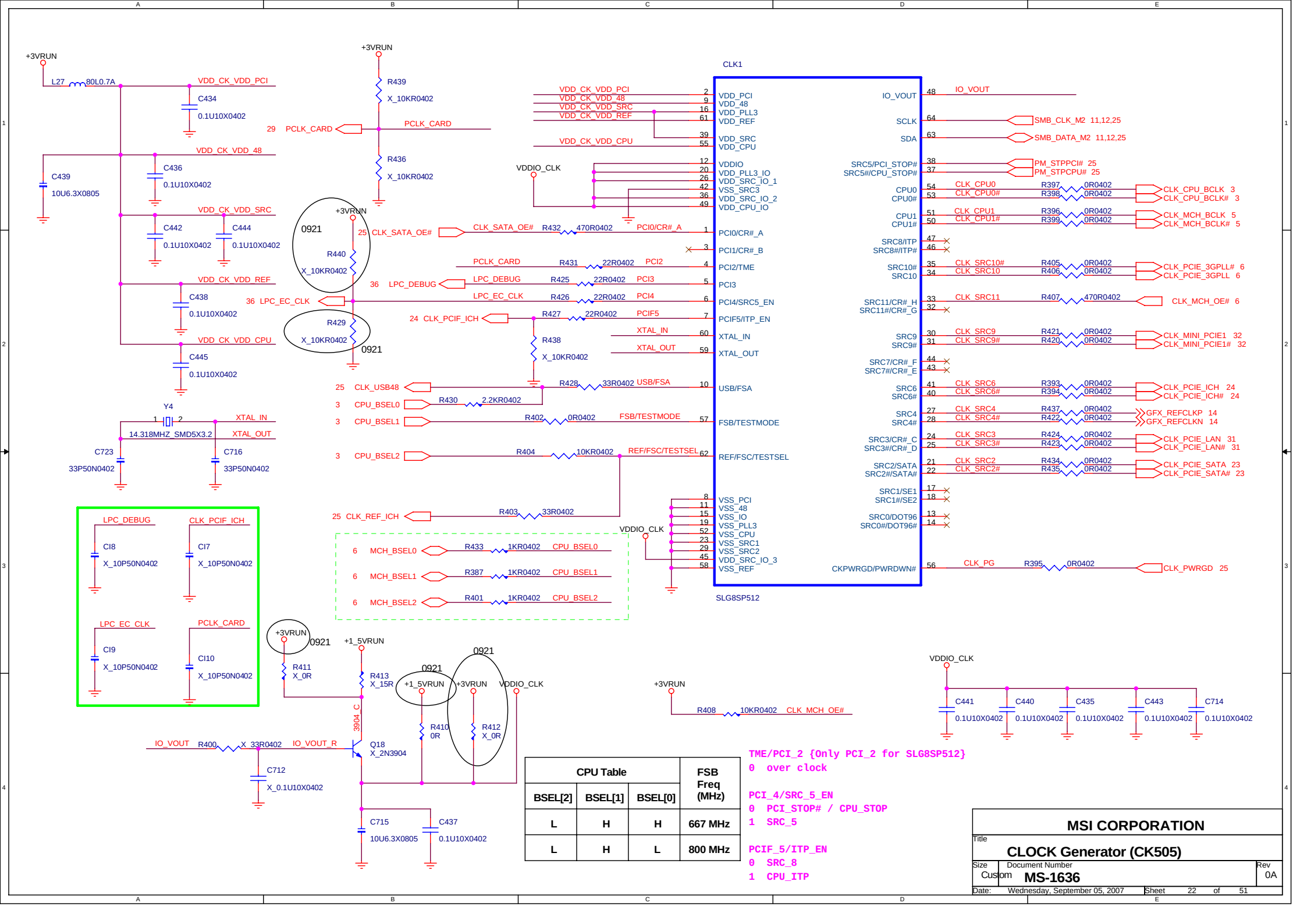


MICRO-STAR INT'L CO.,LTD.

Title: CRT & LVDS & CONN

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 21 of 51



CPU Table			FSB Freq (MHz)
BSEL[2]	BSEL[1]	BSEL[0]	
L	H	H	667 MHz
L	H	L	800 MHz

TME/PCI_2 {Only PCI_2 for SLG8SP512}
0 over clock

PCI_4/SRC_5_EN
0 PCI_STOP# / CPU_STOP
1 SRC_5

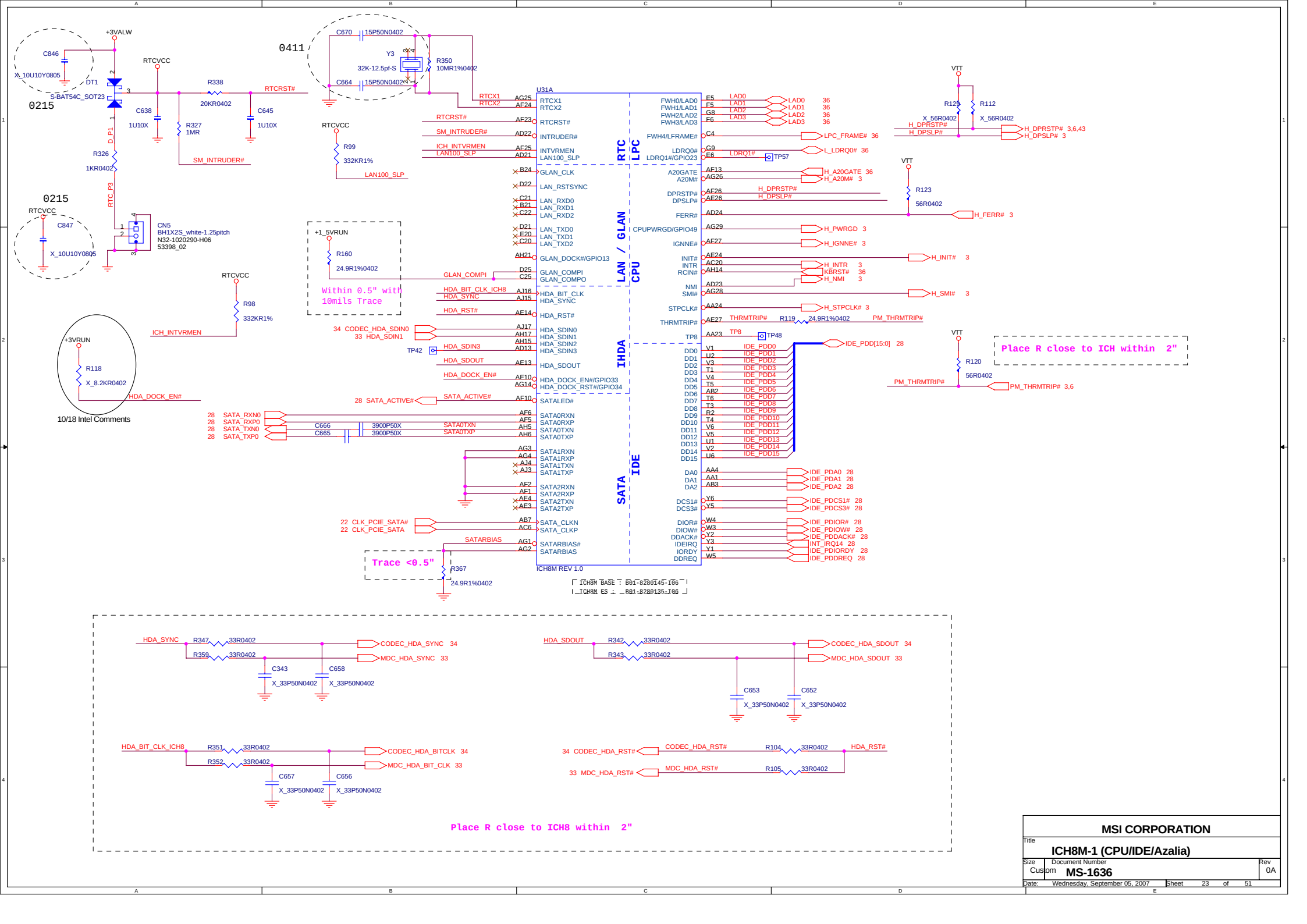
PCIF_5/ITP_EN
0 SRC_8
1 CPU_ITP

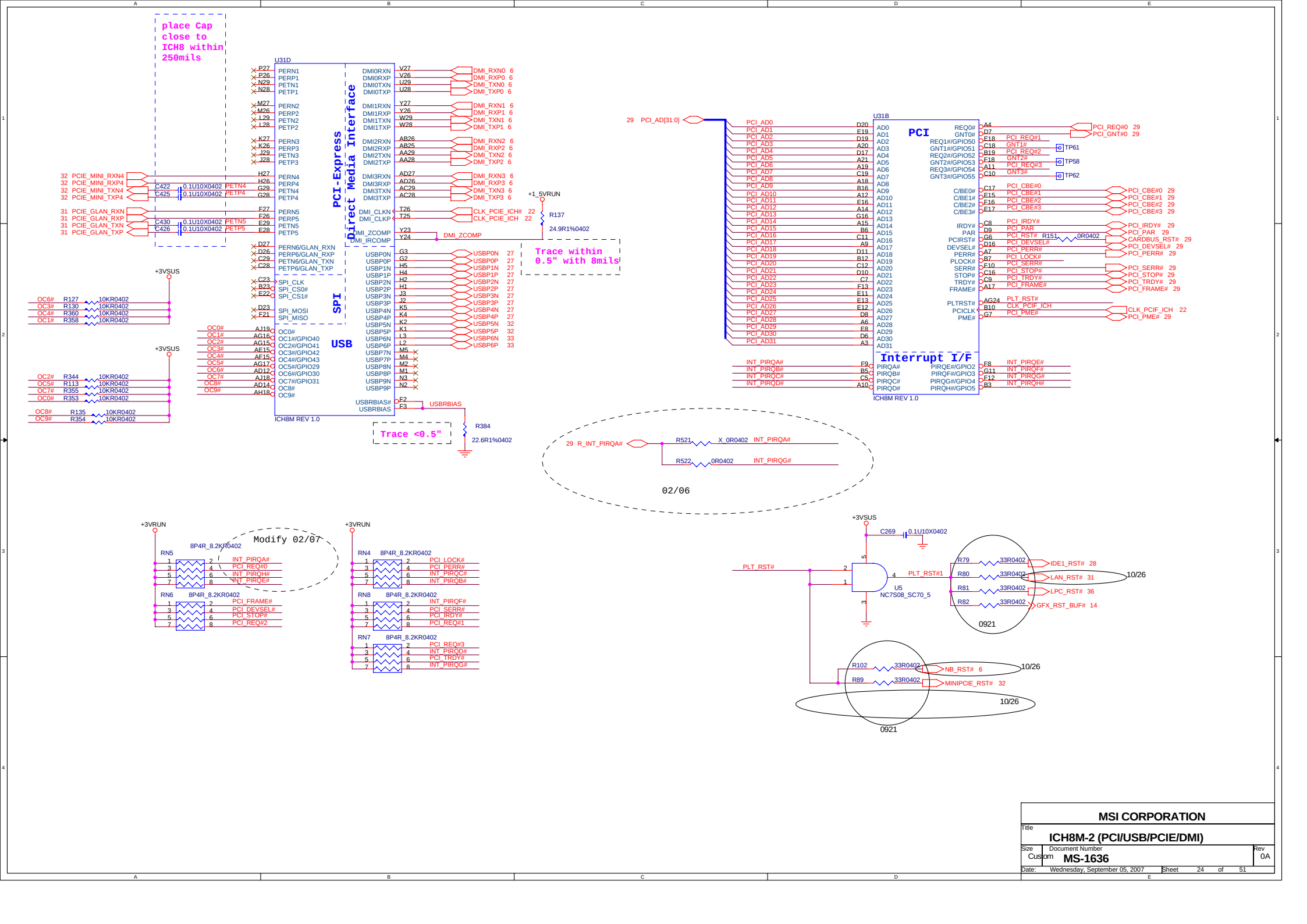
MSI CORPORATION

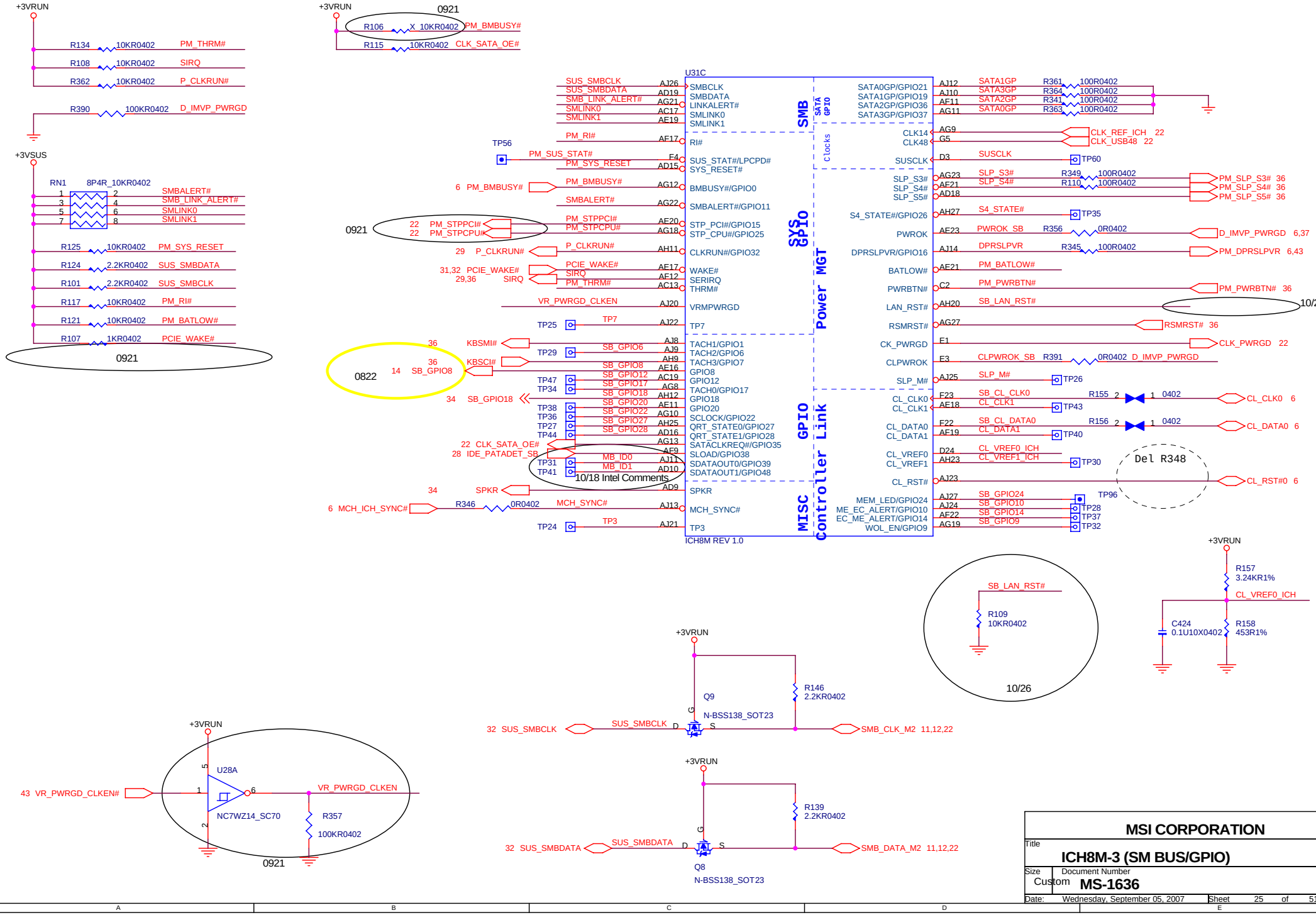
Title: CLOCK Generator (CK505)

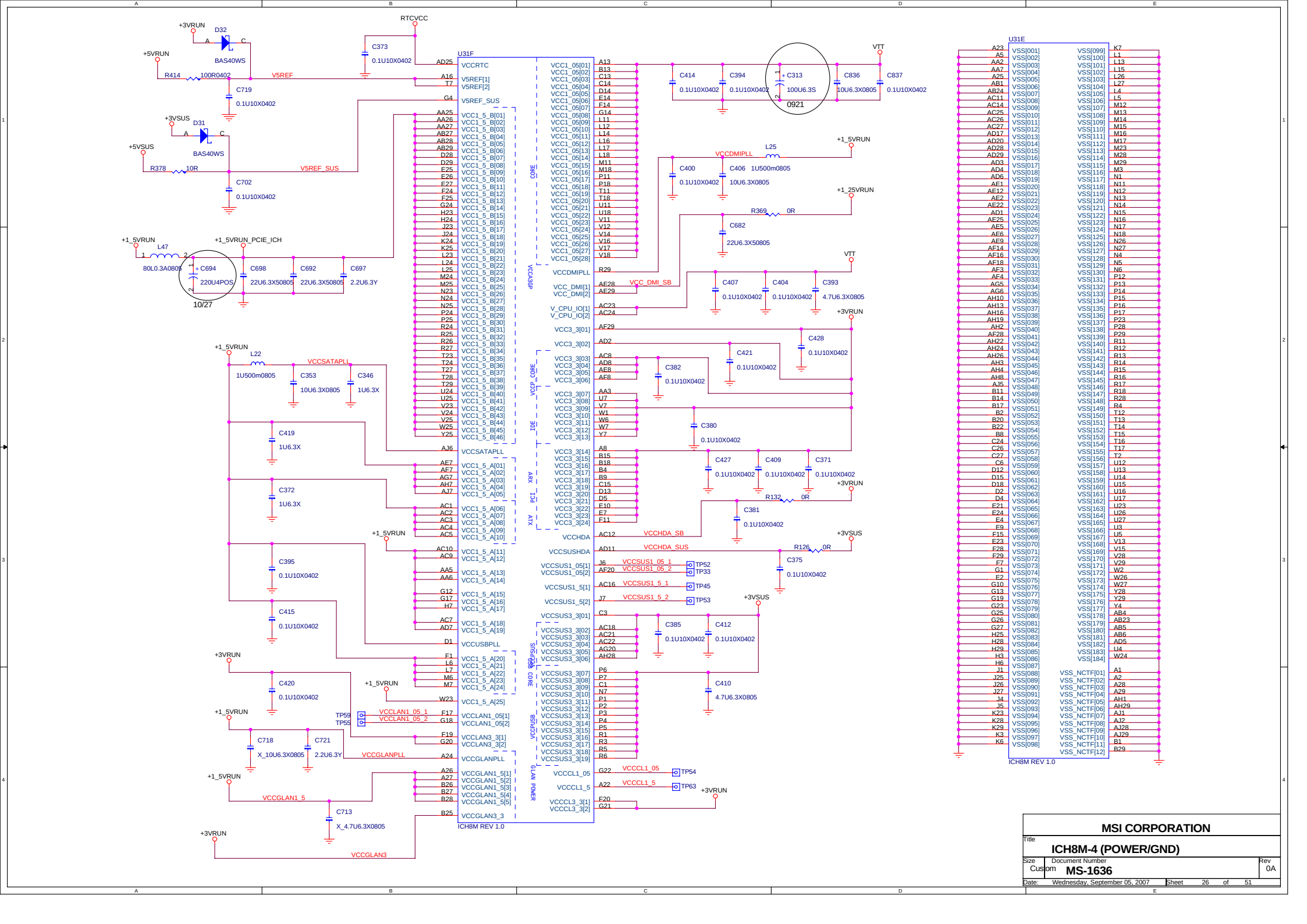
Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 22 of 51


+3VALW
C846
X_10U10Y0805
DT1
S-BAT54C_SOT23
0215
RTCVCC
R338
20KR0402
RTCRST#
C638
1U10X
R327
1MR
C645
1U10X
SM_INTRUDER#
R326
1KR0402
D_P1
RTC_P3
0215
RTCVCC
C847
X_10U10Y0805
CN5
BH1X2S_white-1.25pitch
N32-1020290-H06
53398_02
0411
C670
15P50N0402
Y3
32K-12.5pf-S
R350
10MR1%0402
C664
15P50N0402
RTCVCC
R99
332KR1%
LAN100_SLP
+1_5VRUN
R160
24.9R1%0402
Within 0.5" with
10mils Trace
GLAN_COMPI
RTCVCC
R98
332KR1%
ICH_INTVRMEN
+3VRUN
R118
X_8.2KR0402
HDA_DOCK_EN#
10/18 Intel Comments
U31A
RTCX1
RTCX2
RTCRST#
INTRUDER#
INTVRMEN
LAN100_SLP
RTC
LPC
GLAN_CLK
LAN_RSTSYNC
LAN_RXD0
LAN_RXD1
LAN_RXD2
LAN_TXD0
LAN_TXD1
LAN_TXD2
GLAN_DOCK#/GPIO13
GLAN_COMPI
GLAN_COMPO
LAN / GLAN
CPU
HDA_BIT_CLK
HDA_SYNC
HDA_RST#
HDA_SDIN0
HDA_SDIN1
HDA_SDIN2
HDA_SDIN3
HDA_SDOUT
HDA_DOCK_EN#/GPIO33
HDA_DOCK_RST#/GPIO34
IHDA
SATALED#
SATA0RXN
SATA0RXP
SATA0TXN
SATA0TXP
SATA1RXN
SATA1RXP
SATA1TXN
SATA1TXP
SATA2RXN
SATA2RXP
SATA2TXN
SATA2TXP
SATA_CLKN
SATA_CLKP
SATARBIAS#
SATARBIAS
SATA
IDE
ICH8M REV 1.0
FWH0/LAD0
FWH1/LAD1
FWH2/LAD2
FWH3/LAD3
FWH4/LFRAME#
LDRQ0#
LDRQ1#/GPIO23
A20GATE
A20M#
DPRSTP#
DPSLP#
FERR#
CPUPWRGD/GPIO49
IGNNE#
INIT#
INTR
RCIN#
NMI
SMI#
STPCLK#
THRMTRIP#
TP8
DD0
DD1
DD2
DD3
DD4
DD5
DD6
DD7
DD8
DD9
DD10
DD11
DD12
DD13
DD14
DD15
DA0
DA1
DA2
DCS1#
DCS3#
DIOR#
DIOW#
DDACK#
IDEIRQ
IORDY
DDREQ
ICH8M BASE : B01-8280145-I06
ICH8M ES : B01-8280135-I06
VTT
R125
R112
X_56R0402
H_DPRSTP#
H_DPSLP#
R123
56R0402
H_FERR# 3
H_INIT# 3
H_SMI# 3
H_STPCLK# 3
R119
24.9R1%0402
PM_THRMTRIP#
TP48
R120
56R0402
Place R close to ICH within 2"
PM_THRMTRIP# 3,6
IDE_PDD[15:0] 28
IDE_PDA0 28
IDE_PDA1 28
IDE_PDA2 28
IDE_PDCS1# 28
IDE_PDCS3# 28
IDE_PDIOR# 28
IDE_PDIOW# 28
IDE_PDDACK# 28
INT_IRQ14 28
IDE_PDIORDY 28
IDE_PDDREQ 28
LAD0 36
LAD1 36
LAD2 36
LAD3 36
LDRQ1# TP57
LPC_FRAME# 36
L_LDRQ0# 36
H_A20GATE 36
H_A20M# 3
H_DPRSTP# 3,6,43
H_DPSLP# 3
H_PWRGD 3
H_IGNNE# 3
H_INTR 3
KBRST# 36
H_NMI 3
H_INIT# 3
H_SMI# 3
H_STPCLK# 3
THRMTRIP# R119 24.9R1%0402 PM_THRMTRIP#
SATA_ACTIVE#
SATA_RXN0
SATA_RXP0
SATA_TXN0
SATA_TXP0
C666 3900P50X SATA0TXN
C665 3900P50X SATA0TXP
CLK_PCIE_SATA#
CLK_PCIE_SATA
SATARBIAS
R367
24.9R1%0402
Trace <0.5"
HDA_SYNC R347 33R0402 CODEC_HDA_SYNC 34
R359 33R0402 MDC_HDA_SYNC 33
C343 X_33P50N0402
C658 X_33P50N0402
HDA_SDOUT R342 33R0402 CODEC_HDA_SDOUT 34
R343 33R0402 MDC_HDA_SDOUT 33
C653 X_33P50N0402
C652 X_33P50N0402
HDA_BIT_CLK_ICH8 R351 33R0402 CODEC_HDA_BITCLK 34
R352 33R0402 MDC_HDA_BIT_CLK 33
C657 X_33P50N0402
C656 X_33P50N0402
34 CODEC_HDA_RST# CODEC_HDA_RST# R104 33R0402 HDA_RST#
33 MDC_HDA_RST# MDC_HDA_RST# R105 33R0402
Place R close to ICH8 within 2"
MSI CORPORATION
Title
ICH8M-1 (CPU/IDE/Azalia)
Size Custom
Document Number MS-1636
Rev 0A
Date: Wednesday, September 05, 2007
Sheet 23 of 51


place Cap close to ICH8 within 250mils
U31D
PCI-Express
Direct Media Interface
SPI
USB
ICH8M REV 1.0
Trace within 0.5" with 8mils
Trace <0.5"
+1_5VRUN
+3VSUS
+3VRUN
U31B
PCI
Interrupt I/F
ICH8M REV 1.0
02/06
Modify 02/07
0921
10/26
MSI CORPORATION
ICH8M-2 (PCI/USB/PCIE/DMI)
MS-1636
Wednesday, September 05, 2007


+3VRUN
R134 10KR0402 PM_THRM#
R108 10KR0402 SIRQ
R362 10KR0402 P_CLKRUN#
R390 100KR0402 D_IMVP_PWRGD
+3VSUS
RN1 8P4R_10KR0402
SMBALERT#
SMB_LINK_ALERT#
SMLINK0
SMLINK1
R125 10KR0402 PM_SYS_RESET
R124 2.2KR0402 SUS_SMBDATA
R101 2.2KR0402 SUS_SMBCLK
R117 10KR0402 PM_RI#
R121 10KR0402 PM_BATLOW#
R107 1KR0402 PCIE_WAKE#
0921
+3VRUN
0921
R106 X 10KR0402 PM_BMBUSY#
R115 10KR0402 CLK_SATA_OE#
U31C
SMBCLK
SMBDATA
LINKALERT#
SMLINK0
SMLINK1
RI#
SUS_STAT#/LPCPD#
SYS_RESET#
BMBUSY#/GPIO0
SMBALERT#/GPIO11
STP_PCI#/GPIO15
STP_CPU#/GPIO25
CLKRUN#/GPIO32
WAKE#
SERIRQ
THRM#
VRMPWRGD
TP7
TACH1/GPIO1
TACH2/GPIO6
TACH3/GPIO7
GPIO8
GPIO12
TACH0/GPIO17
GPIO18
GPIO20
SCLOCK/GPIO22
QRT_STATE0/GPIO27
QRT_STATE1/GPIO28
SATACLKREQ#/GPIO35
SLOAD/GPIO38
SDATAOUT0/GPIO39
SDATAOUT1/GPIO48
SPKR
MCH_SYNC#
TP3
ICH8M REV 1.0
SMB
SATA GPIO
Clocks
SYS GPIO
Power MGT
GPIO
Controller Link
MISC
SUS_SMBCLK
SUS_SMBDATA
SMB_LINK_ALERT#
SMLINK0
SMLINK1
PM_RI#
TP56
PM_SUS_STAT#
PM_SYS_RESET
6 PM_BMBUSY#
PM_BMBUSY#
SMBALERT#
0921
22 PM_STPPCI#
22 PM_STPCPU#
29 P_CLKRUN#
31,32 PCIE_WAKE#
29,36 SIRQ
PM_THRM#
VR_PWRGD_CLKEN
TP25
TP7
36 KBSMI#
TP29
SB_GPIO6
0822
36 KBSCI#
14 SB_GPIO8
SB_GPIO8
SB_GPIO12
TP47
SB_GPIO17
TP34
SB_GPIO18
34 SB_GPIO18
SB_GPIO20
TP38
SB_GPIO22
TP36
SB_GPIO27
TP27
SB_GPIO28
TP44
22 CLK_SATA_OE#
28 IDE_PATADET_SB
MB_ID0
TP31
MB_ID1
TP41
10/18 Intel Comments
34 SPKR
6 MCH_ICH_SYNC#
R346 0R0402 MCH_SYNC#
TP24
TP3
SATA0GP/GPIO21
SATA1GP/GPIO19
SATA2GP/GPIO36
SATA3GP/GPIO37
CLK14
CLK48
SUSCLK
SLP_S3#
SLP_S4#
SLP_S5#
S4_STATE#/GPIO26
PWROK
DPRSLPVR/GPIO16
BATLOW#
PWRBTN#
LAN_RST#
RSMRST#
CK_PWRGD
CLPWROK
SLP_M#
CL_CLK0
CL_CLK1
CL_DATA0
CL_DATA1
CL_VREF0
CL_VREF1
CL_RST#
MEM_LED/GPIO24
ME_EC_ALERT/GPIO10
EC_ME_ALERT/GPIO14
WOL_EN/GPIO9
SATA1GP R361 100R0402
SATA3GP R364 100R0402
SATA2GP R341 100R0402
SATA0GP R363 100R0402
CLK_REF_ICH 22
CLK_USB48 22
SUSCLK TP60
SLP_S3# R349 100R0402 PM_SLP_S3# 36
SLP_S4# R110 100R0402 PM_SLP_S4# 36
PM_SLP_S5# 36
S4_STATE# TP35
PWROK_SB R356 0R0402 D_IMVP_PWRGD 6,37
DPRSLPVR R345 100R0402 PM_DPRSLPVR 6,43
PM_BATLOW#
PM_PWRBTN# PM_PWRBTN# 36
SB_LAN_RST# 10/26
RSMRST# 36
CLK_PWRGD 22
CLPWROK_SB R391 0R0402 D_IMVP_PWRGD
SLP_M# TP26
SB_CL_CLK0 R155 2 1 0402 CL_CLK0 6
CL_CLK1 TP43
SB_CL_DATA0 R156 2 1 0402 CL_DATA0 6
CL_DATA1 TP40
CL_VREF0_ICH
CL_VREF1_ICH TP30
Del R348
CL_RST#0 6
SB_GPIO24 TP96
SB_GPIO10 TP28
SB_GPIO14 TP37
SB_GPIO9 TP32
SB_LAN_RST#
R109 10KR0402
10/26
+3VRUN
R157 3.24KR1%
CL_VREF0_ICH
C424 0.1U10X0402
R158 453R1%
+3VRUN
Q9
N-BSS138_SOT23
R146 2.2KR0402
32 SUS_SMBCLK
SUS_SMBCLK
SMB_CLK_M2 11,12,22
+3VRUN
Q8
N-BSS138_SOT23
R139 2.2KR0402
32 SUS_SMBDATA
SUS_SMBDATA
SMB_DATA_M2 11,12,22
+3VRUN
U28A
NC7WZ14_SC70
43 VR_PWRGD_CLKEN#
VR_PWRGD_CLKEN
R357 100KR0402
0921
MSI CORPORATION
Title
ICH8M-3 (SM BUS/GPIO)
Size Custom
Document Number MS-1636
Rev 0A
Date: Wednesday, September 05, 2007
Sheet 25 of 51

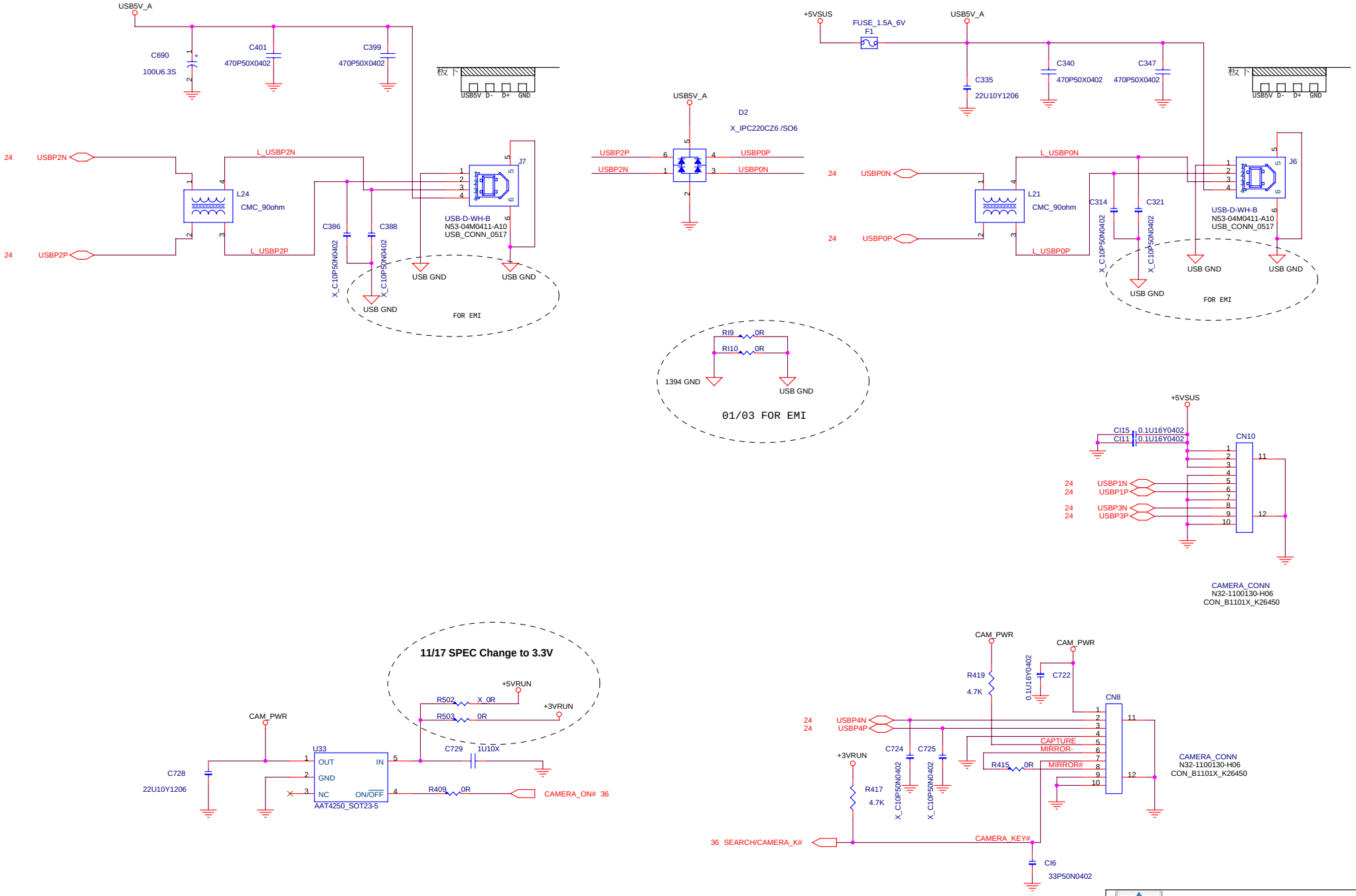


MSI CORPORATION

Title: ICH8M-4 (POWER/GND)

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 26 of 51

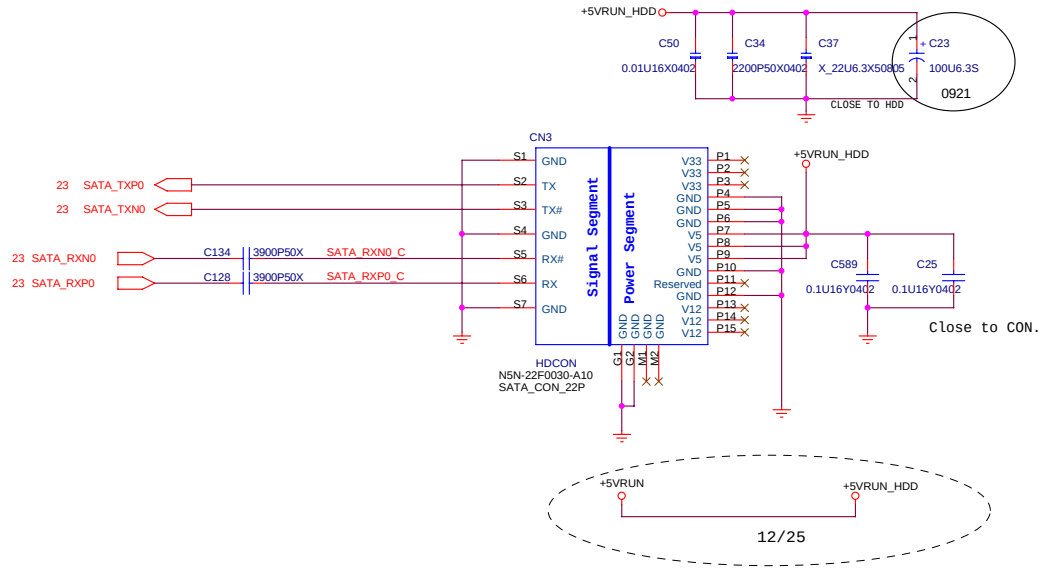


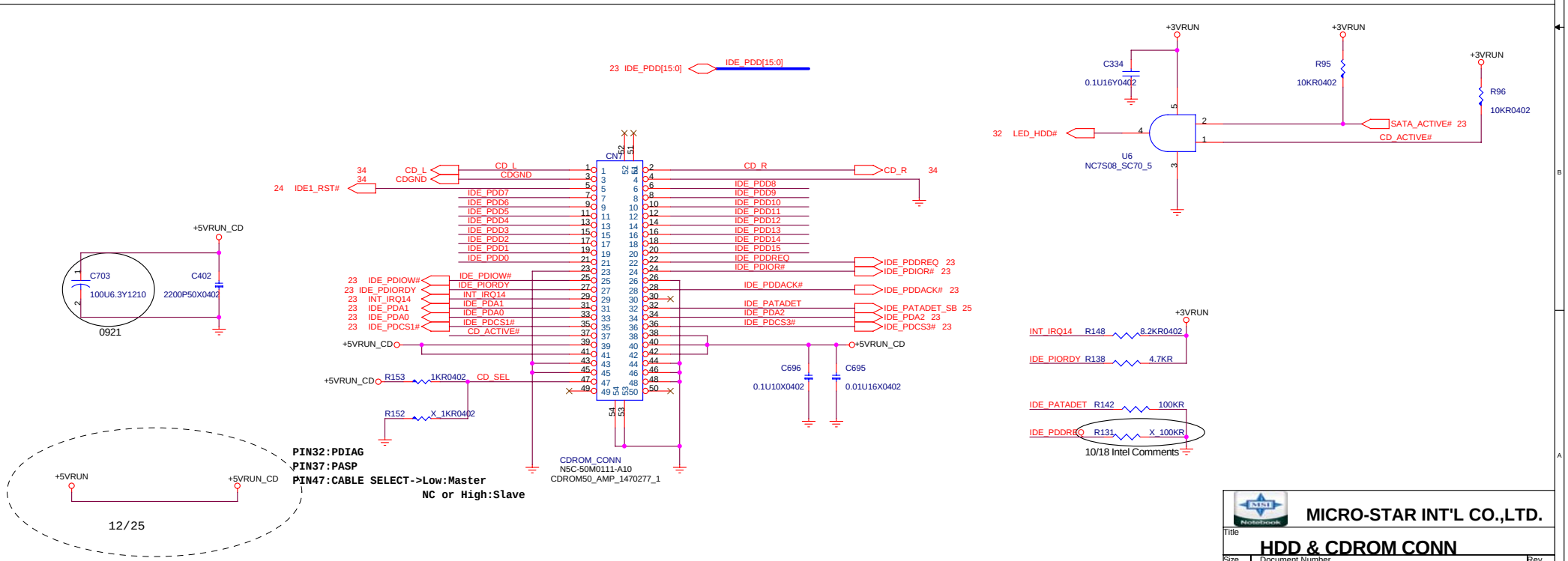
MICRO-STAR INT'L CO.,LTD.

Title: USB2.0 CON X4 & Camera

Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 27 of 51



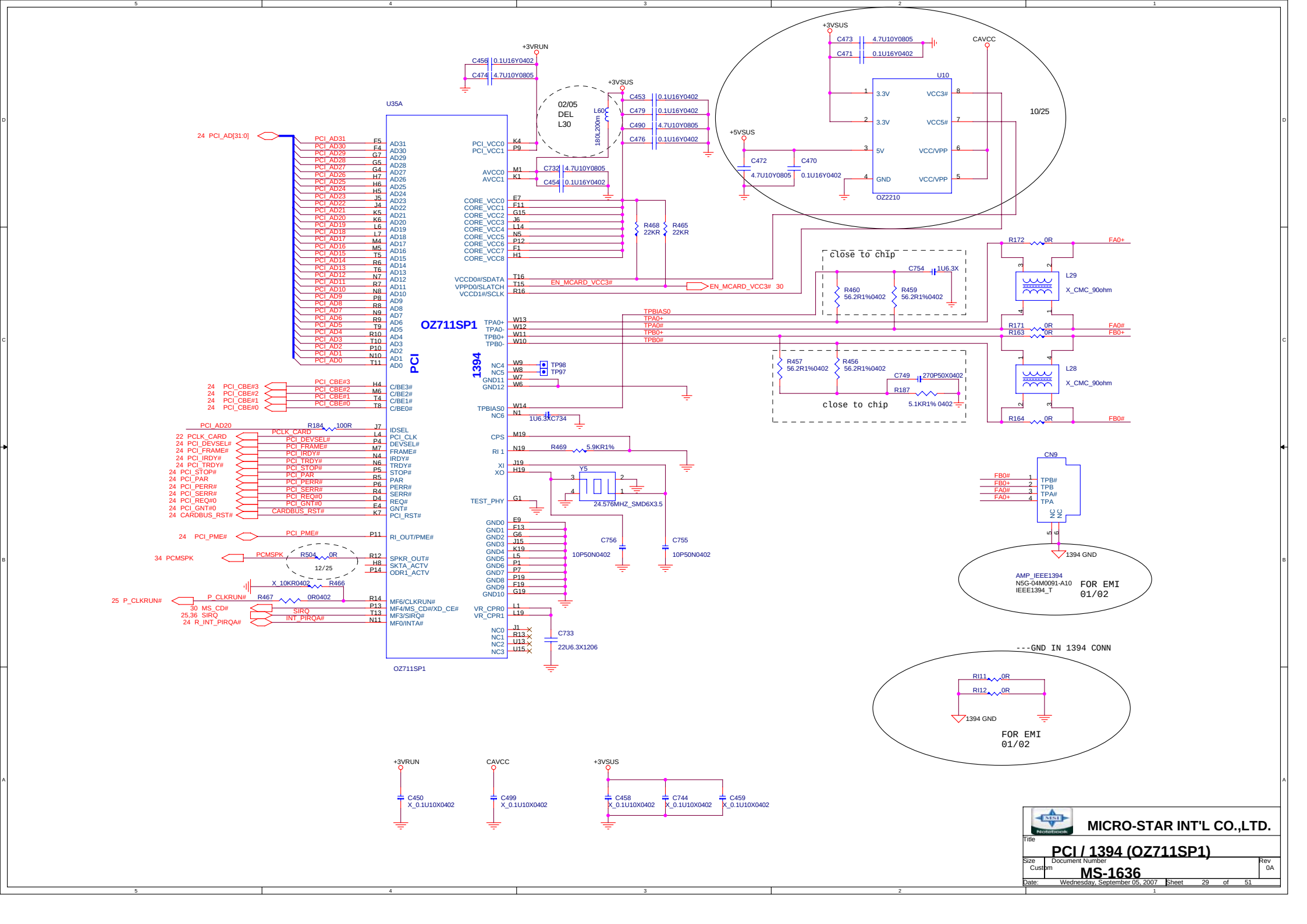


MICRO-STAR INT'L CO.,LTD.

Title: HDD & CDROM CONN

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 28 of 51



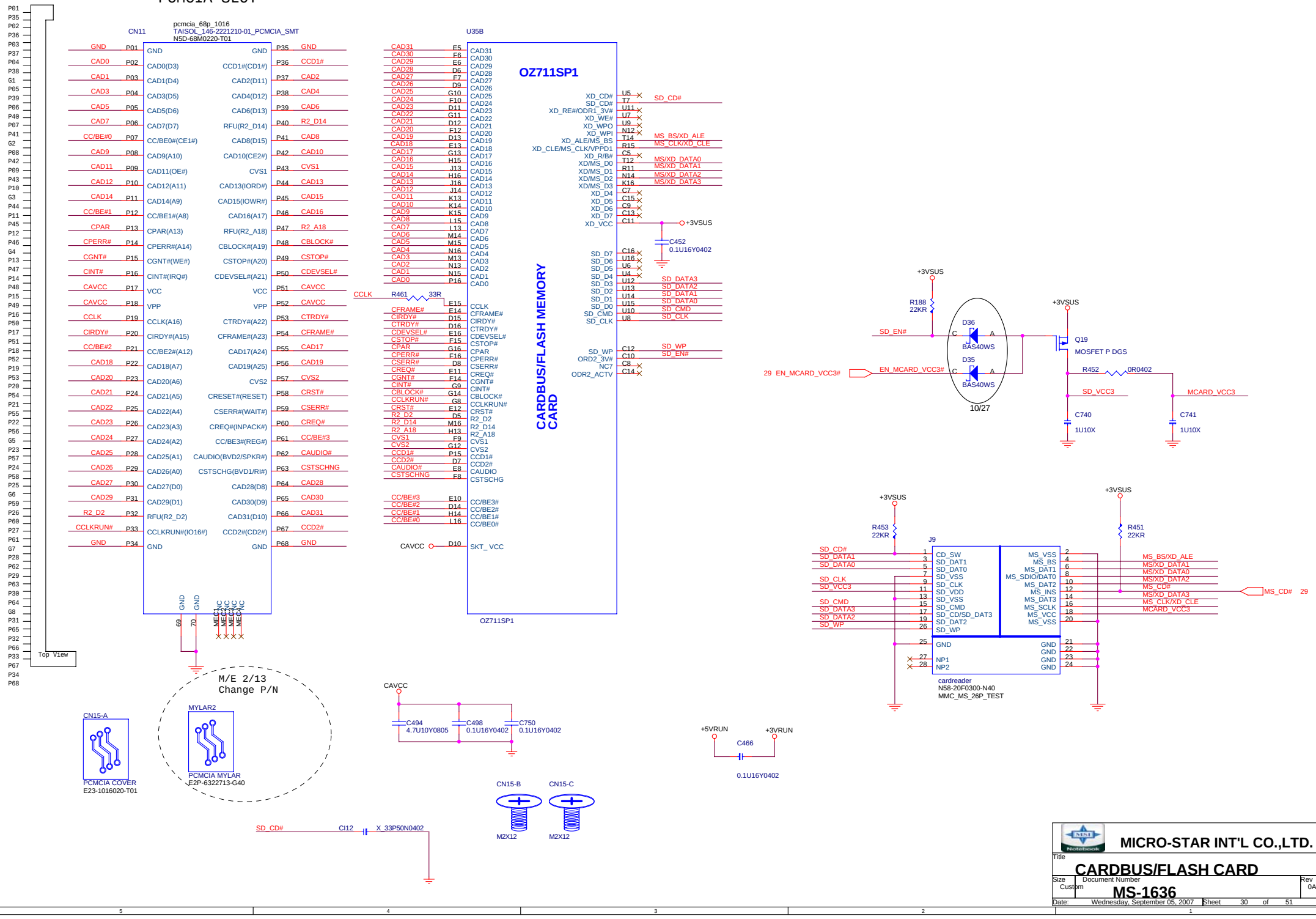
MICRO-STAR INT'L CO.,LTD.

Title: PCI / 1394 (OZ711SP1)

Size: Custom | Document Number: MS-1636 | Rev: 0A

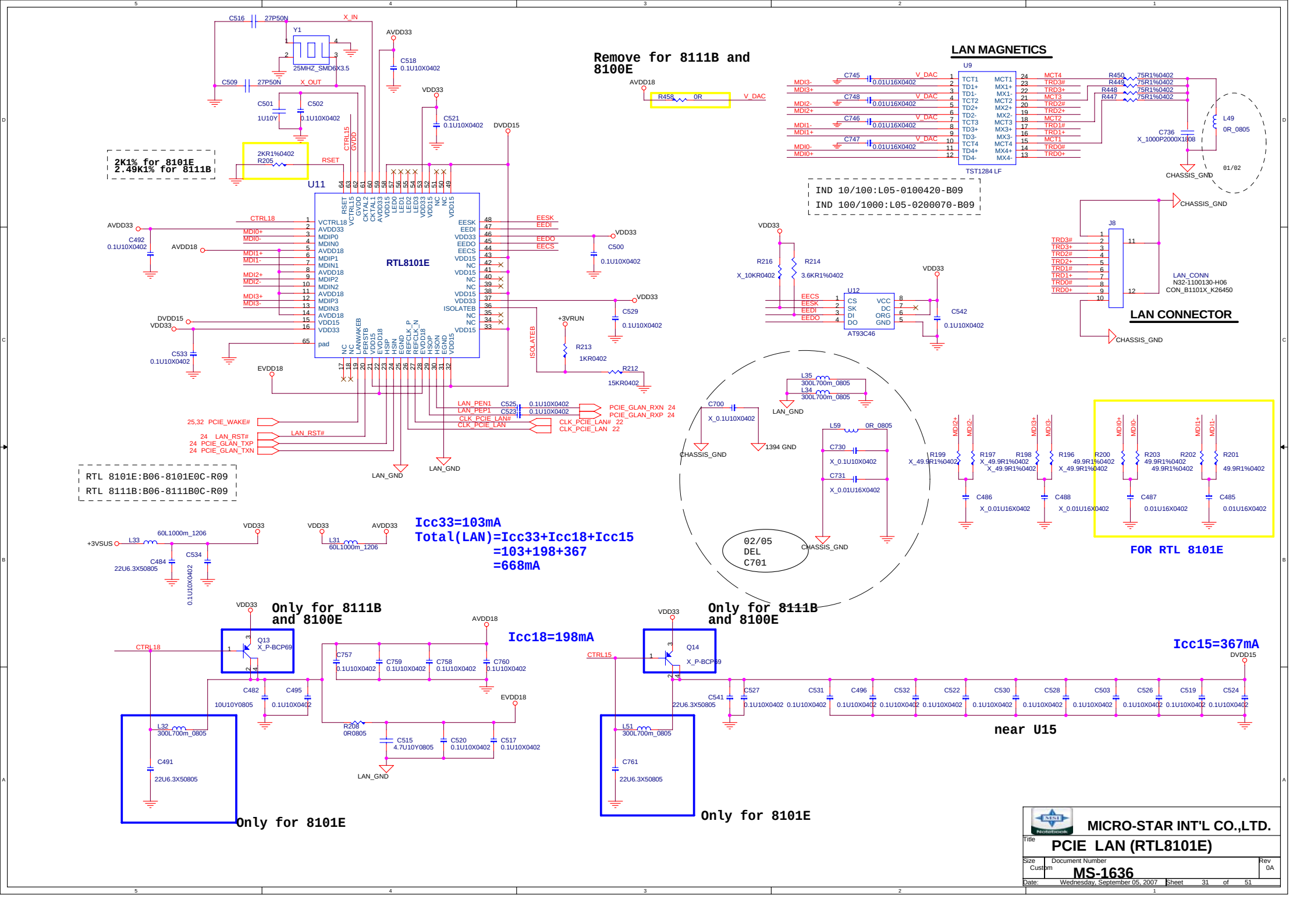
Date: Wednesday, September 05, 2007 | Sheet 29 of 51

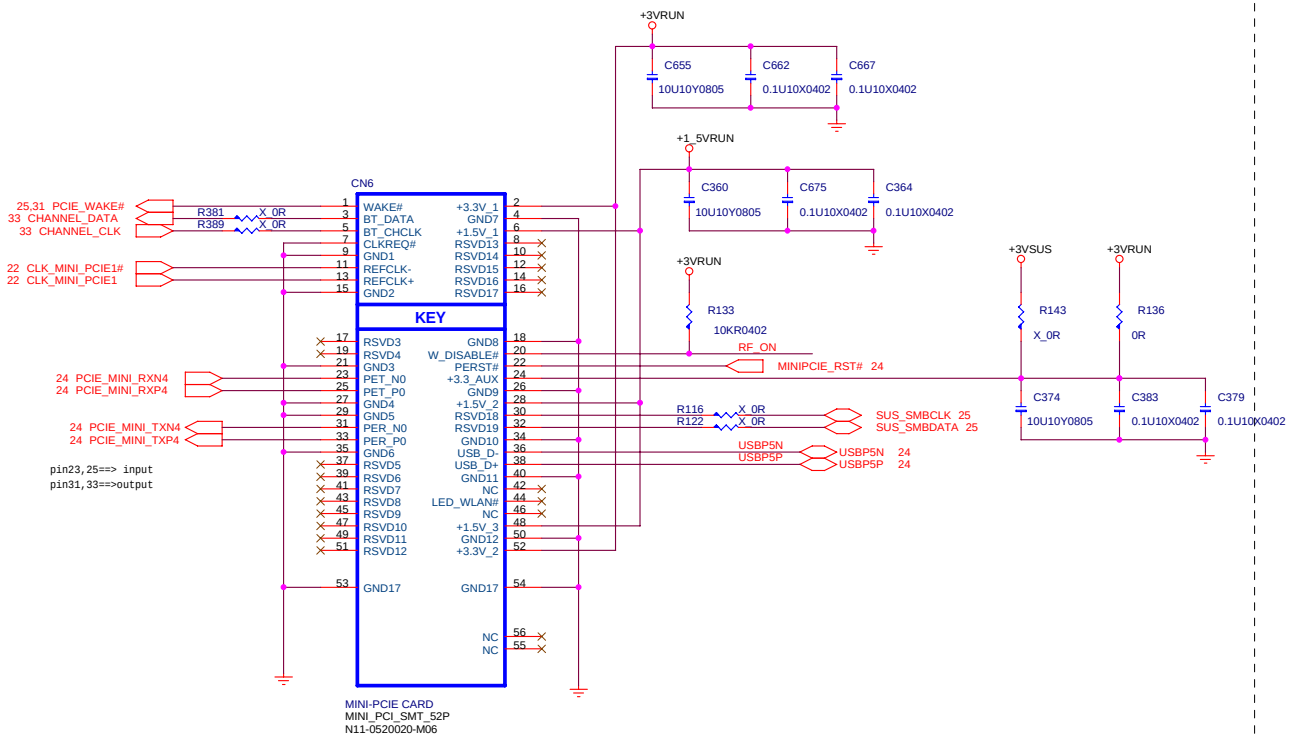
PCMCIA SLOT



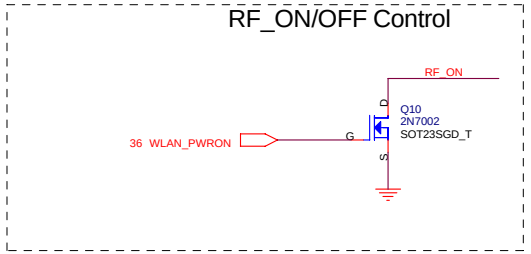
Title	CARDBUS/FLASH CARD
Size	Custom
Document Number	MS-1636
Rev	0A
Date:	Wednesday, September 05, 2007
Sheet	30 of 51

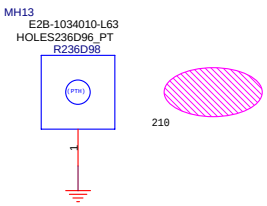
MICRO-STAR INT'L CO.,LTD.

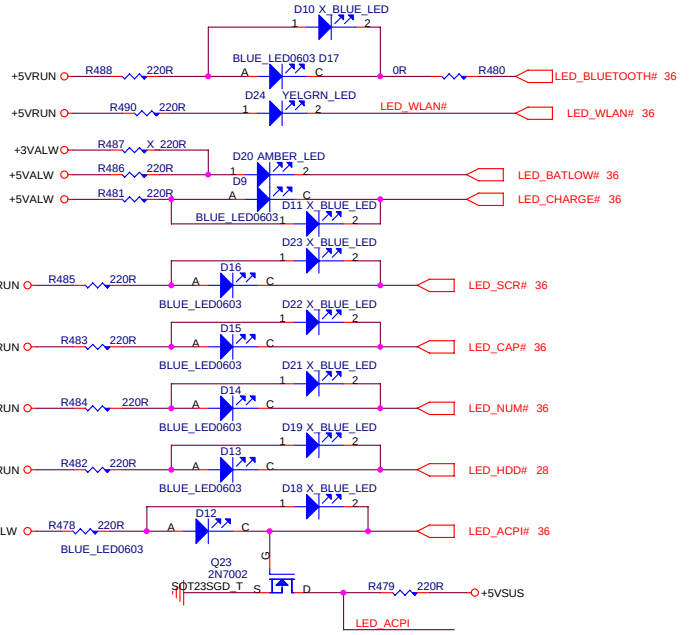

Remove for 8111B and 8100E
LAN MAGNETICS
LAN CONNECTOR
RTL8101E
U11
U9
U12
J8
TST1284 LF
AT93C46
LAN_CONN N32-1100130-H06 CON_B1101X_K26450
IND 10/100:L05-0100420-B09
IND 100/1000:L05-0200070-B09
2K1% for 8101E
2.49K1% for 8111B
RTL 8101E:B06-8101E0C-R09
RTL 8111B:B06-8111B0C-R09
Icc33=103mA
Total(LAN)=Icc33+Icc18+Icc15
=103+198+367
=668mA
Icc18=198mA
Icc15=367mA
Only for 8111B and 8100E
Only for 8111B and 8100E
Only for 8101E
Only for 8101E
FOR RTL 8101E
near U15
02/05 DEL C701
MICRO-STAR INT'L CO.,LTD.
PCIE LAN (RTL8101E)
MS-1636
Wednesday, September 05, 2007

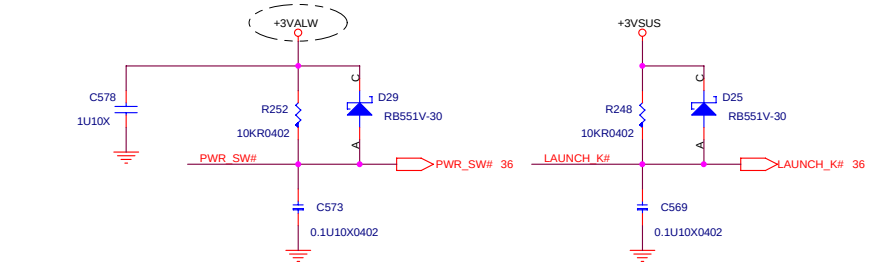


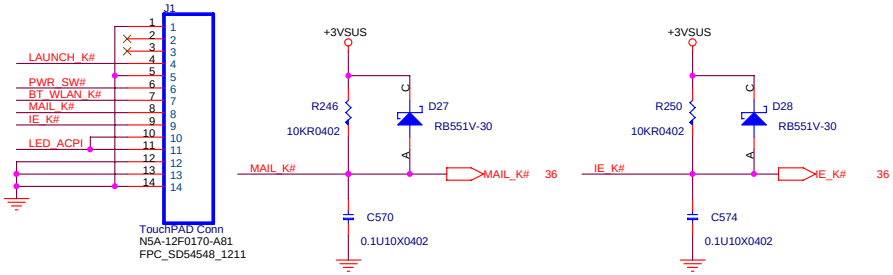
Unmount CN14-A : mount MH18
Unmount MH18 : mount CN14-A

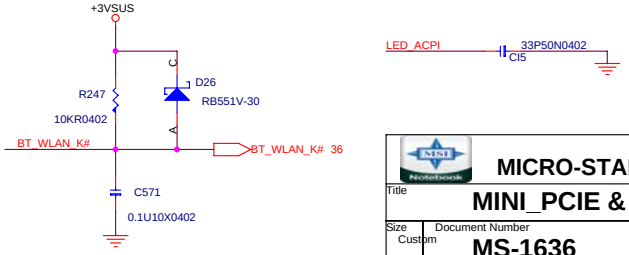












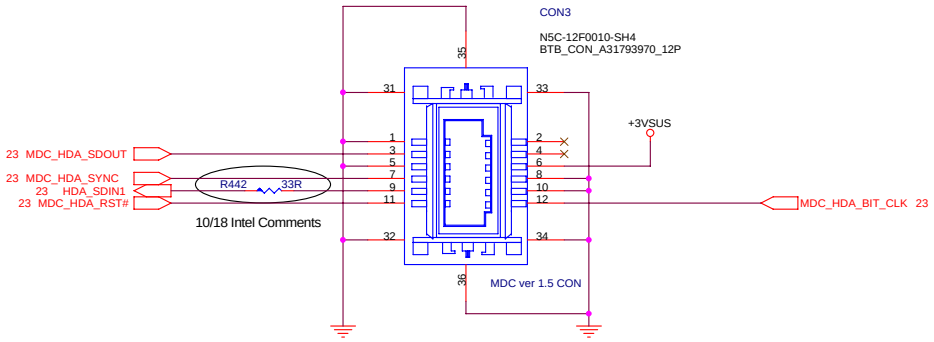
MICRO-STAR INT'L CO.,LTD.

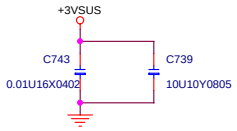
Title: MINI_PCIE & LED & SW

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 32 of 51

MDC Connector

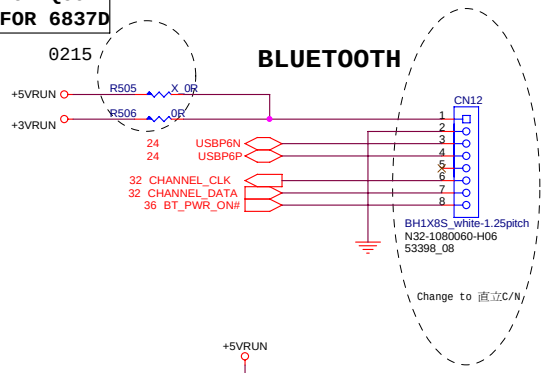


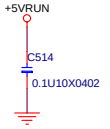


+3VRUN FOR QCOM
+5VRUN FOR 6837D

0215

BLUETOOTH



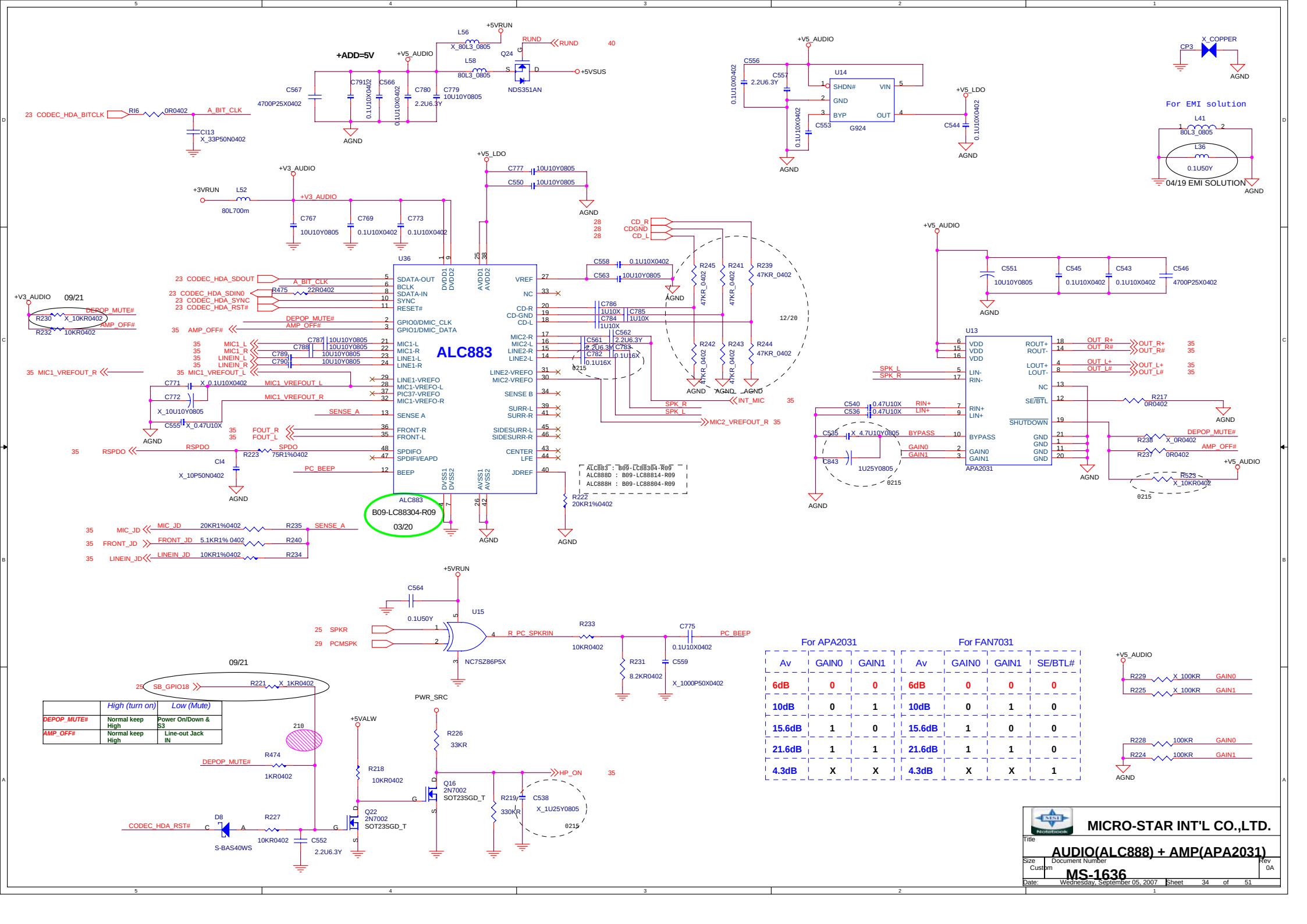


MICRO-STAR INT'L CO.,LTD.

Title: MDC&BLUETOOTH CONN

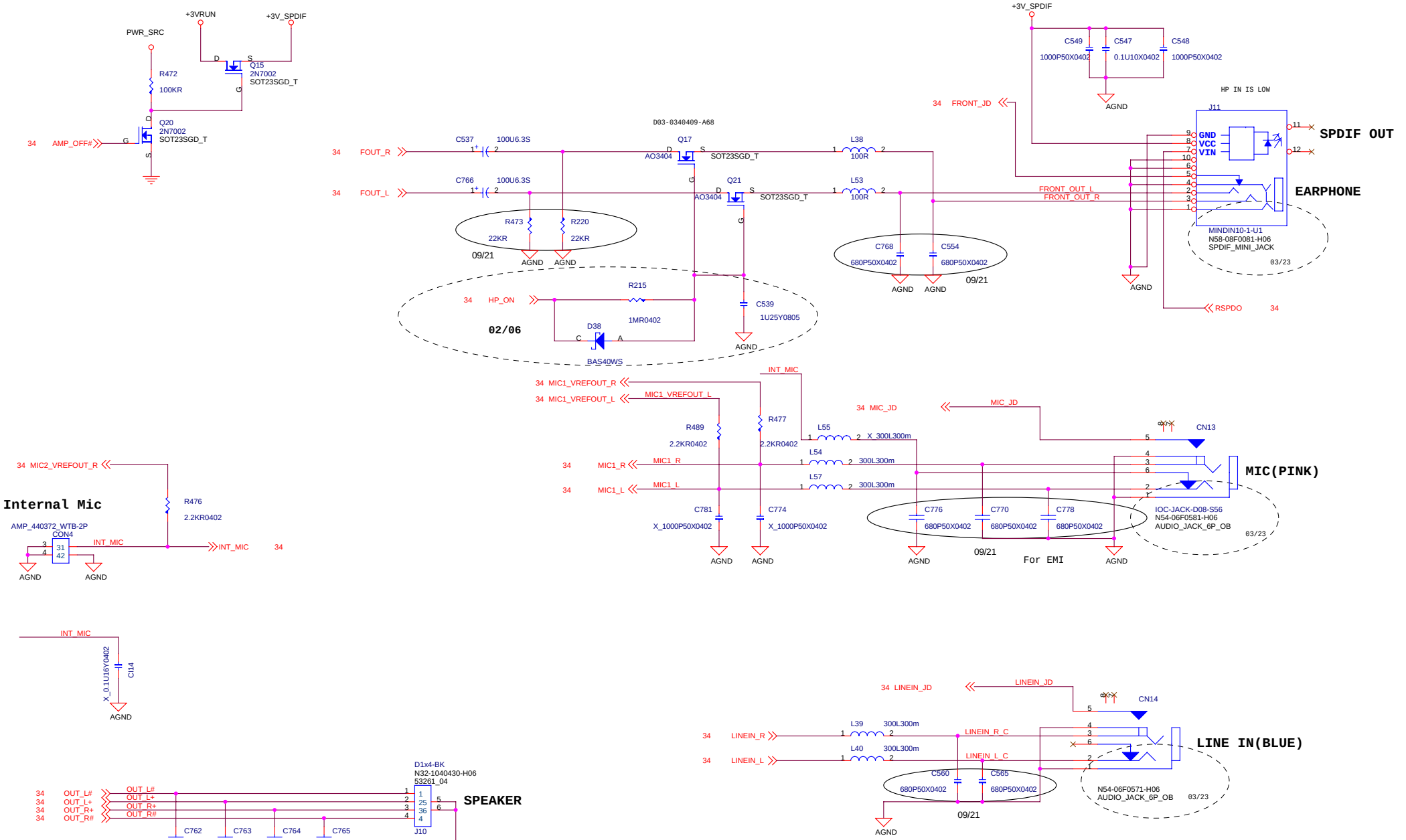
Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 33 of 51



	High (turn on)	Low (Mute)
DEPOP_MUTE#	Normal keep High	Power On/Down & S3
AMP_OFF#	Normal keep High	Line-out Jack IN

For APA2031			For FAN7031			
Av	GAIN0	GAIN1	Av	GAIN0	GAIN1	SE/BTL#
6dB	0	0	6dB	0	0	0
10dB	0	1	10dB	0	1	0
15.6dB	1	0	15.6dB	1	0	0
21.6dB	1	1	21.6dB	1	1	0
4.3dB	X	X	4.3dB	X	X	1

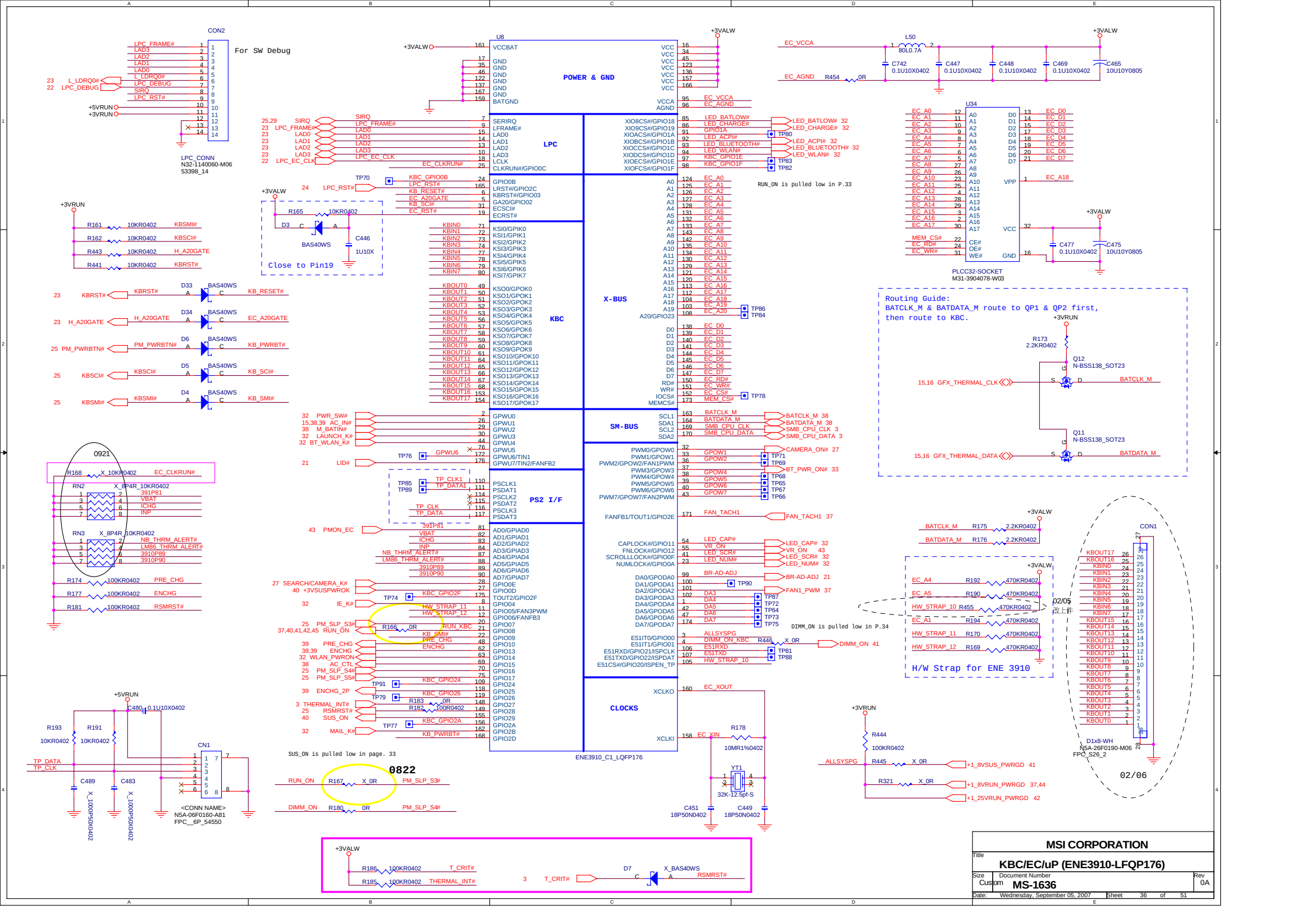


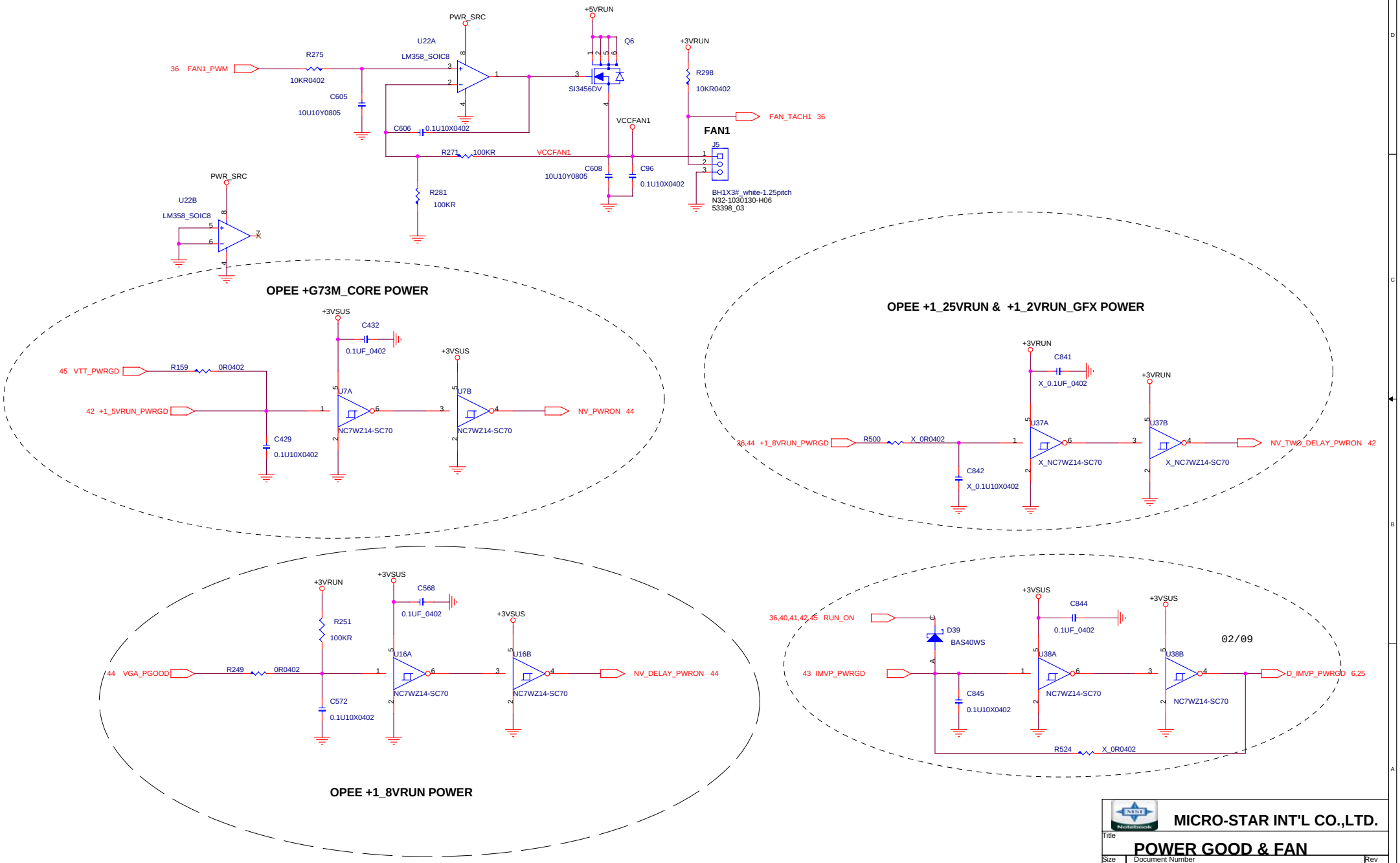
MICRO-STAR INT'L CO.,LTD.

Title: SPK / HP / MIC

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 35 of 51


CON2
For SW Debug
LPC_CONN
N32-1140060-M06
53398_14
U8
POWER & GND
LPC
KBC
X-BUS
SM-BUS
PS2 I/F
CLOCKS
ENE3910_C1_LQFP176
U34
PLCC32-SOCKET
M31-3904078-W03
Routing Guide:
BATCLK_M & BATDATA_M route to QP1 & QP2 first,
then route to KBC.
Close to Pin19
H/W Strap for ENE 3910
CON1
D1x8-WH
N5A-26F0190-M06
FPC_S26_2
<CONN NAME>
N5A-06F0160-A81
FPC__6P_54550
0921
0822
02/05
02/06
MSI CORPORATION
KBC/EC/uP (ENE3910-LFQP176)
Size Custom
Document Number MS-1636
Rev 0A
Date: Wednesday, September 05, 2007
Sheet 36 of 51

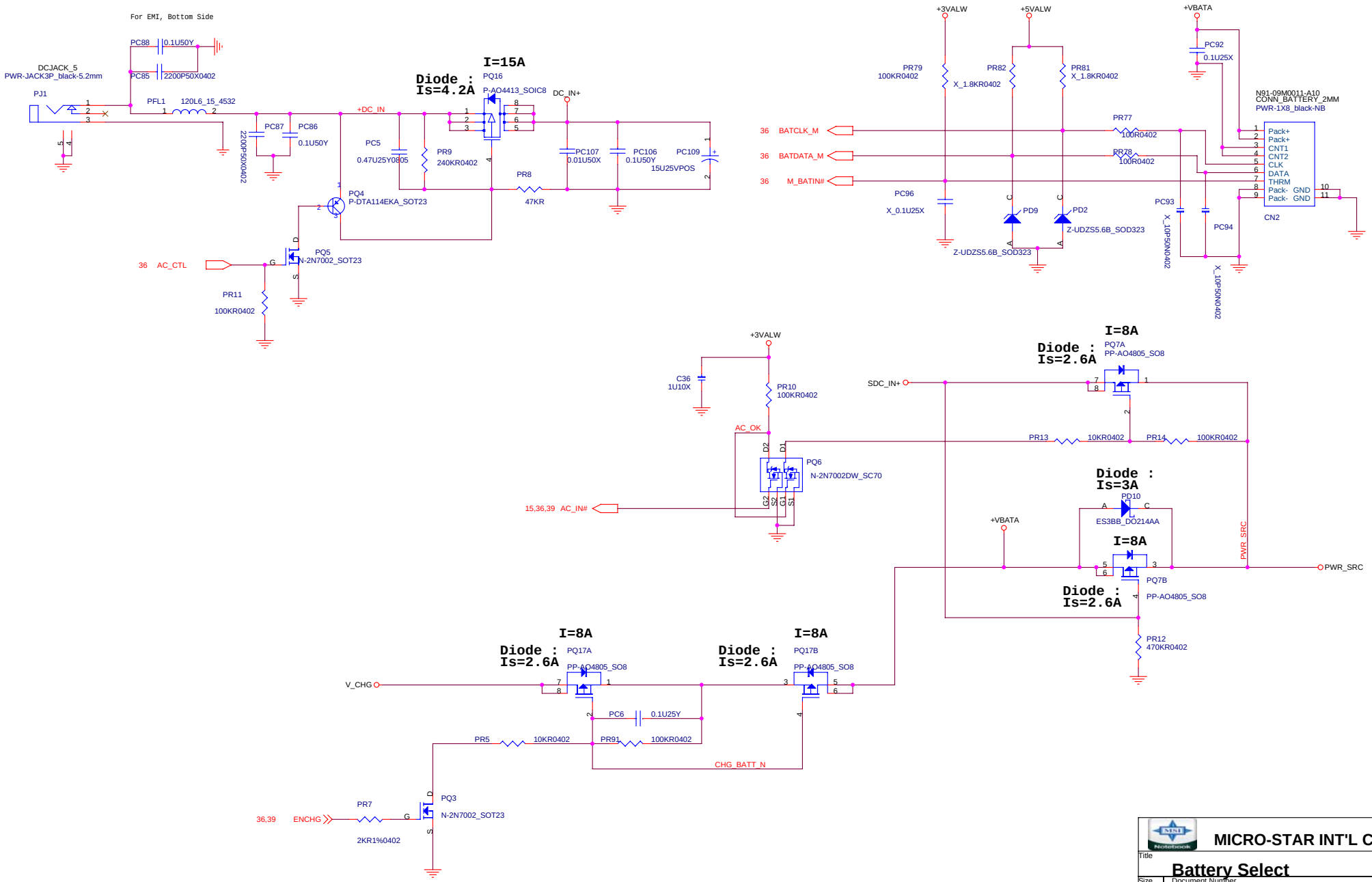


MICRO-STAR INT'L CO.,LTD.

Title: POWER GOOD & FAN

Size	Document Number	Rev
Custom	MS-1636	0A

Date: Wednesday, September 05, 2007 Sheet 37 of 51



MICRO-STAR INT'L CO.,LTD.

Title: Battery Select

Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 38 of 51

Adapter= 90 W 2.048/7.15*(53.6+7.15)=17.4V

Adapter input voltage set 17.4 Voltage

ENCHG-2P	PRE_CHG	ENCHG	
0	1	1	Pre-charge
1	0	1	3S2P-Fast charge
0	0	1	3S3P-Fast charge
0	0	0	STOP CHARGE

CELL GND=2 CELLS
FLOAT=3 CELLS
REFIN=4 CELLS

3S2P: Charge current set 3 Amp
3S3P: Charge current set 4.5 Amp
Pre-charger: Charge current set 220mA

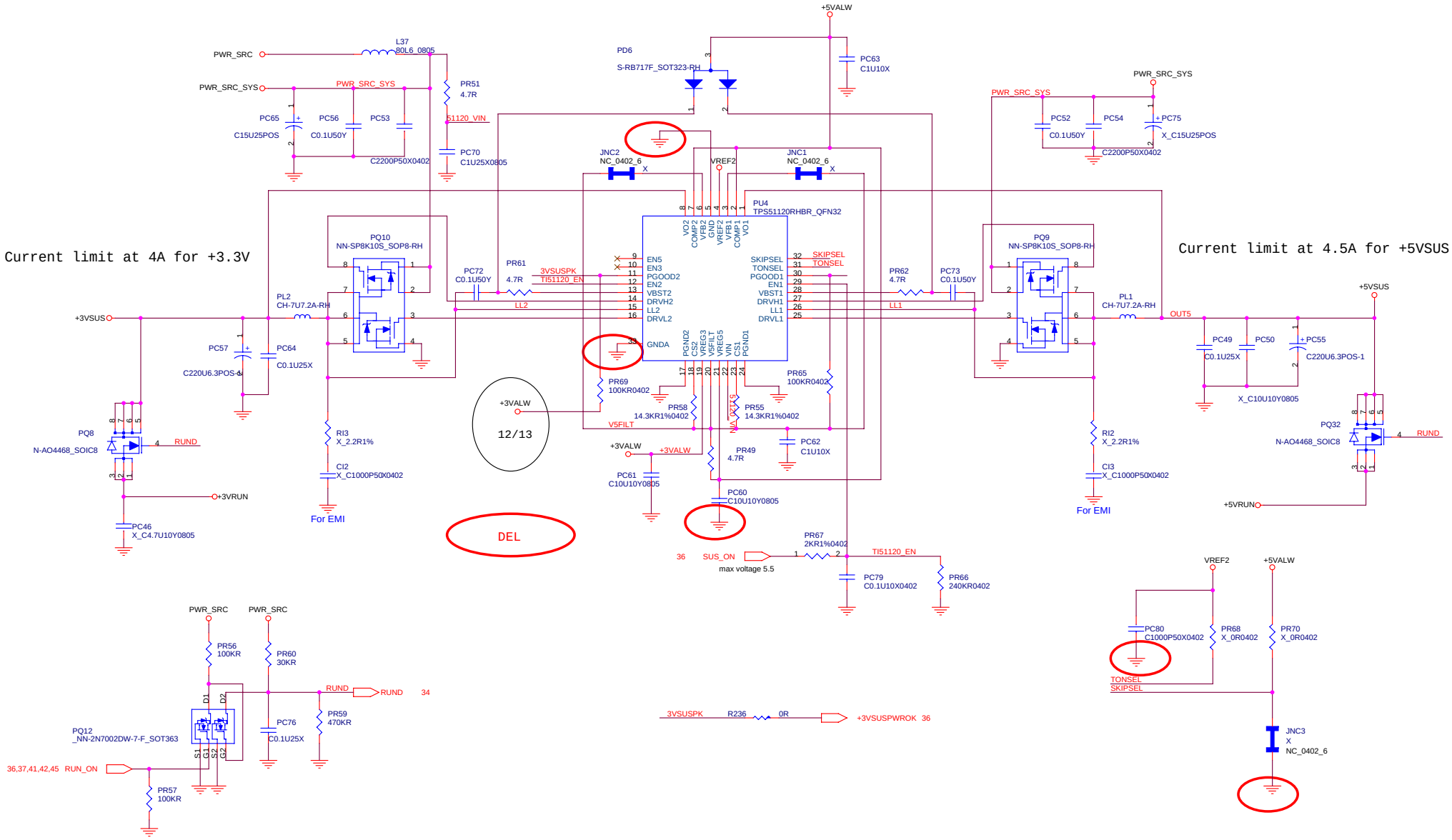
SET Iin MAX = 4.2A

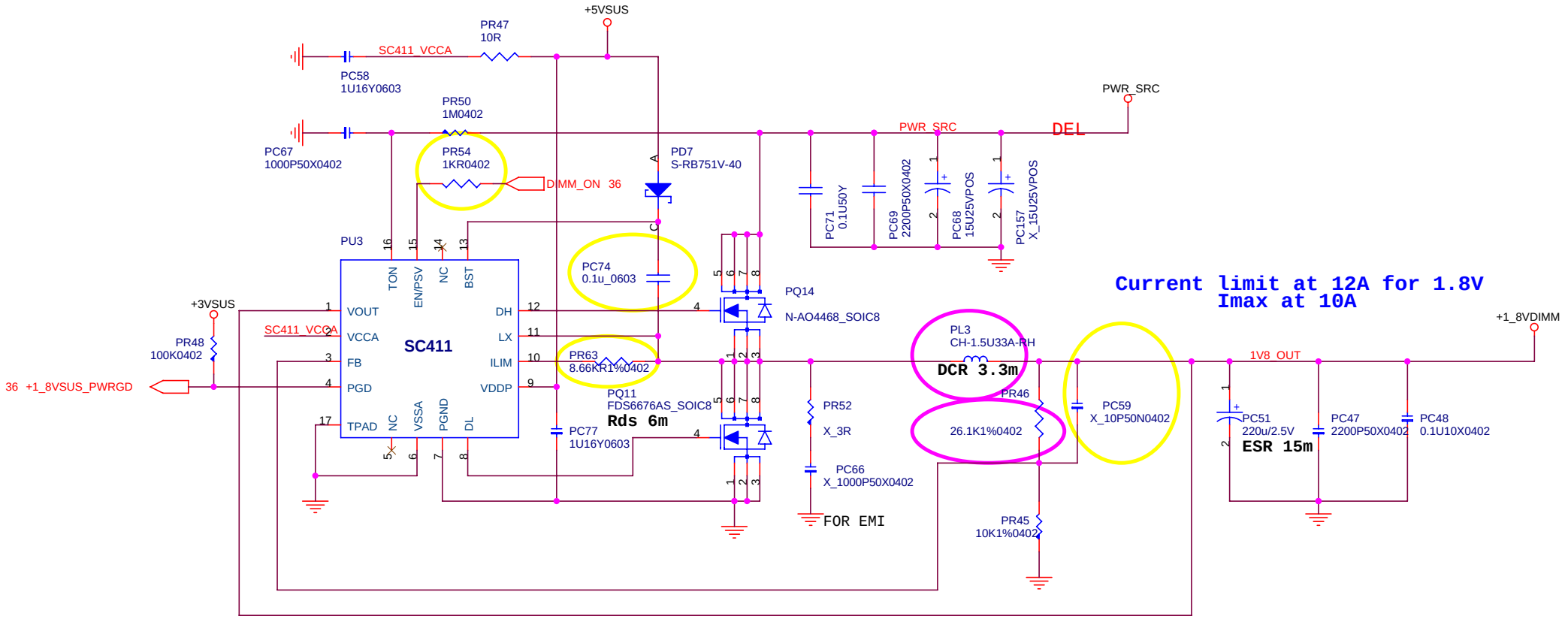
MICRO-STAR INT'L CO.,LTD.

Title: Battery Charger

Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 39 of 51


Current limit at 4A for +3.3V
Current limit at 4.5A for +5VSUS
+5VALW
PWR_SRC
PWR_SRC_SYS
L37
80L6_0805
PR51
4.7R
51120_VIN
PC65
C15U25POS
PC56
C0.1U50Y
PC53
C2200P50X0402
PC70
C1U25X0805
PD6
S-RB717F_SOT323-RH
PC63
C1U10X
JNC2
NC_0402_6
JNC1
NC_0402_6
VREF2
PU4
TPS51120RHBR_QFN32
PQ10
NN-SP8K10S_SOP8-RH
PQ9
NN-SP8K10S_SOP8-RH
PC52
C0.1U50Y
PC54
PC75
X_C15U25POS
C2200P50X0402
PL2
CH-7U7.2A-RH
PL1
CH-7U7.2A-RH
+3VSUS
PC57
C220U6.3POS-1
PC64
C0.1U25X
PR61
4.7R
3VSUSPK
TI51120_EN
LL2
PC72
C0.1U50Y
PR62
4.7R
PC73
C0.1U50Y
LL1
OUT5
PC49
C0.1U25X
PC50
PC55
C220U6.3POS-1
X_C10U10Y0805
+5VSUS
PQ8
N-AO4468_SOIC8
RUND
+3VRUN
PC46
X_C4.7U10Y0805
RI3
X_2.2R1%
CI2
X_C1000P50X0402
For EMI
PR69
100KR0402
PR58
14.3KR1%0402
V5FILT
PR55
14.3KR1%0402
PR65
100KR0402
PR49
4.7R
PC62
C1U10X
PC61
C10U10Y0805
PC60
C10U10Y0805
+3VALW
12/13
DEL
RI2
X_2.2R1%
CI3
X_C1000P50X0402
For EMI
PQ32
N-AO4468_SOIC8
+5VRUN
PR67
2KR1%0402
TI51120_EN
36
SUS_ON
max voltage 5.5
PC79
C0.1U10X0402
PR66
240KR0402
PWR_SRC
PR56
100KR
PR60
30KR
PQ12
_NN-2N7002DW-7-F_SOT363
RUND
34
PR59
470KR
PC76
C0.1U25X
36,37,41,42,45 RUN_ON
PR57
100KR
3VSUSPK
R236
0R
+3VSUSPWROK 36
VREF2
+5VALW
PC80
C1000P50X0402
PR68
X_0R0402
PR70
X_0R0402
TONSEL
SKIPSEL
JNC3
X
NC_0402_6
MICRO-STAR INT'L CO.,LTD.
Title
System power 3/5V
Size
Custom
Document Number
MS-1636
Rev
0.A
Date:
Wednesday, September 05, 2007
Sheet
40
of
51

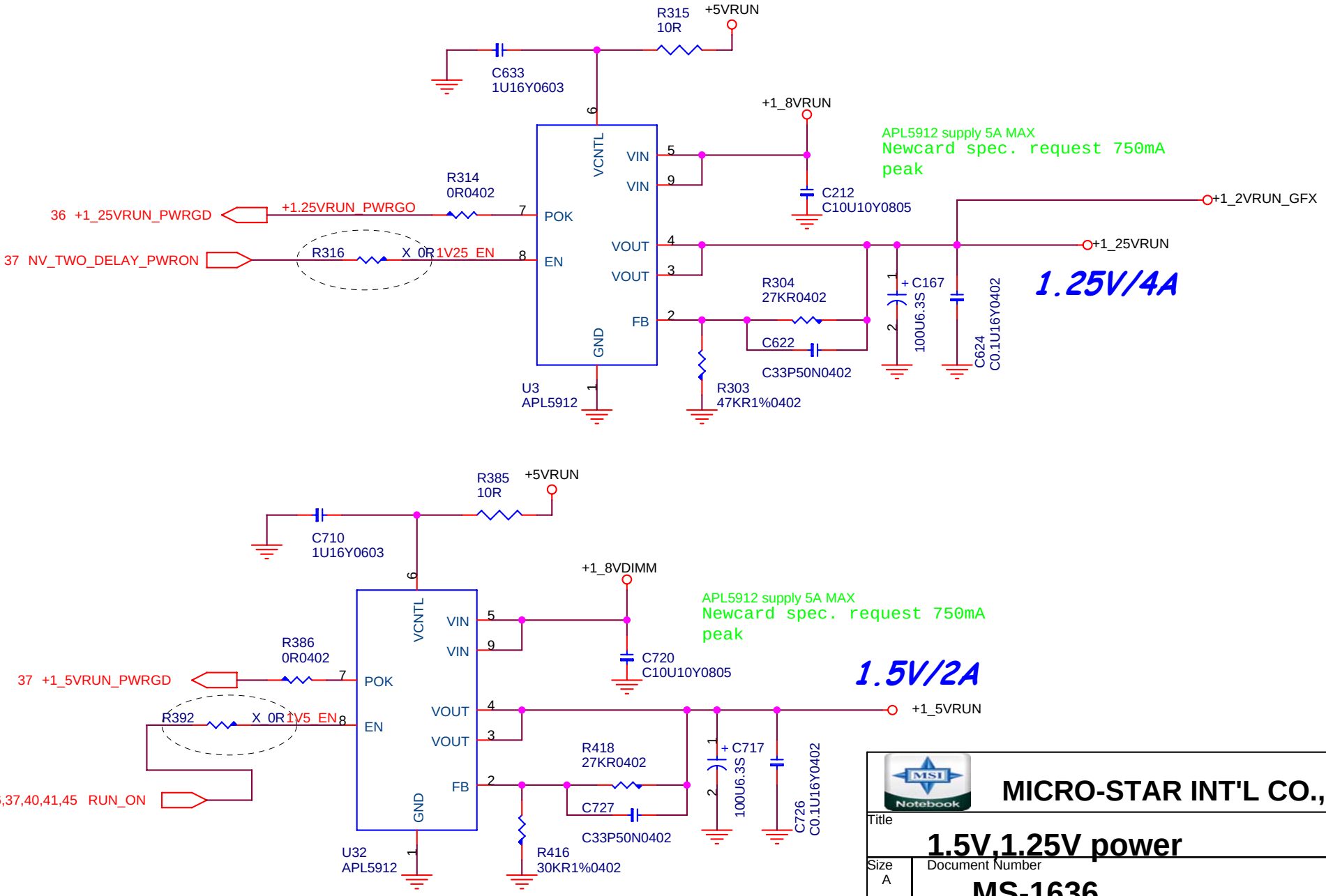


MICRO-STAR INT'L CO.,LTD.

Title: DDR power

Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 41 of 51

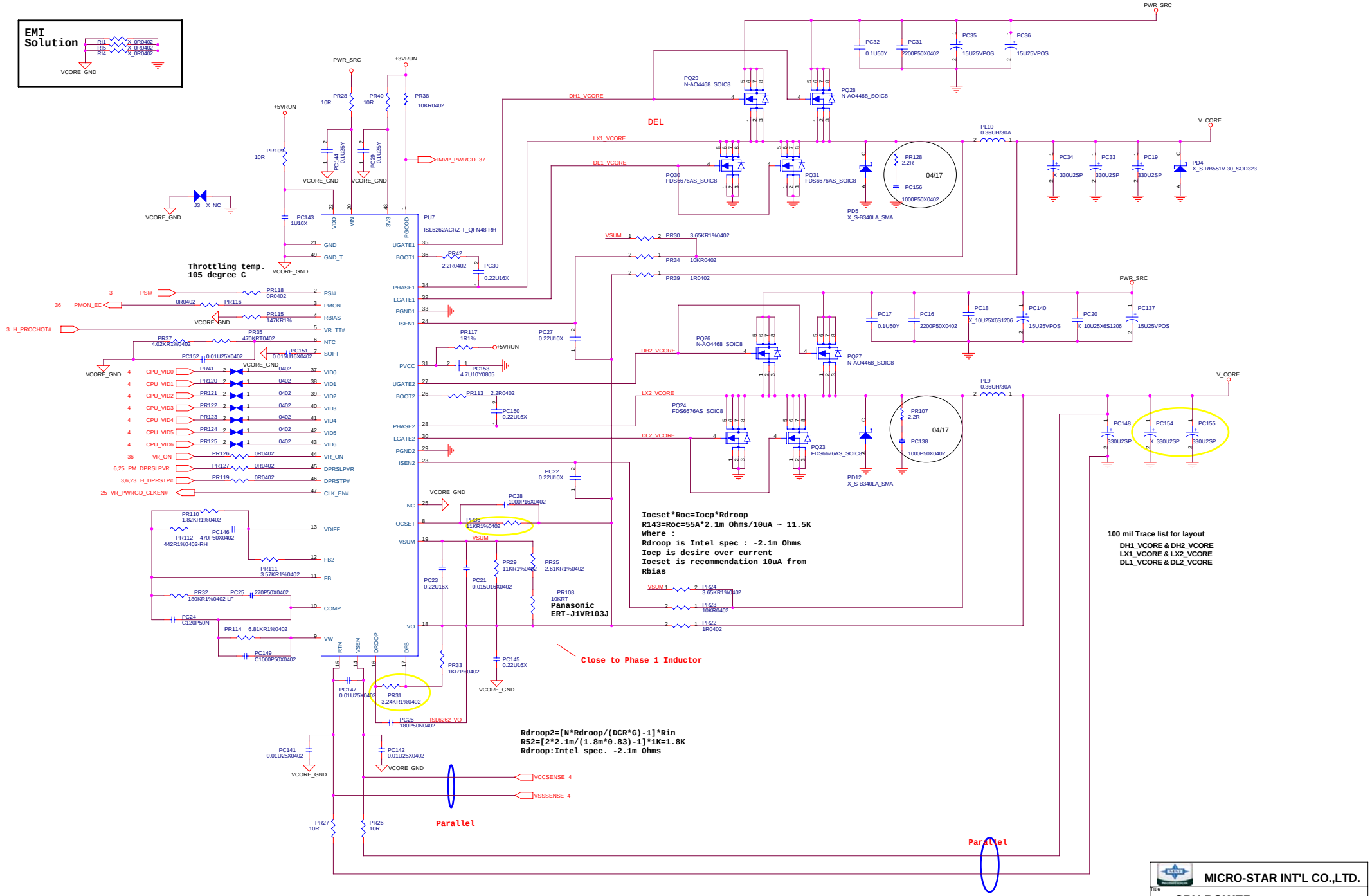


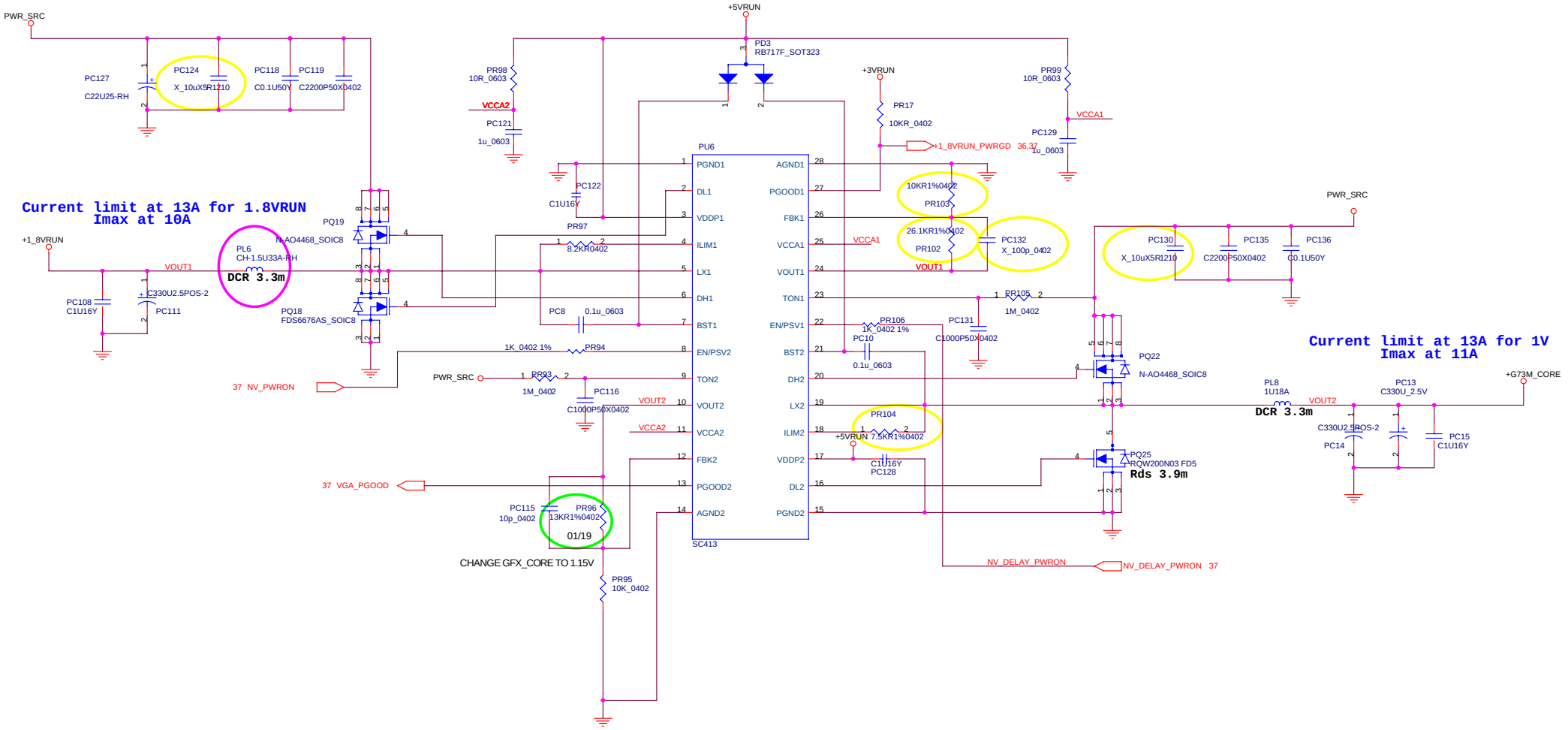
MICRO-STAR INT'L CO.,LTD.

Title: 1.5V,1.25V power

Size	Document Number	Rev
A	MS-1636	0.A

Date: Wednesday, September 05, 2007 Sheet 42 of 51


EMI Solution
R11 X_0R0402
R15 X_0R0402
R14 X_0R0402
VCORE_GND
PWR_SRC
+3VRUN
+5VRUN
PR28 10R
PR40 10R
PR38 10KR0402
PR109 10R
PC144 0.1U25Y
PC29 0.1U25Y
IMVP_PWRGD 37
J3 X_NC
PC143 1U10X
PU7
ISL6262ACRZ-T_QFN48-RH
VDD
VIN
3V3
PGOOD
GND
GND_T
Throttling temp.
105 degree C
PSI#
PMON
RBIAS
VR_TT#
NTC
SOFT
VID0
VID1
VID2
VID3
VID4
VID5
VID6
VR_ON
DPRSLPVR
DPRSTP#
CLK_EN#
VDIFF
FB2
FB
COMP
VW
RTN
VSEN
DROOP
DFB
UGATE1
BOOT1
PHASE1
LGATE1
PGND1
ISEN1
PVCC
UGATE2
BOOT2
PHASE2
LGATE2
PGND2
ISEN2
NC
OCSET
VSUM
VO
PR42 2.2R0402
PC30 0.22U16X
PR117 1R1%
PC153 4.7U10Y0805
PC27 0.22U10X
PR113 2.2R0402
PC150 0.22U16X
PC22 0.22U10X
PC28 1000P16X0402
PR36 11KR1%0402
PR29 11KR1%0402
PR25 2.61KR1%0402
PR108 10KRT
Panasonic
ERT-J1VR103J
PC23 0.22U16X
PC21 0.015U16K0402
PR33 1KR1%0402
PC145 0.22U16X
PC147 0.01U25X0402
PR31 3.24KR1%0402
PC26 180P50N0402
ISL6262_VO
PC141 0.01U25X0402
PC142 0.01U25X0402
VCCSENSE 4
VSSSENSE 4
Parallel
PR27 10R
PR26 10R
DEL
DH1_VCORE
LX1_VCORE
DL1_VCORE
PQ29 N-AO4468_SOIC8
PQ28 N-AO4468_SOIC8
PQ30 FDS6676AS_SOIC8
PQ31 FDS6676AS_SOIC8
PD5 X_S-B340LA_SMA
PR128 2.2R
PC156 1000P50X0402
04/17
PL10 0.36UH/30A
V_CORE
PC34 X_330U2SP
PC33 330U2SP
PC19 330U2SP
PD4 X_S-RB551V-30_SOD323
PC32 0.1U50Y
PC31 2200P50X0402
PC35 15U25VPOS
PC36 15U25VPOS
VSUM
PR30 3.65KR1%0402
PR34 10KR0402
PR39 1R0402
DH2_VCORE
LX2_VCORE
DL2_VCORE
PQ26 N-AO4468_SOIC8
PQ27 N-AO4468_SOIC8
PQ24 FDS6676AS_SOIC8
PQ23 FDS6676AS_SOIC8
PD12 X_S-B340LA_SMA
PR107 2.2R
PC138 1000P50X0402
PL9 0.36UH/30A
PC17 0.1U50Y
PC16 2200P50X0402
PC18 X_10U25X6S1206
PC140 15U25VPOS
PC20 X_10U25X6S1206
PC137 15U25VPOS
PC148 330U2SP
PC154 X_330U2SP
PC155 330U2SP
PR24 3.65KR1%0402
PR23 10KR0402
PR22 1R0402
Iocset*Roc=Iocp*Rdroop
R143=Roc=55A*2.1m Ohms/10uA ~ 11.5K
Where :
Rdroop is Intel spec : -2.1m Ohms
Iocp is desire over current
Iocset is recommendation 10uA from Rbias
100 mil Trace list for layout
DH1_VCORE & DH2_VCORE
LX1_VCORE & LX2_VCORE
DL1_VCORE & DL2_VCORE
Close to Phase 1 Inductor
Rdroop2=[N*Rdroop/(DCR*G)-1]*Rin
R52=[2*2.1m/(1.8m*0.83)-1]*1K=1.8K
Rdroop:Intel spec. -2.1m Ohms
Parallel
MICRO-STAR INT'L CO.,LTD.
Title
CPU POWER
Size A2
Document Number MS-16361
Rev 0.A
Date: Wednesday, September 05, 2007
Sheet 43 of 51

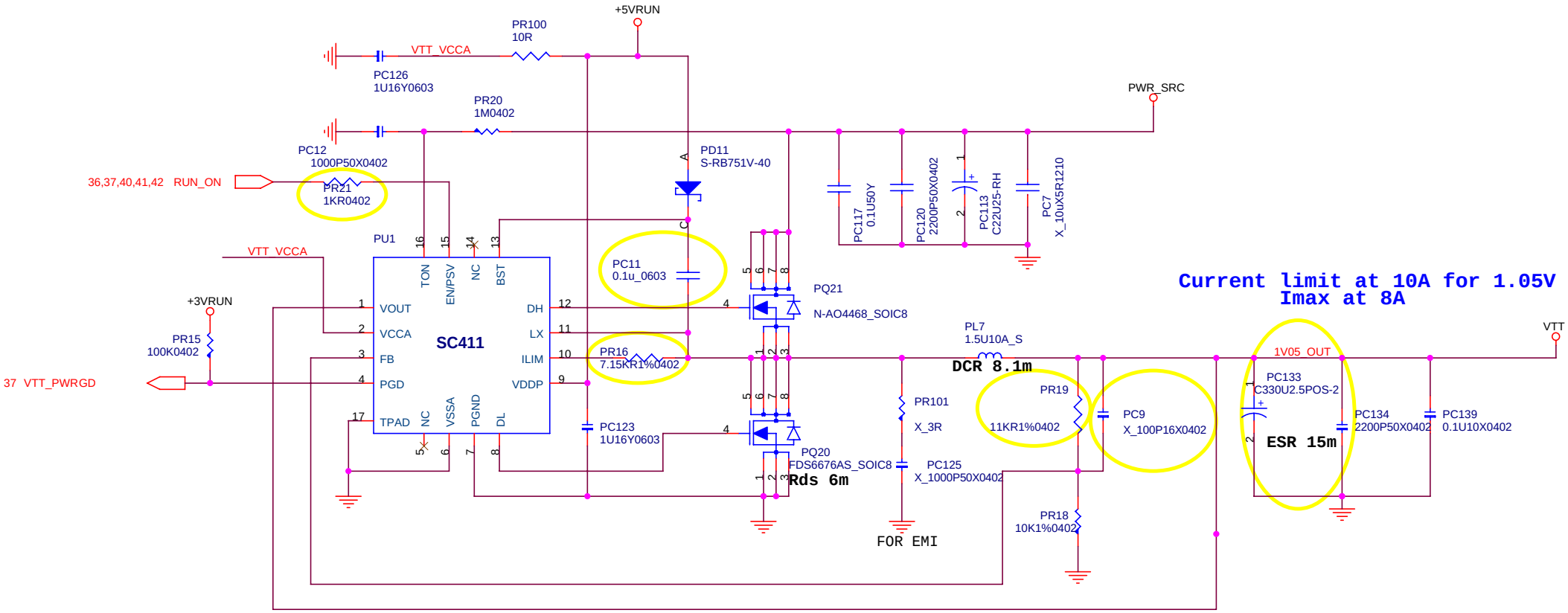


MICRO-STAR INT'L CO.,LTD.

Title: GFX_CORE,1.8VRUN

Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 44 of 51

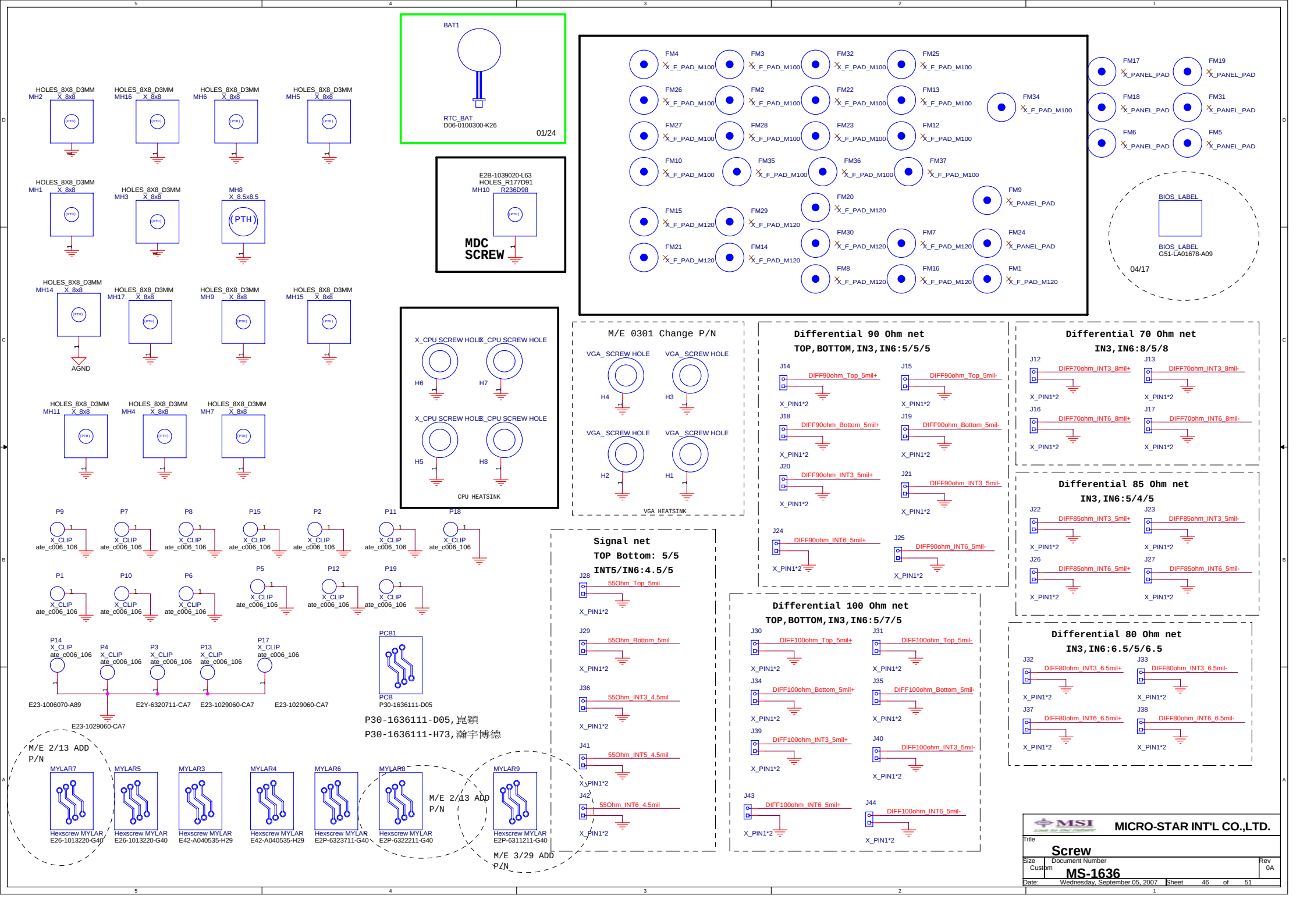


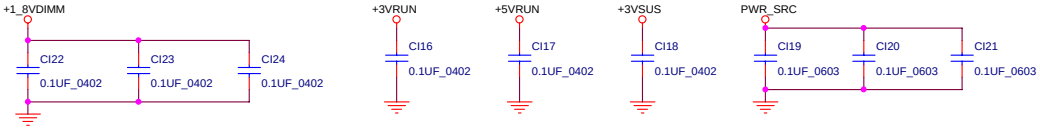
MICRO-STAR INT'L CO.,LTD.

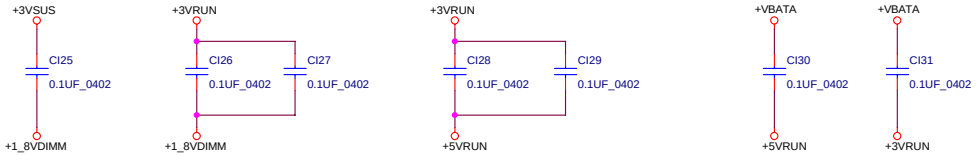
Title: VTT power

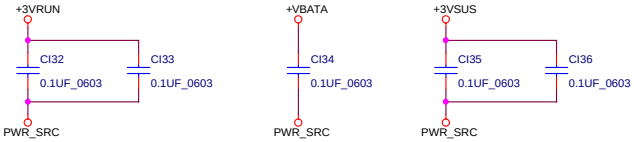
Size: Custom | Document Number: MS-1636 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 45 of 51


BAT1
RTC_BAT
D06-0100300-K26
01/24
E2B-1039020-L63
HOLES_R177D91
MH10 R236D98
MDC SCREW
FM4 X_F_PAD_M100
FM3 X_F_PAD_M100
FM32 X_F_PAD_M100
FM25 X_F_PAD_M100
FM26 X_F_PAD_M100
FM2 X_F_PAD_M100
FM22 X_F_PAD_M100
FM13 X_F_PAD_M100
FM34 X_F_PAD_M100
FM27 X_F_PAD_M100
FM28 X_F_PAD_M100
FM23 X_F_PAD_M100
FM12 X_F_PAD_M100
FM10 X_F_PAD_M100
FM35 X_F_PAD_M100
FM36 X_F_PAD_M100
FM37 X_F_PAD_M100
FM9 X_PANEL_PAD
FM20 X_F_PAD_M120
FM15 X_F_PAD_M120
FM29 X_F_PAD_M120
FM30 X_F_PAD_M120
FM7 X_F_PAD_M120
FM24 X_PANEL_PAD
FM21 X_F_PAD_M120
FM14 X_F_PAD_M120
FM8 X_F_PAD_M120
FM16 X_F_PAD_M120
FM1 X_F_PAD_M120
FM17 X_PANEL_PAD
FM19 X_PANEL_PAD
FM18 X_PANEL_PAD
FM31 X_PANEL_PAD
FM6 X_PANEL_PAD
FM5 X_PANEL_PAD
BIOS_LABEL
BIOS_LABEL
G51-LA01678-A09
04/17
HOLES_8X8_D3MM MH2 X_8x8
HOLES_8X8_D3MM MH16 X_8x8
HOLES_8X8_D3MM MH6 X_8x8
HOLES_8X8_D3MM MH5 X_8x8
HOLES_8X8_D3MM MH1 X_8x8
HOLES_8X8_D3MM MH3 X_8x8
MH8 X_8.5x8.5
HOLES_8X8_D3MM MH14 X_8x8
HOLES_8X8_D3MM MH17 X_8x8
HOLES_8X8_D3MM MH9 X_8x8
HOLES_8X8_D3MM MH15 X_8x8
AGND
HOLES_8X8_D3MM MH11 X_8x8
HOLES_8X8_D3MM MH4 X_8x8
HOLES_8X8_D3MM MH7 X_8x8
X_CPU SCREW HOLE H6
X_CPU SCREW HOLE H7
X_CPU SCREW HOLE H5
X_CPU SCREW HOLE H8
CPU HEATSINK
M/E 0301 Change P/N
VGA_ SCREW HOLE H4
VGA_ SCREW HOLE H3
VGA_ SCREW HOLE H2
VGA_ SCREW HOLE H1
VGA HEATSINK
Differential 90 Ohm net
TOP,BOTTOM,IN3,IN6:5/5/5
J14 DIFF90ohm_Top_5mil+
J15 DIFF90ohm_Top_5mil-
J18 DIFF90ohm_Bottom_5mil+
J19 DIFF90ohm_Bottom_5mil-
J20 DIFF90ohm_INT3_5mil+
J21 DIFF90ohm_INT3_5mil-
J24 DIFF90ohm_INT6_5mil+
J25 DIFF90ohm_INT6_5mil-
X_PIN1*2
Differential 70 Ohm net
IN3,IN6:8/5/8
J12 DIFF70ohm_INT3_8mil+
J13 DIFF70ohm_INT3_8mil-
J16 DIFF70ohm_INT6_8mil+
J17 DIFF70ohm_INT6_8mil-
X_PIN1*2
Differential 85 Ohm net
IN3,IN6:5/4/5
J22 DIFF85ohm_INT3_5mil+
J23 DIFF85ohm_INT3_5mil-
J26 DIFF85ohm_INT6_5mil+
J27 DIFF85ohm_INT6_5mil-
X_PIN1*2
Differential 80 Ohm net
IN3,IN6:6.5/5/6.5
J32 DIFF80ohm_INT3_6.5mil+
J33 DIFF80ohm_INT3_6.5mil-
J37 DIFF80ohm_INT6_6.5mil+
J38 DIFF80ohm_INT6_6.5mil-
X_PIN1*2
Signal net
TOP Bottom: 5/5
INT5/IN6:4.5/5
J28 55Ohm_Top_5mil
J29 55Ohm_Bottom_5mil
J36 55Ohm_INT3_4.5mil
J41 55Ohm_INT5_4.5mil
J42 55Ohm_INT6_4.5mil
X_PIN1*2
Differential 100 Ohm net
TOP,BOTTOM,IN3,IN6:5/7/5
J30 DIFF100ohm_Top_5mil+
J31 DIFF100ohm_Top_5mil-
J34 DIFF100ohm_Bottom_5mil+
J35 DIFF100ohm_Bottom_5mil-
J39 DIFF100ohm_INT3_5mil+
J40 DIFF100ohm_INT3_5mil-
J43 DIFF100ohm_INT6_5mil+
J44 DIFF100ohm_INT6_5mil-
X_PIN1*2
P9 X_CLIP ate_c006_106
P7 X_CLIP ate_c006_106
P8 X_CLIP ate_c006_106
P15 X_CLIP ate_c006_106
P2 X_CLIP ate_c006_106
P11 X_CLIP ate_c006_106
P18 X_CLIP ate_c006_106
P1 X_CLIP ate_c006_106
P10 X_CLIP ate_c006_106
P6 X_CLIP ate_c006_106
P5 X_CLIP ate_c006_106
P12 X_CLIP ate_c006_106
P19 X_CLIP ate_c006_106
P14 X_CLIP ate_c006_106
P4 X_CLIP ate_c006_106
P3 X_CLIP ate_c006_106
P13 X_CLIP ate_c006_106
P17 X_CLIP ate_c006_106
E23-1006070-A89
E2Y-6320711-CA7
E23-1029060-CA7
E23-1029060-CA7
E23-1029060-CA7
PCB1
PCB
P30-1636111-D05
P30-1636111-D05,崑穎
P30-1636111-H73,瀚宇博德
M/E 2/13 ADD P/N
MYLAR7 Hexscrew MYLAR E26-1013220-G40
MYLAR5 Hexscrew MYLAR E26-1013220-G40
MYLAR3 Hexscrew MYLAR E42-A040535-H29
MYLAR4 Hexscrew MYLAR E42-A040535-H29
MYLAR6 Hexscrew MYLAR E2P-6323711-G40
MYLAR8 Hexscrew MYLAR E2P-6322211-G40
M/E 2/13 ADD P/N
MYLAR9 Hexscrew MYLAR E2P-6311211-G40
M/E 3/29 ADD P/N
MSI
MICRO-STAR INT'L CO.,LTD.
Title
Screw
Size Custom
Document Number
MS-1636
Rev 0A
Date: Wednesday, September 05, 2007
Sheet 46 of 51





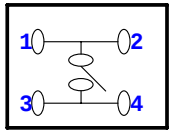


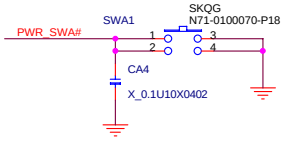
MICRO-STAR INT'L CO.,LTD.

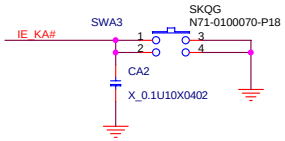
Title: EMI

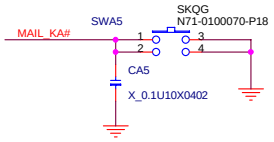
Size: Custom | Document Number: MS-1636 | Rev: 0A

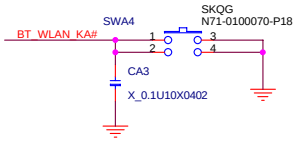
Date: Wednesday, September 05, 2007 | Sheet 47 of 51

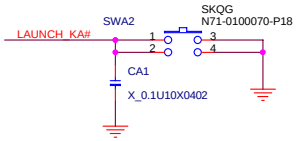


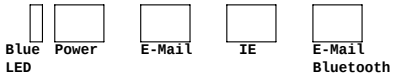


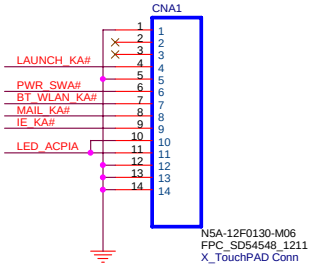


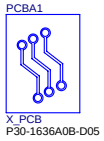


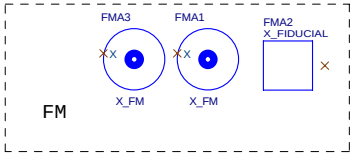




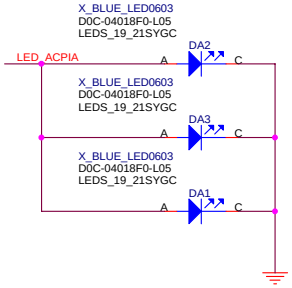


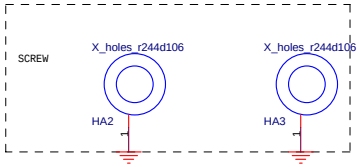


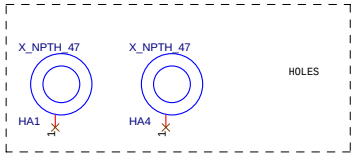




P30-1636A0B-D05,崑穎
P30-1636A0B-T53,健鼎
P30-1636A0B-H73,瀚宇博德



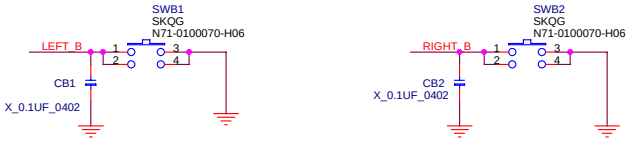




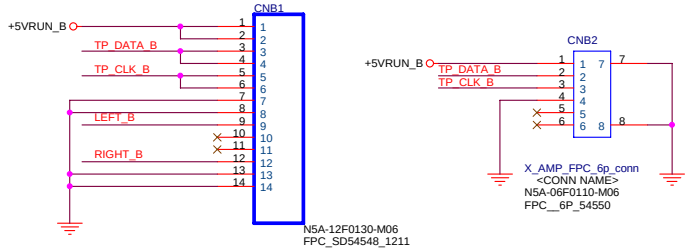
Title
Launch Board for MS1034

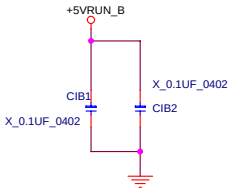
Size	Document Number	Rev
Custom	**MS-1636A**	0A

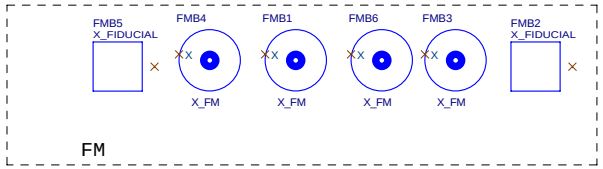
Date: Wednesday, September 05, 2007 Sheet 48 of 51

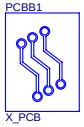


For TM61P-307 pin define

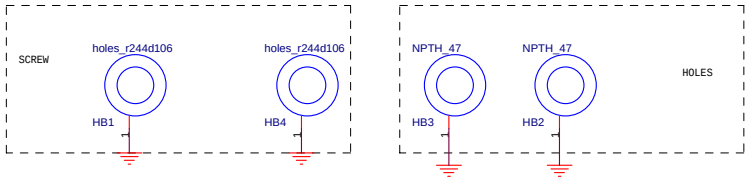








P30-1636B0B-D05,崑穎
P30-1636B0B-T53,健鼎
P30-1636B0B-H73,瀚宇博德



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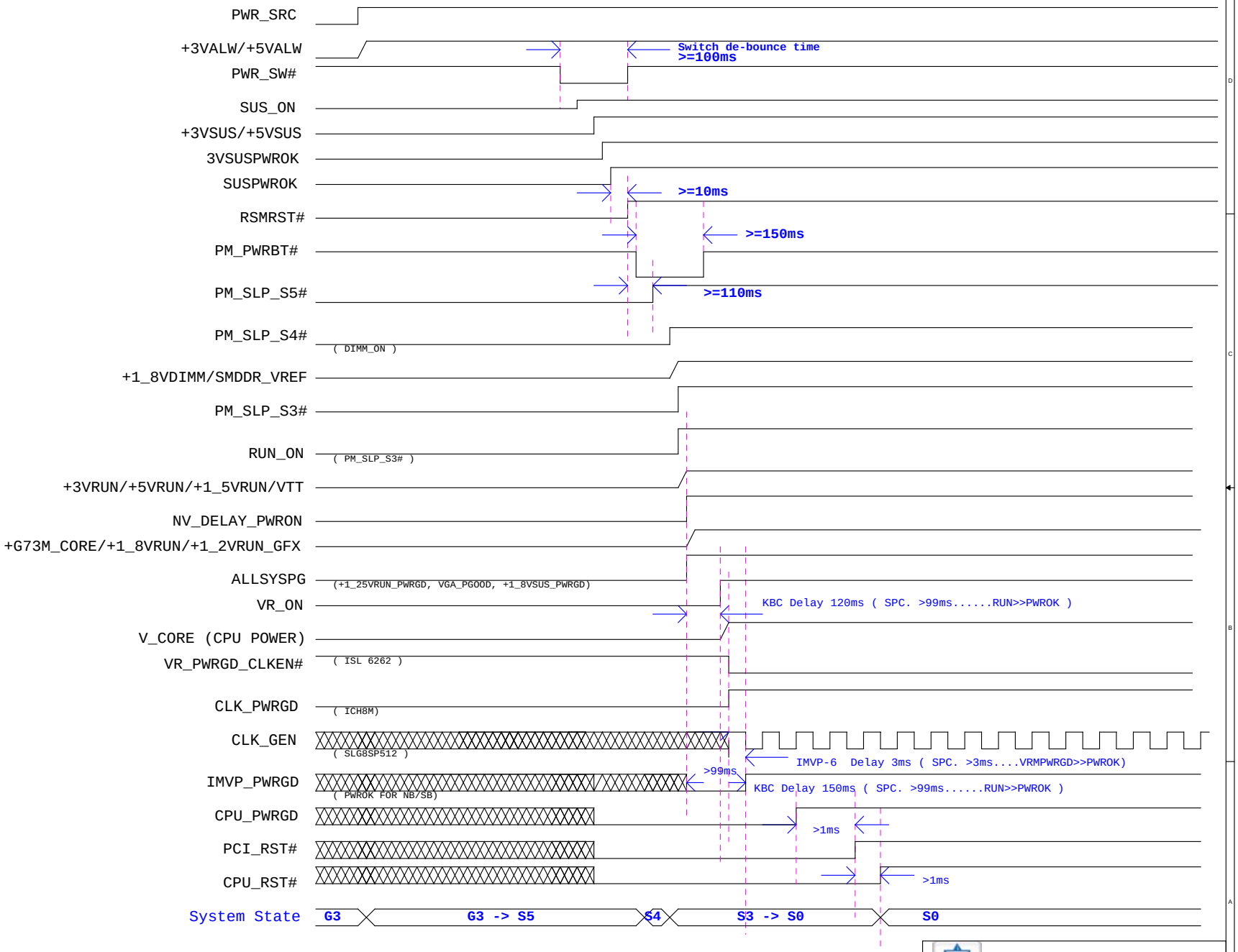
Title: TP Board

Size: Custom | Document Number: MS-1636B | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 49 of 51

SANTA ROSA System Power on Sequence for MS-1636



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Title: Power On Sequency

Size: Custom | Document Number: MS-1636 | Rev: 0A

Date: Wednesday, September 05, 2007 | Sheet 50 of 51

Ver.	date	change note
		ADD Page35 D38 P/N D01-BAS400-W01 Page37 C844,C845,D39,U38 Page24 R522 Page36 R455 Page46 ME ADD Mylar MYLAR7 & MYLAR8
		DEL Page43 0R_0402 PR41,PR120,PR121,PR122, PR123,PR124,PR125 DEL Page23 0R_0402 R103,R111,R114,R128 DEL Page25 0R_0402 R155,R156,R348 DEL Page21 2.2K_0402 R2 DEL Page32 0R R381,R389 DEL Page6 0R_0402 R97,R337
1.0	02/14/2007 by Apin	Change P/N 1uF/Y5V to 1uF/X7R C105,C20,C36,C446,C501,C578,C638,C645 C729,C740,C741,C784,C785,C786 Change P/N 2.2UF/6.3V to 0.1UF/16V Page 34 C782,C783 Change P/N 10K_0402 to 1M_0402 Page35 R215 Change P/N 22UF/10_1206 TO 100UF/6.3V Page27 C690 Change P/N PLCC32_SOCKET FLASH ROM SOCKE Change to Flash ROM Change P/N M/E Mylar MYLAR2 E2P-6322712-G40 Change to E2P-6322713-G40 Change P/N M/E Scerw E2B-1636010-L63 change to E2B-1641010-L63 Page 46 H1,H2,H3,H4
	02/27/2007 by KILL	Change 15p 5% to 18p 5% page 23 delate c538 page 34 change R505 to R506 page 33 ADD C846 and C847 page 23 Change M/E P/N Scerw H1,H2,H3,H4 page 46 E2B-1641010-L63 change to E2B-1037010-L63 5020*4
	03/15/2007 by KILL	Change U25 PN:B03-ONB8M15-N08 to B03-00G8675-N08
	03/20/2007 by KILL	Change U36 to ALC883 page 34 Change P16 P/N to N59-15F0451-A10 page 21 Change CN14 P/N to N56-07F0091-A10 page 20
	03/23/2007 by KILL	Change J11;CN13;CN14 P/N TO N58-09F0051-F02;N54-06F0461-A10;N54-06F0451-A10 page 35 FOR LG
	03/28/2007 by Apin	Power Change Page39 PR4 499_0402 to 270_1%_0402 Delete Page6 ,Page9 R332 ,R333,R334 ADD Page 6 Page9 R518 R519 & R520
	03/29/2007 by Apin	M/E ADD Mylar Page51 Mylar9

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Title: change history

Size: Custom | Document Number: MS-16361 | Rev: 0.A

Date: Wednesday, September 05, 2007 | Sheet 51 of 51