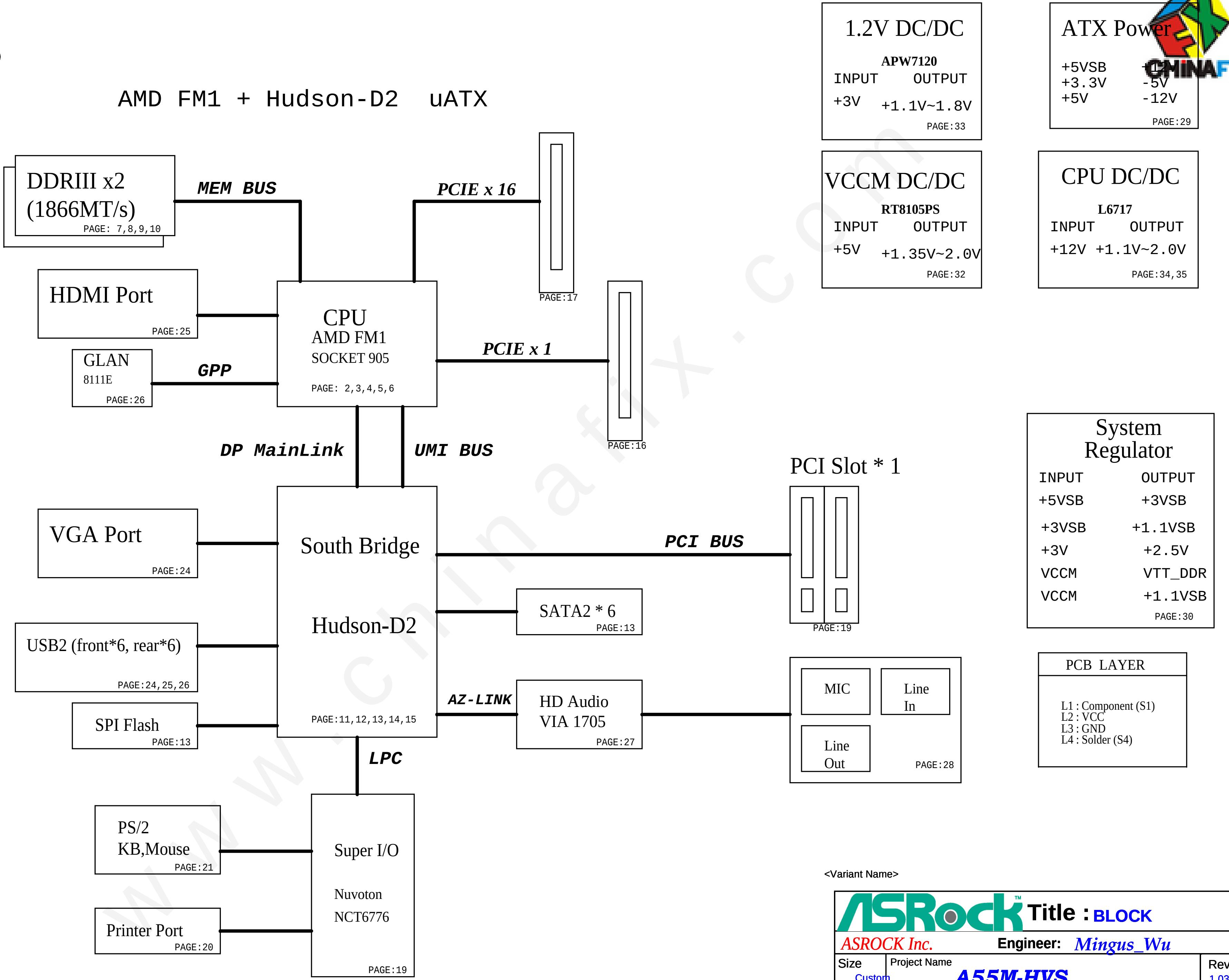
A55M-HVS

Revision: G/A 1.03

AMD FM1 + Hudson-D2 uATX



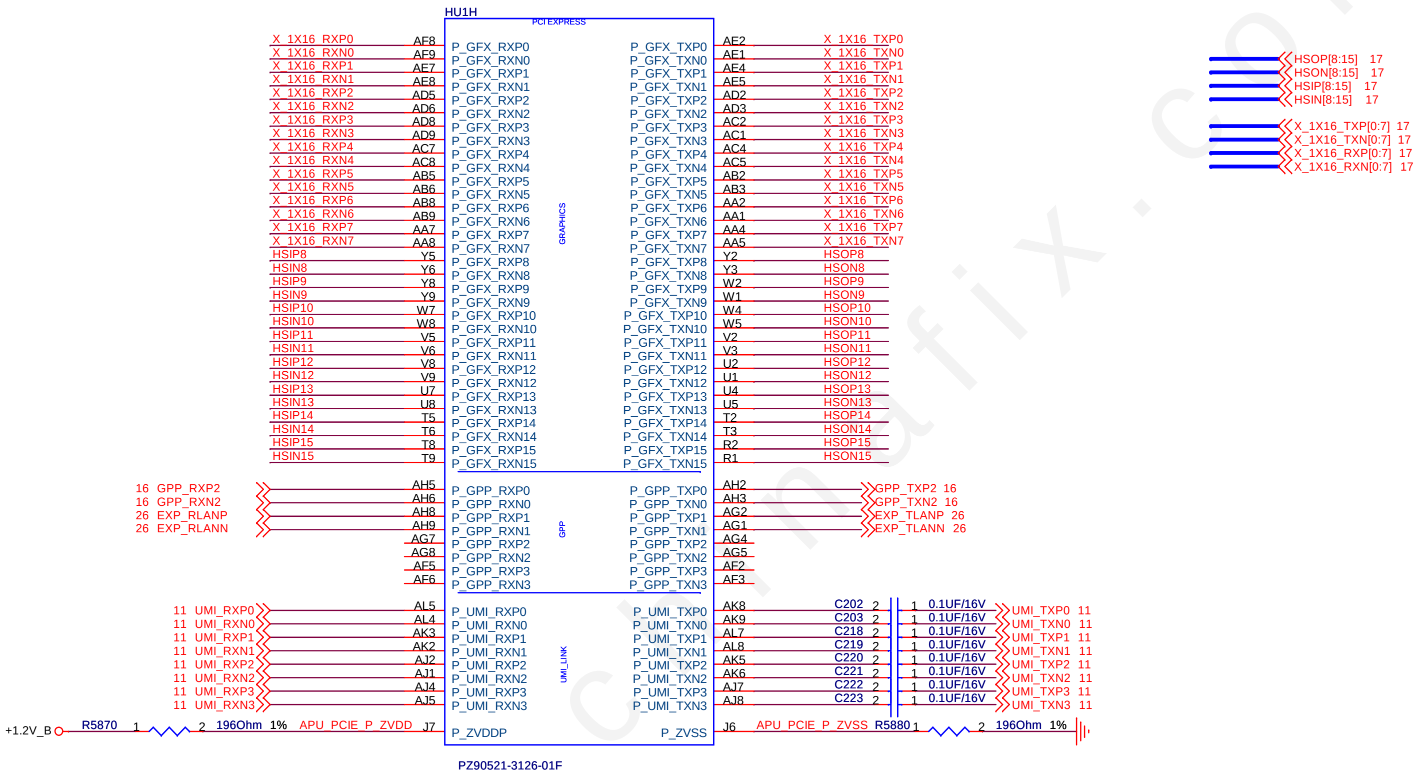
CAD Note:
Default Resistor and Capacitor footprint is SMD 0402 type. Different resistor footprints are showed on schematics.

/ = Not Installed Part

<Variant Name>

ASRock	Title : BLOCK	
ASROCK Inc.	Engineer: Mingus_Wu	
Size: Custom	Project Name: A55M-HVS	Rev: 1.03
Date: Friday, July 22, 2011	Sheet 1 of 39	

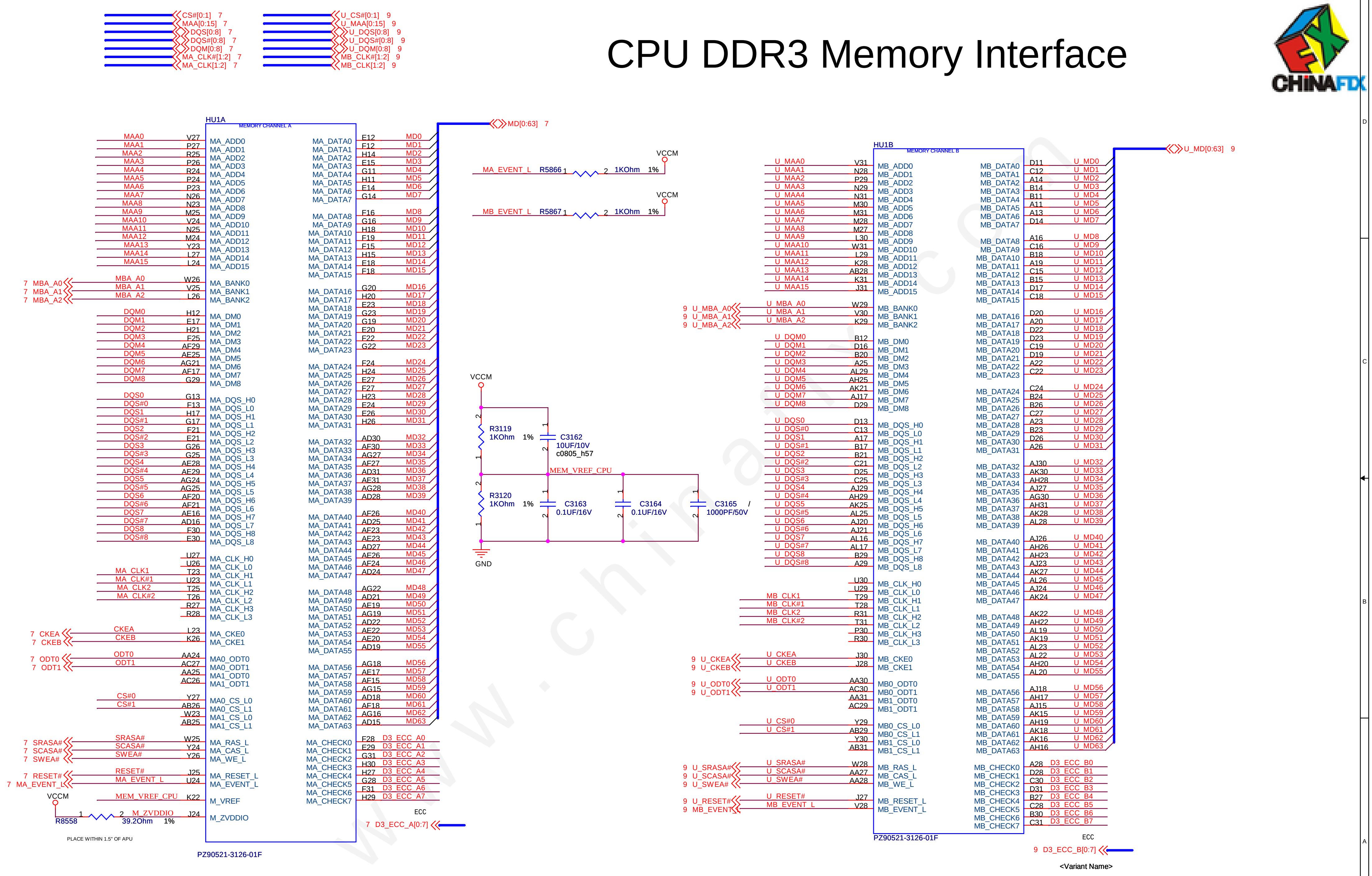
CPU HyperTransport Interface



<Variant Name>

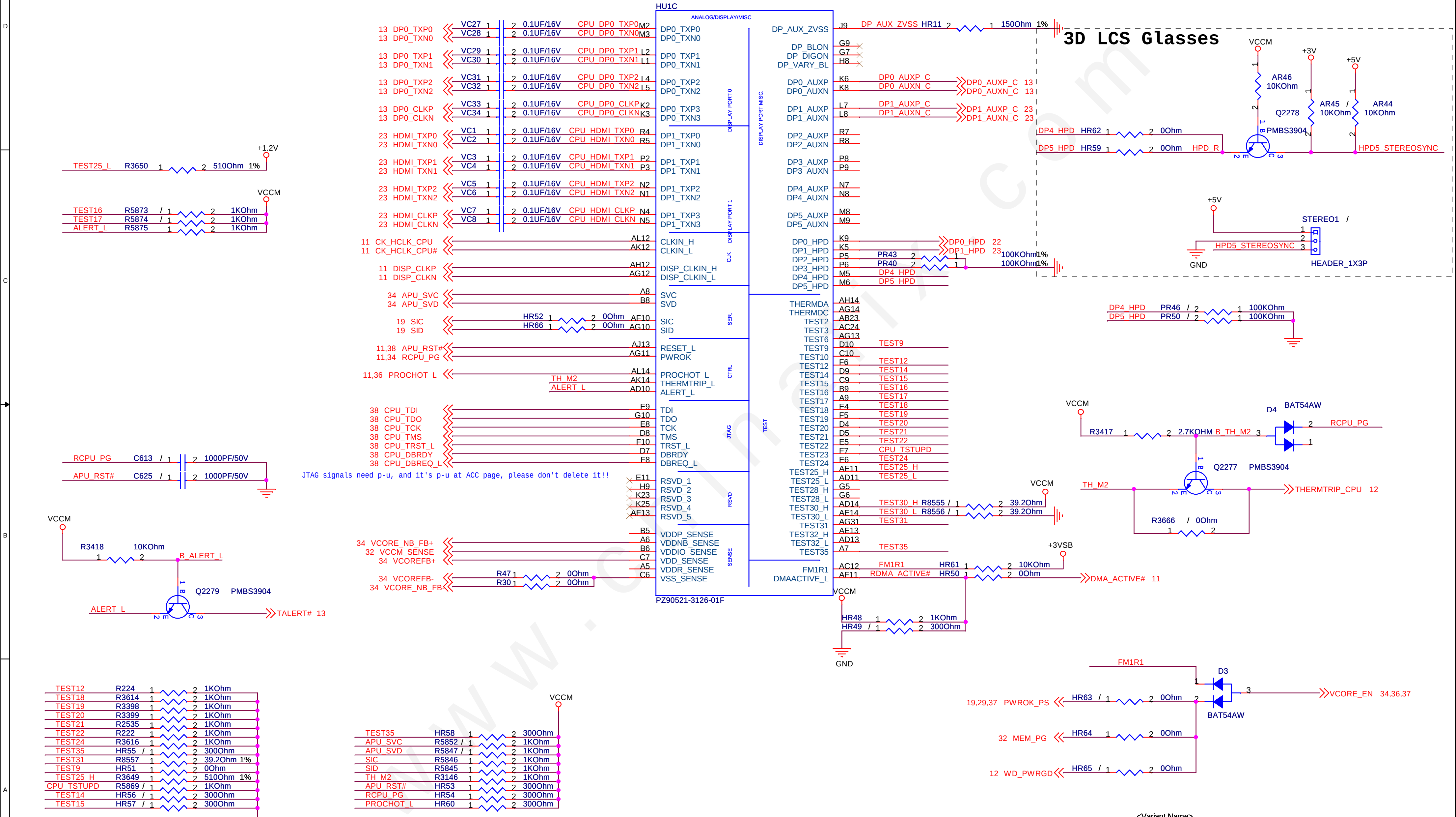
ASRock ASROCK Inc.	Title : M3_HDT	Engineer: Mingus_Wu
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 2 of 39	

CPU DDR3 Memory Interface



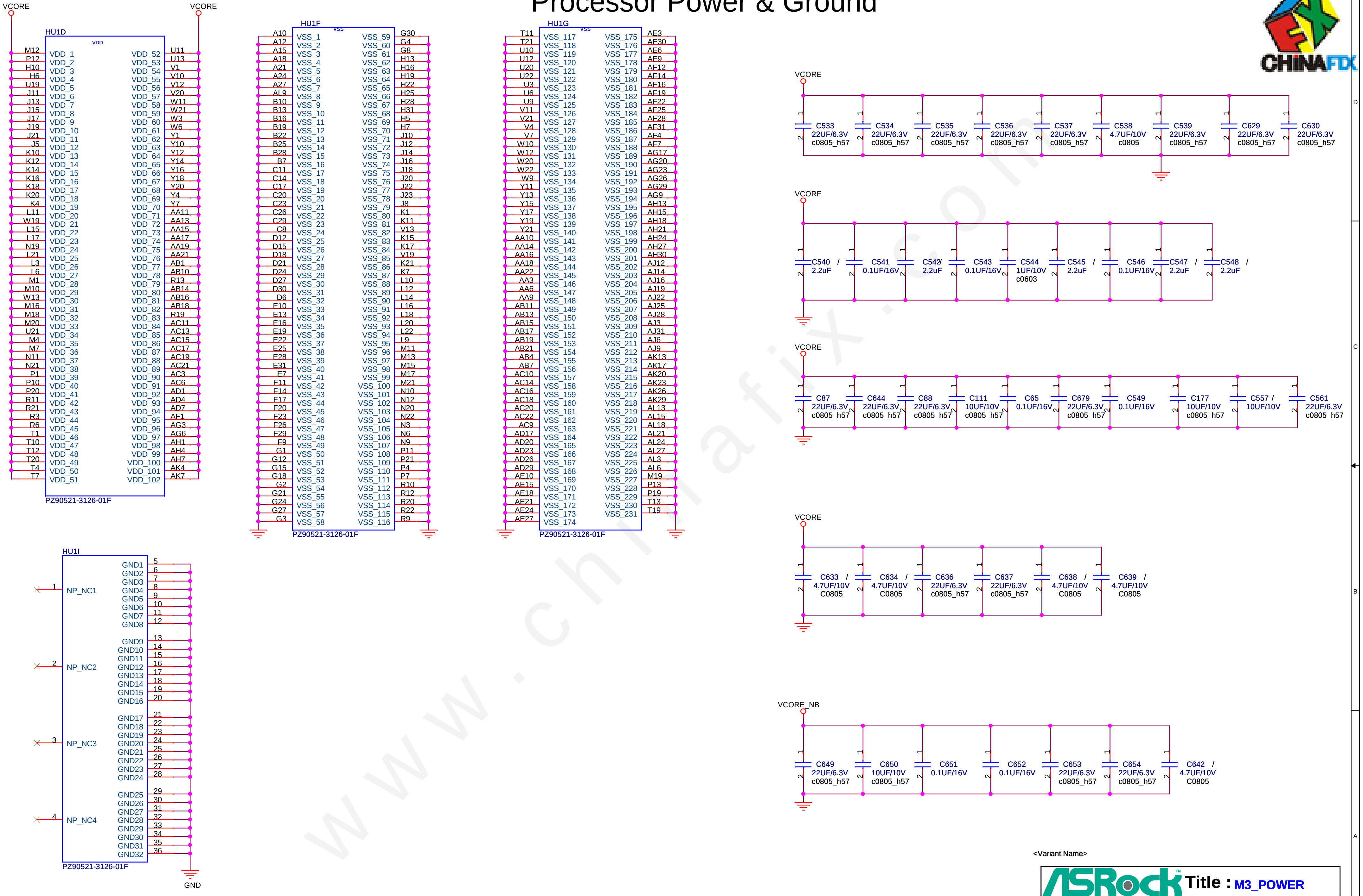
ASRock Inc.	Title : MD_DDRIII
Engineer:	Mingus_Wu
Size: A3	Project Name: A55M-HVS
Rev	1.03
Date: Friday, July 22, 2011	Sheet 3 of 39

CPU Control & Debug Interfaces



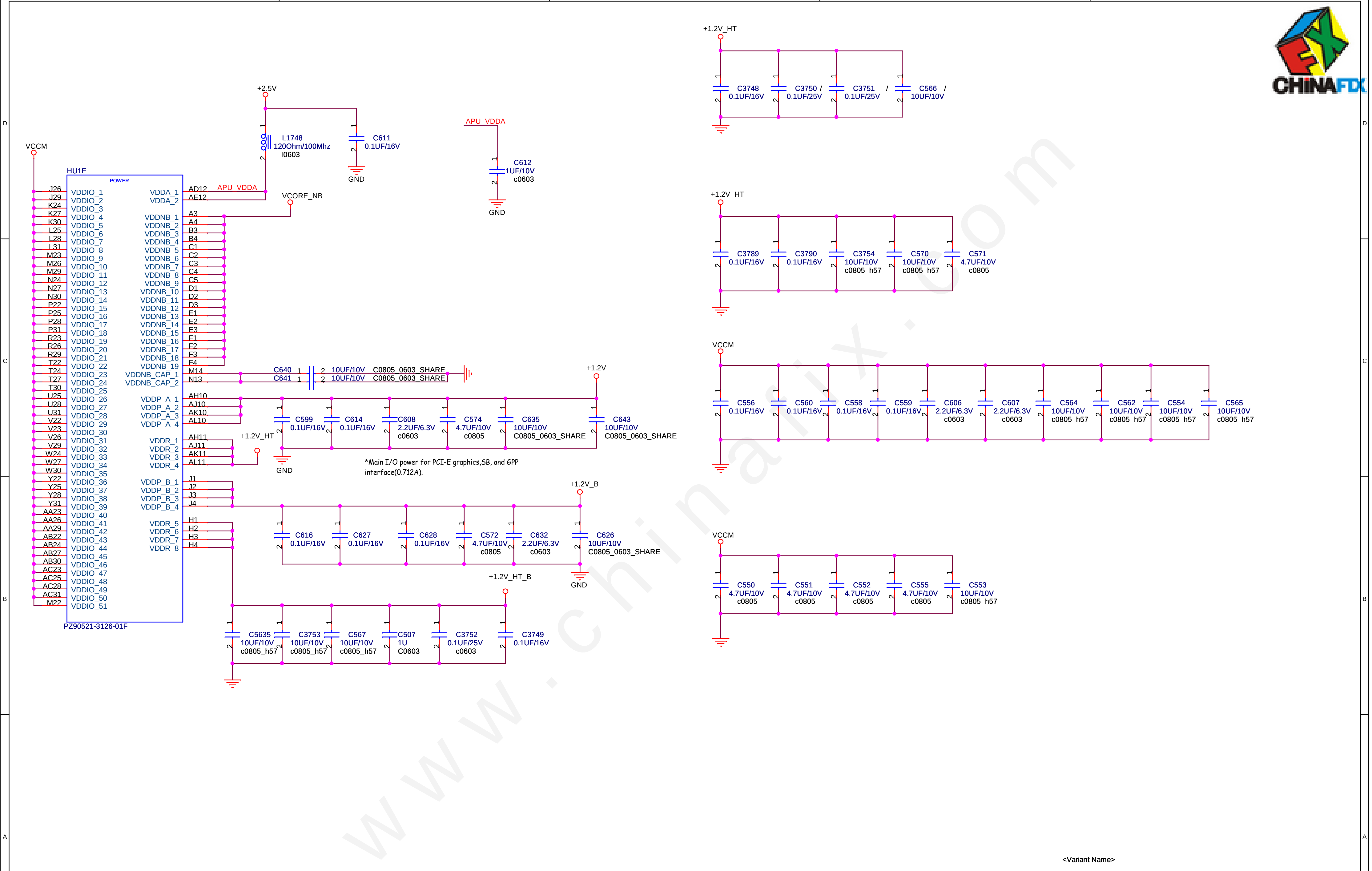
Title :	M3_MISC
ASROCK Inc.	Engineer: Mingus_Wu
Size: A3	Project Name: A55M-HVS
Rev	1.03
Date:	Friday, July 22, 2011
Sheet	4 of 39

Processor Power & Ground



<Variant Name>

ASROCK Inc.	Title : M3_POWER
	Engineer: Mingus_Wu
Size A3	Project Name A55M-HVS — Rev 1.03
Date: Friday, July 22, 2011	Sheet 5 of 39



<Variant Name>

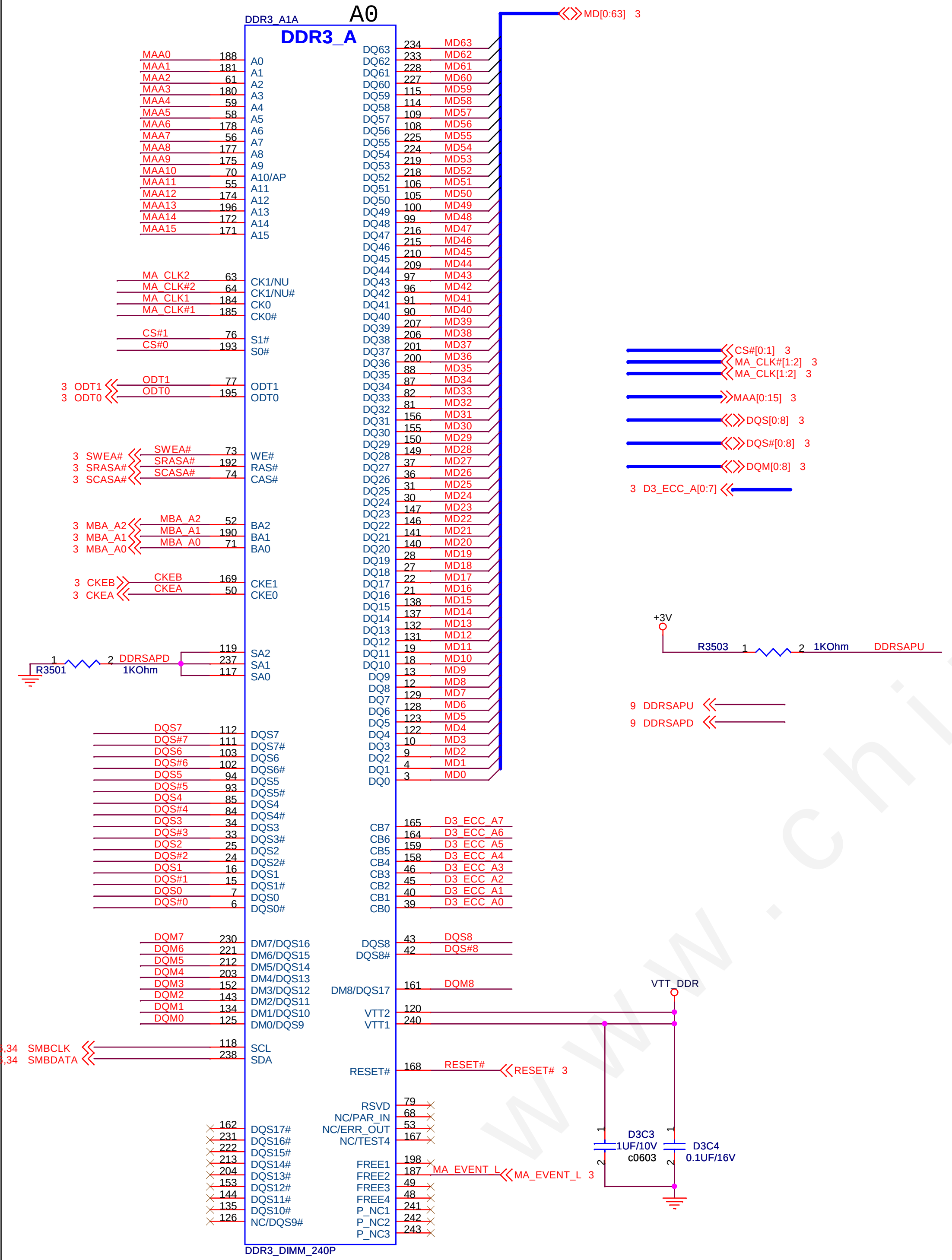
ASRock Title : M3_GOUND

ASROCK Inc. Engineer: Mingus_Wu

Size	Project Name	Rev
A3	A55M-HVS	1.03

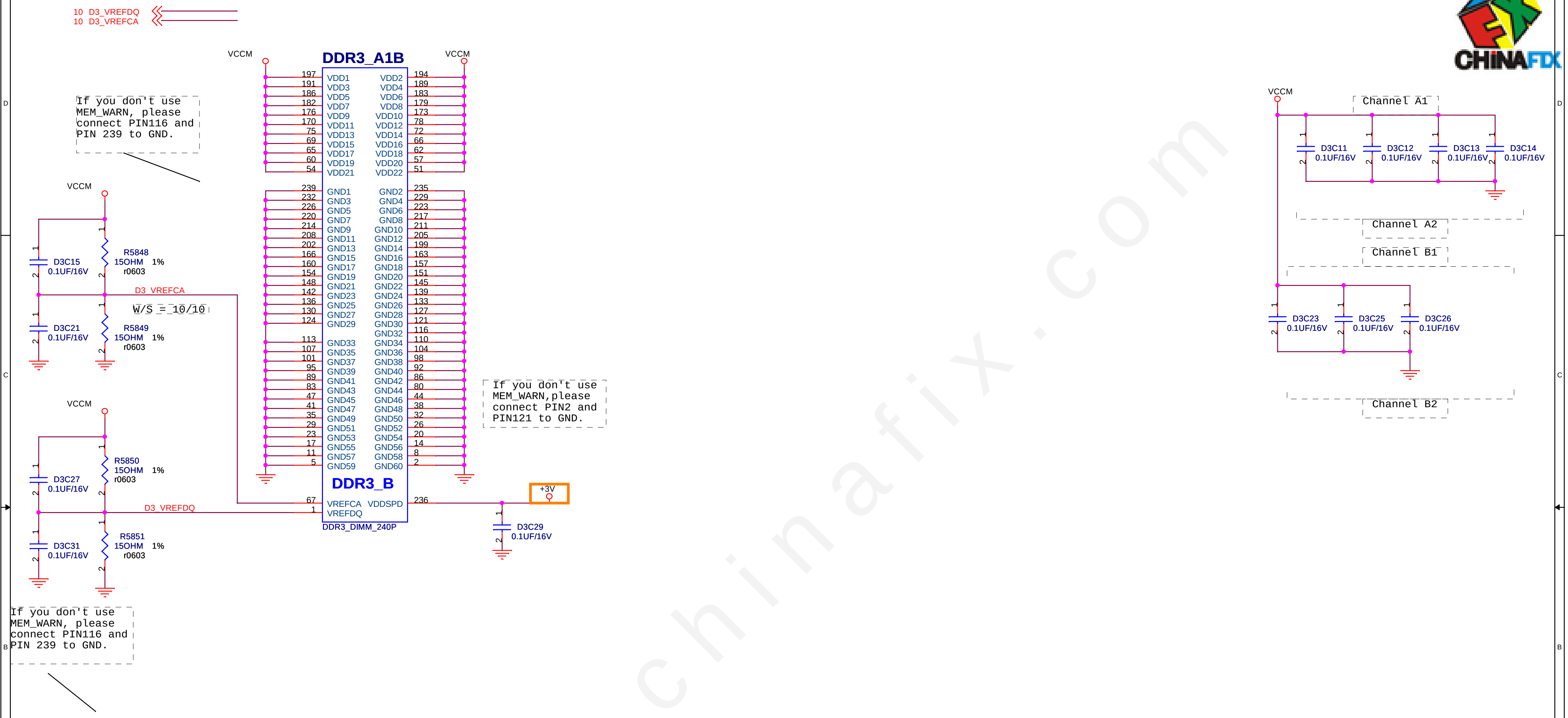
Date: Friday, July 22, 2011 Sheet 6 of 39

DDR SLOT1



<Variant Name>

ASRock	Title : DDRIII_DIMM1&3		
ASROCK Inc.	Engineer: Mingus_Wu		
Size A3	Project Name A55M-HVS		Rev 1.03
Date: Friday, July 22, 2011		Sheet 7 of 39	



With AMT	W/O AMT
+3V_CL	+3V
S0~S5有電	S0有電

<Variant Name>

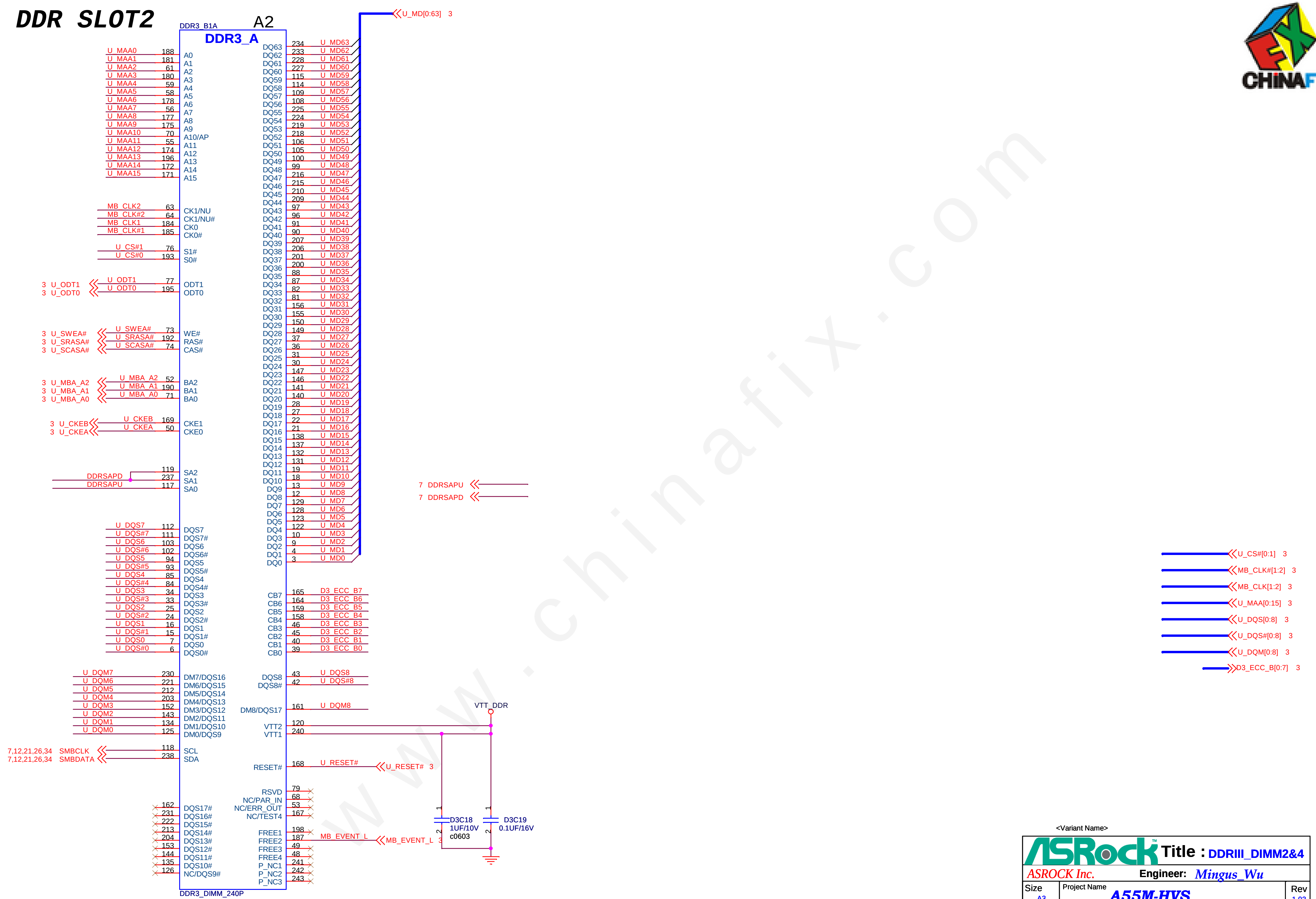
ASRock Title : DDRIII_POWER_Lo

ASROCK Inc. Engineer: Mingus_Wu

Size A3 | Project Name A55M-HVS | Rev 1.03

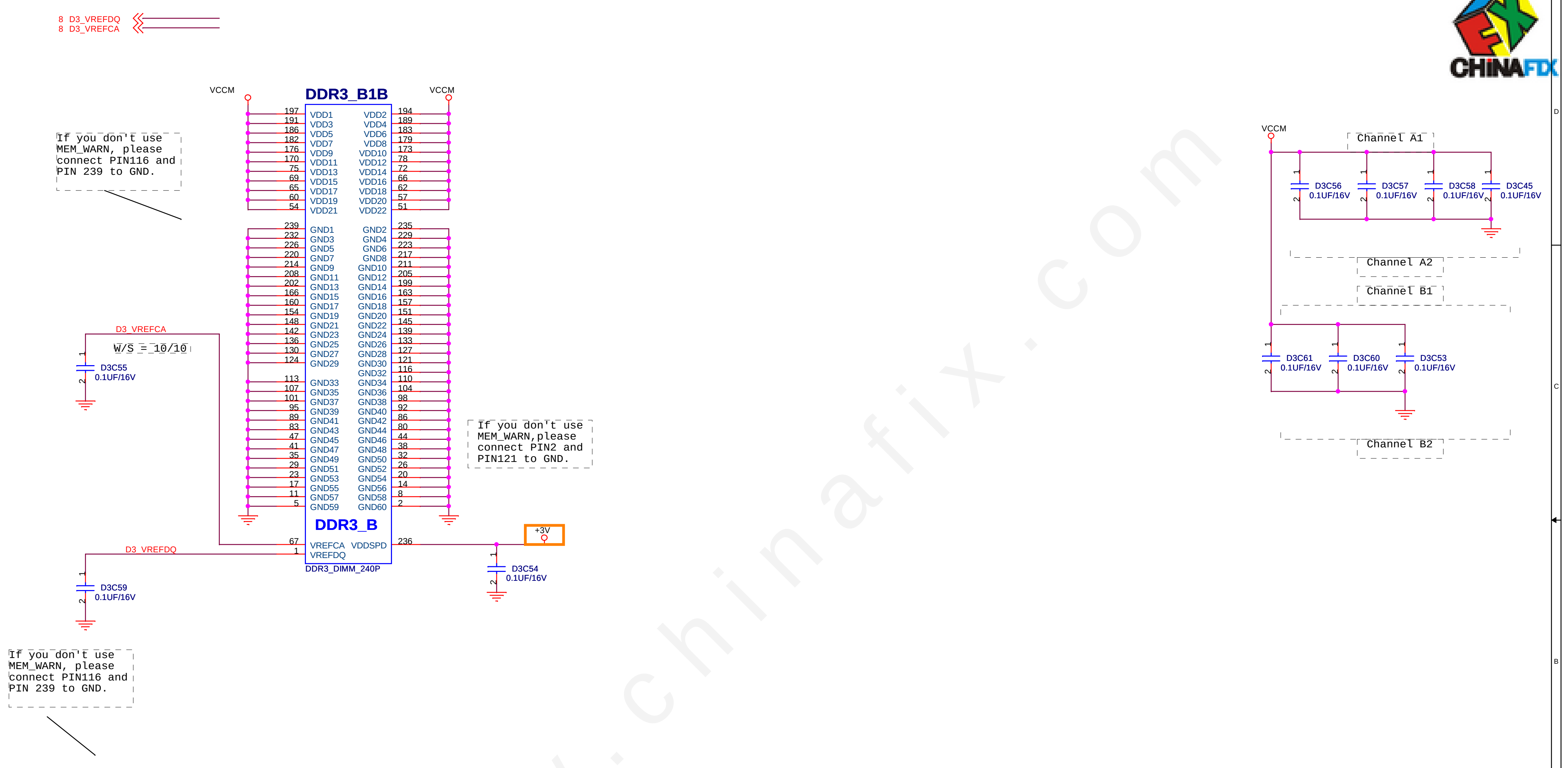
Date: Friday, July 22, 2011 Sheet 8 of 39

DDR SLOT2



<Variant Name>

ASRock	Title : DDRIII_DIMM2&4	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 9 of 39	



With AMT	W/O AMT
+3V_CL	+3V
S0~S5有電	S0有電

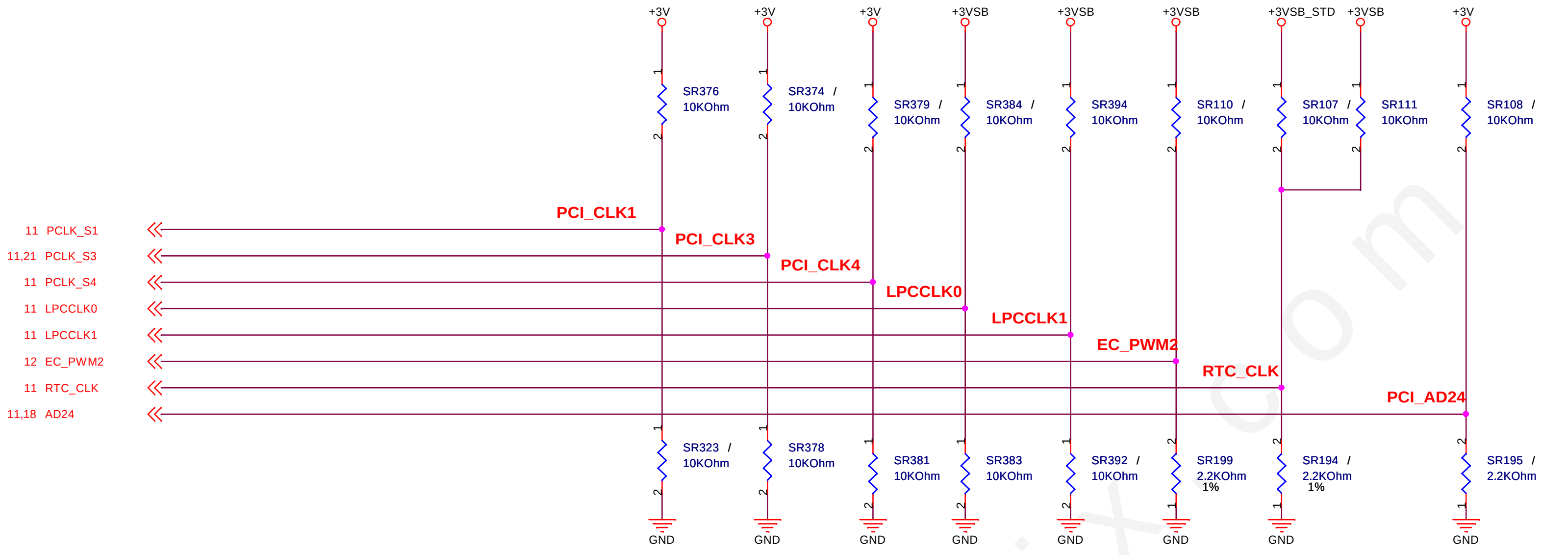
<Variant Name>

ASRock Inc. Title : DDRIII_POWER_H

Engineer: Mingus_Wu

Size A3 | Project Name A55M-HVS | Rev 1.03

Date: Friday, July 22, 2011 | Sheet 10 of 39

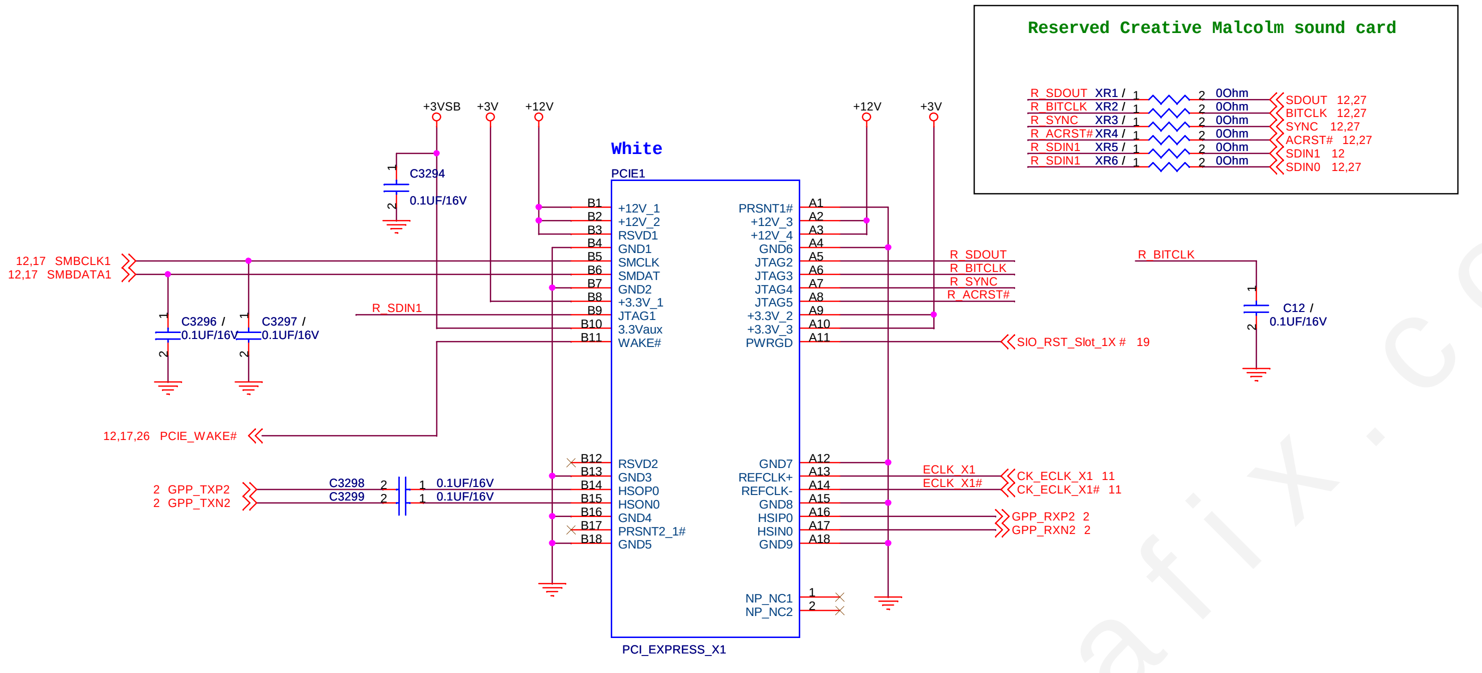


REQUIRED STRAPS

	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPCCLK0	LPCCLK1	RTC_CLK	EC_PWM2	PCI_AD24
PULL HIGH	Allow PCIe Gen2 DEFAULT	Enabled Debug straps	NON-FUSION CLOCK MODE	EC ENABLED	CLKGEN ENABLED DEFAULT	S5+ DISABLE DEFAULT	ROM TYPE: H = LPC ROM	Default PCIe straps
PULL LOW	Force PCIe Gen1	Disabled Debug straps DEFAULT	FUSION CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLED	S5+ ENABLE	L = SPI ROM DEFAULT	EEPROM PCIe straps DEFAULT

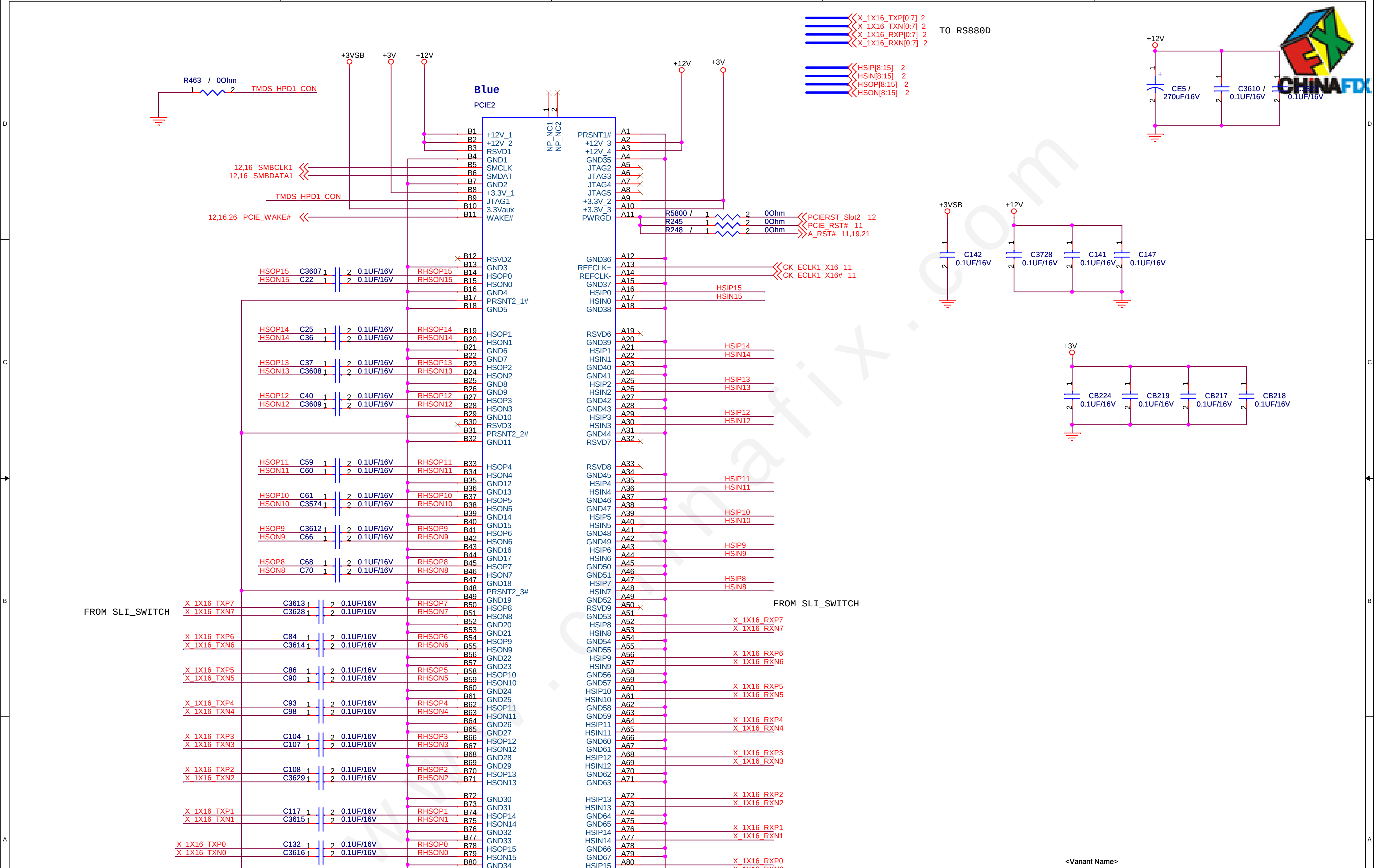
<Variant Name>

ASRock Title : H/W Setting
ASROCK Inc. Engineer: Mingus_Wu
Size A3 | Project Name A55M-HVS | Rev 1.03
Date: Friday, July 22, 2011 | Sheet 15 of 39



<Variant Name>

ASRock	Title : PCIE_X1 SLOT	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 16 of 39	



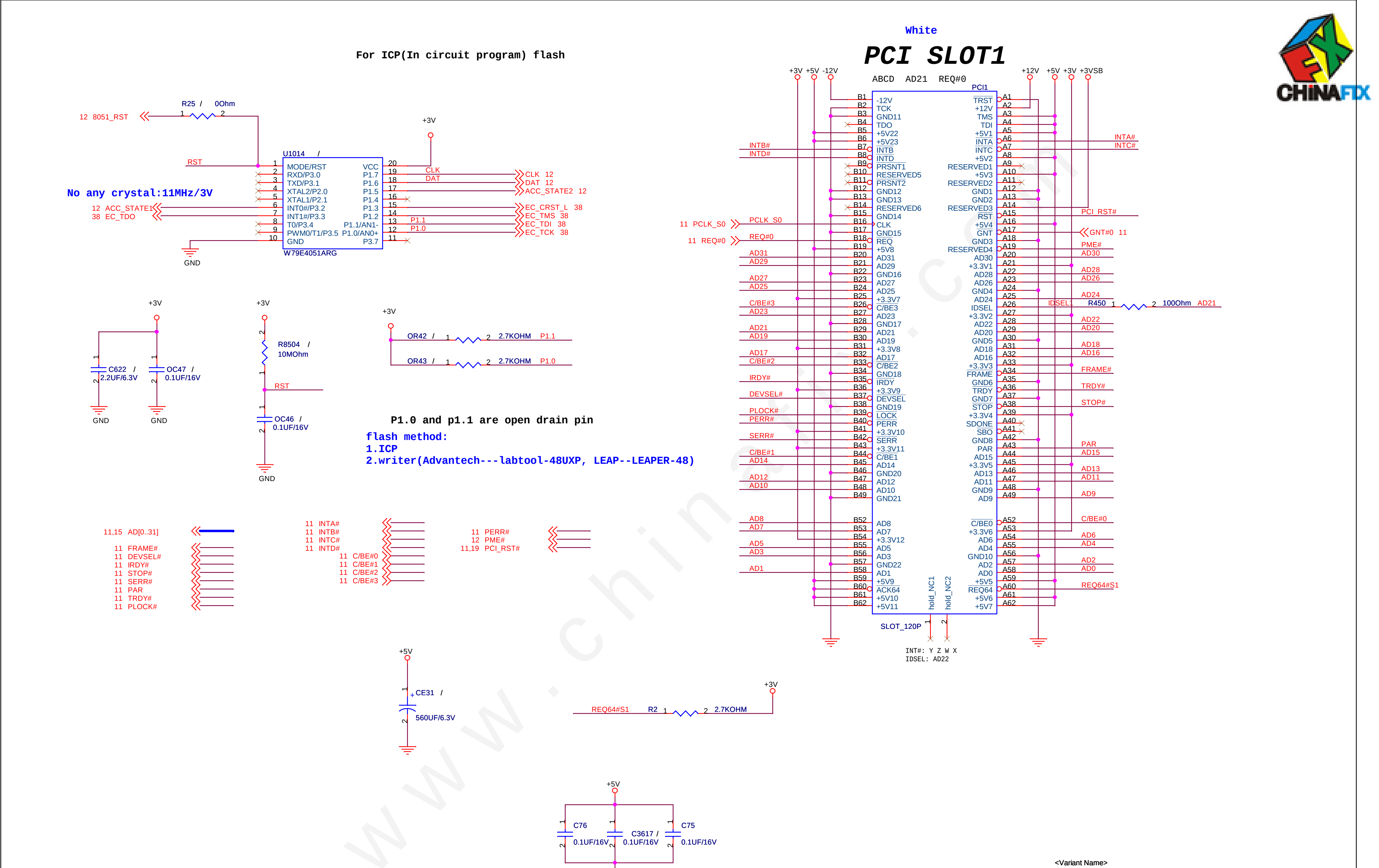
<Variant Name>

ASRock Title : PCIE_x16 SLOT

ASROCK Inc. Engineer: Mingus_Wu

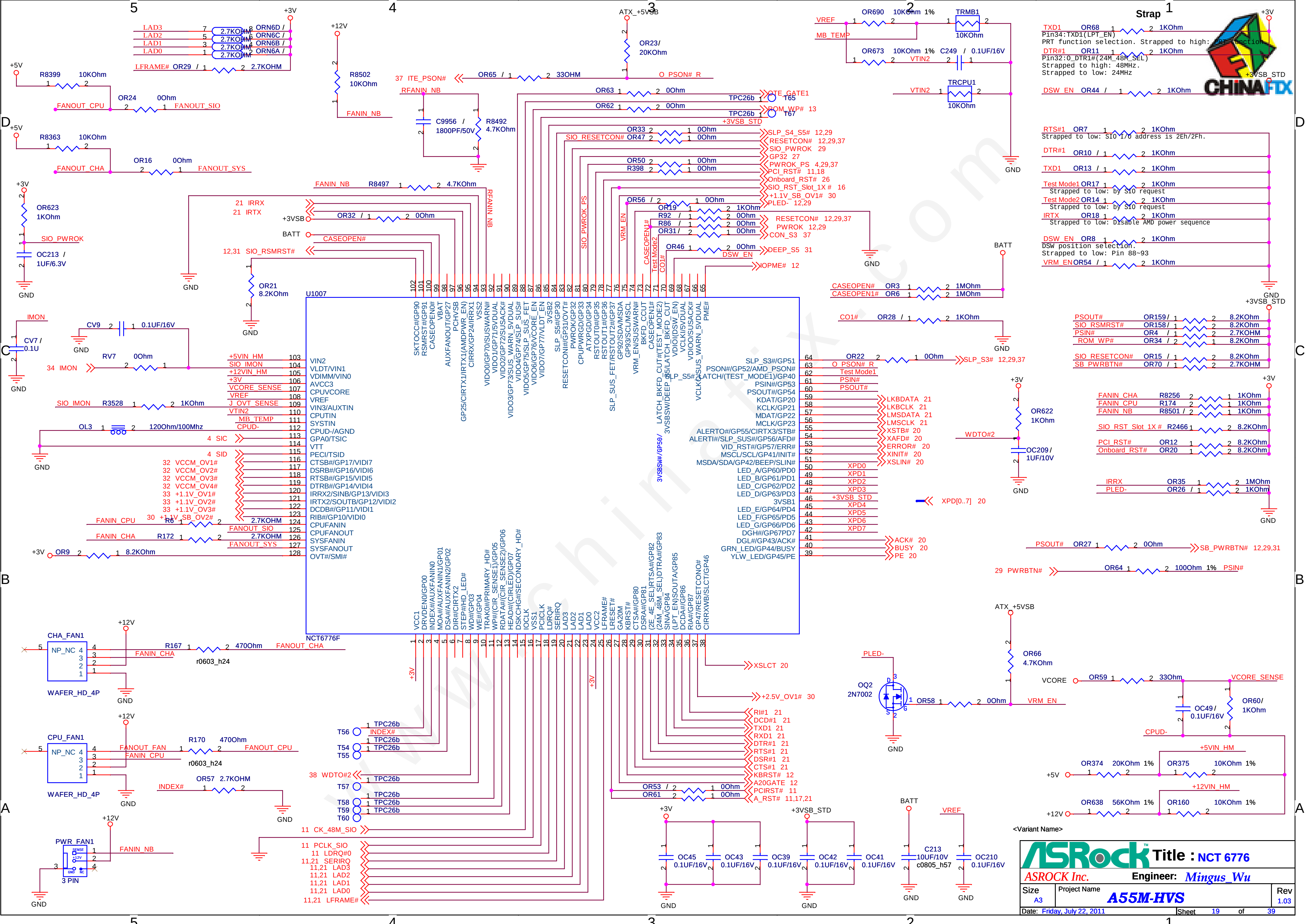
Size A3 | Project Name A55M-HVS | Rev 1.03

Date: Friday, July 22, 2011 Sheet 17 of 39



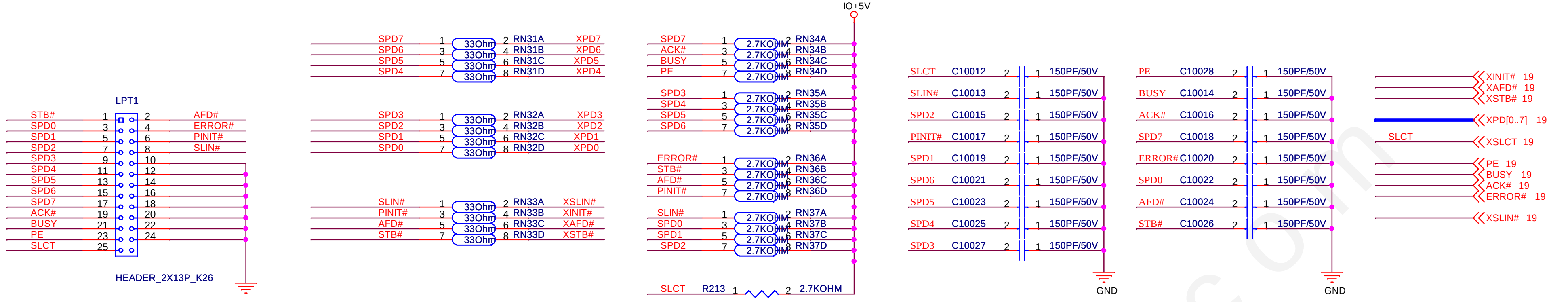
<Variant Name>

ASRock	Title : EC & PCI	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 18 of 39	



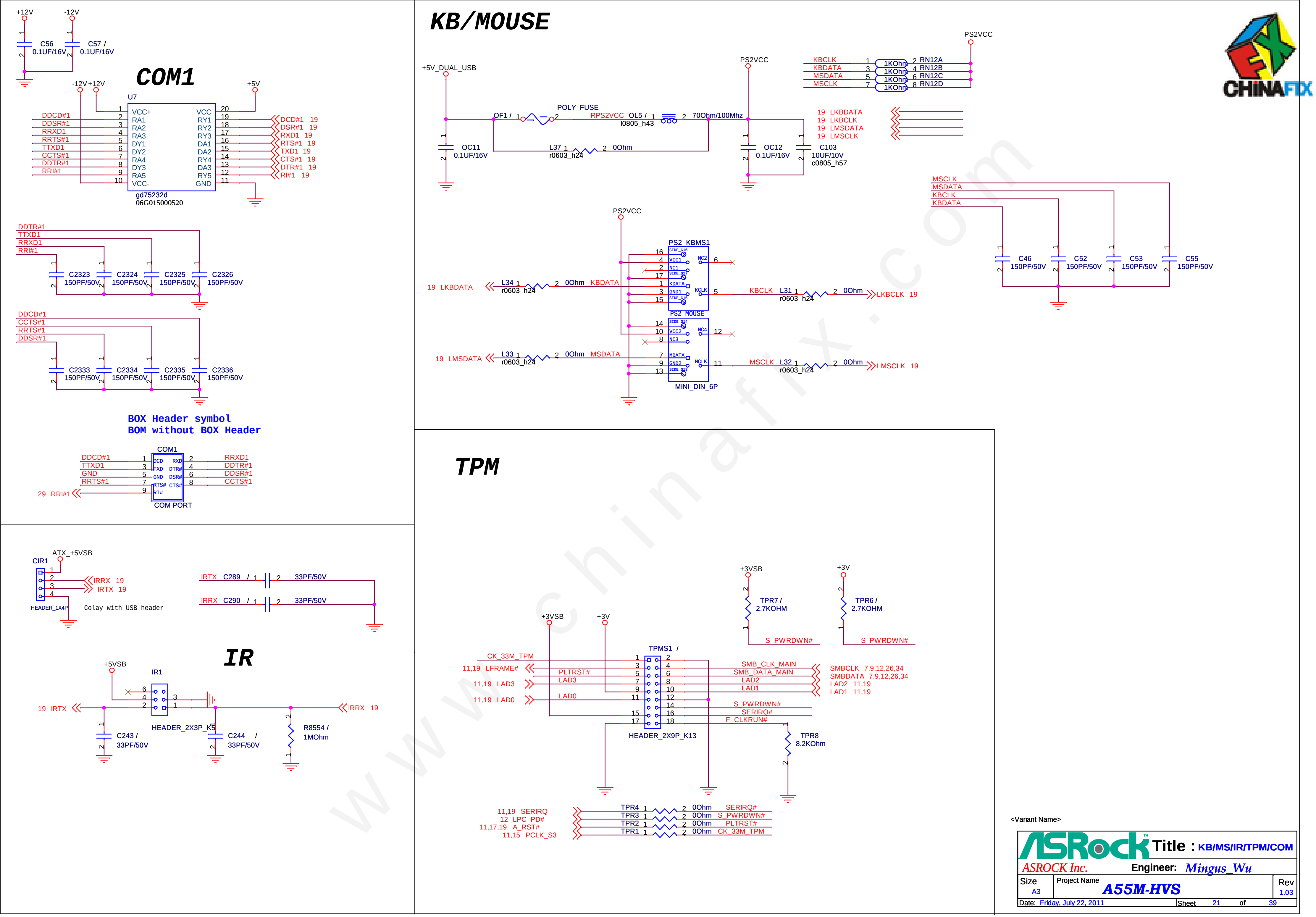
Strap
U1007
NCT6776F
CHA_FAN1
WAFER_HD_4P
CPU_FAN1
WAFER_HD_4P
PWR_FAN1
3 PIN
PRT function selection. Strapped to high:
Strapped to high: 48MHz.
Strapped to low: 24MHz
Strapped to low: SIO I/O address is 2Eh/2Fh.
Strapped to low: by SIO request
Strapped to low: by SIO request
Strapped to low: Disable AMD power sequence
DSW position selection.
Strapped to low: Pin 88~93
<Variant Name>
ASROCK Inc.
Title : NCT 6776
Engineer: Mingus_Wu
Size A3
Project Name A55M-HVS
Rev 1.03
Date: Friday, July 22, 2011
Sheet 19 of 39

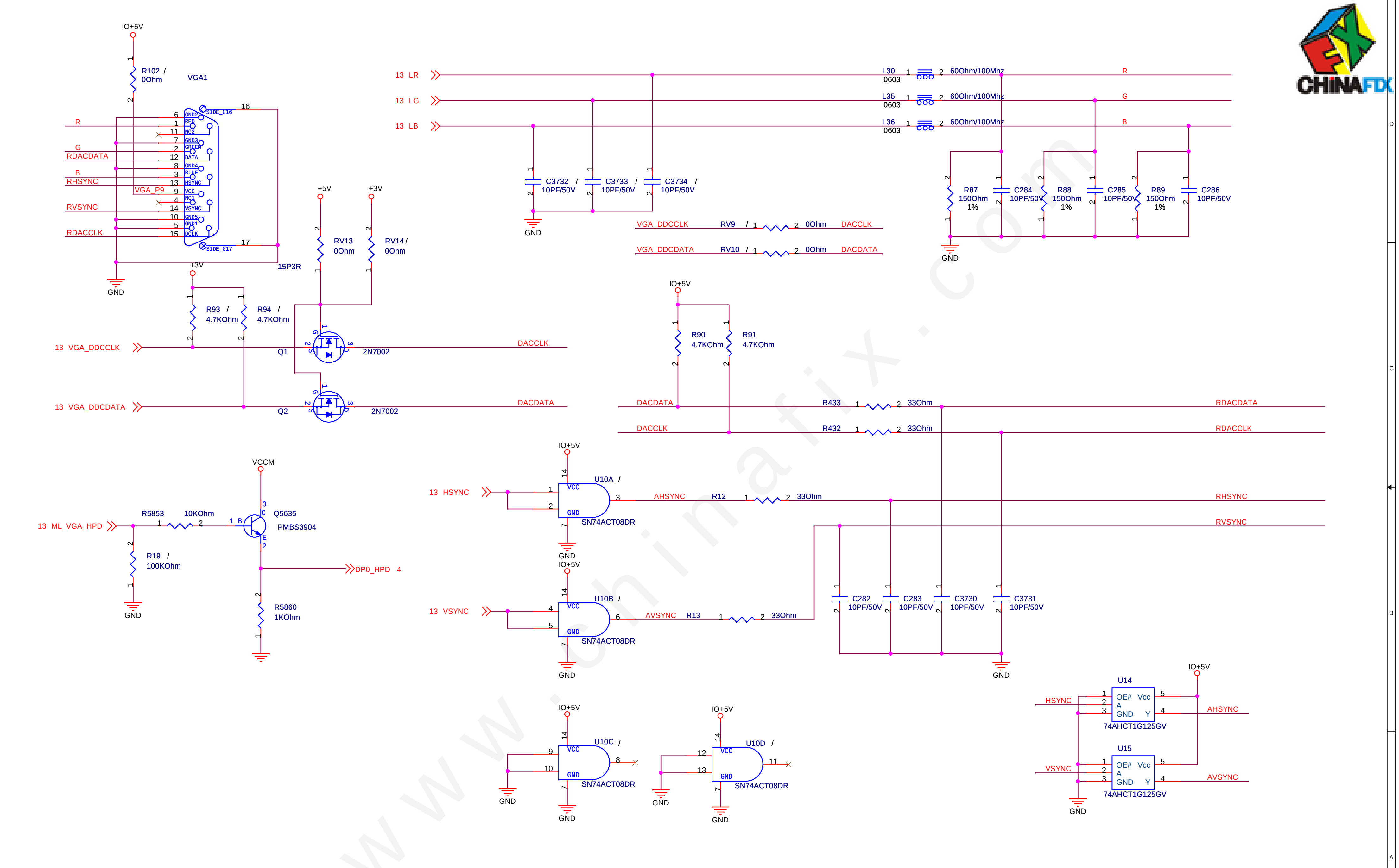
Parallel Port



<Variant Name>

ASRock	Title :	Printer port/FAN
ASROCK Inc.	Engineer:	Mingus_Wu
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 20	of 39

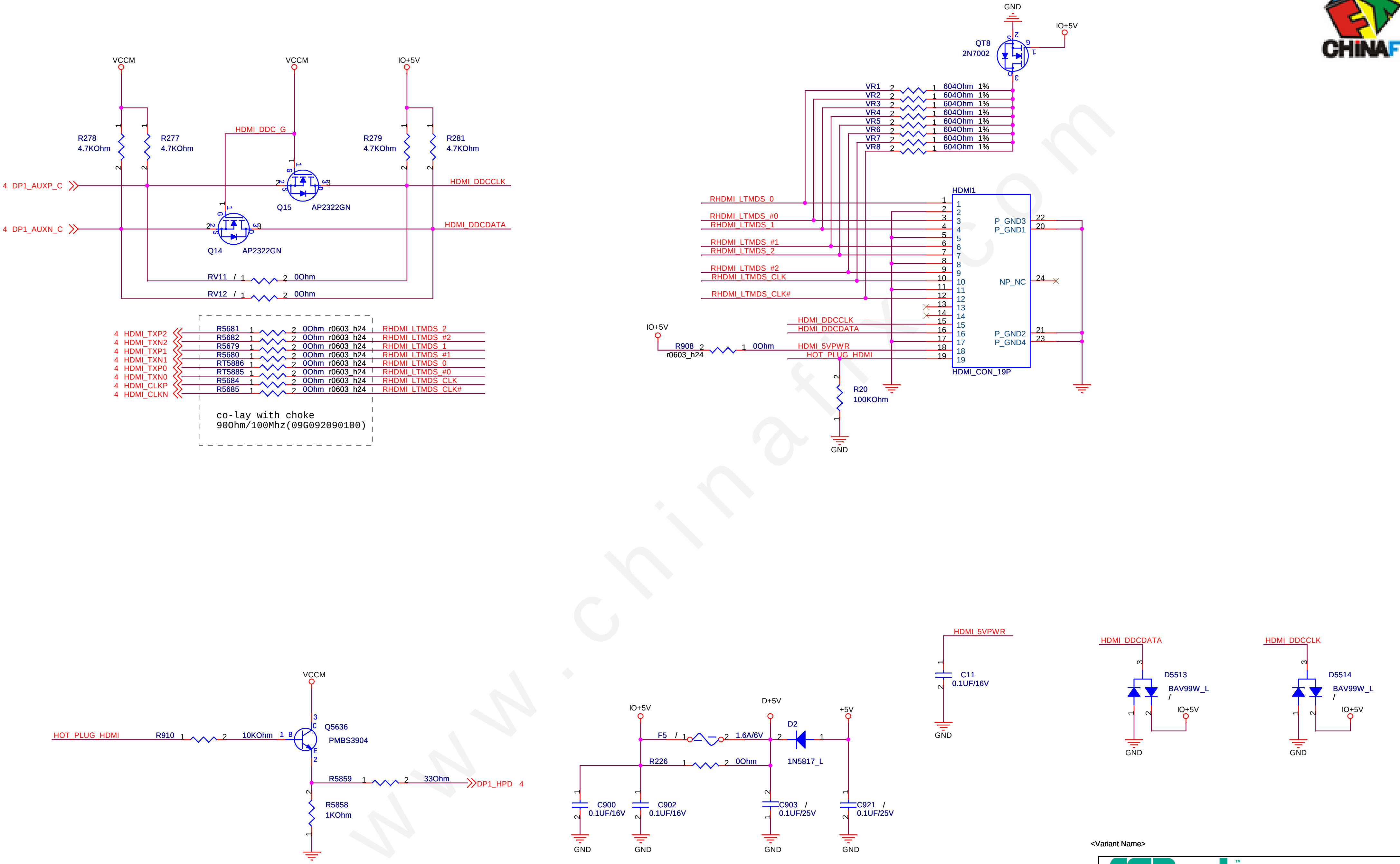

KB/MOUSE
COM1
IR
TPM
+12V
-12V
C56 0.1UF/16V
C57 / 0.1UF/16V
U7
VCC+ RA1 RA2 RA3 DY1 DY2 RA4 DY3 RA5 VCC-
VCC RY1 RY2 RY3 DA1 DA2 RY4 DA3 RY5 GND
gd75232d
06G015000520
+5V
DDCD#1 DDSR#1 RRXD1 RRTS#1 TTXD1 CCTS#1 DDTR#1 RRI#1
DCD#1 19
DSR#1 19
RXD1 19
RTS#1 19
TXD1 19
CTS#1 19
DTR#1 19
RI#1 19
C2323 150PF/50V
C2324 150PF/50V
C2325 150PF/50V
C2326 150PF/50V
C2333 150PF/50V
C2334 150PF/50V
C2335 150PF/50V
C2336 150PF/50V
BOX Header symbol
BOM without BOX Header
COM1
COM PORT
29 RRI#1
+5V_DUAL_USB
OF1 / POLY_FUSE
RPS2VCC OL5 / l0805_h43
70Ohm/100Mhz
L37 r0603_h24 0Ohm
OC11 0.1UF/16V
OC12 0.1UF/16V
C103 10UF/10V c0805_h57
PS2VCC
KBCLK KBDATA MSDATA MSCLK
1KOhm RN12A RN12B RN12C RN12D
19 LKBDATA
19 LKBCLK
19 LMSDATA
19 LMSCLK
C46 150PF/50V
C52 150PF/50V
C53 150PF/50V
C55 150PF/50V
PS2_KBMS1
PS2_MOUSE
MINI_DIN_6P
L34 r0603_h24 0Ohm
L31 r0603_h24 0Ohm
L33 r0603_h24 0Ohm
L32 r0603_h24 0Ohm
LKBCLK 19
LMSCLK 19
ATX_+5VSB
CIR1
HEADER_1X4P
IRRX 19
IRTX 19
Colay with USB header
IRTX C289 / 33PF/50V
IRRX C290 / 33PF/50V
+5VSB
IR1
HEADER_2X3P_K5
C243 / 33PF/50V
C244 / 33PF/50V
R8554 / 1MOhm
19 IRTX
IRRX 19
+3VSB
+3V
TPR7 / 2.7KOHM
TPR6 / 2.7KOHM
S_PWRDWN#
TPMS1 /
HEADER_2X9P_K13
CK_33M_TPM
11,19 LFRAME#
11,19 LAD3
11,19 LAD0
PLTRST# LAD3 LAD0
SMB_CLK_MAIN
SMB_DATA_MAIN
LAD2 LAD1
S_PWRDWN# SERIRQ# F_CLKRUN#
SMBCLK 7,9,12,26,34
SMBDATA 7,9,12,26,34
LAD2 11,19
LAD1 11,19
TPR8 8.2KOhm
11,19 SERIRQ
12 LPC_PD#
11,17,19 A_RST#
11,15 PCLK_S3
TPR4 TPR3 TPR2 TPR1 0Ohm
SERIRQ# S_PWRDWN# PLTRST# CK_33M_TPM
<Variant Name>
ASRock
Title : KB/MS/IR/TPM/COM
ASROCK Inc.
Engineer: Mingus_Wu
Size A3
Project Name A55M-HVS
Rev 1.03
Date: Friday, July 22, 2011
Sheet 21 of 39



<Variant Name>

ASRock	Title : ON_BORAD VGA	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 22 of 39	

HDMI



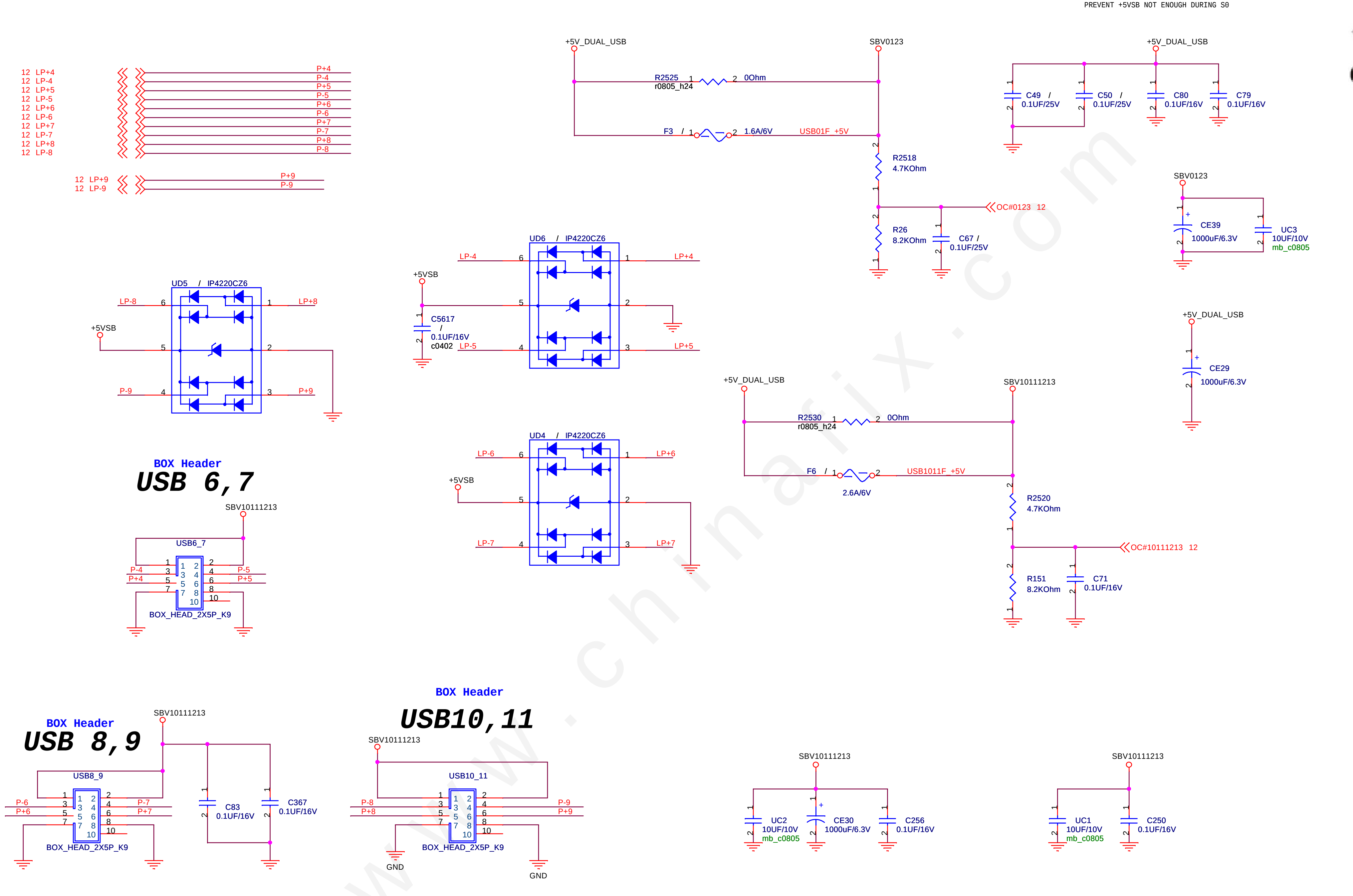
<Variant Name>

ASRock **Title : HDMI**

ASROCK Inc. **Engineer:** *Mingus_Wu*

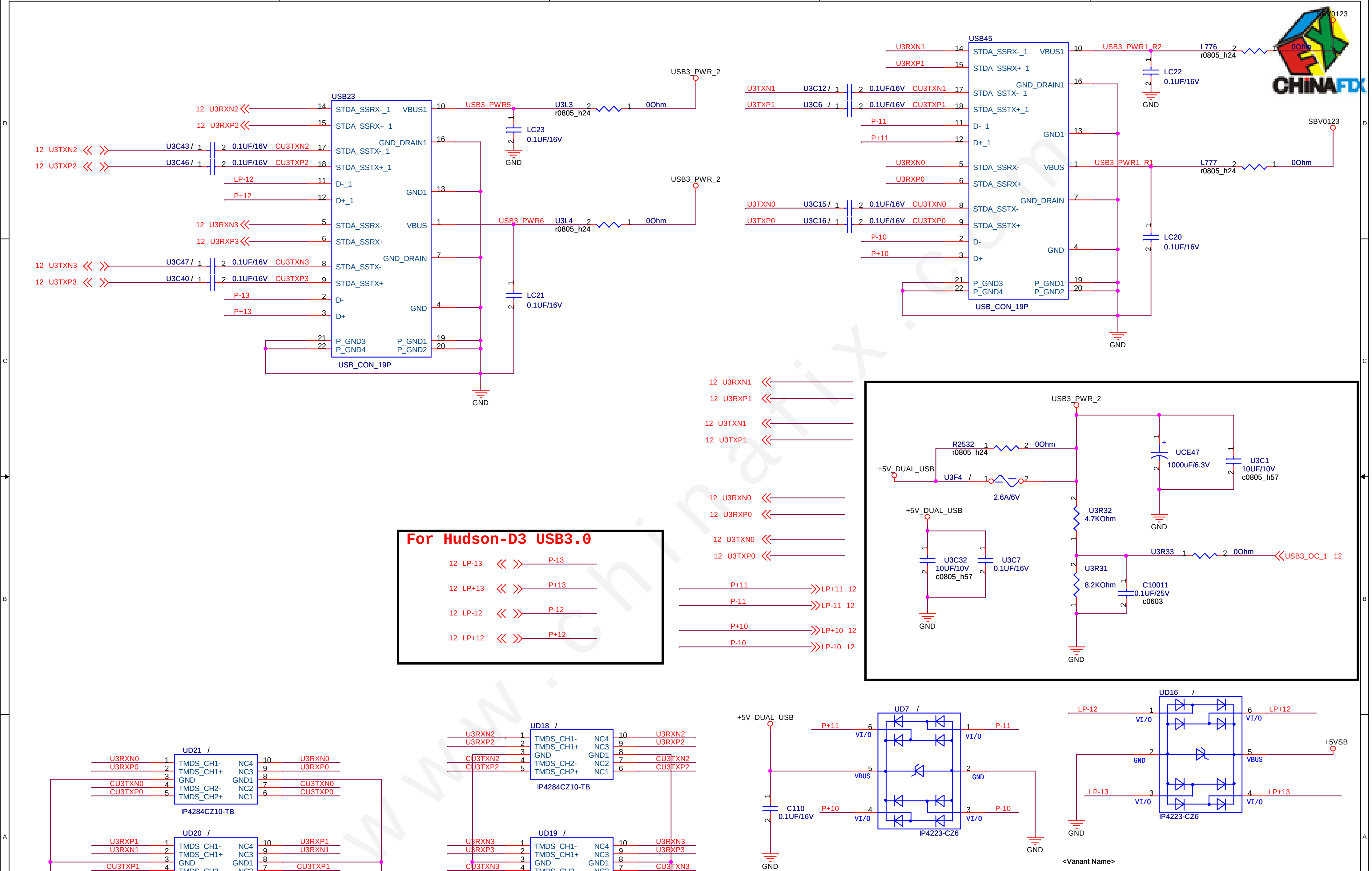
Size	Project Name	Rev
A3	A55M-HVS	1.03

Date: Friday, July 22, 2011 Sheet 23 of 39



ASRock Inc.

Title :	USB_PORT	
Engineer:	Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 24 of 39	



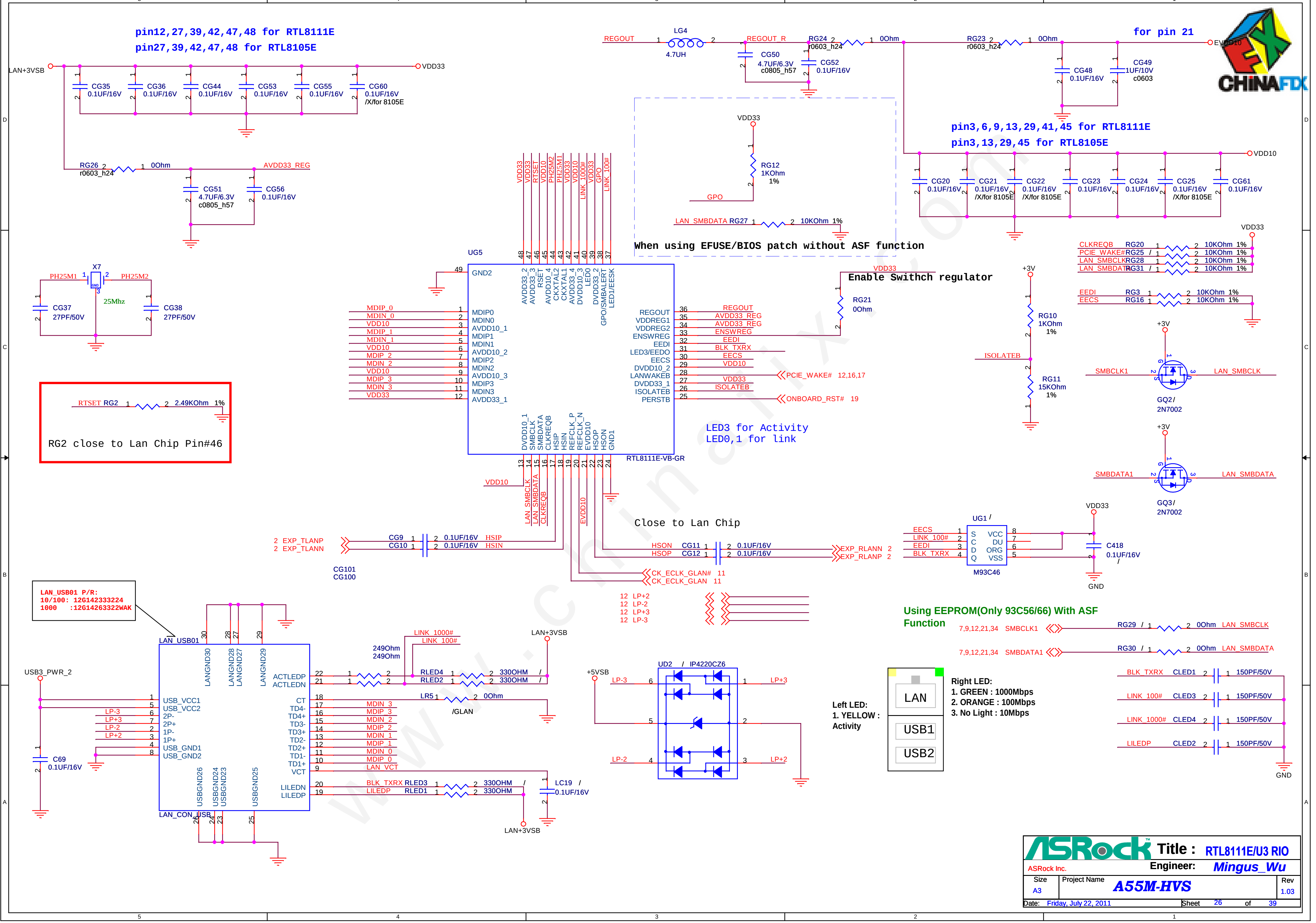
Title : U3 RIO

ASROCK Inc.

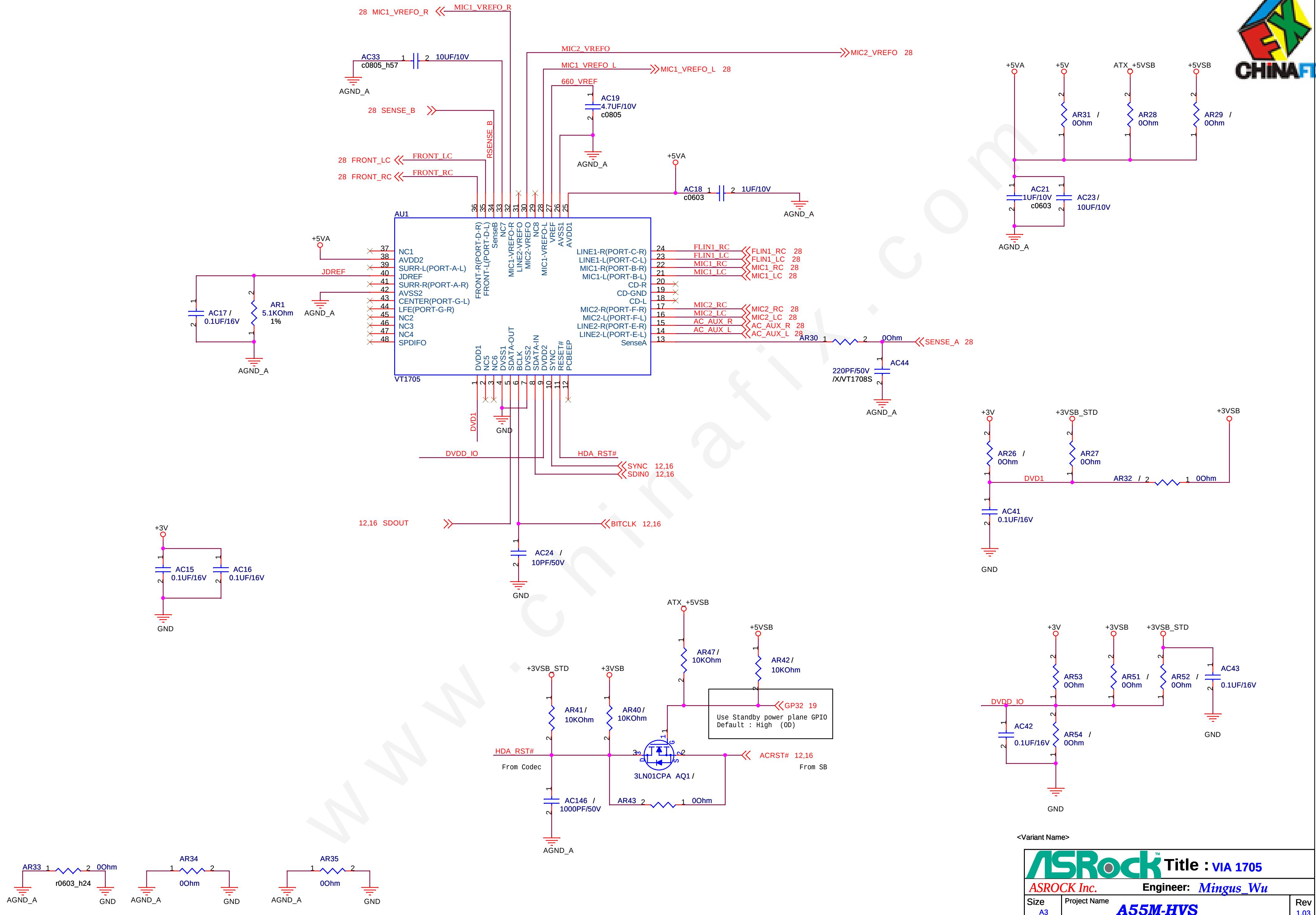
Engineer: *Mingus_Wu*

Size	Project Name	Rev
A3	A55M-HVS	1.03

Date: Friday, July 22, 2011 — Sheet 25 of 39



pin12,27,39,42,47,48 for RTL8111E
pin27,39,42,47,48 for RTL8105E
LAN+3VSB
VDD33
CG35 0.1UF/16V
CG36 0.1UF/16V
CG44 0.1UF/16V
CG53 0.1UF/16V
CG55 0.1UF/16V
CG60 0.1UF/16V /X/for 8105E
RG26 r0603_h24 0Ohm
AVDD33_REG
CG51 4.7UF/6.3V c0805_h57
CG56 0.1UF/16V
REGOUT
LG4 4.7UH
REGOUT_R
CG50 4.7UF/6.3V c0805_h57
CG52 0.1UF/16V
RG24 r0603_h24 0Ohm
RG23 r0603_h24 0Ohm
for pin 21
EVDD10
CG48 0.1UF/16V
CG49 1UF/10V c0603
pin3,6,9,13,29,41,45 for RTL8111E
pin3,13,29,45 for RTL8105E
VDD10
CG20 0.1UF/16V
CG21 0.1UF/16V /X/for 8105E
CG22 0.1UF/16V /X/for 8105E
CG23 0.1UF/16V
CG24 0.1UF/16V
CG25 0.1UF/16V /X/for 8105E
CG61 0.1UF/16V
RG12 1KOhm 1%
GPO
LAN_SMBDATA RG27 10KOhm 1%
When using EFUSE/BIOS patch without ASF function
Enable Swithch regulator
RG21 0Ohm
X7 25Mhz
PH25M1
PH25M2
CG37 27PF/50V
CG38 27PF/50V
UG5
RTL8111E-VB-GR
RTSET RG2 2.49KOhm 1%
RG2 close to Lan Chip Pin#46
PCIE_WAKE# 12,16,17
ONBOARD_RST# 19
LED3 for Activity
LED0,1 for link
CLKREQB RG20 10KOhm 1%
PCIE_WAKE# RG25 10KOhm 1%
LAN_SMBCLK RG28 10KOhm 1%
LAN_SMBDATA RG31 10KOhm 1%
EEDI RG3 10KOhm 1%
EECS RG16 10KOhm 1%
RG10 1KOhm 1%
ISOLATEB
RG11 15KOhm 1%
SMBCLK1
LAN_SMBCLK
GQ2 2N7002
SMBDATA1
LAN_SMBDATA
GQ3 2N7002
Close to Lan Chip
EXP_TLANP
EXP_TLANN
CG9 0.1UF/16V HSIP
CG10 0.1UF/16V HSIN
CG101
CG100
HSON CG11 0.1UF/16V EXP_RLANN
HSOP CG12 0.1UF/16V EXP_RLANP
CK_ECLK_GLAN# 11
CK_ECLK_GLAN 11
UG1 M93C46
C418 0.1UF/16V
Using EEPROM(Only 93C56/66) With ASF Function
7,9,12,21,34 SMBCLK1 RG29 0Ohm LAN_SMBCLK
7,9,12,21,34 SMBDATA1 RG30 0Ohm LAN_SMBDATA
LAN_USB01 P/R:
10/100: 12G142333224
1000 :12G142633222WAK
LAN_USB01
LAN_CON_USB
USB3_PWR_2
C69 0.1UF/16V
249Ohm
249Ohm
LINK_1000#
LINK_100#
RLED4 330OHM
RLED2 330OHM
LR5 0Ohm /GLAN
BLK_TXRX RLED3 330OHM
LILEDP RLED1 330OHM
LC19 0.1UF/16V
UD2 IP4220CZ6
+5VSB
LAN
USB1
USB2
Left LED:
1. YELLOW : Activity
Right LED:
1. GREEN : 1000Mbps
2. ORANGE : 100Mbps
3. No Light : 10Mbps
BLK_TXRX CLED1 150PF/50V
LINK_100# CLED3 150PF/50V
LINK_1000# CLED4 150PF/50V
LILEDP CLED2 150PF/50V
CHINAFIX
ASRock
ASRock Inc.
Title : RTL8111E/U3 RIO
Engineer: Mingus_Wu
Size A3
Project Name A55M-HVS
Rev 1.03
Date: Friday, July 22, 2011
Sheet 26 of 39



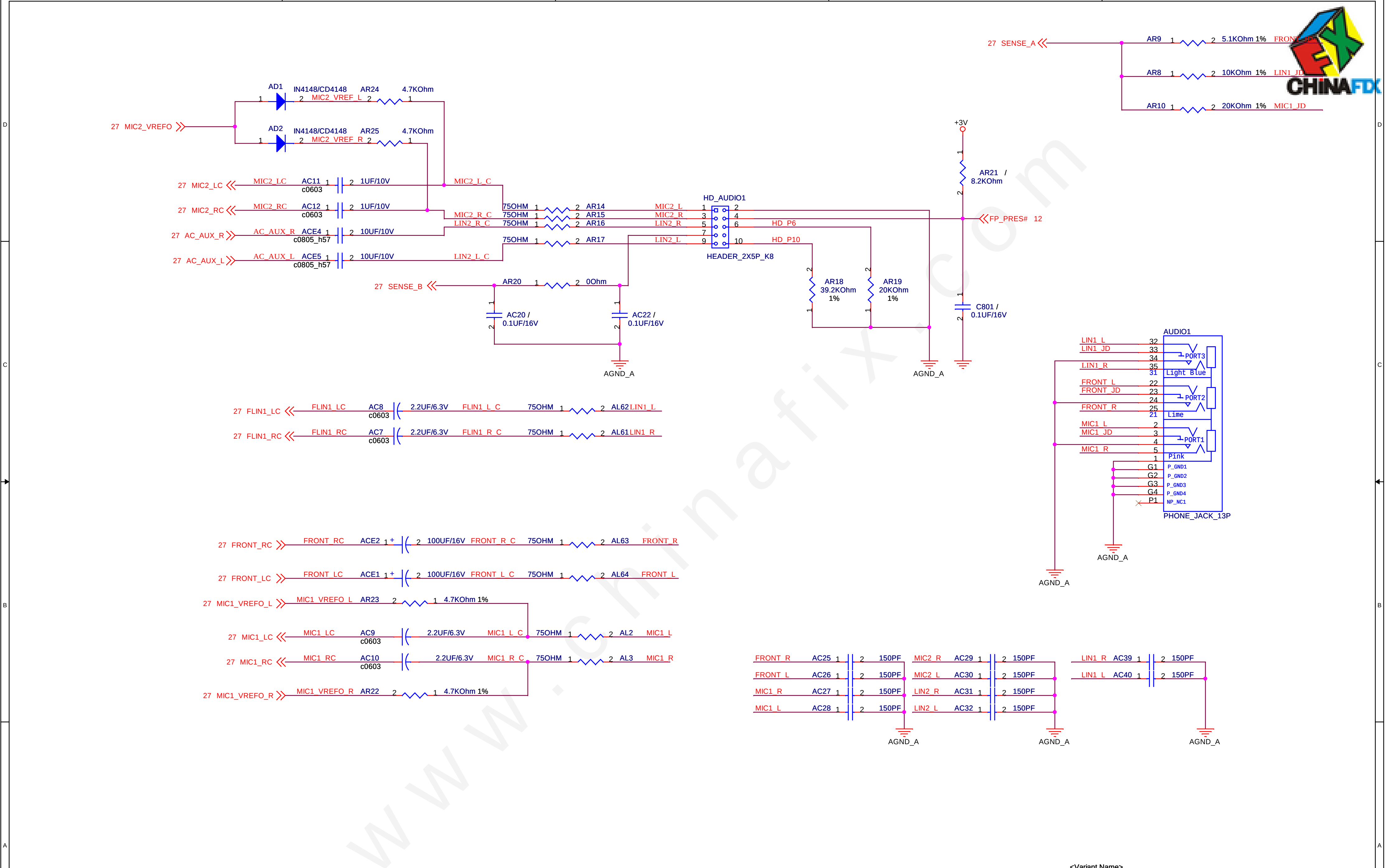
<Variant Name>

Title : VIA 1705

ASROCK Inc. Engineer: Mingus_Wu

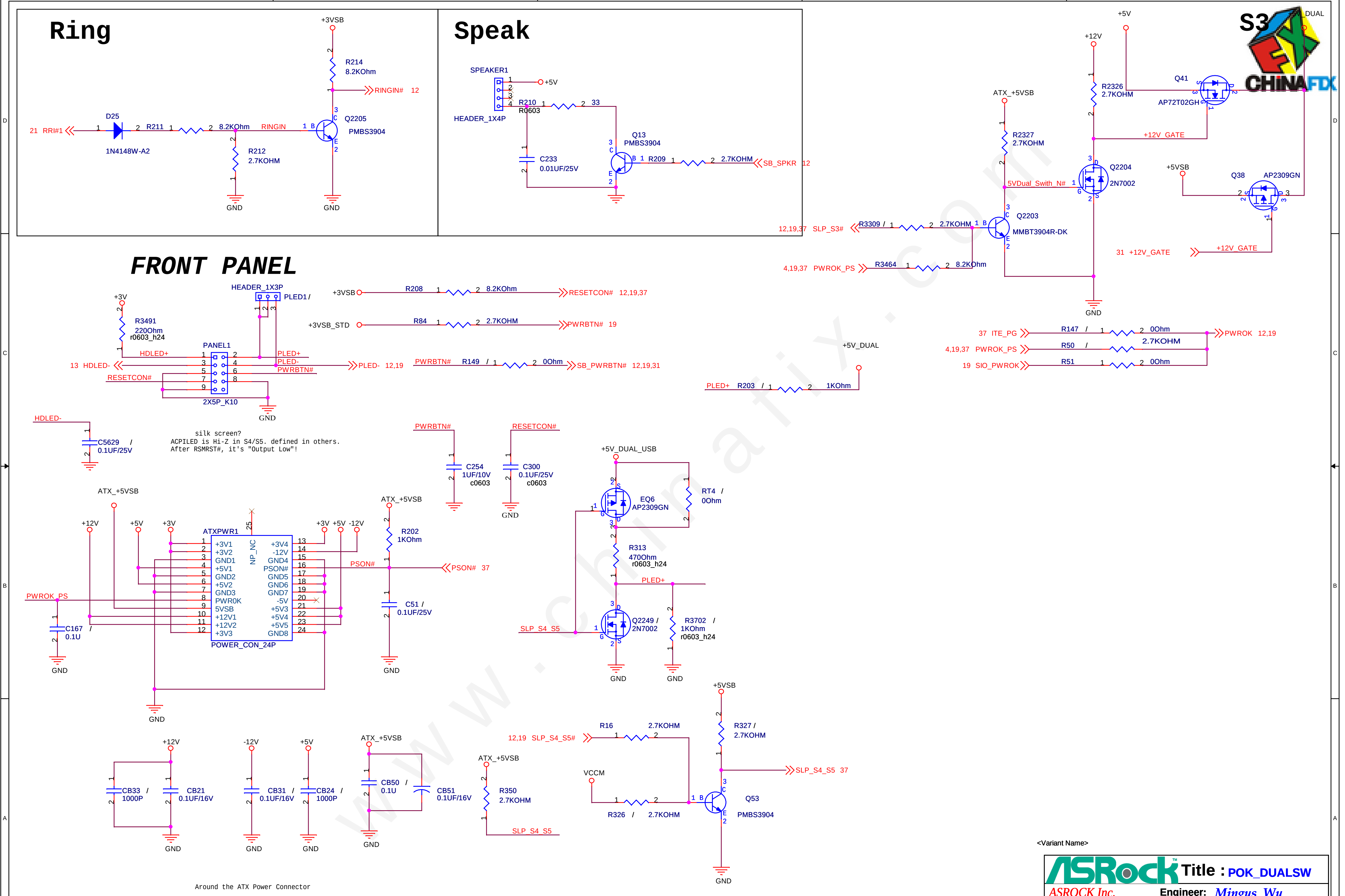
Size A3 | Project Name A55M-HVS | Rev 1.03

Date: Friday, July 22, 2011 Sheet 27 of 39



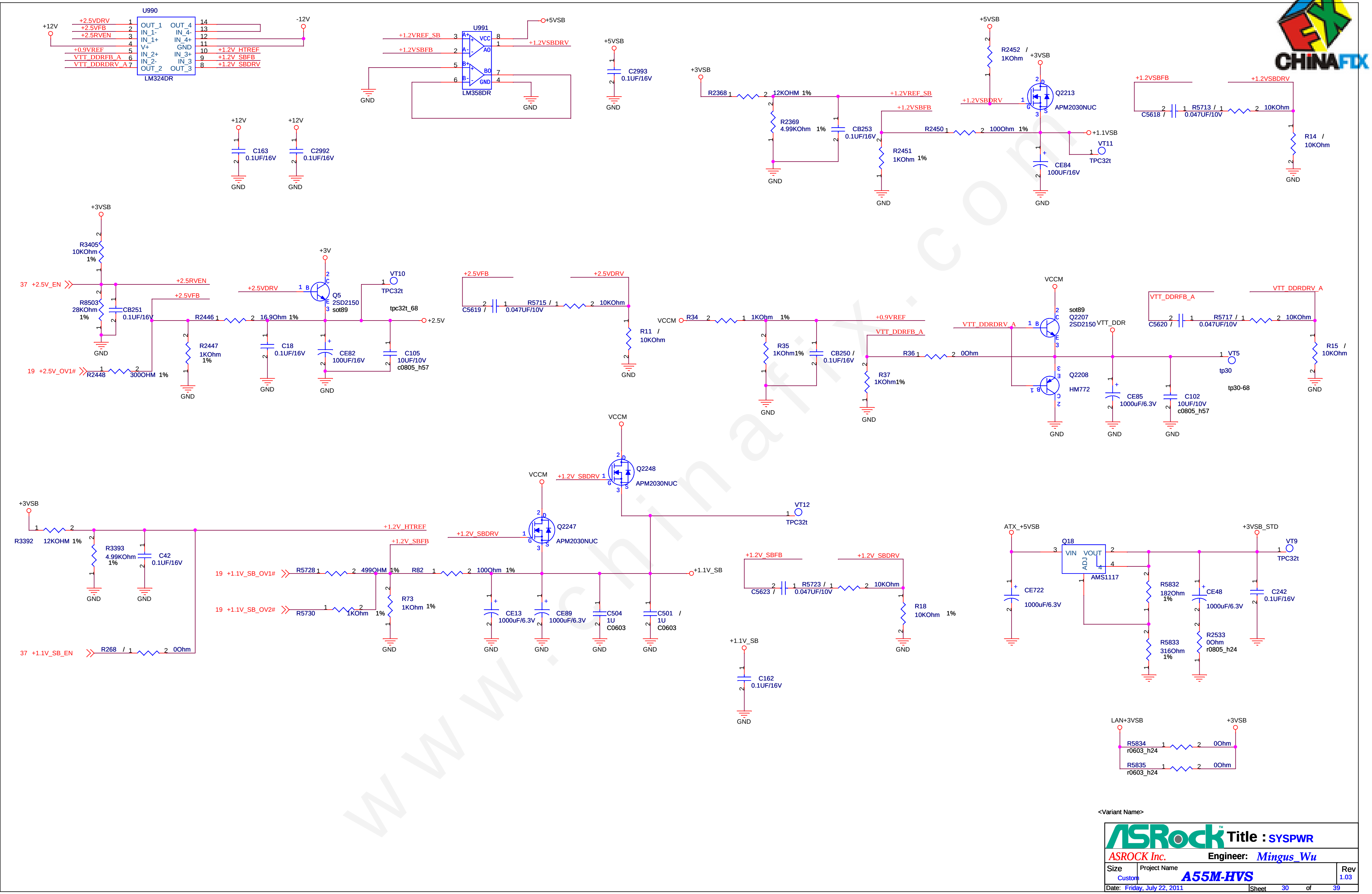
<Variant Name>

ASRock	Title : AUDIO CONN	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 28 of 39	

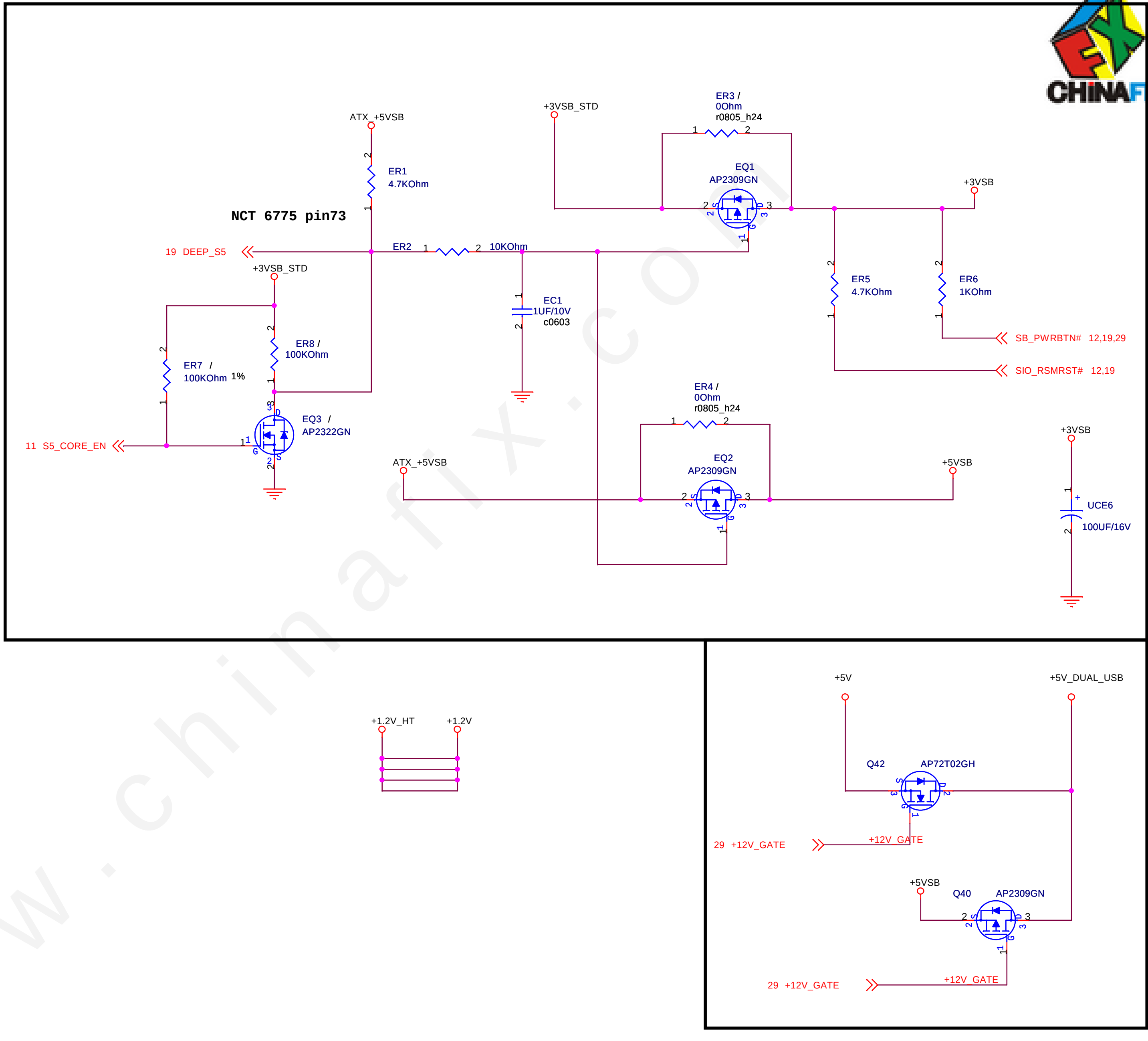


<Variant Name>

ASRock	Title : POK_DUALSW		
ASROCK Inc.	Engineer: Mingus_Wu		
Size A3	Project Name A55M-HVS		Rev 1.03
Date: Friday, July 22, 2011	Sheet 29	of 39	

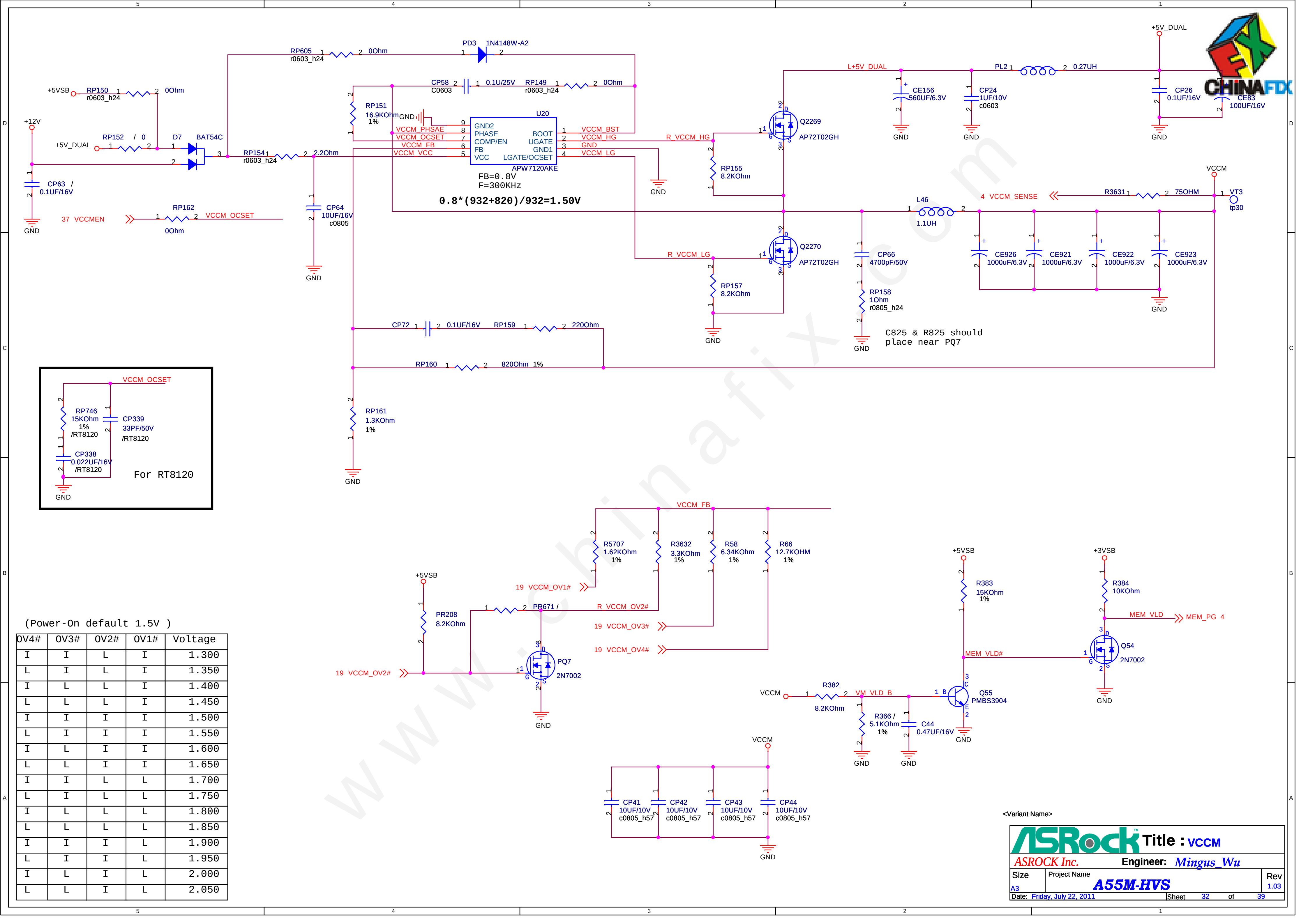


U990
LM324DR
+12V
-12V
+2.5VDRV
+2.5VFB
+2.5VREN
+0.9VREF
VTT_DDRFB_A
VTT_DDRDRV_A
+1.2V_HTREF
+1.2V_SBFB
+1.2V_SBDRV
OUT_1 IN_1- IN_1+ V+ IN_2+ IN_2- OUT_2
OUT_4 IN_4- IN_4+ GND IN_3+ IN_3- OUT_3
U991
LM358DR
+1.2VREF_SB
+1.2VSBFB
+1.2VSBDRV
+5VSB
GND
C2993 0.1UF/16V
C163 0.1UF/16V
C2992 0.1UF/16V
+3VSB
R2368 12KOHM 1%
R2369 4.99KOhm 1%
CB253 0.1UF/16V
R2452 1KOhm
Q2213 APM2030NUC
R2450 100Ohm 1%
R2451 1KOhm 1%
+1.1VSB
VT11 TPC32t
CE84 100UF/16V
C5618 R5713 0.047UF/10V 10KOhm
R14 10KOhm
37 +2.5V_EN
R3405 10KOhm 1%
R8503 28KOhm 1%
CB251 0.1UF/16V
19 +2.5V_OV1#
R2448 300OHM 1%
R2446 16.9Ohm 1%
R2447 1KOhm 1%
C18 0.1UF/16V
+3V
Q5 2SD2150 sot89
VT10 TPC32t tpc32t_68
CE82 100UF/16V
C105 10UF/10V c0805_h57
+2.5V
C5619 R5715 0.047UF/10V 10KOhm
R11 10KOhm
VCCM
R34 1KOhm 1%
R35 1KOhm1%
CB250 0.1UF/16V
R37 1KOhm1%
R36 0Ohm
Q2207 2SD2150 sot89
Q2208 HM772
VTT_DDR
CE85 1000uF/6.3V
C102 10UF/10V c0805_h57
VT5 tp30 tp30-68
C5620 R5717 0.047UF/10V 10KOhm
R15 10KOhm
Q2248 APM2030NUC
Q2247 APM2030NUC
VT12 TPC32t
R3392 12KOHM 1%
R3393 4.99KOhm 1%
C42 0.1UF/16V
19 +1.1V_SB_OV1#
19 +1.1V_SB_OV2#
37 +1.1V_SB_EN
R5728 499OHM 1%
R5730 1KOhm 1%
R82 100Ohm 1%
R73 1KOhm 1%
R268 0Ohm
CE13 1000uF/6.3V
CE89 1000uF/6.3V
C504 1U C0603
C501 1U C0603
+1.1V_SB
C162 0.1UF/16V
C5623 R5723 0.047UF/10V 10KOhm
R18 10KOhm 1%
ATX_+5VSB
Q18 AMS1117
VIN VOUT ADJ
CE722 1000uF/6.3V
R5832 182Ohm 1%
R5833 316Ohm 1%
CE48 1000uF/6.3V
R2533 0Ohm r0805_h24
+3VSB_STD
C242 0.1UF/16V
VT9 TPC32t
LAN+3VSB
R5834 r0603_h24 0Ohm
R5835 r0603_h24 0Ohm
www.chinafix.com
CHINAFIX
<Variant Name>
ASRock
Title : SYSPWR
ASROCK Inc.
Engineer: Mingus_Wu
Size Custom
Project Name A55M-HVS
Rev 1.03
Date: Friday, July 22, 2011
Sheet 30 of 39



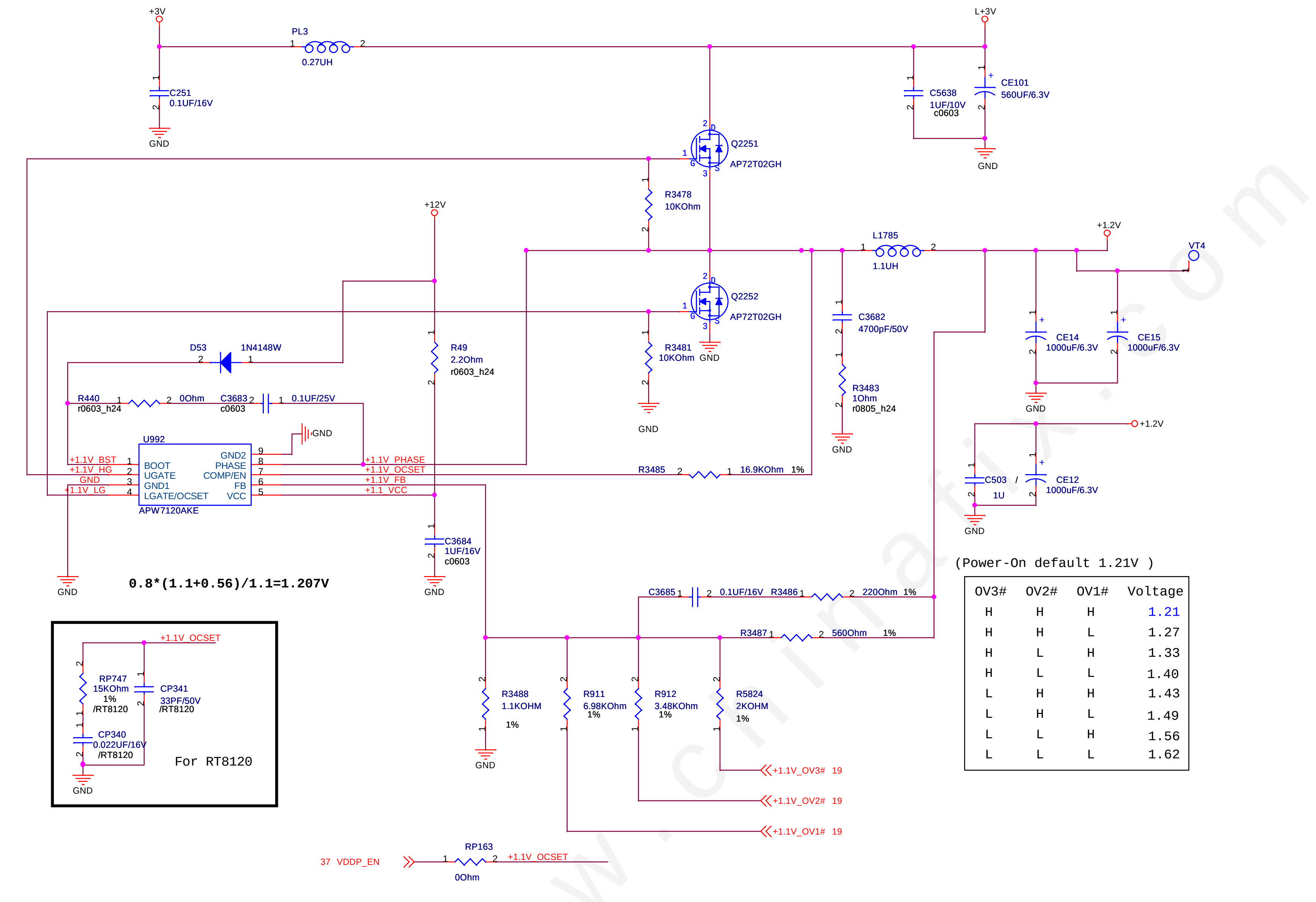
<Variant Name>

ASRock	Title : USB3.0 Power	
ASROCK Inc.	Engineer: Mingus_Wu	
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 31 of 39	



(Power-On default 1.5V)

OV4#	OV3#	OV2#	OV1#	Voltage
I	I	L	I	1.300
L	I	L	I	1.350
I	L	L	I	1.400
L	L	L	I	1.450
I	I	I	I	1.500
L	I	I	I	1.550
I	L	I	I	1.600
L	L	I	I	1.650
I	I	L	L	1.700
L	I	L	L	1.750
I	L	L	L	1.800
L	L	L	L	1.850
I	I	I	L	1.900
L	I	I	L	1.950
I	L	I	L	2.000
L	L	I	L	2.050



(Power-On default 1.21V)

OV3#	OV2#	OV1#	Voltage
H	H	H	1.21
H	H	L	1.27
H	L	H	1.33
H	L	L	1.40
L	H	H	1.43
L	H	L	1.49
L	L	H	1.56
L	L	L	1.62

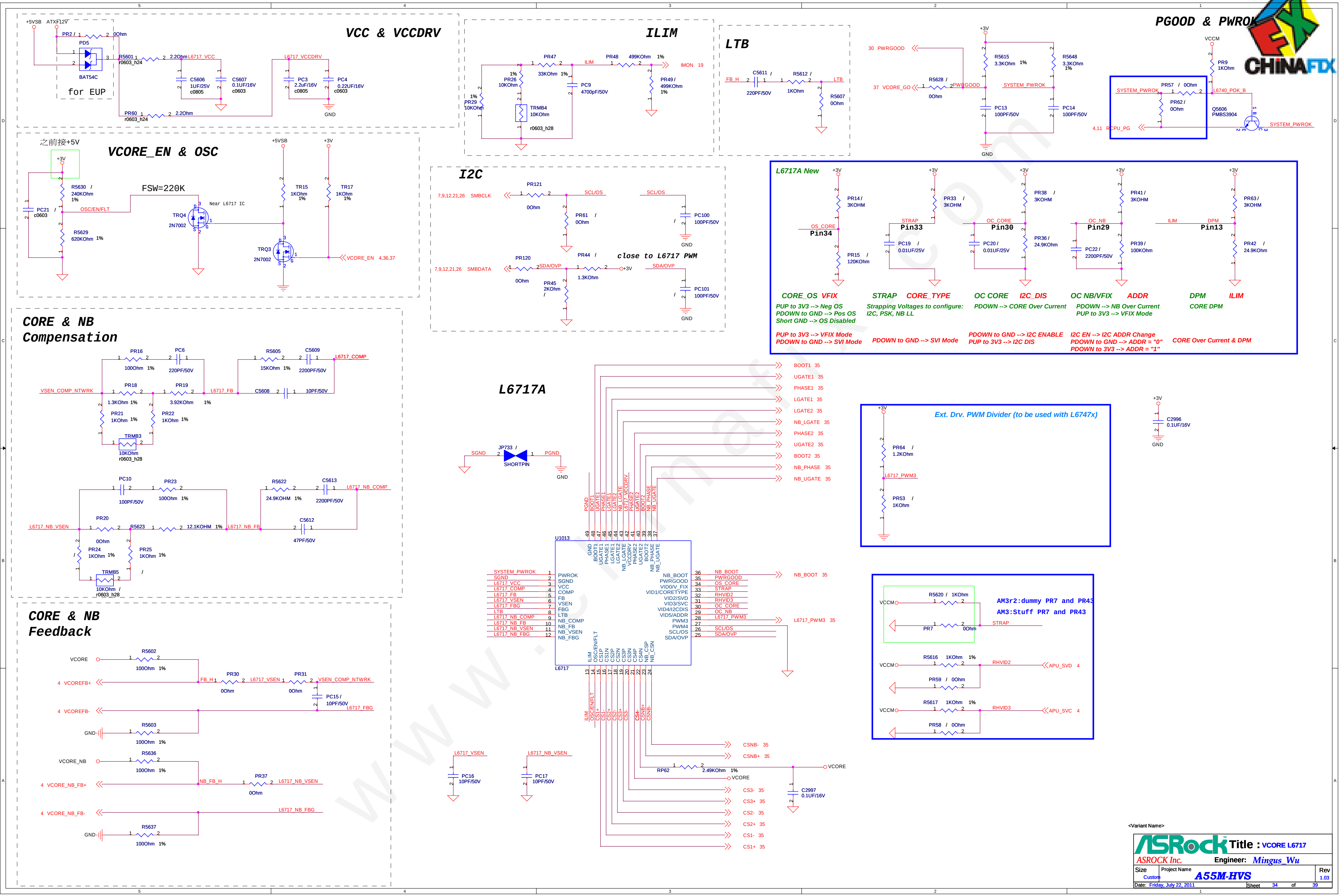
<Variant Name>

ASRock Title : 1.1V switching

ASROCK Inc. Engineer: Mingus_Wu

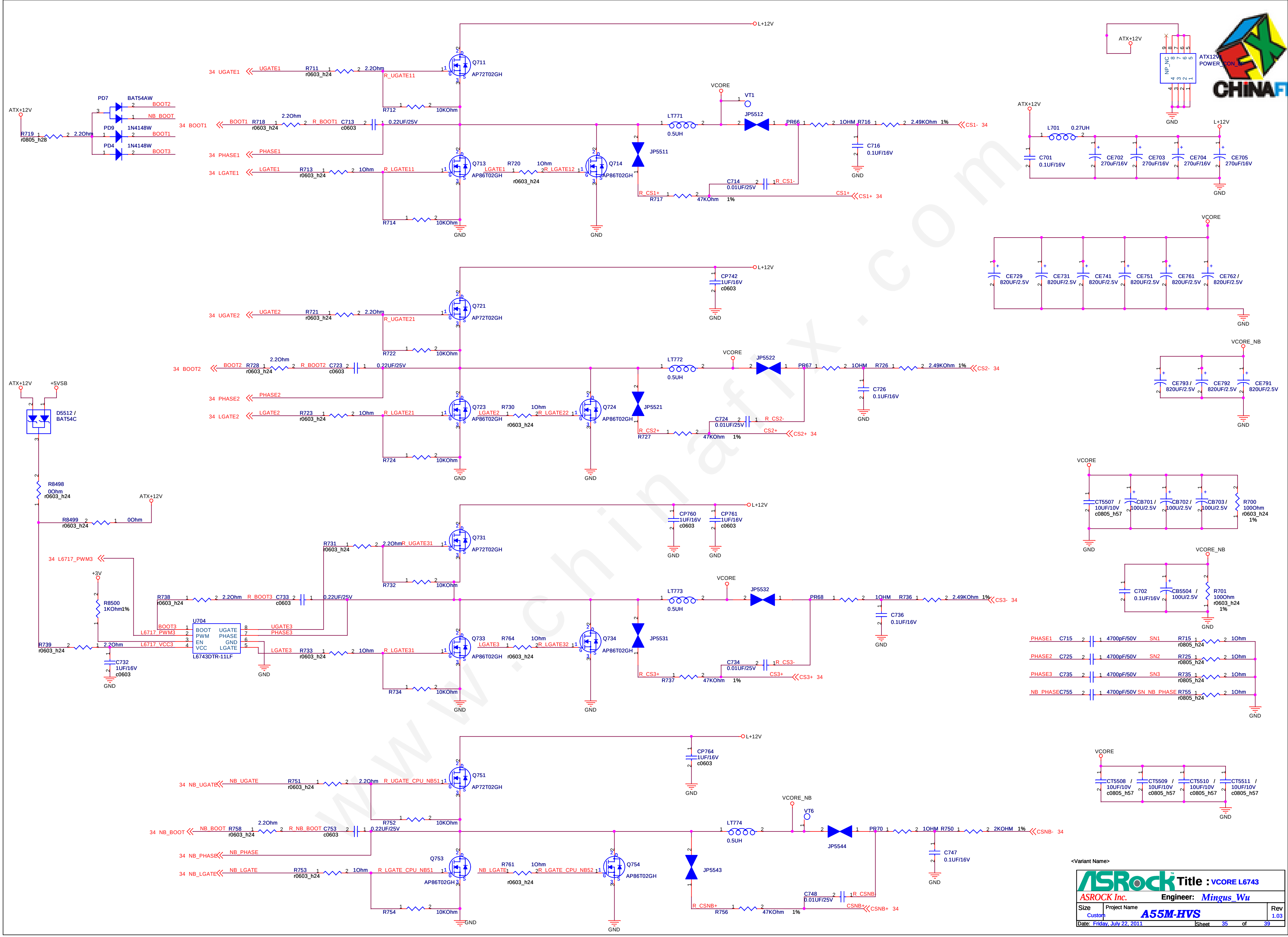
Size A3 | Project Name A55M-HVS | Rev 1.03

Date: Friday, July 22, 2011 | Sheet 33 of 39



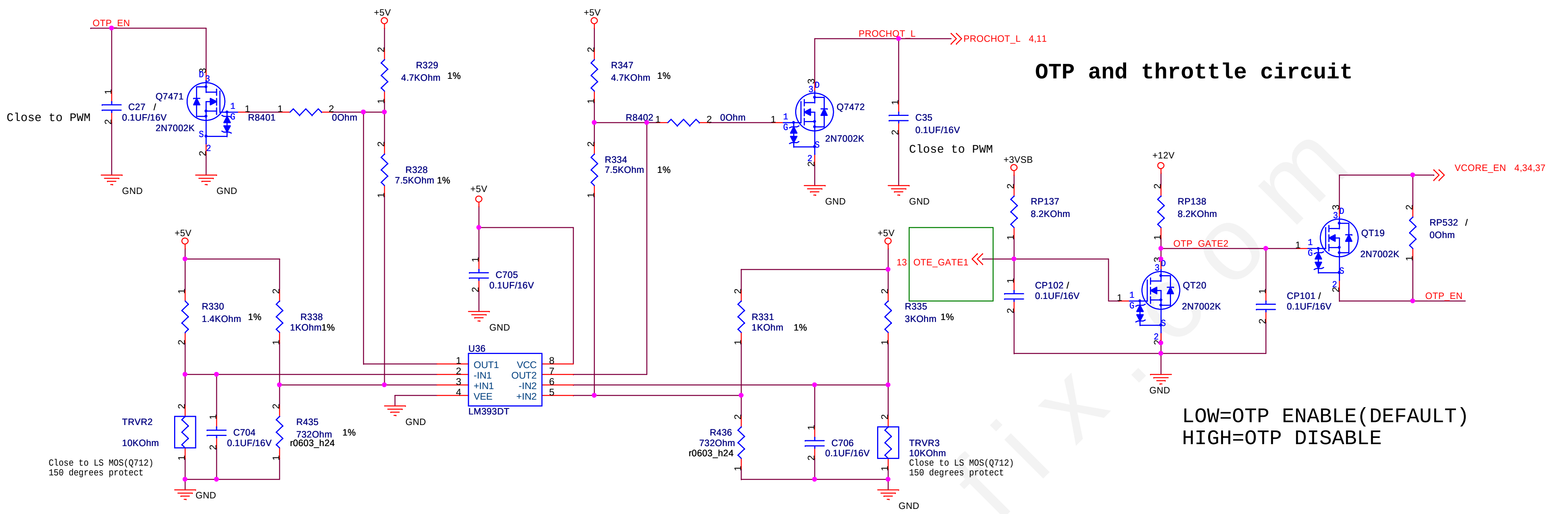
<Variant Name>

ASRock	Title : VCORE L6717	
ASROCK Inc.	Engineer: Mingus_Wu	
Size: Custom	Project Name: A55M-HVS	Rev: 1.03
Date: Friday, July 22, 2011	Sheet 34 of 39	



L+12V
Q711
AP72T02GH
34 UGATE1
UGATE1
R711 r0603_h24 2.2Ohm
R_UGATE11
R712 10KOhm
ATX+12V
PD7 BAT54AW
BOOT2
NB_BOOT
PD9 1N4148W
BOOT1
PD4 1N4148W
BOOT3
R719 r0805_h28 2.2Ohm
34 BOOT1
BOOT1 R718 r0603_h24 2.2Ohm R_BOOT1 C713 c0603 0.22UF/25V
34 PHASE1
PHASE1
34 LGATE1
LGATE1 R713 r0603_h24 1Ohm R_LGATE11
Q713 AP86T02GH
R720 1Ohm LGATE1 R_LGATE12 r0603_h24
Q714 AP86T02GH
R714 10KOhm
GND
VCORE
VT1
LT771 0.5UH
JP5512
JP5511
PR66 1OHM R716 2.49KOhm 1%
CS1- 34
C716 0.1UF/16V
C714 0.01UF/25V R_CS1-
R_CS1+ R717 47KOhm 1%
CS1+ CS1+ 34
ATX12V POWER_CON
L701 0.27UH
C701 0.1UF/16V
CE702 270uF/16V
CE703 270uF/16V
CE704 270uF/16V
CE705 270uF/16V
CE729 820UF/2.5V
CE731 820UF/2.5V
CE741 820UF/2.5V
CE751 820UF/2.5V
CE761 820UF/2.5V
CE762 / 820UF/2.5V
Q721 AP72T02GH
34 UGATE2
UGATE2 R721 r0603_h24 2.2Ohm R_UGATE21
R722 10KOhm
CP742 1UF/16V c0603
34 BOOT2
BOOT2 R728 r0603_h24 2.2Ohm R_BOOT2 C723 c0603 0.22UF/25V
LT772 0.5UH
JP5522
PR67 1OHM R726 2.49KOhm 1%
CS2- 34
C726 0.1UF/16V
34 PHASE2
PHASE2
34 LGATE2
LGATE2 R723 r0603_h24 1Ohm R_LGATE21
Q723 AP86T02GH
R730 1Ohm LGATE2 R_LGATE22 r0603_h24
Q724 AP86T02GH
JP5521
C724 0.01UF/25V R_CS2-
R_CS2+ R727 47KOhm 1%
CS2+ CS2+ 34
R724 10KOhm
+5VSB
D5512 / BAT54C
VCORE_NB
CE793 / 820UF/2.5V
CE792 820UF/2.5V
CE791 820UF/2.5V
R8498 0Ohm r0603_h24
R8499 r0603_h24 0Ohm
CT5507 / 10UF/10V c0805_h57
CB701 / 100U/2.5V
CB702 / 100U/2.5V
CB703 / 100U/2.5V
R700 100Ohm r0603_h24 1%
Q731 AP72T02GH
R731 r0603_h24 2.2Ohm R_UGATE31
R732 10KOhm
CP760 1UF/16V c0603
CP761 1UF/16V c0603
34 L6717_PWM3
+3V
R8500 1KOhm1%
R738 r0603_h24 2.2Ohm R_BOOT3 C733 c0603 0.22UF/25V
U704 L6743DTR-11LF
BOOT UGATE PWM PHASE EN GND VCC LGATE
BOOT3 L6717_PWM3 L6717_VCC3
UGATE3 PHASE3 LGATE3
R739 r0603_h24 2.2Ohm
C732 1UF/16V c0603
LT773 0.5UH
JP5532
PR68 1OHM R736 2.49KOhm 1%
CS3- 34
C736 0.1UF/16V
Q733 AP86T02GH
R733 r0603_h24 1Ohm R_LGATE31
R764 1Ohm LGATE3 R_LGATE32 r0603_h24
Q734 AP86T02GH
JP5531
C734 0.01UF/25V R_CS3-
R_CS3+ R737 47KOhm 1%
CS3+ CS3+ 34
R734 10KOhm
C702 0.1UF/16V
CB5504 / 100U/2.5V
R701 100Ohm r0603_h24 1%
PHASE1 C715 4700pF/50V SN1 R715 r0805_h24 1Ohm
PHASE2 C725 4700pF/50V SN2 R725 r0805_h24 1Ohm
PHASE3 C735 4700pF/50V SN3 R735 r0805_h24 1Ohm
NB_PHASE C755 4700pF/50V SN_NB_PHASE R755 r0805_h24 1Ohm
Q751 AP72T02GH
34 NB_UGATE
NB_UGATE R751 r0603_h24 2.2Ohm R_UGATE_CPU_NB51
R752 10KOhm
CP764 1UF/16V c0603
34 NB_BOOT
NB_BOOT R758 r0603_h24 2.2Ohm R_NB_BOOT C753 c0603 0.22UF/25V
34 NB_PHASE
NB_PHASE
34 NB_LGATE
NB_LGATE R753 r0603_h24 1Ohm R_LGATE_CPU_NB51
Q753 AP86T02GH
R761 1Ohm NB_LGATE R_LGATE_CPU_NB52 r0603_h24
Q754 AP86T02GH
R754 10KOhm
VT6
LT774 0.5UH
JP5544
JP5543
PR70 1OHM R750 2KOHM 1%
CSNB- 34
C747 0.1UF/16V
C748 0.01UF/25V R_CSNB-
R_CSNB+ R756 47KOhm 1%
CSNB+ CSNB+ 34
CT5508 / 10UF/10V c0805_h57
CT5509 / 10UF/10V c0805_h57
CT5510 / 10UF/10V c0805_h57
CT5511 / 10UF/10V c0805_h57
<Variant Name>
ASRock Title : VCORE L6743
ASROCK Inc. Engineer: Mingus_Wu
Size Custom Project Name A55M-HVS Rev 1.03
Date: Friday, July 22, 2011 Sheet 35 of 39

OTP and throttle circuit



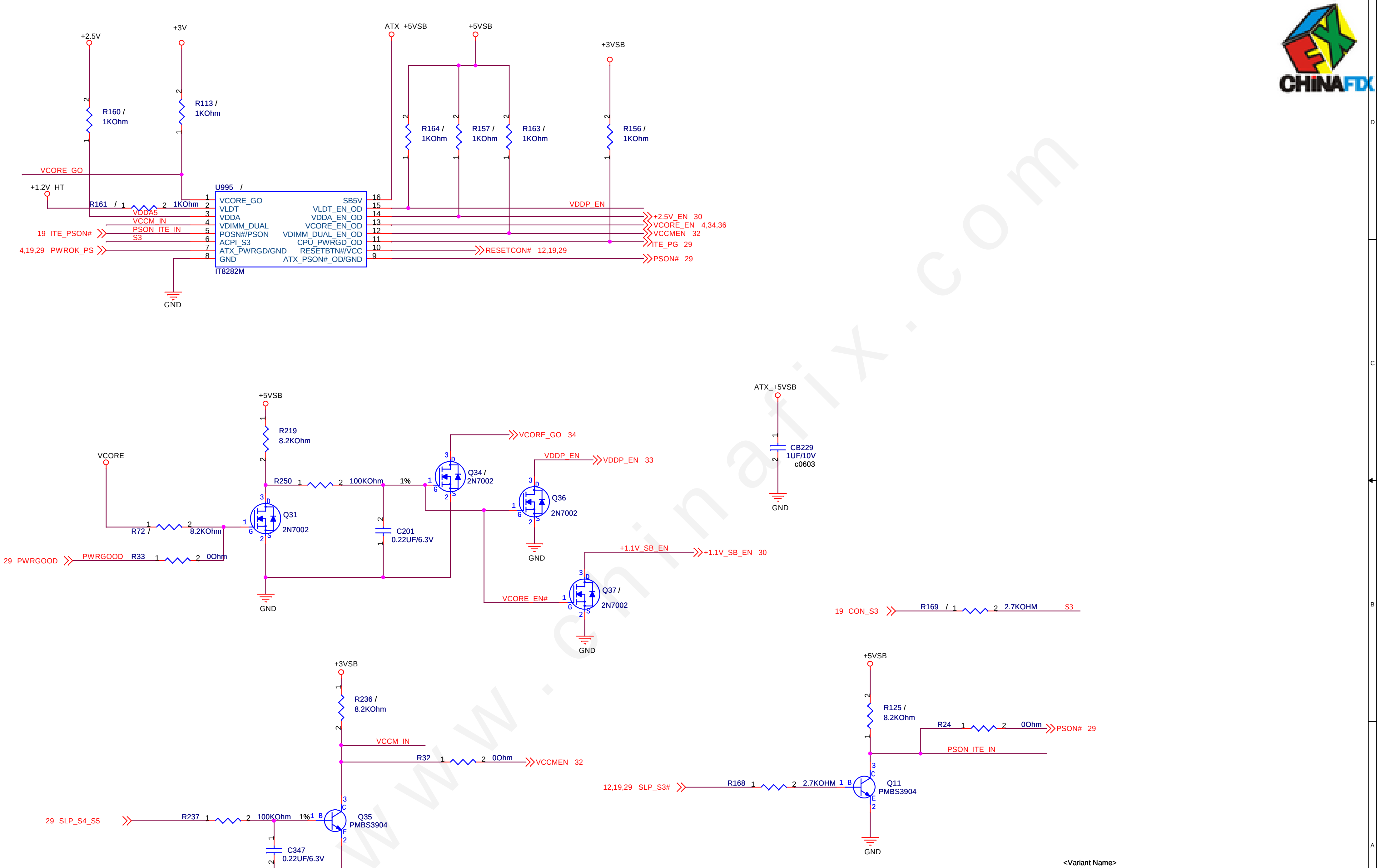
<Variant Name>

ASRock Title : OTP

ASROCK Inc. Engineer: Mingus_Wu

Size	Project Name	Rev
A3	A55M-HVS	1.03

Date: Friday, July 22, 2011 Sheet 36 of 39



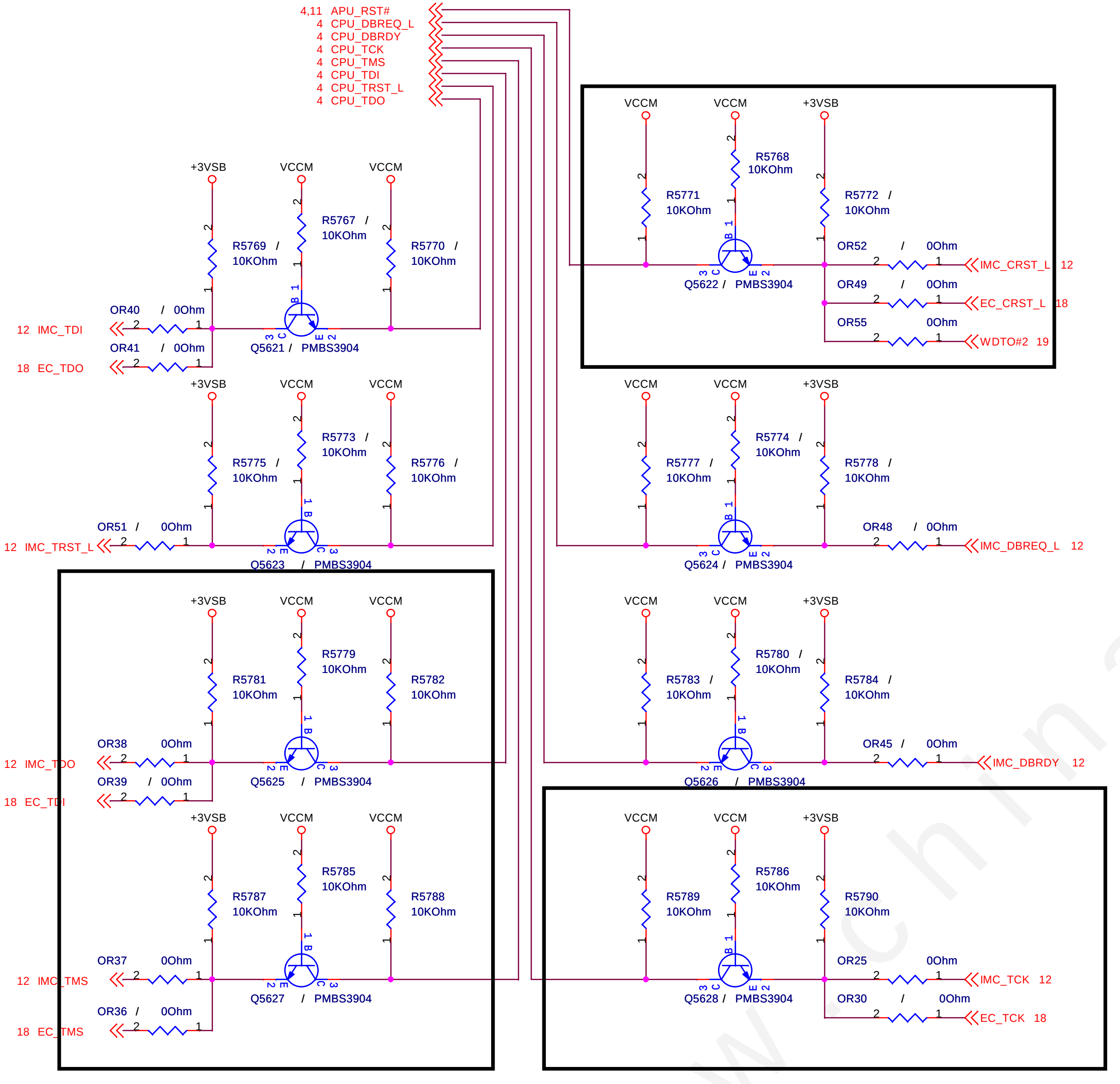
<Variant Name>

ASRock Title : IT8282

ASROCK Inc. Engineer: Mingus_Wu

Size	Project Name	Rev
A3	A55M-HVS	1.03

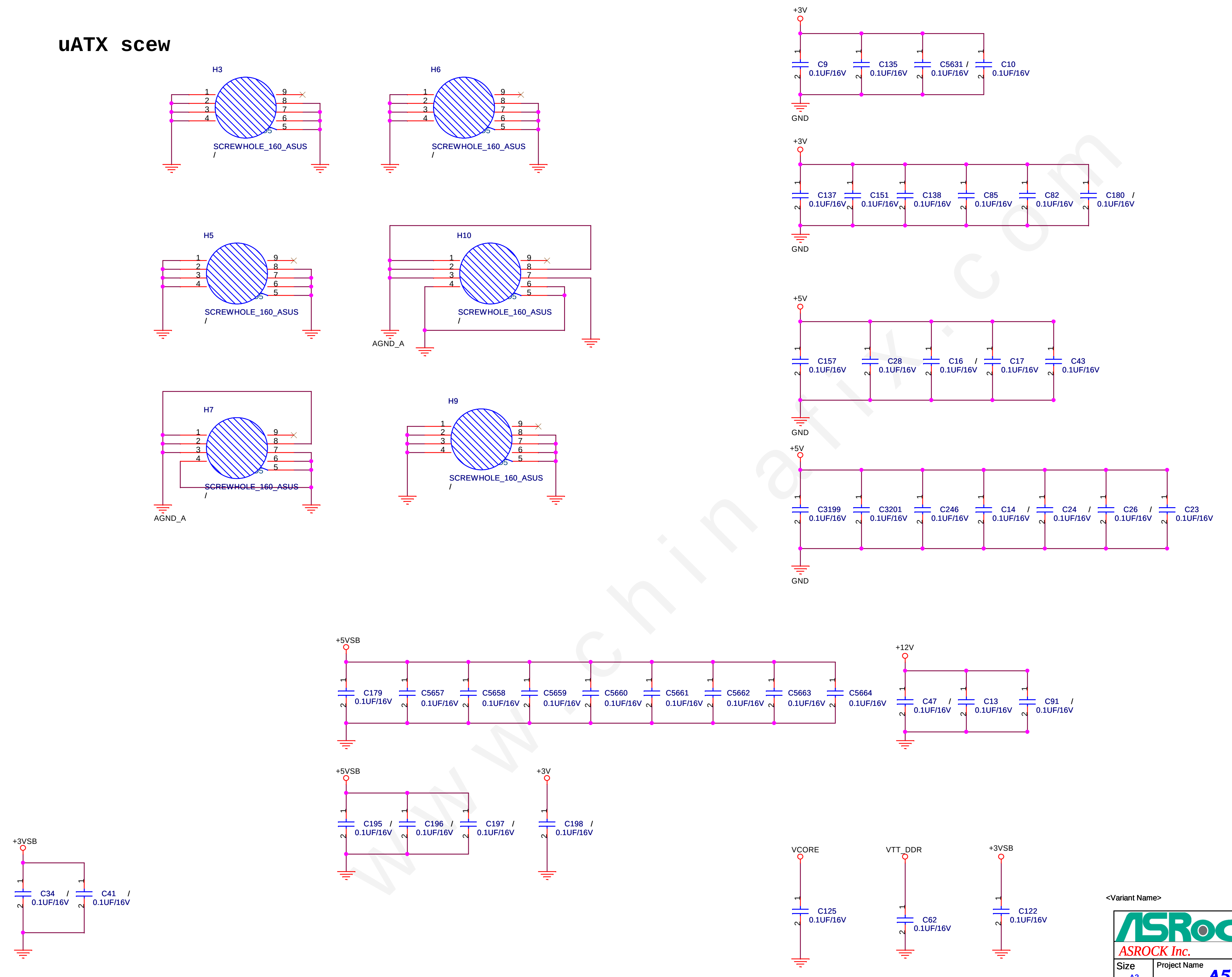
Date: Friday, July 22, 2011 Sheet 37 of 39



<Variant Name>

ASRock Inc.	Title :	ACC & EUP & IOT
	Engineer:	Mingus_Wu
Size A3	Project Name A55M-HVS	Rev 1.03
Date: Friday, July 22, 2011	Sheet 38 of 39	

uATX scew



<Variant Name>

ASRock Title : SCREW

ASROCK Inc. Engineer: Mingus_Wu

Size	Project Name	Rev
A3	A55M-HVS	1.03

Date: Friday, July 22, 2011 Sheet 39 of 39