SCHEMATIC DIAGRAMS

PRODUCT NAME Henderson10/10C (VP)

(System 基板)

UNIT NAME FHNSY1

UNIT NO. DPL99060G01
A5A002688010

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	00	26	167	Ibex Peak-M (8)	00	51	231	DOCKING I/F 2	00	76	402	LAN EEPROM(8103EL)	00
2	---	COVER & REV. SHEET	00	27	168	Ibex Peak-M (9)	00	52	251	EXPRESSCARD	00	77	403	LAN I/F&LED(8111DL/8103E	00
3	---	BLOCK DIAGRAM	00	28	169	Ibex Peak-M (10)	00	53	252	EXPRESSCARD I/F	00	78	443	Bluetooth Power	00
4	---	PCB Connection	00	29	170	Ibex Peak-M (11)	00	54	259	WLAN／WiMAX Power	00	79	444	Bluetooth I/F	00
5	100	CK505 32pin	00	30	171	Ibex Peak-M (12)	00	55	260	PCI-E MINI CARD	00	80	460	USB POWER	00
6	105	rPGA989 (1)	00	31	172	Ibex Peak-M (13)	00	56	261	PCI-E MINI Card(3G)	00	81	462	USB I/F	00
7	106	rPGA989 (2)	00	32	173	Ibex Peak-M (14)	00	57	267	3G(WWAN) LED	00	82	465	USB CHARGER 3/4(1)	00
8	107	rPGA989 (3)	00	33	174	Ibex Peak-M (15)	00	58	300	FLASH ROM	00	83	466	USB CHARGER 3/4(2)	00
9	108	rPGA989 (4)	00	34	175	IMVP-6.5 POWER OK	00	59	301	MDC I/F	00	84	530	LCD PS	00
10	109	rPGA989 (5)	00	35	177	PCH ITP-XDP 24pin	00	60	310	PC Health PJ	00	85	539	LCD I/F	00
11	110	rPGA989 (6)	00	36	190	SATA HDD I/F	00	61	311	F/T PJ	00	86	540	CRT RGB	00
12	111	rPGA989 (7)	00	37	191	Dedicated SSD I/F	00	62	320	EC/KBC(1)	00	87	541	CRT DDC	00
13	112	rPGA989 (8)	00	38	195	SATA ODD I/F	00	63	321	EC/KBC(2)	00	88	542	CRT H/VSync	00
14	113	rPGA989 (9)	00	39	197	eSATA/USB I/F	00	64	322	EC/KBC(3)	00	89	550	DVI Level Shifter	00
15	115	THERMAL SENSOR	00	40	198	SATA REPEATER	00	65	323	GPIO Expander	00	90	551	DVI Jumper	00
16	117	Proc.ITP-XDP 24pin	00	41	200	PC CARD CONT(1)	00	66	324	KB I/F	00	91	560	Displayport Dongle	00
17	144	DDR3 SO-DIMM A	00	42	201	PC CARD CONT(2)	00	67	325	LED	00	92	569	Displayport I/F	00
18	146	DDR3 SO-DIMM B	00	43	202	PC CARD CONT(3)	00	68	326	WIRELESS SW,LED	00	93	603	ALC268	00
19	160	Ibex Peak-M (1)	00	44	210	PC-Card POWER	00	69	330	TPM CONT	00	94	607	Int-MIC	00
20	161	Ibex Peak-M (2)	00	45	211	PC-CARD I/F	00	70	335	ACCELEROMETER (FRONT)	00	95	615	AN12947AA(1)	00
21	162	Ibex Peak-M (3)	00	46	214	MEDIA BRIDGE I/F	00	71	336	AMP for AXIS	00	96	616	AN12947AA(2)	00
22	163	Ibex Peak-M (4)	00	47	216	Smart Card Reader	00	72	338	FAN I/F	00	97	645	ANALOG POWER	00
23	164	Ibex Peak-M (5)	00	48	217	Smart Card I/F	00	73	340	SUPER I/O	00	98	648	Pass-con	00
24	165	Ibex Peak-M (6)	00	49	220	CARD CONT	00	74	400	Ethernet PHY(Hanksville)	00	99	700	Reduce S3 Power	00
25	166	Ibex Peak-M (7)	00	50	230	DOCKING I/F 1	00	75	401	LAN CONTROLLER(8103EL)	00	100	750	EMI CAP.	00

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV.MARK	DRAWING.NO.
[PC設](PC設7) K.Yamamori	[PC設](PC設7) T.Miyairi	[PC設](PC設7) T.Ichimura	FHNSY1	131	001	00	360069769

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SCHEMATIC DIAGRAMS

PRODUCT NAME

UNIT NAME

UNIT NO.

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
101	800	[PS]DC-IN	00	126	953	FHNJK* I/F	00
102	801	[PS]PVT-SW	00	127	954	WebCam I/F	00
103	802	[PS]1st Battery	00	128	955	FHNFS* I/F	00
104	804	[PS]E5V/E3V	00	129	957	FHNSM* I/F	00
105	805	[PS] B*V/P*V	00	130	970	FELICA I/F	00
106	806	[PS]E10V	00	131	990	Accessories	00
107	807	[PS]CURRENT AMP	00	132			
108	808	[PS] CHARGE	00	133			
109	809	[PS]S5V/S3V	00	134			
110	810	[PS]AnalogInput	00	135			
111	811	[PS]PSC(1)	00	136			
112	812	[PS]PSC(2)	00	137			
113	813	[PS]CPUVCC	00	138			
114	815	[PS]1R8-P1V	00	139			
115	818	[PS]LOAD SW2	00	140			
116	819	[PS]PTV	00	141			
117	820	[PS]1R5-B1V,0R75-P0V	00	142			
118	821	[PS]1R05-E1V	00	143			
119	822	[PS]RTCVCC	00	144			
120	832	[PS]IGD-PGV	00	145			
121	841	[PS]LAN-PS	00	146			
122	901	EC TEST PAD	00	147			
123	950	FHNSW* I/F	00	148			
124	951	RGB Unit I/F	00	149			
125	952	Serial Unit I/F	00	150			

REV. MARK	CONTENTS	APPROVED BY	REVISED BY	REGISTERED
00	ISSUE(Release for VP)	2009/10/15 [PC設] (PC設7) K.Yamamori	2009/10/15 [PC設] (PC設7) T.Ichimura	

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV.MARK	DRAWING.NO.
		T.Ichimura	FHNSY1		002	00	360069769

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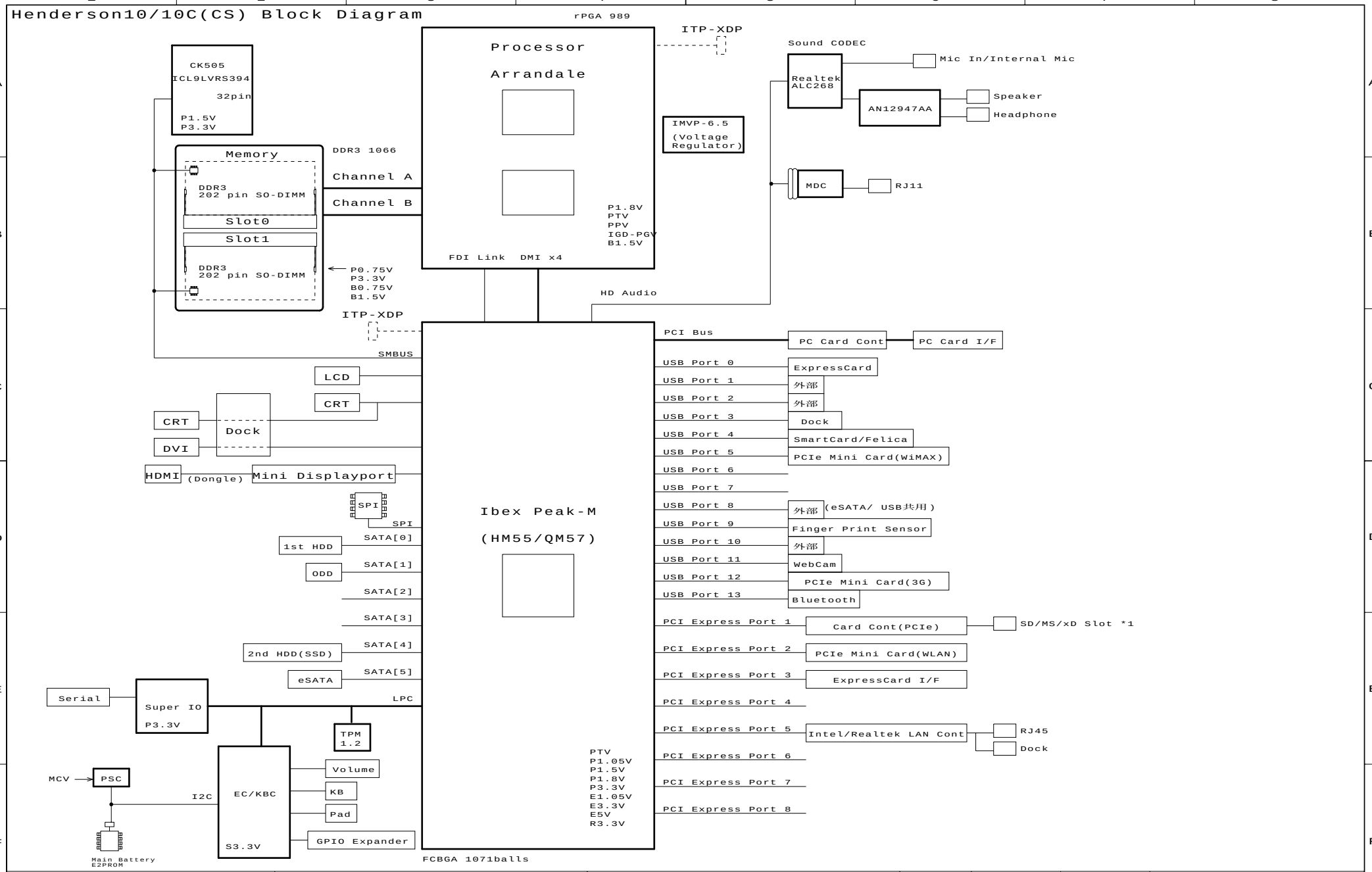
Henderson10/10C(CS) Block Diagram



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Fujiwara	FHNSY1	BLOCK DIAGRAM	003	003	00	360069769

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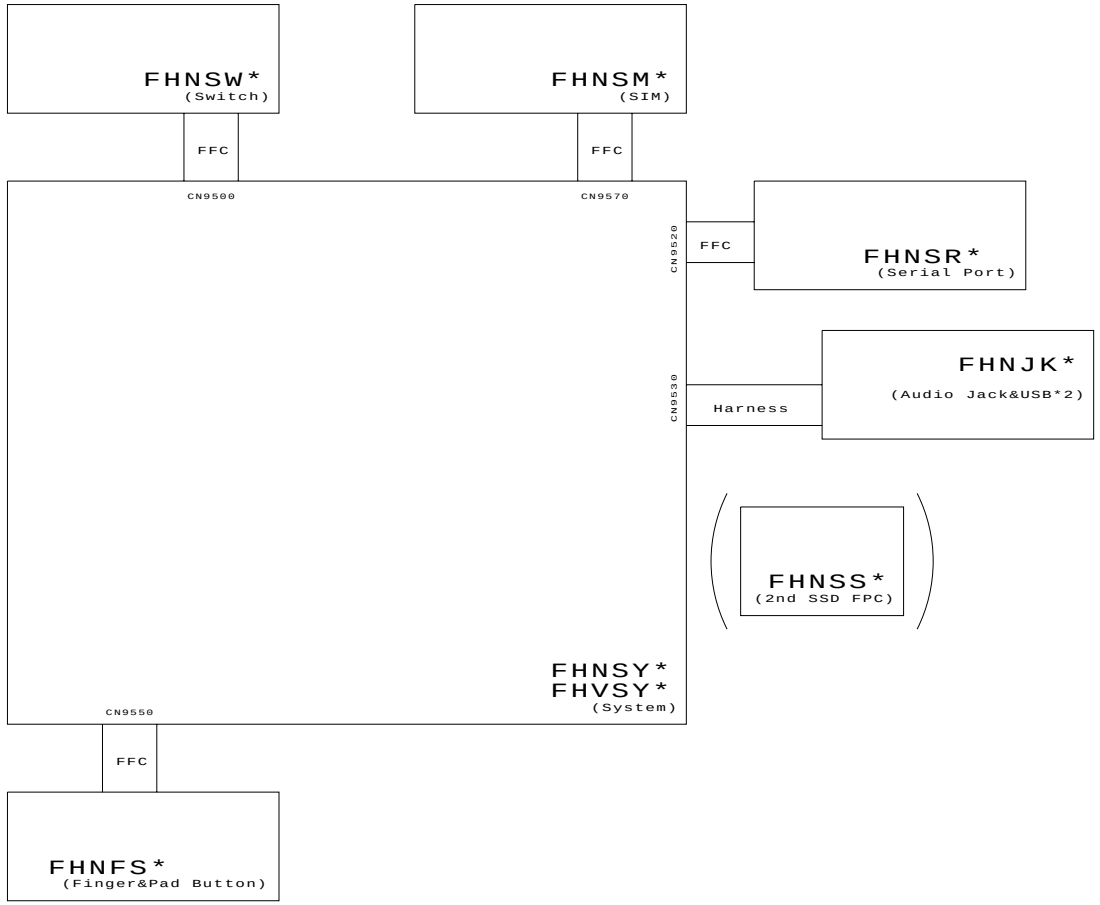
Henderson10/10C/10GC基板接続概略図

Main PCB
FHNSY*:10/10C System (8ply)
FHVSY*:10GC System (8ply)

Sub PCB

FHNSW*:Switch
FHNJK*:Audio Jack&USB*2
FHNFS*:Fingerprint&Pad Button
FHNTP*:Pad Button
FHNSR*:Serial Port Unit
FHNSM*:SIM

FPC
FHNSS*:2nd SSD FPC



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	PCB Connection	004	004	00	360069769

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仕向け設定有

Henderson-CS
Delete W1009,W1010
■ 2009/7/2

Henderson-CS
IC1000 GDMコードに置き換え
■ 2009/08/04

Henderson-CS
N.M. R1015,Add Bypass Pattern
■ 2009/07/15

Henderson-CS
Add Schimtt Invertor related cir.
■ 2009/07/02

Calpella Base
SATACLKREQ#切り離し、 GND固定
■

必要か実機確認必要

GDM450000355

GDM010002536
2.2K(J) 63mW
R1003

813 CLKEN-P3N

[7SH14]
IC1001

33(J) 63mW
GDM010002527
R1015

TC7SH14FU(TE85L,F)

GDM010001695
0(X) 63mW
W1002

CKPWGD-P3P

GDM470001566

CLOCK GENERATOR
[L9LVRS394]
IC1000

CPUT0_LPRS
CPUC0_LPRS
CLKREQA#
SRC0T_LPRS
SRC0C_LPRS
LCD_SST/SRC1T_LPRS
LCD_SSC/SRC1C_LPRS
SRC2T_LPRS/SATAT
SRC2C_LPRS/SATAC
DOT96T_LPRS
DOT96C_LPRS
USB_48MHz/FSLA
SEL_SATA#/27_SS
SEL_LCD#/27FIX
CK_PWRGD/PD#
SCLK
SDATA
X1
SEL_SE0/REF0_3X
X2
TH_PAD
VDDCPU_LV
VDDSRC_LV
VDDLCD_LV
VDD48
VDD27
VDDREF
GND
ICS9LVRS394

63mW 10K(J)
GDM010002517
X4
R1004 R1006 R1008 R1010

Calpella Base
シリーズジャンパを未実装直結
■

パターン
パッド

63mW 0(X)
GDM010001695

W1003 XCPPCH-P0P 169
W1004 XCPPCH-P0N 169
PCH(Host)

W1005 XPEPCH-P0P 169
W1006 XPEPCH-P0N 169
PCH(PCIe)

W1007 XSATA-P0P 169
W1008 XSATA-P0N 169
PCH(SATA)

(int Gfx使わない場合も必要)

W1011 X96DOT-P0P 169
W1012 X96DOT-P0N 169
PCH(DOT)

*R1013
X48SCR-P3P 216
SmartCRD Cont.

GDM10002527
63mW 33(J)

Henderson-CS
R1014&25Mhz For PCH Delete.
R1011 N.M->M R1010:M->N.M (Strap)
■ 2009/07/23

R1012
X14PCH-P3P 169
PCH
63mW 33(J)
GDM010002527

SEL_SE0
H=2.0V -
M=0.7V - 1.5V
L= - 0.35V
X=

R1005 R1007 R1009 R1011
63mW 10K(J)
GDM010002517 X4

TXC製発振器
14.31818MHz50ppm
7X14380003
GDM100000362

[XMBA]
*X1000
OE OUT
VCC GND
GDM100000379
*C1000
6.3V 1uF(K)
GDM030000828

[XF]
*X1001
NC NC
14.31818MHz
30ppm

*C1006 *C1007
GDM030000767
12pF(J) 50V
x2
1005

*R1000 *R1001
GDM010001437 GDM010001445
270(D) 63mW 560(D) 63mW

GDM010001695
0(X) 63mW
*W1001

30ppm
CL=8pF
CX3225GB14318D0KEQ01

SMBCK1-P3P
SMBDT1-P3P
GDM010002527
33(J) 63mW
*R1002

1R5-P1V
Vmin 1.425V
Imax 0.07A

Vmin 3.135V
Imax 0.08A

Henderson-CS
X1000 Size Change:
5032 → 3225
■ 2009/07/17

Henderson10-CS
IC1154 GDMコードに置き換え
■ 2009/08/04

Henderson-VP
C1006,1007:15pF→12pF
■ 2009/10/10

C1001 C1002 C1003 C1004 C1005
6.3V 10uF(K)x2
GDM030000748
16V 100nF(Z)
GDM030000765 x3

C1008 C1009 C1010 C1011
6.3V 10uF(K)
GDM030000748
16V 100nF(Z)
GDM030000765 x3

Henderson-CS
1uF Capacitor 1608→1005:
C1000
■ 2009/07/16

■ REF: Calpella Platform Base. FCLBS0

Henderson-CS
24MHZ Cir→25MHZ Cir
As backup plan
■ 2009/07/17

Henderson-CS
X27GPU-P3P→X24FMC-P3P
W1013→R1014
■ 2009/07/09

Henderson-CS
Add 48MHZ CLK I/F
■ 2009/7/2

Henderson-CS
X48FMC→X48SCR
■ 2009/7/7

ICS9LVRS394 Strapping Table:
2009/07/22

FSLA	CPUCLK
0	133.33
1	100.00

SEL_SATA#	CPUCLK
0	SATACLK
1	SRCCLK

SEL_LCD#	
0	LCD_SSC
1	SRC1C

SEL_SE0	27SS	27NSS
H	HiZ	25MHZ
M	HiZ	24.576MHZ
L	HiZ	27NSS
X	27SS	HiZ

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	CK505 32pin	100	005	00	360069769

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GDM250000249

Henderson-CS
IS1050変更
→K2500331
■ 2009/06/15

Henderson-CS
IS1050変更
→GDM250000249
■ 2009/06/29

Arrandale/Clarksfield
ARRANDALE 1/10
[SOCKET989A]
IS1050

144	ADQ00-B1P	A10	SA_DQ[0]	SA_CK[0]	AA6	XACK0-B1P	144
144	ADQ01-B1P	C10	SA_DQ[1]	SA_CK#[0]	AA7	XACK0-B1N	144
144	ADQ02-B1P	C7	SA_DQ[2]	SA_CK[1]	Y6	XACK1-B1P	144
144	ADQ03-B1P	A7	SA_DQ[3]	SA_CK#[1]	Y5	XACK1-B1N	144
144	ADQ04-B1P	B10	SA_DQ[4]				
144	ADQ05-B1P	D10	SA_DQ[5]				
144	ADQ06-B1P	E10	SA_DQ[6]	SA_CKE[0]	P7	ACKE0-B1P	144
144	ADQ07-B1P	A8	SA_DQ[7]	SA_CKE[1]	P6	ACKE1-B1P	144
144	ADQ08-B1P	D8	SA_DQ[8]	SA_ODT[0]	AD8	AODT0-B1P	144
144	ADQ09-B1P	F10	SA_DQ[9]	SA_ODT[1]	AF9	AODT1-B1P	144
144	ADQ10-B1P	E6	SA_DQ[10]				
144	ADQ11-B1P	F7	SA_DQ[11]	SA_CS#[0]	AE2	ACS0-B1N	144
144	ADQ12-B1P	E9	SA_DQ[12]	SA_CS#[1]	AE8	ACS1-B1N	144
144	ADQ13-B1P	B7	SA_DQ[13]				
144	ADQ14-B1P	E7	SA_DQ[14]	SA_RAS#	AB3	ARAS-B1N	144
144	ADQ15-B1P	C6	SA_DQ[15]	SA_CAS#	AE1	ACAS-B1N	144
144	ADQ16-B1P	H10	SA_DQ[16]	SA_WE#	AE9	AWE-B1N	144
144	ADQ17-B1P	G8	SA_DQ[17]				
144	ADQ18-B1P	K7	SA_DQ[18]	SA_MA[0]	Y3	AMA00-B1P	144
144	ADQ19-B1P	J8	SA_DQ[19]	SA_MA[1]	W1	AMA01-B1P	144
144	ADQ20-B1P	G7	SA_DQ[20]	SA_MA[2]	AA8	AMA02-B1P	144
144	ADQ21-B1P	G10	SA_DQ[21]	SA_MA[3]	AA3	AMA03-B1P	144
144	ADQ22-B1P	J7	SA_DQ[22]	SA_MA[4]	V1	AMA04-B1P	144
144	ADQ23-B1P	J10	SA_DQ[23]	SA_MA[5]	AA9	AMA05-B1P	144
144	ADQ24-B1P	L7	SA_DQ[24]	SA_MA[6]	V8	AMA06-B1P	144
144	ADQ25-B1P	M6	SA_DQ[25]	SA_MA[7]	T1	AMA07-B1P	144
144	ADQ26-B1P	M8	SA_DQ[26]	SA_MA[8]	Y9	AMA08-B1P	144
144	ADQ27-B1P	L9	SA_DQ[27]	SA_MA[9]	U6	AMA09-B1P	144
144	ADQ28-B1P	L6	SA_DQ[28]	SA_MA[10]	AD4	AMA10-B1P	144
144	ADQ29-B1P	K8	SA_DQ[29]	SA_MA[11]	T2	AMA11-B1P	144
144	ADQ30-B1P	N8	SA_DQ[30]	SA_MA[12]	U3	AMA12-B1P	144
144	ADQ31-B1P	P9	SA_DQ[31]	SA_MA[13]	AG8	AMA13-B1P	144
144	ADQ32-B1P	AH5	SA_DQ[32]	SA_MA[14]	T3	AMA14-B1P	144
144	ADQ33-B1P	AF5	SA_DQ[33]	SA_MA[15]	V9	AMA15-B1P	144
144	ADQ34-B1P	AK6	SA_DQ[34]				
144	ADQ35-B1P	AK7	SA_DQ[35]	SA_BS[0]	AC3	ABS0-B1P	144
144	ADQ36-B1P	AF6	SA_DQ[36]	SA_BS[1]	AB2	ABS1-B1P	144
144	ADQ37-B1P	AG5	SA_DQ[37]	SA_BS[2]	U7	ABS2-B1P	144
144	ADQ38-B1P	AJ7	SA_DQ[38]				
144	ADQ39-B1P	AJ6	SA_DQ[39]	SA_DM[0]	B9	ADM0-B1P	144
144	ADQ40-B1P	AJ10	SA_DQ[40]	SA_DM[1]	D7	ADM1-B1P	144
144	ADQ41-B1P	AJ9	SA_DQ[41]	SA_DM[2]	H7	ADM2-B1P	144
144	ADQ42-B1P	AL10	SA_DQ[42]	SA_DM[3]	M7	ADM3-B1P	144
144	ADQ43-B1P	AK12	SA_DQ[43]	SA_DM[4]	AG6	ADM4-B1P	144
144	ADQ44-B1P	AK8	SA_DQ[44]	SA_DM[5]	AM7	ADM5-B1P	144
144	ADQ45-B1P	AL7	SA_DQ[45]	SA_DM[6]	AN10	ADM6-B1P	144
144	ADQ46-B1P	AK11	SA_DQ[46]	SA_DM[7]	AN13	ADM7-B1P	144
144	ADQ47-B1P	AL8	SA_DQ[47]				
144	ADQ48-B1P	AN8	SA_DQ[48]	SA_DQS[0]	C8	ADQS0-B1P	144
144	ADQ49-B1P	AM10	SA_DQ[49]	SA_DQS#[0]	C9	ADQS0-B1N	144
144	ADQ50-B1P	AR11	SA_DQ[50]	SA_DQS[1]	F9	ADQS1-B1P	144
144	ADQ51-B1P	AL11	SA_DQ[51]	SA_DQS#[1]	F8	ADQS1-B1N	144
144	ADQ52-B1P	AM9	SA_DQ[52]	SA_DQS[2]	H9	ADQS2-B1P	144
144	ADQ53-B1P	AN9	SA_DQ[53]	SA_DQS#[2]	J9	ADQS2-B1N	144
144	ADQ54-B1P	AT11	SA_DQ[54]	SA_DQS[3]	M9	ADQS3-B1P	144
144	ADQ55-B1P	AP12	SA_DQ[55]	SA_DQS#[3]	N9	ADQS3-B1N	144
144	ADQ56-B1P	AM12	SA_DQ[56]	SA_DQS[4]	AH8	ADQS4-B1P	144
144	ADQ57-B1P	AN12	SA_DQ[57]	SA_DQS#[4]	AH7	ADQS4-B1N	144
144	ADQ58-B1P	AM13	SA_DQ[58]	SA_DQS[5]	AK10	ADQS5-B1P	144
144	ADQ59-B1P	AT14	SA_DQ[59]	SA_DQS#[5]	AK9	ADQS5-B1N	144
144	ADQ60-B1P	AT12	SA_DQ[60]	SA_DQS[6]	AN11	ADQS6-B1P	144
144	ADQ61-B1P	AL13	SA_DQ[61]	SA_DQS#[6]	AP11	ADQS6-B1N	144
144	ADQ62-B1P	AR14	SA_DQ[62]	SA_DQS[7]	AR13	ADQS7-B1P	144
144	ADQ63-B1P	AP14	SA_DQ[63]	SA_DQS#[7]	AT13	ADQS7-B1N	144

[SSTL1.5]

[DDR3 INTERFACE A]

PZ98927-3641-41F

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (1)	105	006	00	360069769

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1 2 3 4 5 6 7 8

A B C D E F

GDM250000249

Henderson-CS
IS1050変更
→K2500331
■ 2009/06/15

Henderson-CS
IS1050変更
→GDM250000249
■ 2009/06/29

Arrandale/Clarksfield
ARRANDALE 2/10
[SOCKET989A]
IS1050

146	BDQ00-B1P	B5	SB_DQ[0]	SB_CK[0]	W8	XBCK0-B1P	146
146	BDQ01-B1P	A5	SB_DQ[1]	SB_CK#[0]	W9	XBCK0-B1N	146
146	BDQ02-B1P	C3	SB_DQ[2]	SB_CK[1]	V7	XBCK1-B1P	146
146	BDQ03-B1P	B3	SB_DQ[3]	SB_CK#[1]	V6	XBCK1-B1N	146
146	BDQ04-B1P	E4	SB_DQ[4]				
146	BDQ05-B1P	A6	SB_DQ[5]				
146	BDQ06-B1P	A4	SB_DQ[6]	SB_CKE[0]	M3	BCKE0-B1P	146
146	BDQ07-B1P	C4	SB_DQ[7]	SB_CKE[1]	M2	BCKE1-B1P	146
146	BDQ08-B1P	D1	SB_DQ[8]	SB_ODT[0]	AC7	BODT0-B1P	146
146	BDQ09-B1P	D2	SB_DQ[9]	SB_ODT[1]	AD1	BODT1-B1P	146
146	BDQ10-B1P	F2	SB_DQ[10]				
146	BDQ11-B1P	F1	SB_DQ[11]	SB_CS#[0]	AB8	BCS0-B1N	146
146	BDQ12-B1P	C2	SB_DQ[12]	SB_CS#[1]	AD6	BCS1-B1N	146
146	BDQ13-B1P	F5	SB_DQ[13]				
146	BDQ14-B1P	F3	SB_DQ[14]	SB_RAS#	Y7	BRAS-B1N	146
146	BDQ15-B1P	G4	SB_DQ[15]	SB_CAS#	AC5	BCAS-B1N	146
146	BDQ16-B1P	H6	SB_DQ[16]	SB_WE#	AC6	BWE-B1N	146
146	BDQ17-B1P	G2	SB_DQ[17]				
146	BDQ18-B1P	J6	SB_DQ[18]	SB_MA[0]	U5	BMA00-B1P	146
146	BDQ19-B1P	J3	SB_DQ[19]	SB_MA[1]	V2	BMA01-B1P	146
146	BDQ20-B1P	G1	SB_DQ[20]	SB_MA[2]	T5	BMA02-B1P	146
146	BDQ21-B1P	G5	SB_DQ[21]	SB_MA[3]	V3	BMA03-B1P	146
146	BDQ22-B1P	J2	SB_DQ[22]	SB_MA[4]	R1	BMA04-B1P	146
146	BDQ23-B1P	J1	SB_DQ[23]	SB_MA[5]	T8	BMA05-B1P	146
146	BDQ24-B1P	J5	SB_DQ[24]	SB_MA[6]	R2	BMA06-B1P	146
146	BDQ25-B1P	K2	SB_DQ[25]	SB_MA[7]	R6	BMA07-B1P	146
146	BDQ26-B1P	L3	SB_DQ[26]	SB_MA[8]	R4	BMA08-B1P	146
146	BDQ27-B1P	M1	SB_DQ[27]	SB_MA[9]	R5	BMA09-B1P	146
146	BDQ28-B1P	K5	SB_DQ[28]	SB_MA[10]	AB5	BMA10-B1P	146
146	BDQ29-B1P	K4	SB_DQ[29]	SB_MA[11]	P3	BMA11-B1P	146
146	BDQ30-B1P	M4	SB_DQ[30]	SB_MA[12]	R3	BMA12-B1P	146
146	BDQ31-B1P	N5	SB_DQ[31]	SB_MA[13]	AF7	BMA13-B1P	146
146	BDQ32-B1P	AF3	SB_DQ[32]	SB_MA[14]	P5	BMA14-B1P	146
146	BDQ33-B1P	AG1	SB_DQ[33]	SB_MA[15]	N1	BMA15-B1P	146
146	BDQ34-B1P	AJ3	SB_DQ[34]				
146	BDQ35-B1P	AK1	SB_DQ[35]	SB_BS[0]	AB1	BBS0-B1P	146
146	BDQ36-B1P	AG4	SB_DQ[36]	SB_BS[1]	W5	BBS1-B1P	146
146	BDQ37-B1P	AG3	SB_DQ[37]	SB_BS[2]	R7	BBS2-B1P	146
146	BDQ38-B1P	AJ4	SB_DQ[38]				
146	BDQ39-B1P	AH4	SB_DQ[39]	SB_DM[0]	D4	BDM0-B1P	146
146	BDQ40-B1P	AK3	SB_DQ[40]	SB_DM[1]	E1	BDM1-B1P	146
146	BDQ41-B1P	AK4	SB_DQ[41]	SB_DM[2]	H3	BDM2-B1P	146
146	BDQ42-B1P	AM6	SB_DQ[42]	SB_DM[3]	K1	BDM3-B1P	146
146	BDQ43-B1P	AN2	SB_DQ[43]	SB_DM[4]	AH1	BDM4-B1P	146
146	BDQ44-B1P	AK5	SB_DQ[44]	SB_DM[5]	AL2	BDM5-B1P	146
146	BDQ45-B1P	AK2	SB_DQ[45]	SB_DM[6]	AR4	BDM6-B1P	146
146	BDQ46-B1P	AM4	SB_DQ[46]	SB_DM[7]	AT8	BDM7-B1P	146
146	BDQ47-B1P	AM3	SB_DQ[47]				
146	BDQ48-B1P	AP3	SB_DQ[48]	SB_DQS[0]	C5	BDQS0-B1P	146
146	BDQ49-B1P	AN5	SB_DQ[49]	SB_DQS#[0]	D5	BDQS0-B1N	146
146	BDQ50-B1P	AT4	SB_DQ[50]	SB_DQS[1]	E3	BDQS1-B1P	146
146	BDQ51-B1P	AN6	SB_DQ[51]	SB_DQS#[1]	F4	BDQS1-B1N	146
146	BDQ52-B1P	AN4	SB_DQ[52]	SB_DQS[2]	H4	BDQS2-B1P	146
146	BDQ53-B1P	AN3	SB_DQ[53]	SB_DQS#[2]	J4	BDQS2-B1N	146
146	BDQ54-B1P	AT5	SB_DQ[54]	SB_DQS[3]	M5	BDQS3-B1P	146
146	BDQ55-B1P	AT6	SB_DQ[55]	SB_DQS#[3]	L4	BDQS3-B1N	146
146	BDQ56-B1P	AN7	SB_DQ[56]	SB_DQS[4]	AG2	BDQS4-B1P	146
146	BDQ57-B1P	AP6	SB_DQ[57]	SB_DQS#[4]	AH2	BDQS4-B1N	146
146	BDQ58-B1P	AP8	SB_DQ[58]	SB_DQS[5]	AL5	BDQS5-B1P	146
146	BDQ59-B1P	AT9	SB_DQ[59]	SB_DQS#[5]	AL4	BDQS5-B1N	146
146	BDQ60-B1P	AT7	SB_DQ[60]	SB_DQS[6]	AP5	BDQS6-B1P	146
146	BDQ61-B1P	AP9	SB_DQ[61]	SB_DQS#[6]	AR5	BDQS6-B1N	146
146	BDQ62-B1P	AR10	SB_DQ[62]	SB_DQS[7]	AR7	BDQS7-B1P	146
146	BDQ63-B1P	AT10	SB_DQ[63]	SB_DQS#[7]	AR8	BDQS7-B1N	146

[SSTL1.5]

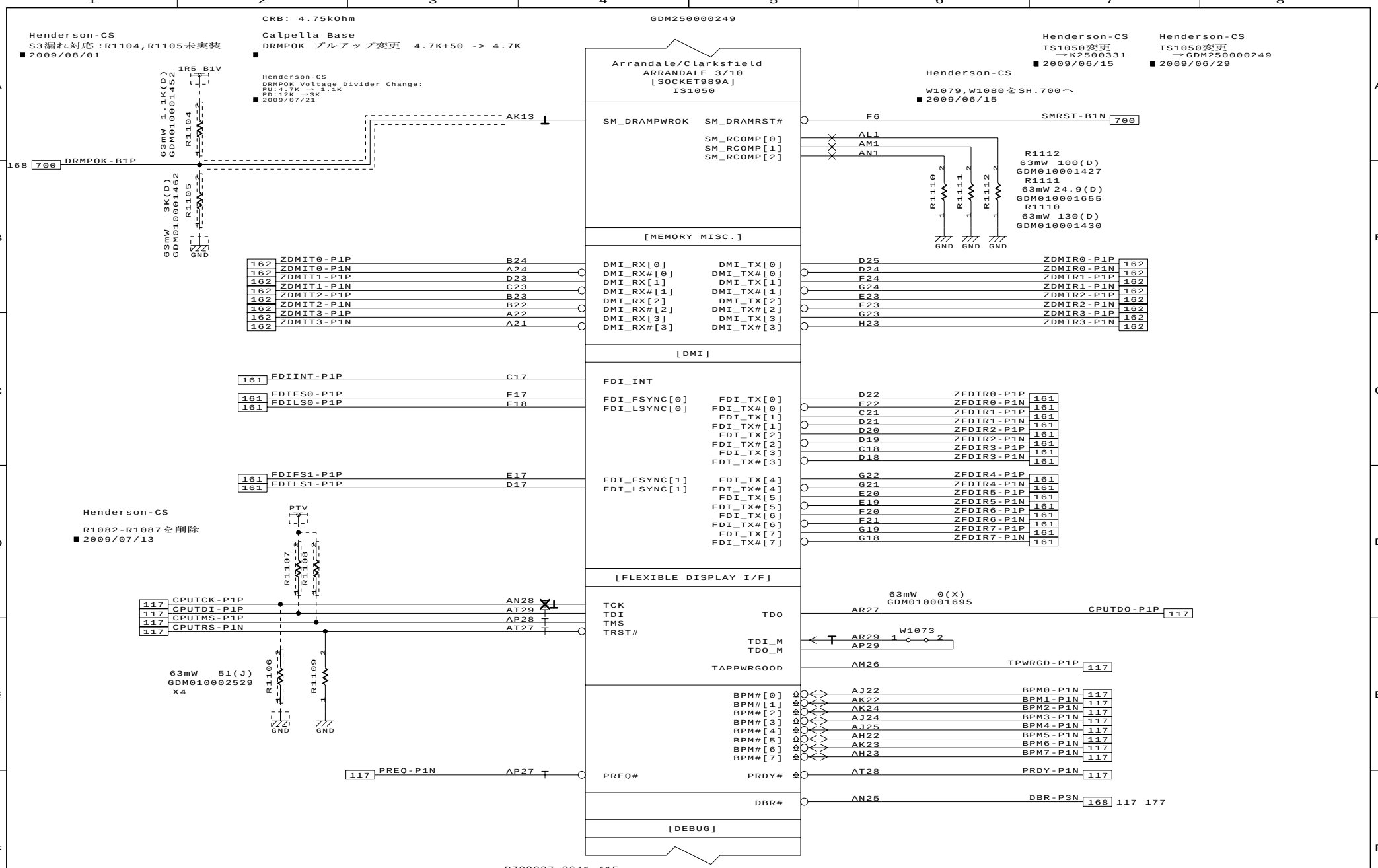
[DDR3 INTERFACE B]

PZ98927-3641-41F

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (2)	106	007	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	rPGA989 (3)	107	008	00	360069769

2009.10.15 17:09 G11

GDM250000249

Henderson-CS
IS1050変更
→GDM250000249
■ 2009/06/29

Henderson-CS
IS1050変更
→K2500331
■ 2009/06/15

Arrandale/Clarksfield
ARRANDALE 4/10
[SOCKET989A]
IS1050

J35	PEG_RX[0]	PEG_TX[0]	L34
K35	PEG_RX#[0]	PEG_TX#[0]	L33
H34	PEG_RX[1]	PEG_TX[1]	M34
J34	PEG_RX#[1]	PEG_TX#[1]	M35
H33	PEG_RX[2]	PEG_TX[2]	M32
J33	PEG_RX#[2]	PEG_TX#[2]	M33
F35	PEG_RX[3]	PEG_TX[3]	L30
G35	PEG_RX#[3]	PEG_TX#[3]	M30
G33	PEG_RX[4]	PEG_TX[4]	M31
G32	PEG_RX#[4]	PEG_TX#[4]	L31
E34	PEG_RX[5]	PEG_TX[5]	K31
F34	PEG_RX#[5]	PEG_TX#[5]	K32
F32	PEG_RX[6]	PEG_TX[6]	M28
F31	PEG_RX#[6]	PEG_TX#[6]	M29
D34	PEG_RX[7]	PEG_TX[7]	H31
D35	PEG_RX#[7]	PEG_TX#[7]	J31
F33	PEG_RX[8]	PEG_TX[8]	K28
E33	PEG_RX#[8]	PEG_TX#[8]	K29
B33	PEG_RX[9]	PEG_TX[9]	G30
C33	PEG_RX#[9]	PEG_TX#[9]	H30
D31	PEG_RX[10]	PEG_TX[10]	G29
D32	PEG_RX#[10]	PEG_TX#[10]	H29
A32	PEG_RX[11]	PEG_TX[11]	F28
B32	PEG_RX#[11]	PEG_TX#[11]	F29
C30	PEG_RX[12]	PEG_TX[12]	E27
C31	PEG_RX#[12]	PEG_TX#[12]	E28
A28	PEG_RX[13]	PEG_TX[13]	D28
B28	PEG_RX#[13]	PEG_TX#[13]	D29
B29	PEG_RX[14]	PEG_TX[14]	C27
B30	PEG_RX#[14]	PEG_TX#[14]	D27
A30	PEG_RX[15]	PEG_TX[15]	C25
A31	PEG_RX#[15]	PEG_TX#[15]	C26

PEG_ICOMPI	B26
PEG_ICOMPO	A26
PEG_RCOMPO	B27
PEG_RBIAS	A25

[PCI EXPRESS GRAPHICS]

PZ98927-3641-41F

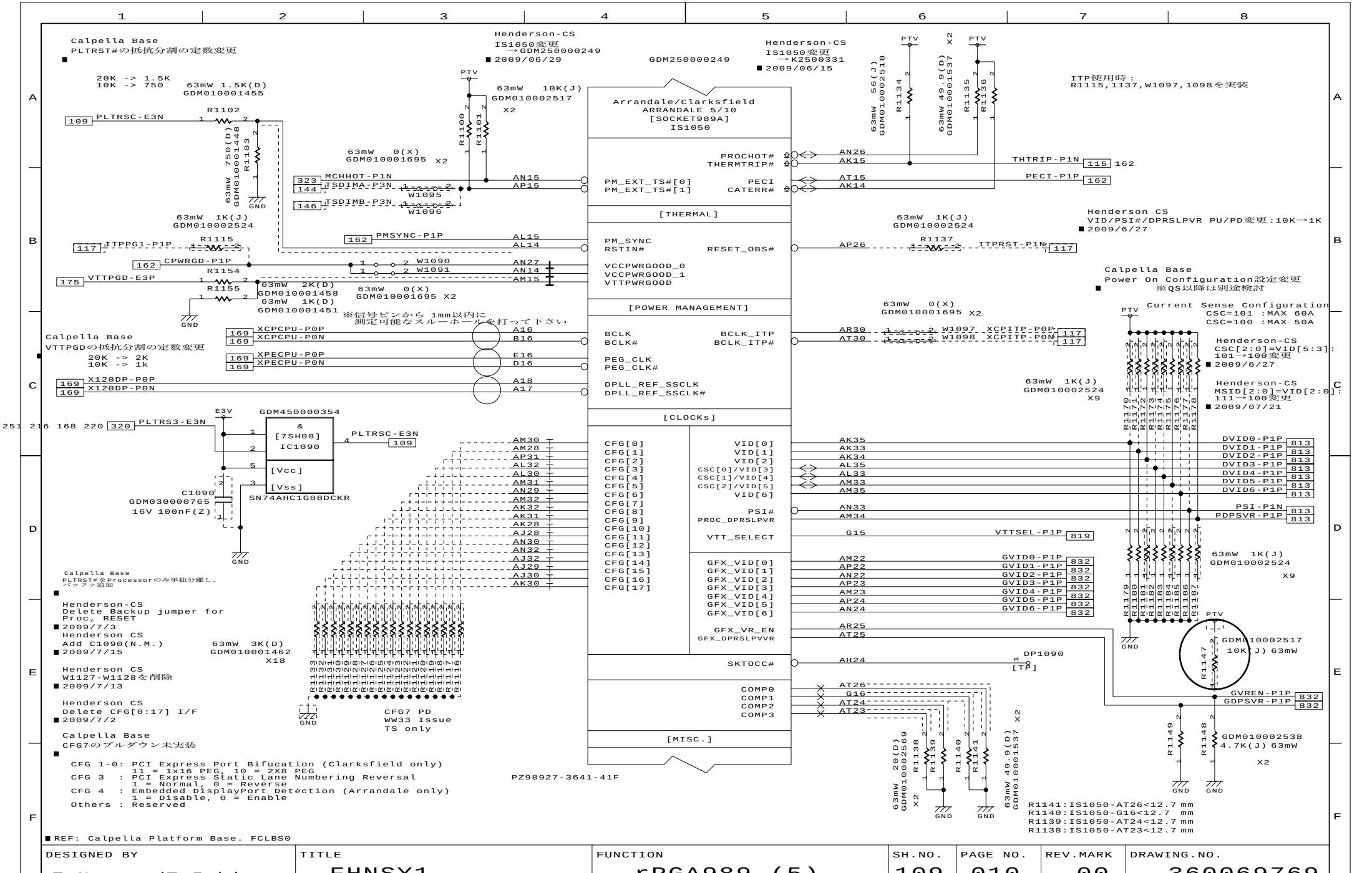
The PEG_ICOMPI&PEG_ICOMPO&PEG_RCOMPO
PEG_RBIAS Pins should be shorted at the pins.

R1113 63mW 750(D) GDM01000144B
R1114 63mW 49.9(D) GDM01000153?
GND GND

■ REF: Calpella Platform Base. FCLBS0

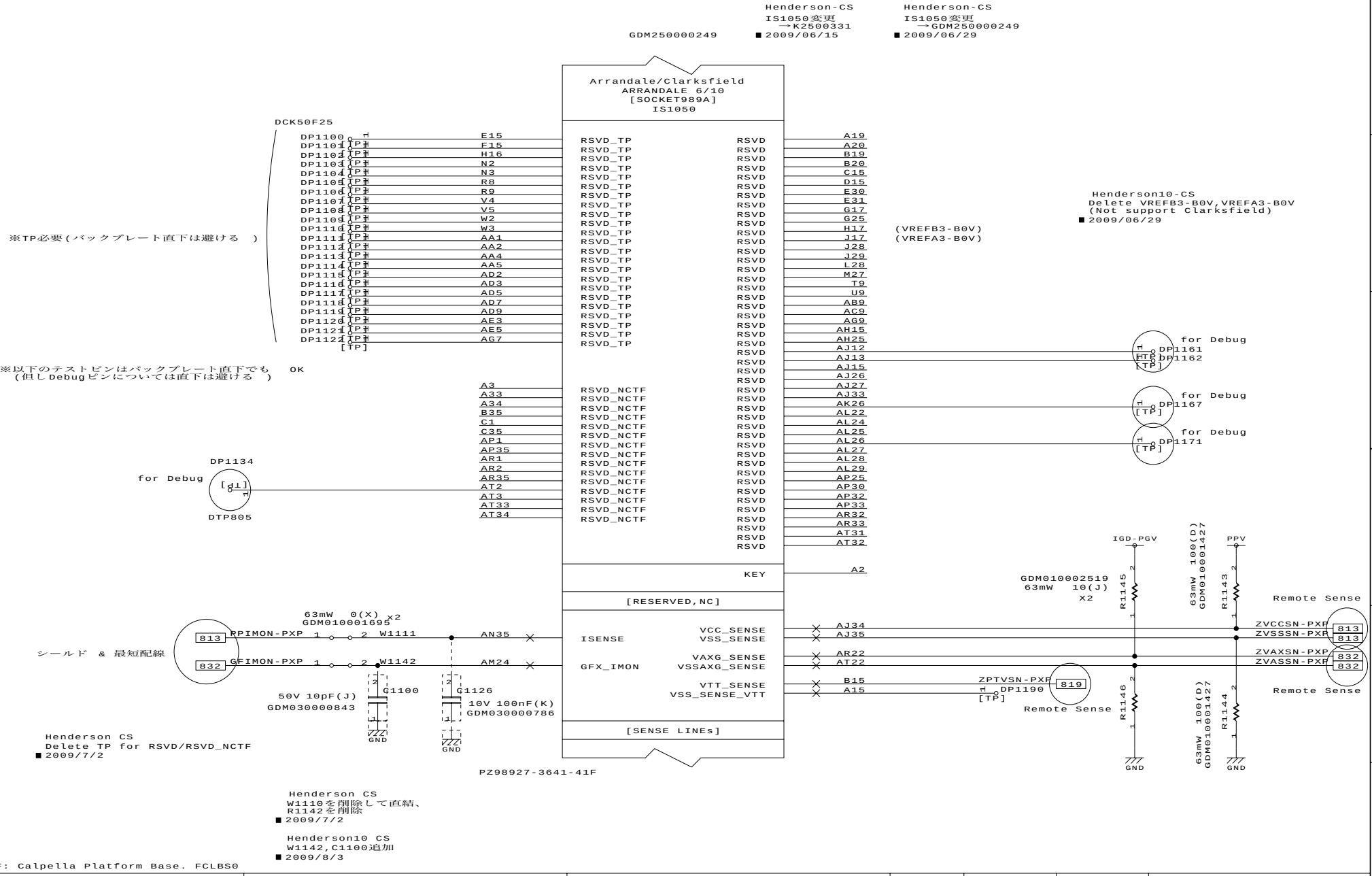
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	rPGA989 (4)	108	009	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	rPGA989 (5)	109	010	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	rPGA989 (6)	110	011	00	360069769

2009.10.15 17:09 G11

Henderson10-CS
C1136-C1151 10uF→22uF
(Calpella Platform MOW WW31)
C1121-C1124,C1128,C1130-C1133,
C1136,C1138,C1139,C1142,C1144,
C1147,C1150 Mount→Not mount
(シミュレーション結果より)
■ 2009/08/04

01 Henderson10-CS
C1136-C1151 22uF→10uF
に戻す
■ 2009/08/06
01 Henderson10-CS
C1120-C1133 GDMコード変更
GDM030001131→GDM030001646
■ 2009/08/18

GDM250000249

Henderson-CS
IS1050変更
→K2500331
■ 2009/06/15

Henderson-CS
IS1050変更
→GDM250000249
■ 2009/06/29

Arrandale/Clarksfield
ARRANDALE 7/10
[SOCKET989A]
IS1050

(SV)
Vmin TBD
Imax 48A/52A

(SV)
Vmin TBD
Imax 22A

6.3V 22uF(M) X6
GDM030001646
C1120 C1121 C1122 C1123 C1124 C1125

6.3V 22uF(M) X6
GDM030001646
C1128 C1129 C1130 C1131 C1132 C1133

4V 10uF(K) X7
GDM030000831
C1136 C1137 C1138 C1139 C1140 C1141 C1142

4V 10uF(K) X9
GDM030000831
C1143 C1144 C1145 C1146 C1147 C1148 C1149 C1150 C1151

4V 10uF(K) X2
GDM030000831
C1189 C1190

4V 10uF(K) X2
GDM030000831
C1191 C1192

Henderson10-CS
C1191,C1192 22uF→10uF
(シミュレーション結果より)
■ 2009/08/04

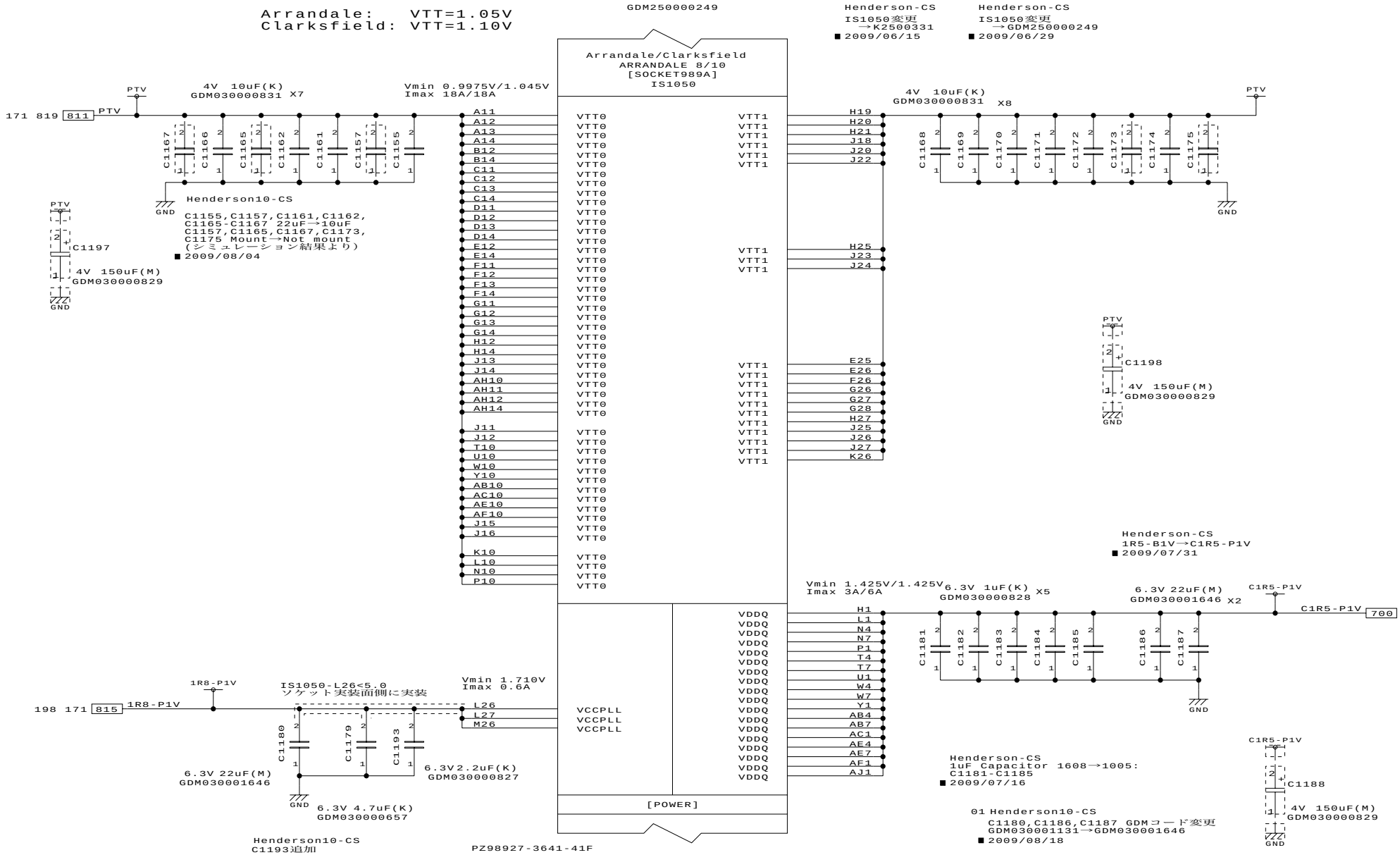
Gfx	int Gfx	ext Gfx
c1189-1192	MOUNT	NOT MOUNT

PZ98927-3641-41F

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	rPGA989 (7)	111	012	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (8)	112	013	00	360069769

Henderson-CS
IS1050変更
→K2500331
■ 2009/06/15

Henderson-CS
IS1050変更
→GDM250000249
■ 2009/06/29

GDM250000249

Arrandale/Clarksfield
ARRANDALE 9/10
[SOCKET989A]
IS1050

A9	VSS	VSS	K6
A23	VSS	VSS	K9
A27	VSS	VSS	K27
A29	VSS	VSS	K30
B4	VSS	VSS	K33
B6	VSS	VSS	K34
B8	VSS	VSS	L2
B11	VSS	VSS	L5
B13	VSS	VSS	L8
B17	VSS	VSS	L29
B18	VSS	VSS	L32
B21	VSS	VSS	L35
B25	VSS	VSS	M10
B31	VSS	VSS	N6
C16	VSS	VSS	N26
C19	VSS	VSS	N27
C20	VSS	VSS	N28
C22	VSS	VSS	N29
C24	VSS	VSS	N30
C28	VSS	VSS	N31
C29	VSS	VSS	N32
C32	VSS	VSS	N33
C34	VSS	VSS	N34
D3	VSS	VSS	N35
D6	VSS	VSS	P2
D9	VSS	VSS	P4
D26	VSS	VSS	P8
D30	VSS	VSS	R10
D33	VSS	VSS	T6
E2	VSS	VSS	T26
E5	VSS	VSS	T27
E8	VSS	VSS	T28
E11	VSS	VSS	T29
E13	VSS	VSS	T30
E18	VSS	VSS	T31
E21	VSS	VSS	T32
E24	VSS	VSS	T33
E29	VSS	VSS	T34
E32	VSS	VSS	T35
E35	VSS	VSS	U2
F16	VSS	VSS	U4
F19	VSS	VSS	U8
F22	VSS	VSS	V10
F25	VSS	VSS	W6
F27	VSS	VSS	W26
F30	VSS	VSS	W27
G3	VSS	VSS	W28
G6	VSS	VSS	W29
G9	VSS	VSS	W30
G20	VSS	VSS	W31
G31	VSS	VSS	W32
G34	VSS	VSS	W33
H2	VSS	VSS	W34
H5	VSS	VSS	W35
H8	VSS	VSS	Y2
H11	VSS	VSS	Y4
H13	VSS	VSS	Y8
H15	VSS	VSS	AA10
H18	VSS	VSS	AB6
H22	VSS	VSS	AB26
H24	VSS	VSS	AB27
H26	VSS	VSS	AB28
H28	VSS	VSS	AB29
H32	VSS	VSS	AB30
H35	VSS	VSS	AB31
J19	VSS	VSS	AB32
J21	VSS	VSS	AB33
J30	VSS	VSS	AB34
J32	VSS	VSS	AB35
K3	VSS	VSS	AC2

GND GND

[GROUND]

PZ98927-3641-41F

GDM250000249

Arrandale/Clarksfield
ARRANDALE 10/10
[SOCKET989A]
IS1050

AC4	VSS	VSS	AN31
AC8	VSS	VSS	AN34
AD10	VSS	VSS	AP2
AE6	VSS	VSS	AP4
AE26	VSS	VSS	AP7
AE27	VSS	VSS	AP10
AE28	VSS	VSS	AP13
AE29	VSS	VSS	AP17
AE30	VSS	VSS	AP20
AE31	VSS	VSS	AP34
AE32	VSS	VSS	AR3
AE33	VSS	VSS	AR6
AE34	VSS	VSS	AR9
AE35	VSS	VSS	AR12
AF2	VSS	VSS	AR15
AF4	VSS	VSS	AR17
AF8	VSS	VSS	AR20
AG10	VSS	VSS	AR23
AH3	VSS	VSS	AR24
AH6	VSS	VSS	AR26
AH9	VSS	VSS	AR28
AH13	VSS	VSS	AR31
AH17	VSS	VSS	AT17
AH20	VSS	VSS	AT20
AH26	VSS		
AH27	VSS		
AH28	VSS		
AH29	VSS		
AH30	VSS		
AH31	VSS		
AH32	VSS		
AH33	VSS		
AH34	VSS		
AH35	VSS		
AJ2	VSS	VSS_NCTF	A35
AJ5	VSS	VSS_NCTF	B1
AJ8	VSS	VSS_NCTF	B2
AJ11	VSS	VSS_NCTF	B34
AJ14	VSS	VSS_NCTF	AR34
AJ17	VSS	VSS_NCTF	AT1
AJ20	VSS	VSS_NCTF	AT35
AJ23	VSS		
AJ31	VSS		
AK17	VSS		
AK20	VSS		
AK25	VSS		
AK27	VSS		
AK29	VSS		
AL3	VSS		
AL6	VSS		
AL9	VSS		
AL12	VSS		
AL17	VSS		
AL20	VSS		
AL23	VSS		
AL31	VSS		
AL34	VSS		
AM2	VSS		
AM5	VSS		
AM8	VSS		
AM11	VSS		
AM14	VSS		
AM17	VSS		
AM20	VSS		
AM25	VSS		
AM27	VSS		
AM29	VSS		
AN17	VSS		
AN20	VSS		
AN23	VSS		

GND

[GROUND]

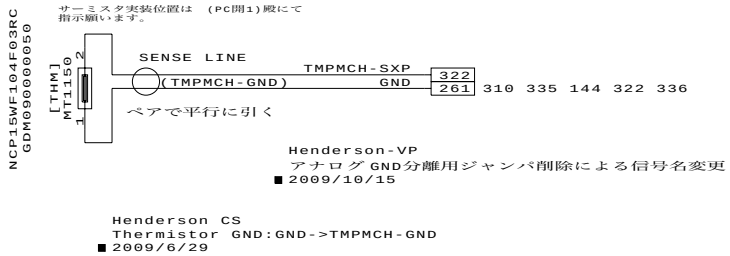
PZ98927-3641-41F

Henderson CS
delete TP for VSS_NCTF
■ 2009/7/2

■ REF: Calpella Platform Base. FCLBS0

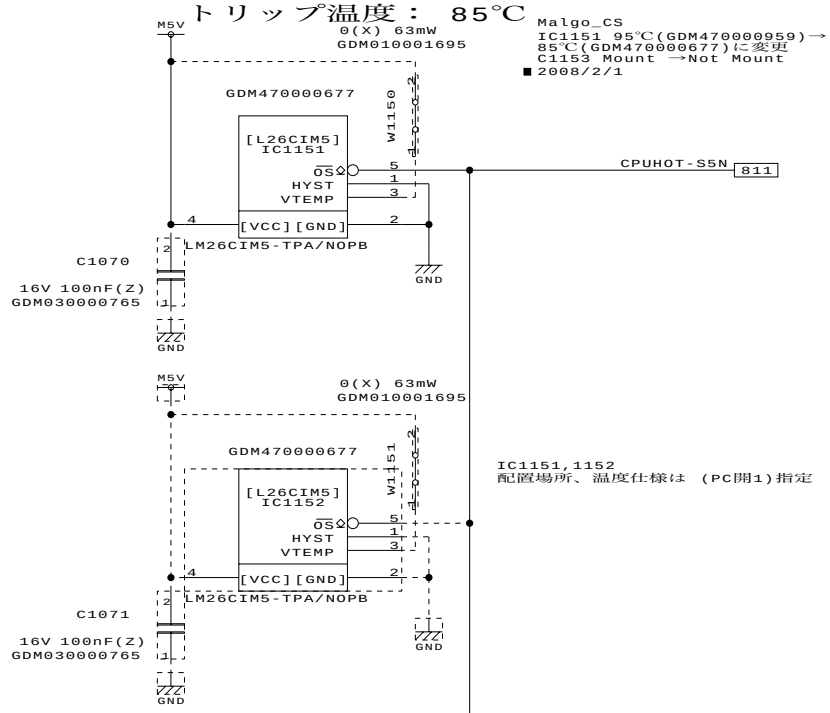
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (9)	113	014	00	360069769

2009.10.15 17:09 G11



Henderson-VP
アナログGND分離用ジャンパ削除による信号名変更
■ 2009/10/15

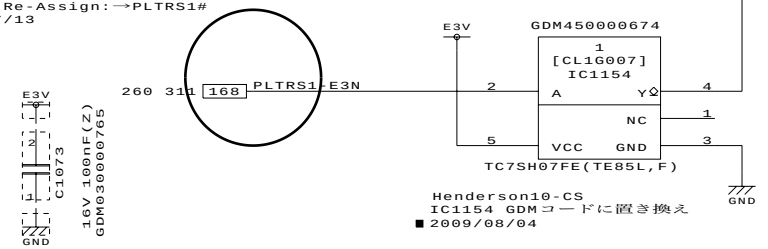
Henderson CS
Thermistor GND:GND->TMPMCH-GND
■ 2009/6/29



Malgo_CS
IC1151 95℃(GDM470000959)→
85℃(GDM470000677)に変更
C1153 Mount →Not Mount
■ 2008/2/1

IC1151,1152
配置場所、温度仕様は (PC開1)指定

Henderson-CS
PLTRS* Re-Assign:→PLTRS1#
■ 2009/07/13



Henderson10-CS
IC1154 GDMコードに置き換え
■ 2009/08/04

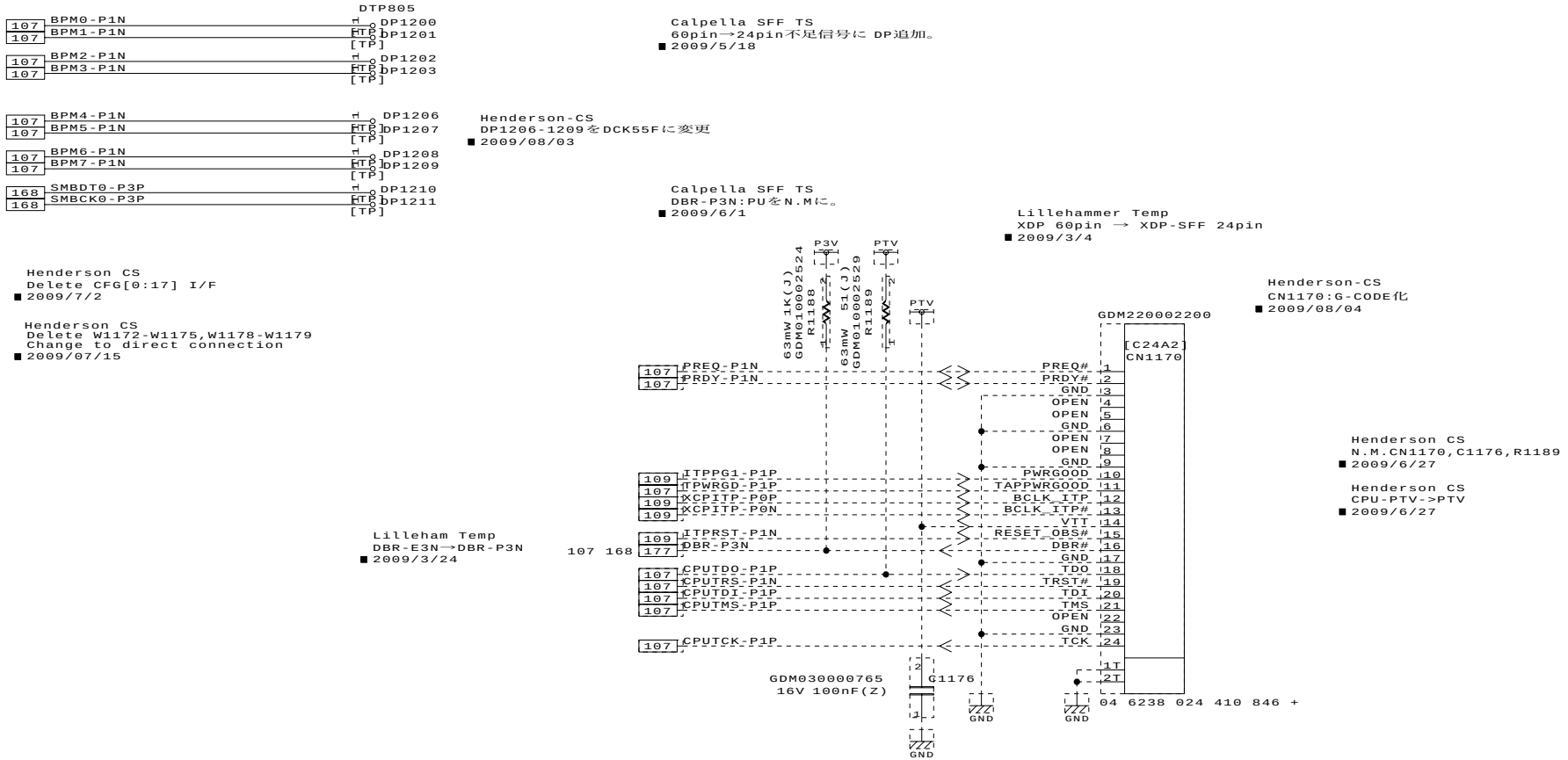
温度リセット IC(ナショセミ)	
トリップ温度	鉛フリー対応品
75℃	GDM470000797 LM26CIM5X-SPA/NOPB
85℃	GDM470000677 LM26CIM5-TPA/NOPB
95℃	GDM470000859 LM26CIM5X-VPA/NOPB
105℃	GDM470000945 LM26CIM5X-XPA/NOPB

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	THERMAL SENSOR	115	015	00	360069769

2009.10.15 17:09

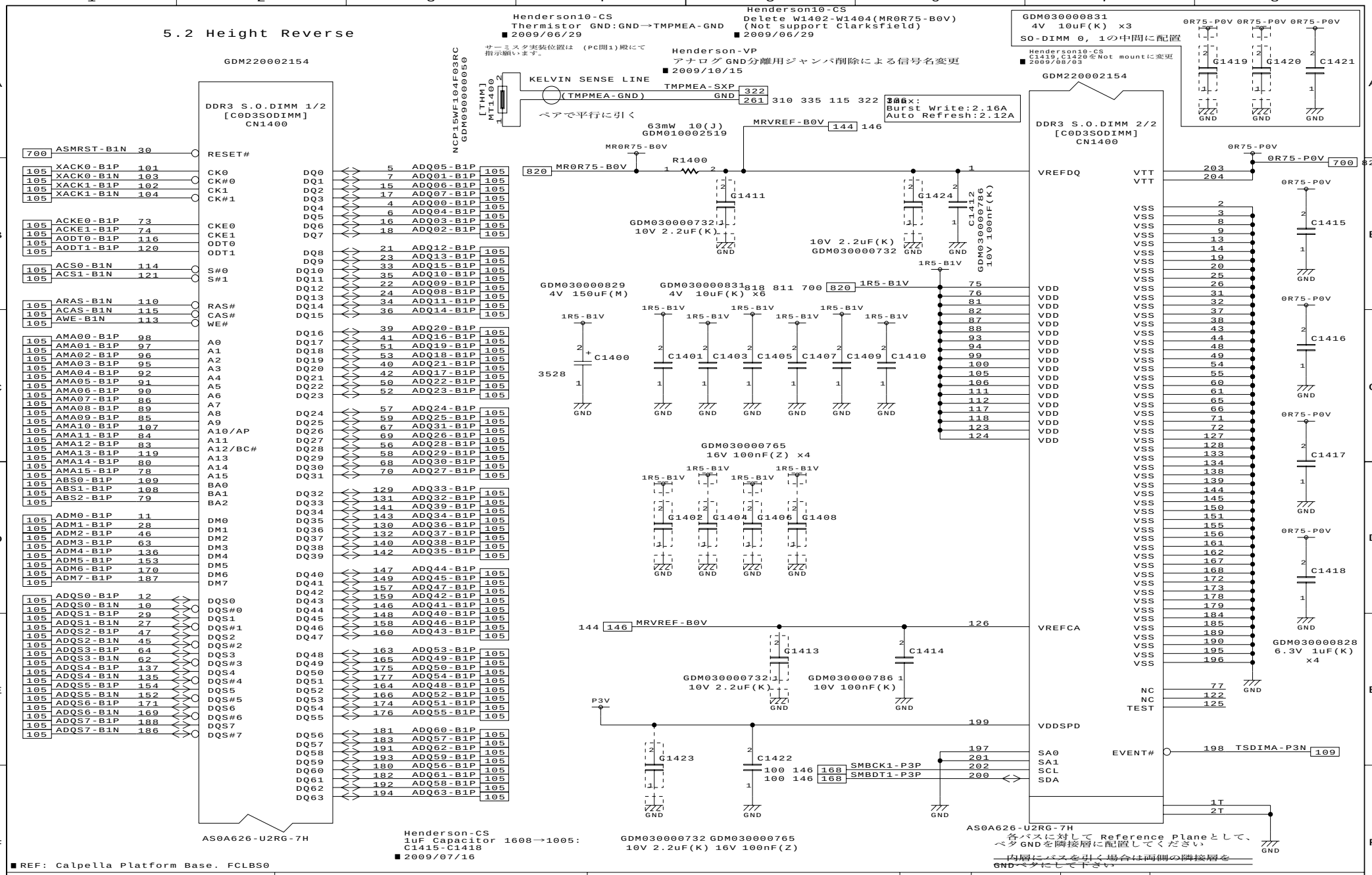
TP:CN1170の近くに配置



■ REF: Calpella SFF TS. FCLST0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	Proc.ITP-XDP 24pin	117	016	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	DDR3 SO-DIMM A	144	017	00	360069769

2009.10.15 17:09

9.2 Height Reverse

Henderson10-CS
Delete MT1410
■ 2009/07/21

Henderson10-CS
Delete W1410,W1413,W1414(MRVREF-B0V)
(Not support Clarksfield)
■ 2009/06/29

GDM220002153

DDR3 S.O.DIMM 1/2
[C0D3SODIMM]
CN1410

RESET#
CK0
CK#0
CK1
CK#1
CKE0
CKE1
ODT0
ODT1
S#0
S#1
RAS#
CAS#
WE#
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10/AP
A11
A12/BC#
A13
A14
A15
BA0
BA1
BA2
DM0
DM1
DM2
DM3
DM4
DM5
DM6
DM7
DQS0
DQS#0
DQS1
DQS#1
DQS2
DQS#2
DQS3
DQS#3
DQS4
DQS#4
DQS5
DQS#5
DQS6
DQS#6
DQS7
DQS#7

AS0A626-JARG-7H

Henderson-CS
1uF Capacitor 1608→1005:
C1465-C1468
■ 2009/07/16

■ REF: Calpella Platform Base. FCLBS0

Imax:
Burst Write:2.16A
Auto Refresh:2.12A

AS0A626-JARG-7H

GDM220002153

DDR3 S.O.DIMM 2/2
[C0D3SODIMM]
CN1410

VREFDQ
VREFCA
VTT
VSS
VDD
NC
TEST
VDDSPD
SA0
SA1
SCL
SDA
EVENT#

MRVREF-B0V
G1461
C1462
GDM030000732 10V 2.2uF(K)
GDM030000786 10V 100nF(K)
GDM030000829 4V 150uF(M)
GDM030000831 4V 10uF(K) x6
C1450
C1451
C1453
C1455
C1457
C1459
C1460
GDM030000765 16V 100nF(Z) x4
G1452
G1454
G1456
G1458
G1463
C1464
G1470
C1469
SMBCK1-P3P
SMBDT1-P3P
TSDIMB-P3N
0R75-P0V
C1465
C1466
C1467
C1468
GDM030000828 6.3V 1uF(K) x4
1R5-B1V
P3V
GND

GDM030000732 10V 2.2uF(K)
GDM030000765 16V 100nF(Z)

各パスに対して Reference Planeとして、ベタGNDを隣接層に配置してください
内層にパスを引く場合は両側の隣接層をGNDベタにして下さい

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	DDR3 SO-DIMM B	146	018	00	360069769

2009.10.15 17:09

仕向け設定有

QM57-QS:GDM460002391
QM57-VP:GDM460002418
HM55-QS:GDM460002387
HM55-VP:GDM460002417

Henderson-VP
IC1600:→VP-CODE化
■ 2009/10/15

Henderson-CS
IC1600:→G-CODE化
■ 2009/08/04

GDM460002418

PCH	用途	接続先	備考	PU
SDVO_CTRL	DVI DDC	DigtalPort B	DVI	Sh.550
DDPC_CTRL	Displayport DDC	DigtalPort C	Displayport	Sh.560
DDPD_CTRL	Not Used	DigtalPort D		N/A
L_CTRL	Not Used	N/A		N/A
L_DDC	LCD Detect	P3V		Sh.160
CRT_DDC	CRT DDC	CRT		Sh.541

IBEX PEAK PCH 1/16
[UIBEXPEAKA]
*IC1600

DISPLAY SIGNALS
[Vcc3_3]

SDVO_CTRLCLK — T51 — IDBSCL-P3P 550
SDVO_CTRLDATA — T53 — IDBSDA-P3P 550
DDPC_CTRLCLK — Y49 — IDCSCL-P3P 560
DDPC_CTRLDATA — AB49 — IDCSDA-P3P 560
DDPD_CTRLCLK — U50 — NC
DDPD_CTRLDATA — U52 — NC
L_CTRL_CLK — AB46 — NC
L_CTRL_DATA — V48 — NC
L_DDC_CLK — AB48
L_DDC_DATA — Y45

P3V
R1795 2.2K(J) 63mW
R1796 2.2K(J) 63mW
GDM010002536 2.2K(J)63mW X2

L_BKLTCTL — Y48 — R1790 IC1600-Y48<10 — BKLPWM-P3P 539
L_BKLTEN — T48 — R1791 IC1600-T48<10 — BLON-P3P 530
L_VDD_EN — T47 — R1792 IC1600-T47<10 — LCDON-P3P 530
GDM010002527 X3
33(J) 63mW

CRT_DDC_CLK — V51 — IDSCL-P3P 231 541
CRT_DDC_DATA — V53 — IDSDA-P3P 231 541

CRT_HSYNC — Y53 — R1793 IC1600-Y53<10 — IHSYNC-P3P 542
CRT_VSYNC — Y51 — R1794 IC1600-Y51<10 — IVSYNC-P3P 542
GDM010002527 33(J) 63mW X2

[VccADAC]

POWER AD48 — R1603 IC1600-AD48<5 — 63mW 1K(D) GDM010001451 — GND
DAC_IREF

CRT_RED — AD53 — IRED-PXP 540
CRT_GREEN — AB53 — IGREEN-PXP 540
CRT_BLUE — AA52 — IBLUE-PXP 540
CRT_IRTN — AB51 — GND

63mW 120(D) GDM010001429
*R1600 IC1600-AA52<10
*R1601 IC1600-AB53<10
*R1602 IC1600-AD53<10
GND GND GND

Docker

	Yes	No
R1600-R1602	GDM010001429 120ohm	GDM010001672 75ohm

LVDS
[VccTxLVDS]

63mW 2.4K(D) GDM010001460
R1625 — POWER IC1600-AP39<5 — NC AP41 — AP39 — GND

LVD_VBG
LVD_IBG

LVDSA_CLK — AV51 — XITXCA-PYP 539
LVDSA_CLK# — AV53 — XITXCA-PYN 539
LVDSA_DATA0 — BB48 — ZITDA0-PYP 539
LVDSA_DATA#0 — BB47 — ZITDA0-PYN 539
LVDSA_DATA1 — BA50 — ZITDA1-PYP 539
LVDSA_DATA#1 — BA52 — ZITDA1-PYN 539
LVDSA_DATA2 — AY49 — ZITDA2-PYP 539
LVDSA_DATA#2 — AY48 — ZITDA2-PYN 539
LVDSA_DATA3 — AV48 — NC
LVDSA_DATA#3 — AV47 — NC

AT43 — LVDS_VREFH
AT42 — LVDS_VREFL
GND

LVDSB_CLK — AP47 — XITXCB-PYP 539
LVDSB_CLK# — AP48 — XITXCB-PYN 539
LVDSB_DATA0 — AY51 — ZITDB0-PYP 539
LVDSB_DATA#0 — AY53 — ZITDB0-PYN 539
LVDSB_DATA1 — AT48 — ZITDB1-PYP 539
LVDSB_DATA#1 — AT49 — ZITDB1-PYN 539
LVDSB_DATA2 — AU50 — ZITDB2-PYP 539
LVDSB_DATA#2 — AU52 — ZITDB2-PYN 539
LVDSB_DATA3 — AT51 — NC
LVDSB_DATA#3 — AT53 — NC

BD82QM57 SLGZQ

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	Ibex Peak-M (1)	160	019	00	360069769

2009.10.15 17:09 G11

仕向け設定有

Docker

	Yes	No
C1600-C1607	Mount	NotMount
R1798	NotMount	Mount

Mini-DP

	Yes	No
C1608-C1615	Mount	NotMount
W1611	Mount	Mount

Henderson-VP
HPDにプルダウン抵抗追加。
■ 2009/09/24

GDM460002418

IBEX PEAK PCH 2/16
[UIBEXPEAKA]
*IC1600

DIGITAL DISPLAY SIGNALS
[VccIO]

SDVO_INTP
SDVO_INTN
SDVO_STALLP
SDVO_STALLN
SDVO_TVCLKINP
SDVO_TVCLKINN

DDPB_AUXP
DDPB_AUXN
DDPB_HPD

DDPB_0P
DDPB_0N
DDPB_1P
DDPB_1N
DDPB_2P
DDPB_2N
DDPB_3P
DDPB_3N

DDPC_AUXP
DDPC_AUXN
DDPC_HPD

DDPC_0P
DDPC_0N
DDPC_1P
DDPC_1N
DDPC_2P
DDPC_2N
DDPC_3P
DDPC_3N

DDPD_AUXP
DDPD_AUXN
DDPD_HPD

DDPD_0P
DDPD_0N
DDPD_1P
DDPD_1N
DDPD_2P
DDPD_2N
DDPD_3P
DDPD_3N

FDI
[VccIO]

FDI_INT

FDI_RXP0
FDI_RXN0
FDI_RXP1
FDI_RXN1
FDI_RXP2
FDI_RXN2
FDI_RXP3
FDI_RXN3
FDI_RXP4
FDI_RXN4
FDI_RXP5
FDI_RXN5
FDI_RXP6
FDI_RXN6
FDI_RXP7
FDI_RXN7

FDI_FSYNC0
FDI_LSYNC0
FDI_FSYNC1
FDI_LSYNC1

BD82QM57 SLGZQ

Digtal Port	HDMI
DDPB_0	TMDSB_DATA2
DDPB_1	TMDSB_DATA1
DDPB_2	TMDSB_DATA0
DDPB_3	TMDSB_CLK

Digtal Port	Displayport
DDPC_0	DDPC_0
DDPC_1	DDPC_1
DDPC_2	DDPC_2
DDPC_3	DDPC_3

DG ver1.6より

10V 100nF(K)
GDM030000786

DVI
Displayport

*C1600 ZIDPB2-P1P 550
*C1601 ZIDPB2-P1N 550
*C1602 ZIDPB1-P1P 550
*C1603 ZIDPB1-P1N 550
*C1604 ZIDPB0-P1P 550
*C1605 ZIDPB0-P1N 550
*C1606 XIDPBC-P1P 550
*C1607 XIDPBC-P1N 550

*C1608 ZDPTX0-P1P 569
*C1609 ZDPTX0-P1N 569
*C1610 ZDPTX1-P1P 569
*C1611 ZDPTX1-P1N 569
*C1612 ZDPTX2-P1P 569
*C1613 ZDPTX2-P1N 569
*C1614 ZDPTX3-P1P 569
*C1615 ZDPTX3-P1N 569

550 DPBHPD-P3P
*R1798
GDM010002517
10K(J) 63mW
GND

560 ZICAUX-P1P
560 ZICAUX-P1N
569 323 HPDDPG-S3P
323 HPDDPE-S3P
W1610
W1611
GDM010001695
0(X) 63mW

Henderson-CS
W1610,W1611,HPDDPE-P3P追加
■ 2009/07/23

GDM010002517
10K(J)63mW
R1799
GND

Henderson-CS
HPDDPG-P3P->HPDPG-S3P
■ 2009/7/23

Henderson-CS
HPDDPE-P3P->HPDPE-S3P
■ 2009/7/23

BJ14 FDIINT-P1P 107

BF13 FDIFS0-P1P 107
BJ12 FDILS0-P1P 107

BH13 FDIFS1-P1P 107
BG14 FDILS1-P1P 107

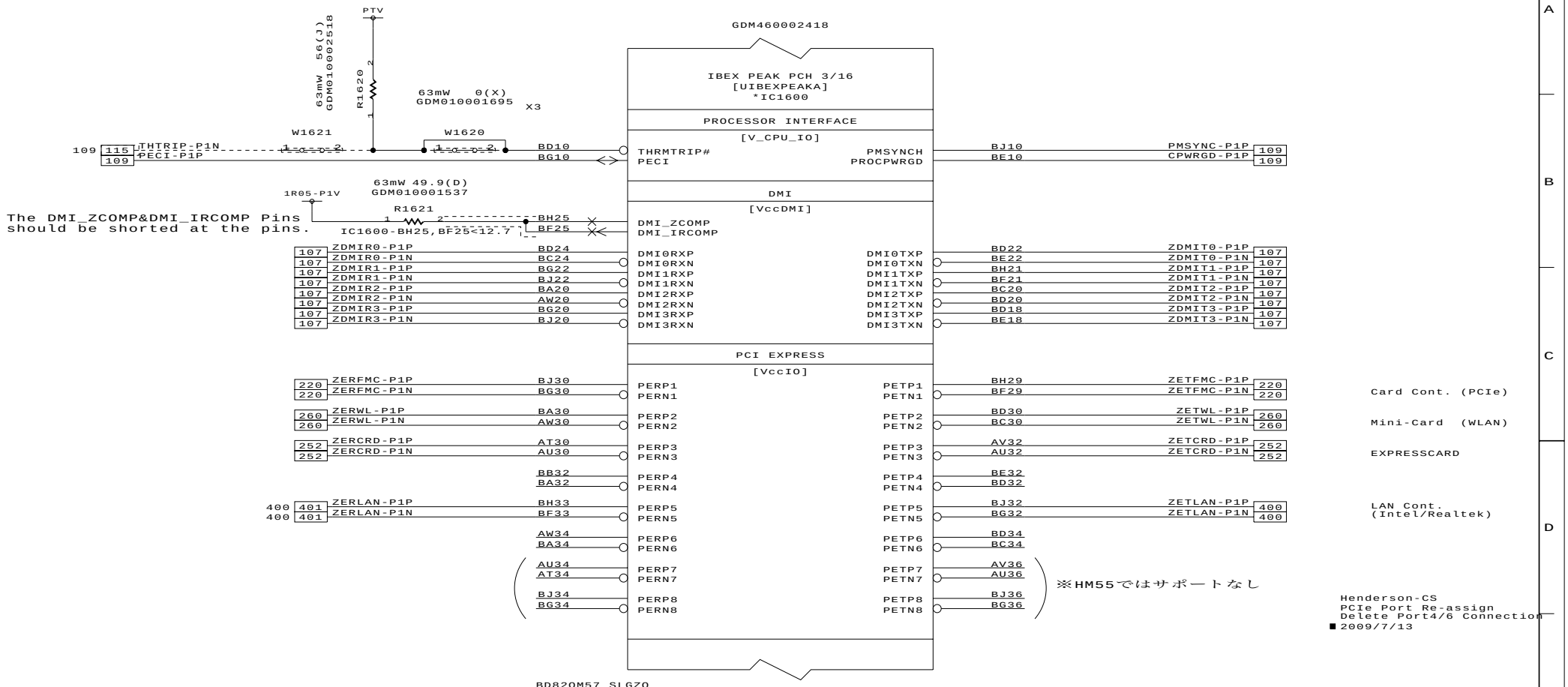
107 ZFDIR0-P1P BB18
107 ZFDIR0-P1N BA18
107 ZFDIR1-P1P BF17
107 ZFDIR1-P1N BH17
107 ZFDIR2-P1P BC16
107 ZFDIR2-P1N BD16
107 ZFDIR3-P1P BG16
107 ZFDIR3-P1N BJ16
107 ZFDIR4-P1P AW16
107 ZFDIR4-P1N BA16
107 ZFDIR5-P1P BD14
107 ZFDIR5-P1N BE14
107 ZFDIR6-P1P BB14
107 ZFDIR6-P1N BA14
107 ZFDIR7-P1P BD12
107 ZFDIR7-P1N BC12

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	Ibex Peak-M (2)	161	020	00	360069769

2009.10.15 17:09 G11

仕向け設定有



Henderson-CS
PCIe Port Re-assign
Delete Port4/6 Connection
■ 2009/7/13

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (3)	162	021	00	360069769

2009.10.15 17:09 G11

仕向け設定有

NAND	w/ Braidwood	w/o Braidwood
R1637	MOUNT	NOT MOUNT

GDM460002418

IBEX PEAK PCH 4/16
[UIBEXPEAKA]
*IC1600

DC NAND
[VccPNAND]

SERIAL PERIPHERAL INTERFACE
[VccME3_3]

SERIAL ATA INTERFACE
[VccIO]

BD82QM57 SLGZQ

1R8-P1V
R1638 2K
63mW 1K(J)
GDM010002524

Henderson-CS
Delete R1635
(No support ATT)
■ 2009/07/21

Henderson-CS
DC NAND I/F Change to N.C.
■ 2009/6/27

Henderson-CS
DC NAND I/F Change to N.C.
■ 2009/6/27

Henderson-CS
Change R1637 to N.M.
■ 2009/6/27

Henderson-CS
Add Backup 2nd SPI I/F
■ 2009/6/27

Henderson-CS
Delete Backup 2nd SPI I/F
■ 2009/07/21

Henderson-VP
SPI:0Ω→33Ω
■ 2009/10/15

※Gコード仮置き
R1637
32.4ohm_1%

R1637
63mW 33(D)
GDM010001654
GND

GDM010002527 x3
63mW 33(J)

16V 10nF(K)
GDM030000763 X12

AC COUPLING CAPs
10nF +-15%
16V 10nF(K)
GDM030000763
X8

※HM55ではサポートなし

IC1600-AF15,AF16<12.7

1R05-P1V
R1636
63mW 37.4(D)
GDM010001540

Henderson-CS
Change SATA Port Assign
Mount AC.Cap for SATA Port4/5
■ 2009/6/27

Henderson-CS
ZSTRES-P1P → ZSTRP5-P1P
ZSTRES-P1N → ZSTRP5-P1N
■ 2009/07/08

Henderson-CS
ZSTTES-P1P → ZSTTP5-P1P
ZSTTES-P1N → ZSTTP5-P1N
■ 2009/07/08

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (4)	163	022	00	360069769

2009.10.15 17:09 G11

仕向け設定有

GDM460002418

IBEX PEAK PCH 5/16
[UIBEXPEAKA]
*IC1600

USB INTERFACE
[VccSus3_3]

P1:Debug Port! Don't move

EHCI #1

USBP0P	J18	ZUSBEX-E3P	252
USBP0N	H18	ZUSBEX-E3N	252
USBP1P	C18	ZUSBP1-E3P	953
USBP1N	A18	ZUSBP1-E3N	953
USBP2P	P20	ZUSBP2-E3P	953
USBP2N	N20	ZUSBP2-E3N	953
USBP3P	L20	ZUSBDC-E3P	230
USBP3N	J20	ZUSBDC-E3N	230
USBP4P	G20	ZUSBSF-E3P	216
USBP4N	F20	ZUSBSF-E3N	216
USBP5P	C20	ZUSBWM-E3P	260
USBP5N	A20	ZUSBWM-E3N	260
USBP6P	N22		
USBP6N	M22		
USBP7P	D21		
USBP7N	B21		

※HM55ではサポートなし

EHCI #2

USBP8P	J22	ZUSBES-E3P	465
USBP8N	H22	ZUSBES-E3N	465
USBP9P	F22	ZUSBFS-E3P	955
USBP9N	E22	ZUSBFS-E3N	955
USBP10P	C22	ZUSBPA-E3P	466
USBP10N	A22	ZUSBPA-E3N	466
USBP11P	H24	ZUSBCM-E3P	954
USBP11N	G24	ZUSBCM-E3N	954
USBP12P	M24	ZUSB3G-E3P	261
USBP12N	L24	ZUSB3G-E3N	261
USBP13P	C24	ZUSBBT-E3P	444
USBP13N	A24	ZUSBBT-E3N	444

ExpressCard
本体Port(Debug Port) (Right Rear Port)
本体Port (Right Front Port)
Docker
SmartCard/Felica
PCIe Mini Card (WiMAX)

本体Port (eSATA/USB)(W/ Charger) (Left Front Port)
Finger Print Sensor
本体Port(w/ Charger) (Left Rear Port)
WebCam
PCIe Mini Card (3G)
Bluetooth

USBRBIAS — D25
USBRBIAS# — B25
R1655 63mW 22.6(D) GDM010001543
IC1600-B25,D25<12.7
63mW 47(J) GDM010002528 X8
GND

Henderson10GC-VP
R1640,R1644,R1648,R1652 33Ω→47Ω
R1641,R1645,R1649,R1653 33Ω→39Ω
■ 2009/10/13

INTEL HD AUDIO LINK
[VccSusHDA]

HDA_BCLK	A30	R1640	XAZBC0-E3P	603
		*R1641	XAZBC1-E3P	301
			(XAZBC2-E3P)	
HDA_SYNC	D29	R1644	AZSYC0-E3P	603
		*R1645	AZSYC1-E3P	301
			(AZSYC2-E3P)	
HDA_SDO	B29	R1648	AZSD00-E3P	603
		*R1649	AZSD01-E3P	301
			(AZSD02-E3P)	
HDA_RST#	C30	R1652	AZRST0-E3N	603
		*R1653	AZRST1-E3N	301
			(AZRST2-E3N)	

Audio Codec
MDC
(~~HDMI~~)

63mW 39(J) GDM010002725

Henderson-CS
Delete HD Audio I/F for HDMI
■ 2009/6/27

603	AZSDI0-P3P	G30 HDA_SDIN0
301	AZSDI1-E3P	F30 HDA_SDIN1
	(AZSDI2-E3P)	E32 HDA_SDIN2
		F32 HDA_SDIN3

LPC/FWH INTERFACE
[Vcc3_3]

340 311 330 320	LAD0-P3P	D33	FWH0/LAD0
340 311 330 320	LAD1-P3P	B33	FWH1/LAD1
340 311 330 320	LAD2-P3P	C32	FWH2/LAD2
340 311 330 320	LAD3-P3P	A32	FWH3/LAD3
340 320	LDRQ0-P3N	A34	LDRQ0#
542	CRTEN-P3N	F34	LDRQ1#/GPIO23

FWH4/LFRAME# — C34 — LPCFRM-P3N — 340 311 330 320

R1737 63mW 1K(J) GDM010002524
E3V
Low Power Mode: 1.5V

Function Strap
HDA_SYNC:int VR Select
L:1.8V H:1.5V

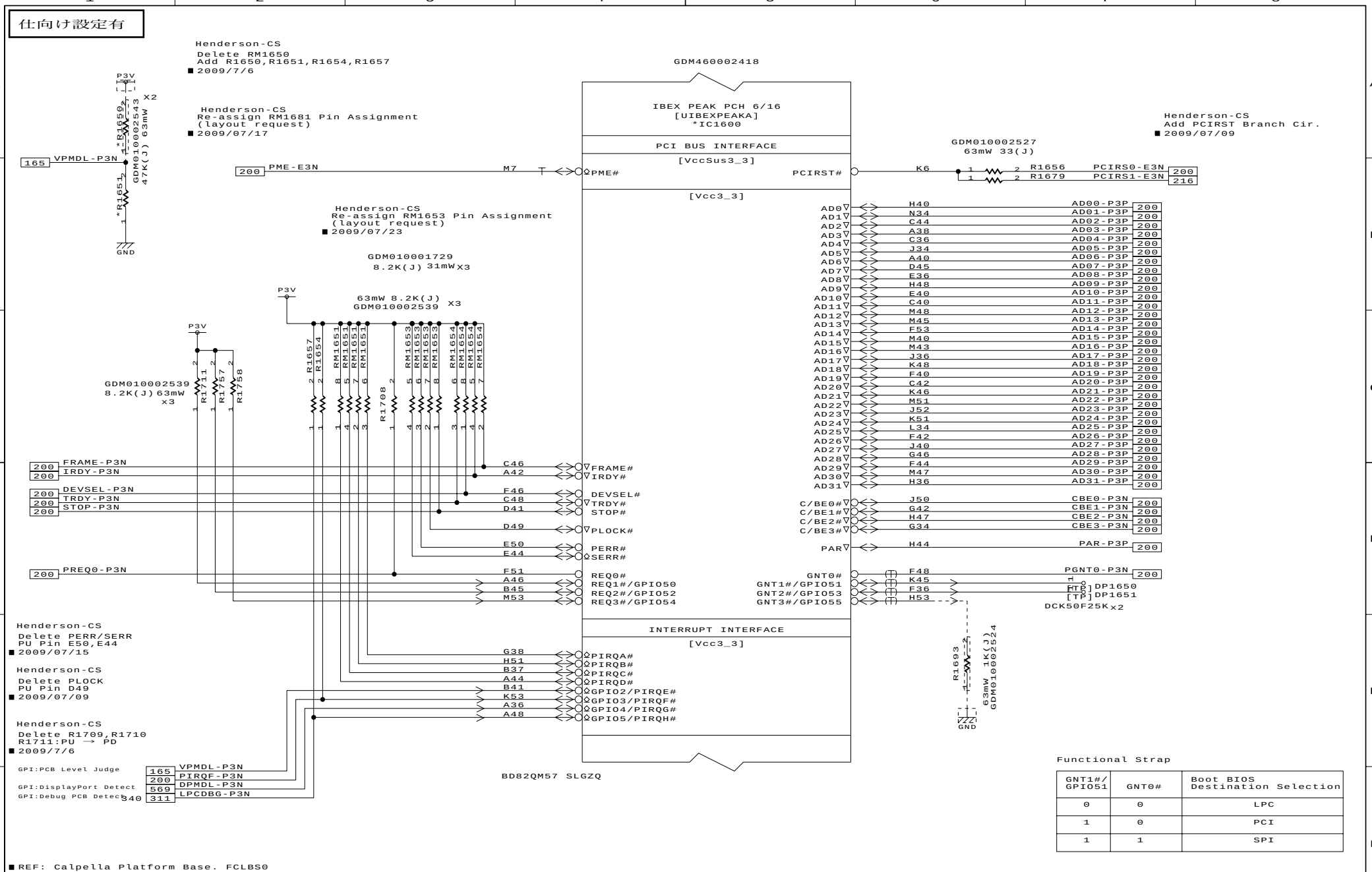
BD82QM57 SLGZQ

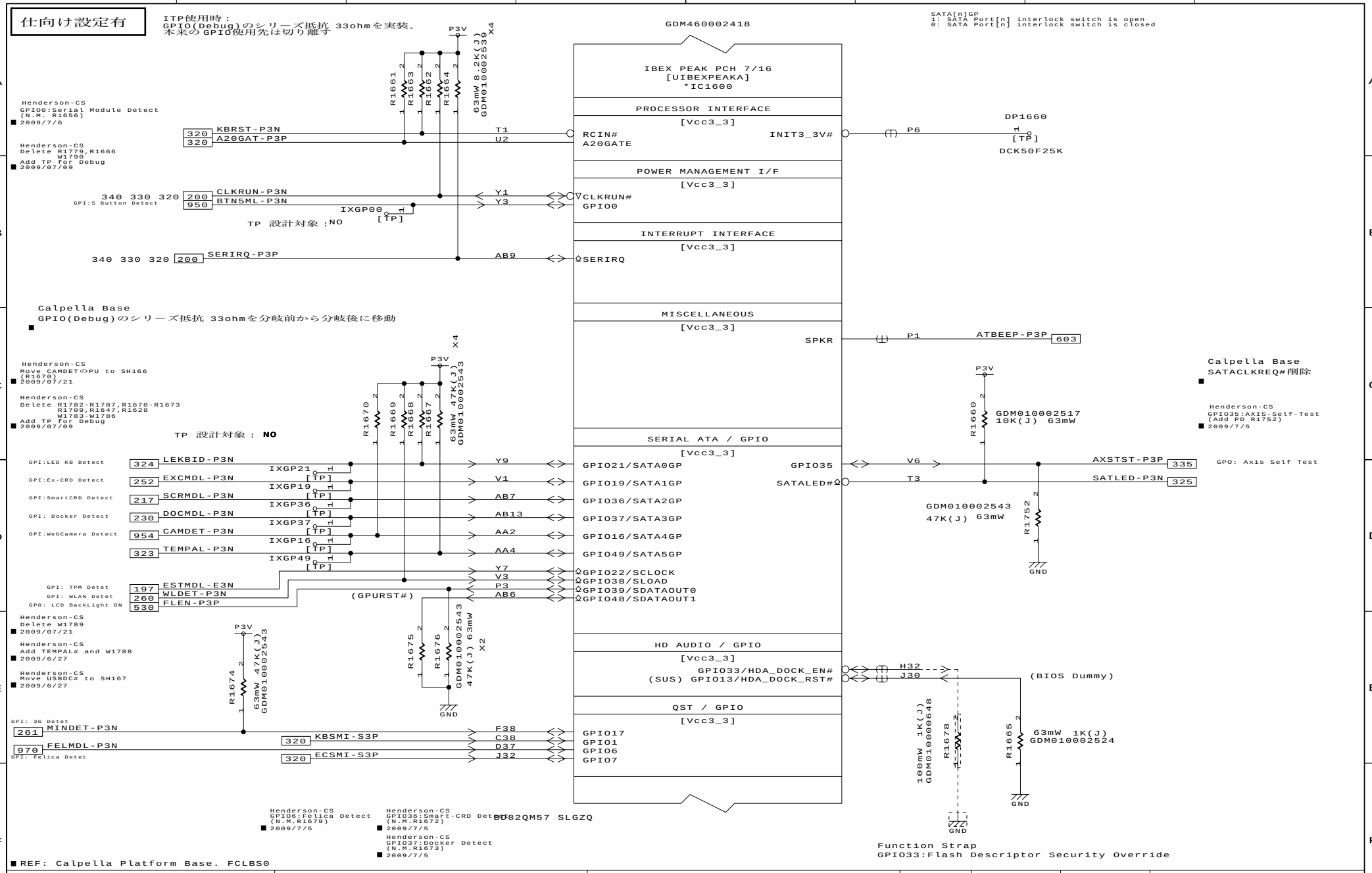
奥
P10
(eSATA Combo) P8
TOSHIBA
(Sub)
P1
P2
手前
<TOP VIEW>

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (5)	164	023	00	360069769

2009.10.15 17:09 G11





DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (7)	166	025	00	360069769

2009.10.15 17:09 G11

仕向け設定有

ITP使用時：
GPIO(Debug)のシリーズ抵抗 33ohmを実装、
本来のGPIO使用先は切り離す

ITP使用時：
R1681,1682及びR1760(SH176)を実装
QS以降、使用しないときは未実装可

Calpella Base
JTAGのプルアップ変更 (E3Vからの抵抗分割から)

GDM460002418

IBEX PEAK PCH 8/16
[UIBEXPEAKA]
*IC1600

JTAG
[VccSus1_05]
JTAG_RST#
JTAG_TCK
JTAG_TDI
JTAG_TMS
JTAG_TDO

Quiet System Technology
[DcpSST]
TP24

USB / GPIO
[VccSus3_3]
OC0#/GPIO59
OC1#/GPIO40
OC2#/GPIO41
OC3#/GPIO42
OC4#/GPIO43
OC5#/GPIO09
OC6#/GPIO10
OC7#/GPIO14

GPIO
[VccSus3_3]
GPIO57
GPIO8
GPIO15
GPIO27
GPIO28

BD82QM57 SLGZQ

1R05-E1V
63mW 51(J) GDM010002529 X2
R1681 R1682
R1683 63mW 10K(J) GDM010002517
177 PCHTCK-E1P
177 PCHTDI-E1P
177 PCHTMS-E1P
J4 M3 K1 K3
J2 PCHTDO-E1P 177
63mW 51(J) GDM010002529 R1680
GND

Calpella Base
GPIO(Debug)のシリーズ抵抗 33ohmを分岐前から分岐後に移動

TP 設計対象： NO x8

E3V
63mW 47K(J) GDM010002543
R1692 R1691 R1690 R1689 R1688 R1687 R1686 R1685

X8
IXGP59 [TP] N16
IXGP40 [TP] J16
IXGP41 [TP] F16
465 466 MOD3EN-E3N IXGP42 [TP] L16
251 CRDSHD-E3N IXGP43 [TP] E14
301 MDMEN-E3P IXGP09 [TP] G16
465 CHGONP-E3N IXGP10 [TP] F12
465 466 MOD4EN-E3N IXGP14 [TP] T15

C10 DP1670 [TP]

Henderson-CS
Delete GPU RST circuit
2009/07/08

Henderson-CS
Delete W1712-W1714
2009/7/7

Henderson-CS
Delete R1771-R1778
W1775-W1778,W1780
Add TP for Debug
2009/07/09

GPO:USB D-Charge
GPO:ExCard PWR ShutDown
GPO:MDC Device Lock
GPO:USB Charge SW Cont.
GPO:USB IP-Charge

Henderson-CS
信号名変更：
USBSC-E3N→CHGONP-E3N
USBDC-E3N→MOD3EN-E3N
USBIP-E3N→MOD4EN-E3N
2009/7/29

Henderson-CS
USBDC-E3N move to GPIO42
Add W1780
2009/6/27

Henderson-CS
GPIO9:MDC Device Lock
Mount W1776-W1778
2009/7/5

E3V
63mW 10K(J) GDM010002517 R1694
168 GPSTS-E3N

F8

E3V
63mW 1K(J) GDM010002524 R1677
F10 DP1671 [TP] DCK50F25K
T7
AB12
V13 FTDET-E3N 311
GPI:PCB FT Detection
IXGP28 [TP]
63mW 1K(J) GDM010002524 R1696
GND

TP 設計対象 :NO

E3V
63mW 47K(J) GDM010002543 R1710
63mW 47K(J) GDM010002543 R1719
GND

AMT Model判別
H:AMT
L:NO AMT
AMTMDL-E3N 168

Henderson-CS
GPIO41:AMT Module Detect
(Add W1714,R1710,R1719, N.M.R1687)
2009/7/6

Henderson-CS
GPIO40:eSATA Module Detect
(Add W1713, N.M.R1686)
2009/7/6

Henderson-CS
GPIO39:PC/Ex-CRD Module Detect
(Add W1712, N.M.R1685)
2009/7/6

Henderson-CS
GPIO28:PCB FT Detect
Add W1677
2009/7/5

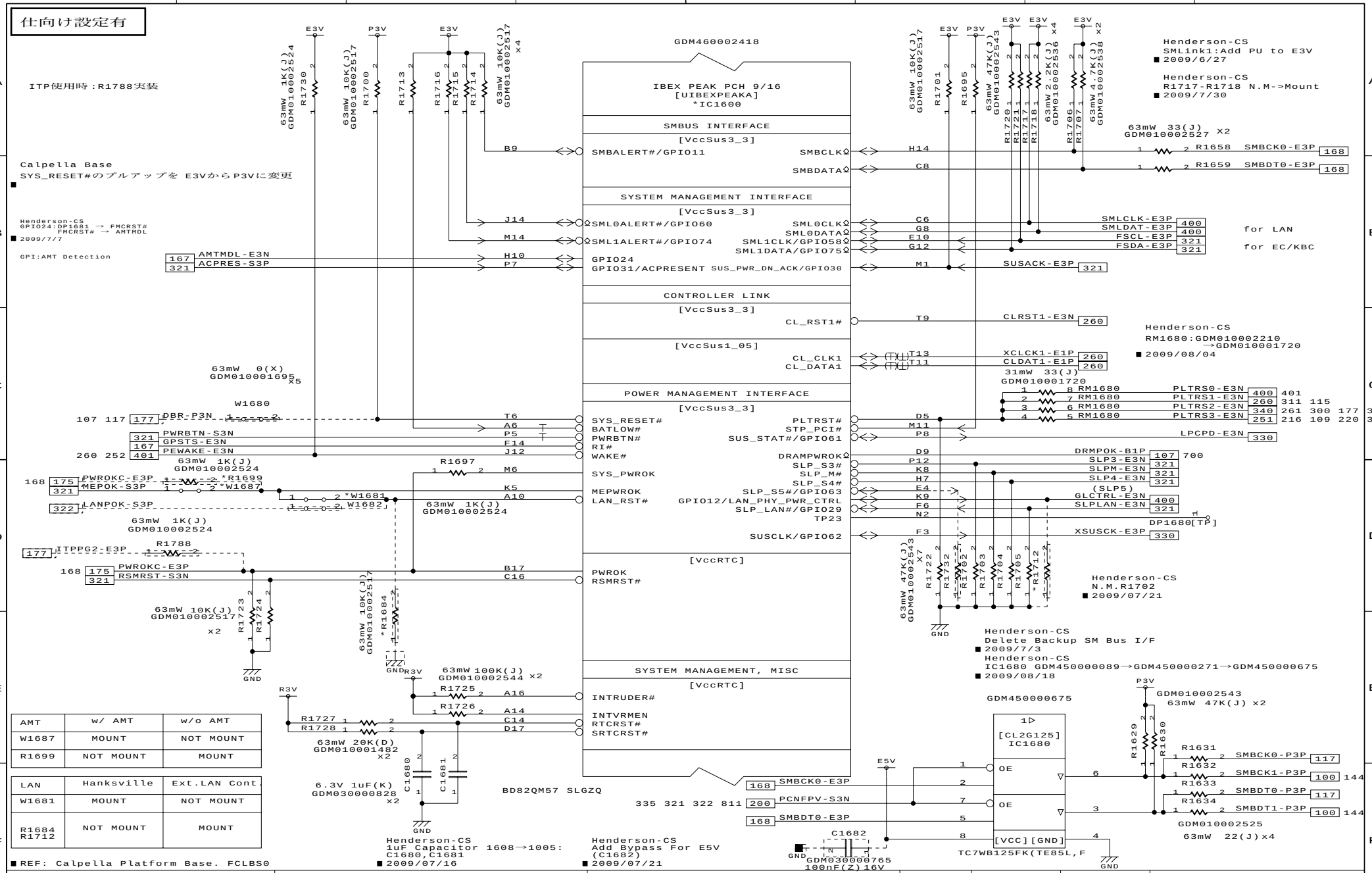
Henderson-CS
Delete R1770
W1677
Add TP for Debug
2009/07/09

Function Strap
GPIO27: Internal VRM (H:enable)

REF: Calpella Platform Base. FCLBS0

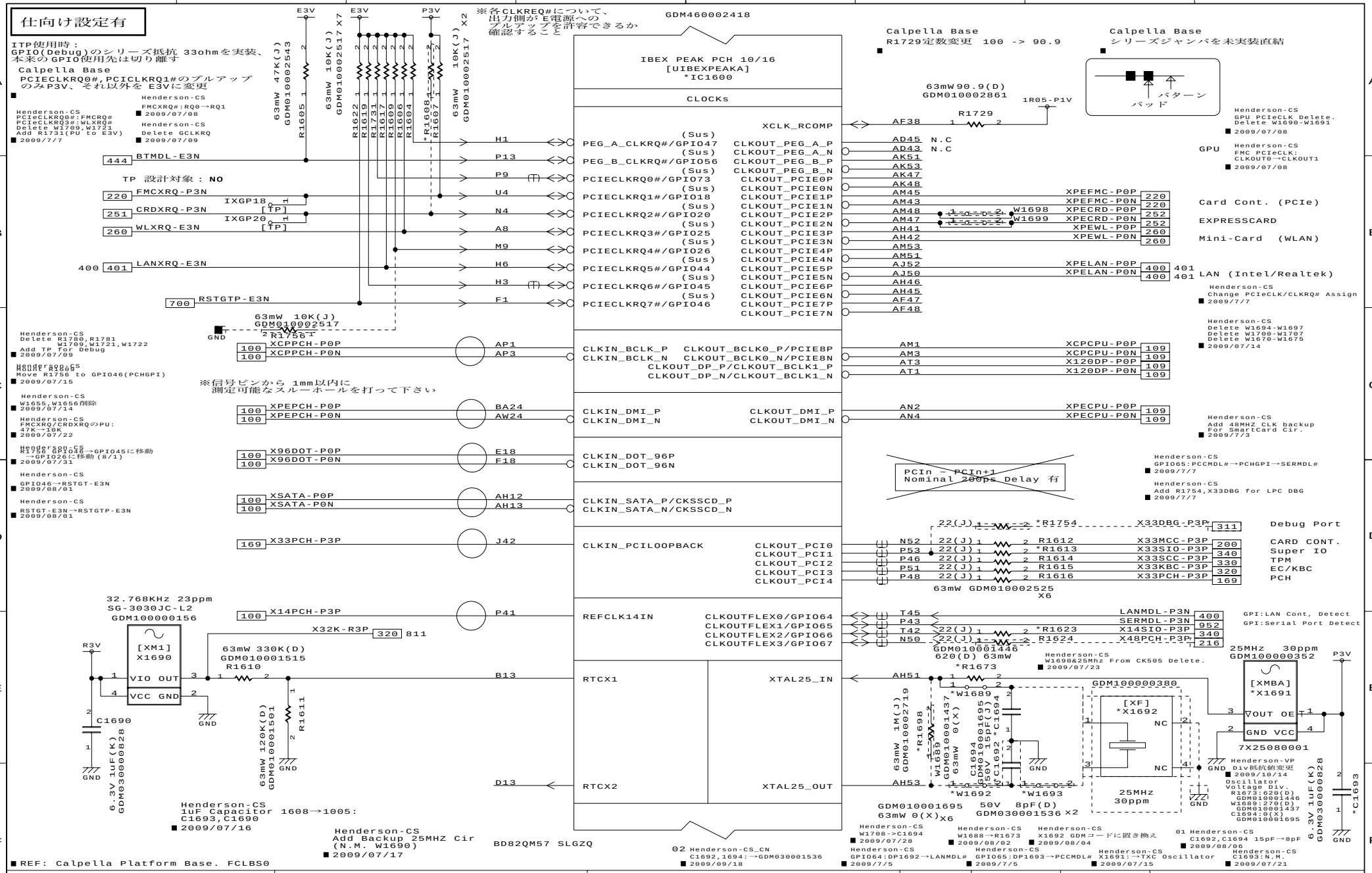
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (8)	167	026	00	360069769

2009.10.15 17:09 G11



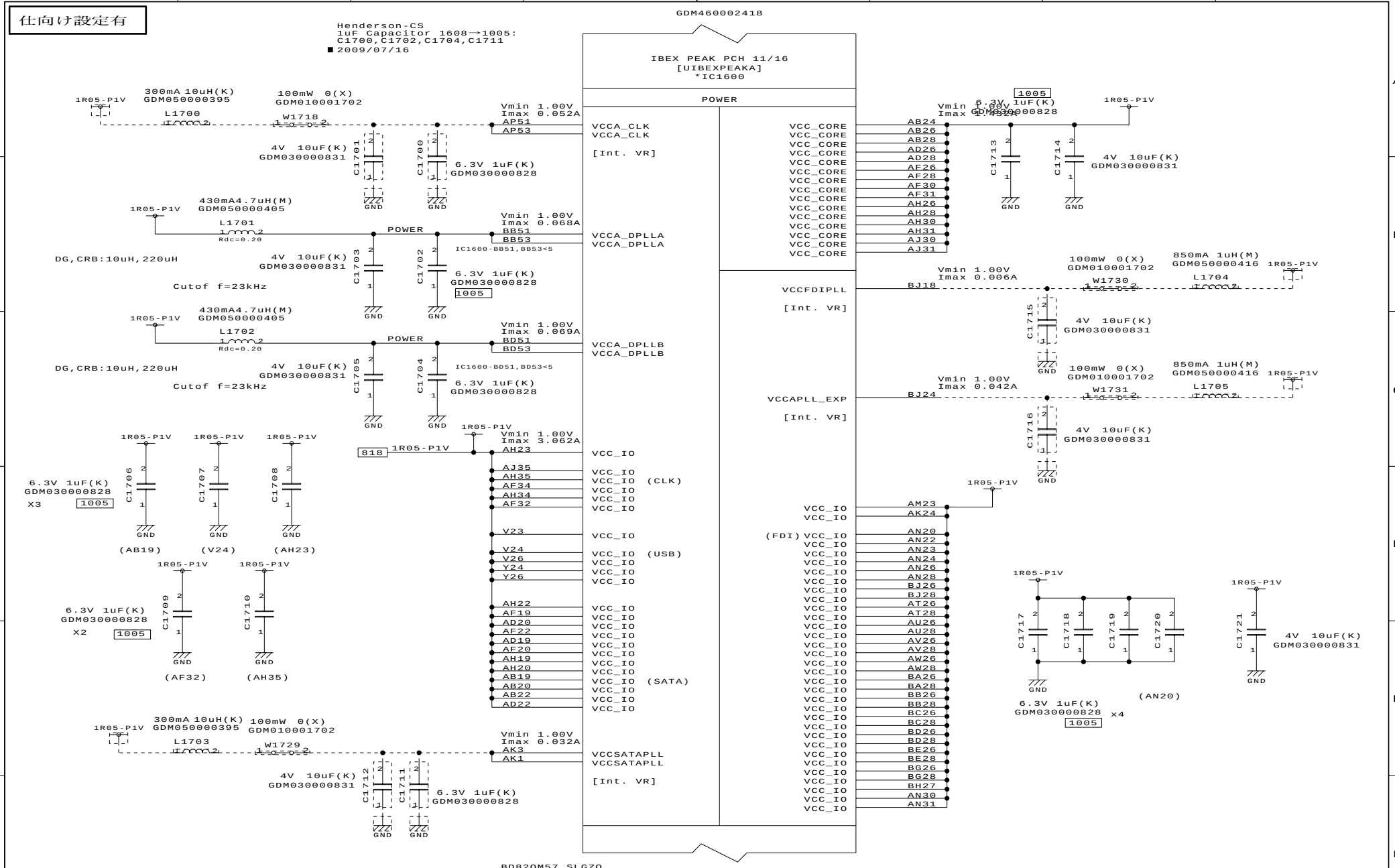
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (9)	168	027	00	360069769

2009.10.15 17:09 G11



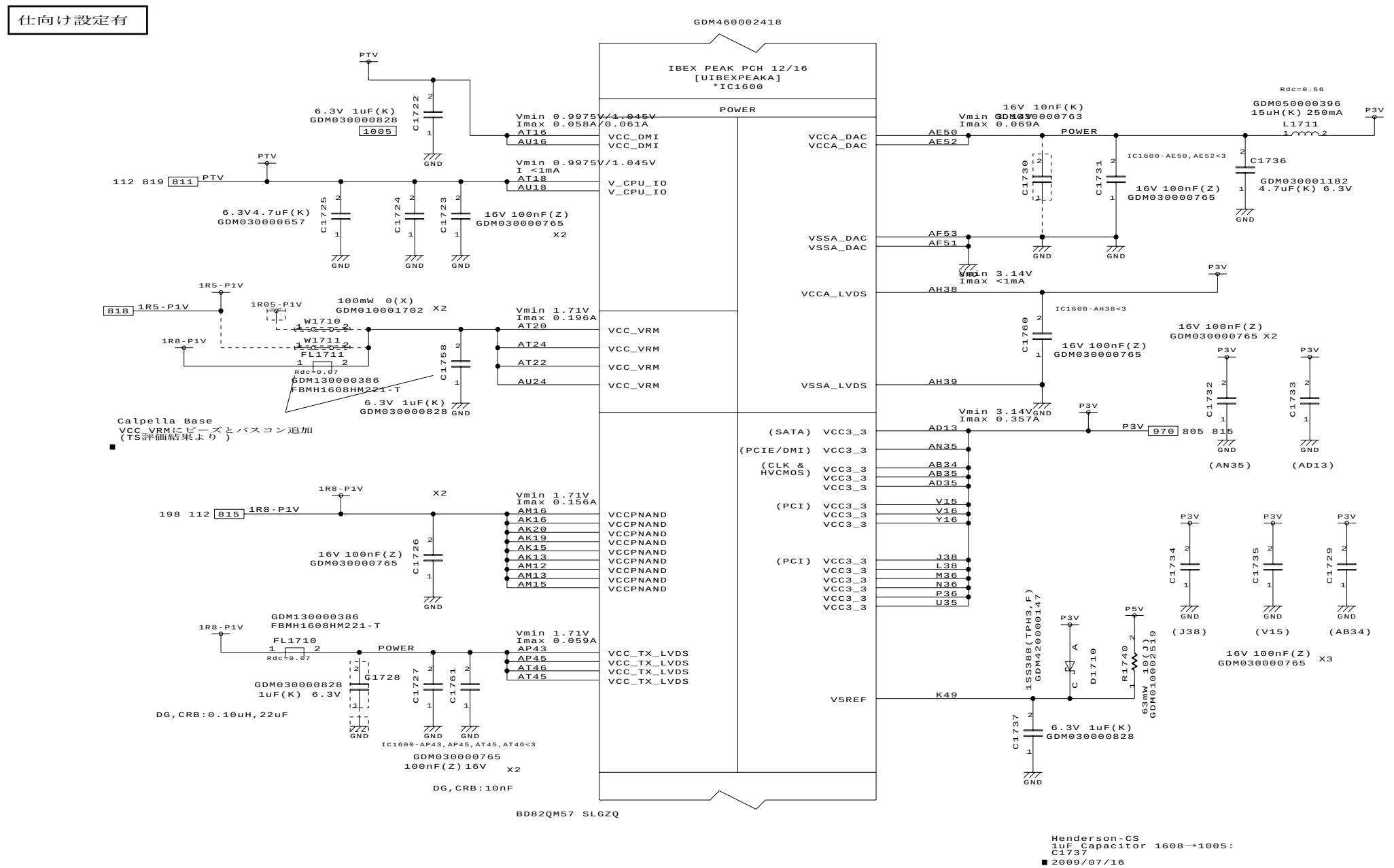
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (10)	169	028	00	360069769

2009.10.15 17:09 G11



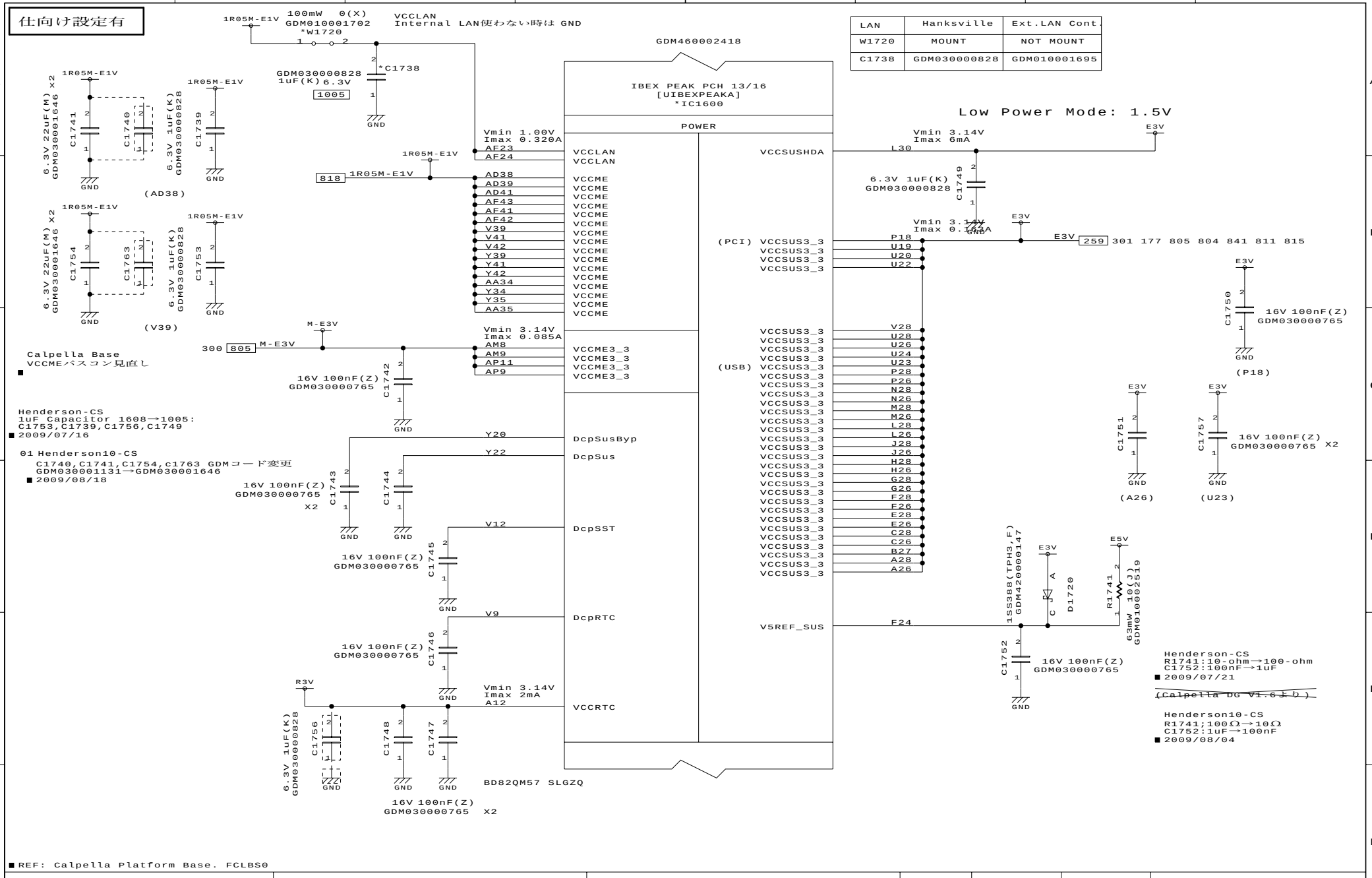
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	Ibex Peak-M (11)	170	029	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	Ibex Peak-M (12)	171	030	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	Ibex Peak-M (13)	172	031	00	360069769

2009.10.15 17:09 G11

仕向け設定有

GDM460002418

IBEX PEAK PCH 14/16
[UIBEXPEAKA]
*IC1600

GROUND

B7	VSS	VSS	P22
B11	VSS	VSS	P24
B15	VSS	VSS	P30
B19	VSS	VSS	P32
B23	VSS	VSS	P34
B31	VSS	VSS	P38
B35	VSS	VSS	P42
B39	VSS	VSS	P45
B43	VSS	VSS	P47
B47	VSS	VSS	P49
C12	VSS	VSS	R2
C50	VSS	VSS	R52
D51	VSS	VSS	T5
E6	VSS	VSS	T8
E8	VSS	VSS	T12
E12	VSS	VSS	T41
E16	VSS	VSS	T43
E20	VSS	VSS	T46
E24	VSS	VSS	T49
E30	VSS	VSS	U30
E34	VSS	VSS	U31
E38	VSS	VSS	U32
E42	VSS	VSS	U34
E46	VSS	VSS	V5
E48	VSS	VSS	V7
F5	VSS	VSS	V8
F49	VSS	VSS	V11
G2	VSS	VSS	V19
G10	VSS	VSS	V20
G14	VSS	VSS	V22
G18	VSS	VSS	V30
G22	VSS	VSS	V31
G32	VSS	VSS	V32
G36	VSS	VSS	V34
G40	VSS	VSS	V35
G44	VSS	VSS	V38
G52	VSS	VSS	V43
H5	VSS	VSS	V45
H16	VSS	VSS	V46
H20	VSS	VSS	V47
H30	VSS	VSS	V49
H34	VSS	VSS	W2
H38	VSS	VSS	W52
H42	VSS	VSS	Y5
H49	VSS	VSS	Y6
J24	VSS	VSS	Y8
K7	VSS	VSS	Y11
K11	VSS	VSS	Y12
K43	VSS	VSS	Y13
K47	VSS	VSS	Y15
L2	VSS	VSS	Y19
L14	VSS	VSS	Y23
L18	VSS	VSS	Y28
L22	VSS	VSS	Y30
L32	VSS	VSS	Y31
L36	VSS	VSS	Y32
L40	VSS	VSS	Y38
L52	VSS	VSS	Y43
M5	VSS	VSS	Y46
M8	VSS	VSS	Y47
M12	VSS	VSS	AA19
M16	VSS	VSS	AA20
M20	VSS	VSS	AA22
M34	VSS	VSS	AM19
M38	VSS	VSS	AA24
M42	VSS	VSS	AA26
M46	VSS	VSS	AA28
M49	VSS	VSS	AA30
N24	VSS	VSS	AA31
N38	VSS	VSS	AA32
P11	VSS	VSS	AA50
P16	VSS	VSS	AB5

GND GND

BD82QM57 SLGZQ

GDM460002418

IBEX PEAK PCH 15/16
[UIBEXPEAKA]
*IC1600

GROUND

AB8	VSS	VSS	AK35
AB11	VSS	VSS	AK38
AB15	VSS	VSS	AK39
AB16	VSS	VSS	AK43
AB23	VSS	VSS	AK45
AB30	VSS	VSS	AK46
AB31	VSS	VSS	AK49
AB32	VSS	VSS	AL2
AB39	VSS	VSS	AL52
AB43	VSS	VSS	AM5
AB47	VSS	VSS	AM6
AC2	VSS	VSS	AM7
AC52	VSS	VSS	AM11
AD7	VSS	VSS	AM20
AD11	VSS	VSS	AM22
AD12	VSS	VSS	AM24
AD15	VSS	VSS	AM26
AD16	VSS	VSS	AM28
AD23	VSS	VSS	AM30
AD24	VSS	VSS	AM31
AD30	VSS	VSS	AM32
AD31	VSS	VSS	AM34
AD32	VSS	VSS	AM35
AD34	VSS	VSS	AM38
AD42	VSS	VSS	AM39
AD46	VSS	VSS	AM41
AD47	VSS	VSS	AM42
AD49	VSS	VSS	AM46
AD51	VSS	VSS	AM49
AE2	VSS	VSS	AN19
AE4	VSS	VSS	AN32
AF5	VSS	VSS	AN34
AF8	VSS	VSS	AN50
AF12	VSS	VSS	AN52
AF35	VSS	VSS	AP5
AF39	VSS	VSS	AP8
AF45	VSS	VSS	AP12
AF46	VSS	VSS	AP13
AF49	VSS	VSS	AP42
AG2	VSS	VSS	AP46
AG52	VSS	VSS	AP49
AH7	VSS	VSS	AR2
AH11	VSS	VSS	AR52
AH15	VSS	VSS	AT5
AH16	VSS	VSS	AT7
AH24	VSS	VSS	AT8
AH32	VSS	VSS	AT11
AH43	VSS	VSS	AT12
AH47	VSS	VSS	AT13
AH48	VSS	VSS	AT32
AH49	VSS	VSS	AT36
AJ2	VSS	VSS	AT41
AJ4	VSS	VSS	AT47
AJ19	VSS	VSS	AU4
AJ20	VSS	VSS	AU20
AJ22	VSS	VSS	AU22
AJ23	VSS	VSS	AV5
AJ26	VSS	VSS	AV8
AJ28	VSS	VSS	AV12
AJ32	VSS	VSS	AV14
AJ34	VSS	VSS	AV16
AK5	VSS	VSS	AV18
AK8	VSS	VSS	AV20
AK12	VSS	VSS	AV22
AK22	VSS	VSS	AV24
AK23	VSS	VSS	AV30
AK26	VSS	VSS	AV34
AK28	VSS	VSS	AV38
AK30	VSS	VSS	AV42
AK31	VSS	VSS	AV46
AK32	VSS	VSS	AV49
AK34	VSS	VSS	AW2

GND GND

BD82QM57 SLGZQ

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	Ibex Peak-M (14)	173	032	00	360069769

2009.10.15 17:09 G11

仕向け設定有

GDM460002418

IBEX PEAK PCH 16/16
[UIBEXPEAKA]
*IC1600

GROUND		TP, NC, NCTF		
AW14	VSS	TP1	BA22	HTP DP1700
AW18	VSS	TP2	AW22	HTP DP1701
AW32	VSS	TP3	BB22	HTP DP1702
AW36	VSS	TP4	AY45	HTP DP1703
AW40	VSS	TP5	AY46	HTP DP1704
AW52	VSS	TP6	AV43	HTP DP1705
AY7	VSS	TP7	AV45	HTP DP1706
AY11	VSS	TP8	AF13	HTP DP1707
AY43	VSS	TP9	M18	HTP DP1708
AY47	VSS	TP10	N18	HTP DP1709
BA12	VSS	TP11	AJ24	HTP DP1710
BA42	VSS	TP12	AK41	HTP DP1711
BB5	VSS	TP13	AK42	HTP DP1712
BB10	VSS	TP14	M32	HTP DP1713
BB12	VSS	TP15	N32	HTP DP1714
BB16	VSS	TP16	M30	HTP DP1715
BB20	VSS	TP17	N30	HTP DP1716
BB24	VSS	TP18	H12	HTP DP1717
BB30	VSS	TP19	AA23	HTP DP1718
BB34	VSS			[TP]
BB38	VSS			
BB42	VSS			
BB44	VSS			
BB49	VSS	NC	AB45	
BC2	VSS	NC	AB38	
BC10	VSS	NC	AB42	
BC14	VSS	NC	AB41	
BC18	VSS	NC	T39	
BC22	VSS			
BC32	VSS			
BC36	VSS	VSS_NCTF	A4	
BC40	VSS	VSS_NCTF	A5	
BC44	VSS	VSS_NCTF	B2	
BC52	VSS	VSS_NCTF	B4	
BD5	VSS	VSS_NCTF	D1	
BD48	VSS	VSS_NCTF	D2	
BD49	VSS	VSS_NCTF	E1	
BE6	VSS			
BE8	VSS			
BE12	VSS			
BE16	VSS	VSS_NCTF	A49	
BE20	VSS	VSS_NCTF	A50	
BE24	VSS	VSS_NCTF	A52	
BE30	VSS	VSS_NCTF	A53	
BE34	VSS	VSS_NCTF	B52	
BE38	VSS	VSS_NCTF	B53	
BE42	VSS	VSS_NCTF	D53	
BE46	VSS	VSS_NCTF	E53	
BE48	VSS			
BE50	VSS			
BF3	VSS			
BF9	VSS	VSS_NCTF	BE1	
BF49	VSS	VSS_NCTF	BF1	
BF51	VSS	VSS_NCTF	BH1	
BG4	VSS	VSS_NCTF	BH2	
BG12	VSS	VSS_NCTF	BJ1	
BG18	VSS	VSS_NCTF	BJ2	
BG24	VSS	VSS_NCTF	BJ4	
BG50	VSS	VSS_NCTF	BJ5	
BH7	VSS			
BH9	VSS			
BH11	VSS			
BH15	VSS	VSS_NCTF	BE53	
BH19	VSS	VSS_NCTF	BF53	
BH23	VSS	VSS_NCTF	BH52	
BH31	VSS	VSS_NCTF	BH53	
BH35	VSS	VSS_NCTF	BJ49	
BH39	VSS	VSS_NCTF	BJ50	
BH43	VSS	VSS_NCTF	BJ52	
BH47	VSS	VSS_NCTF	BJ53	

BD82QM57 SLGZQ

GND

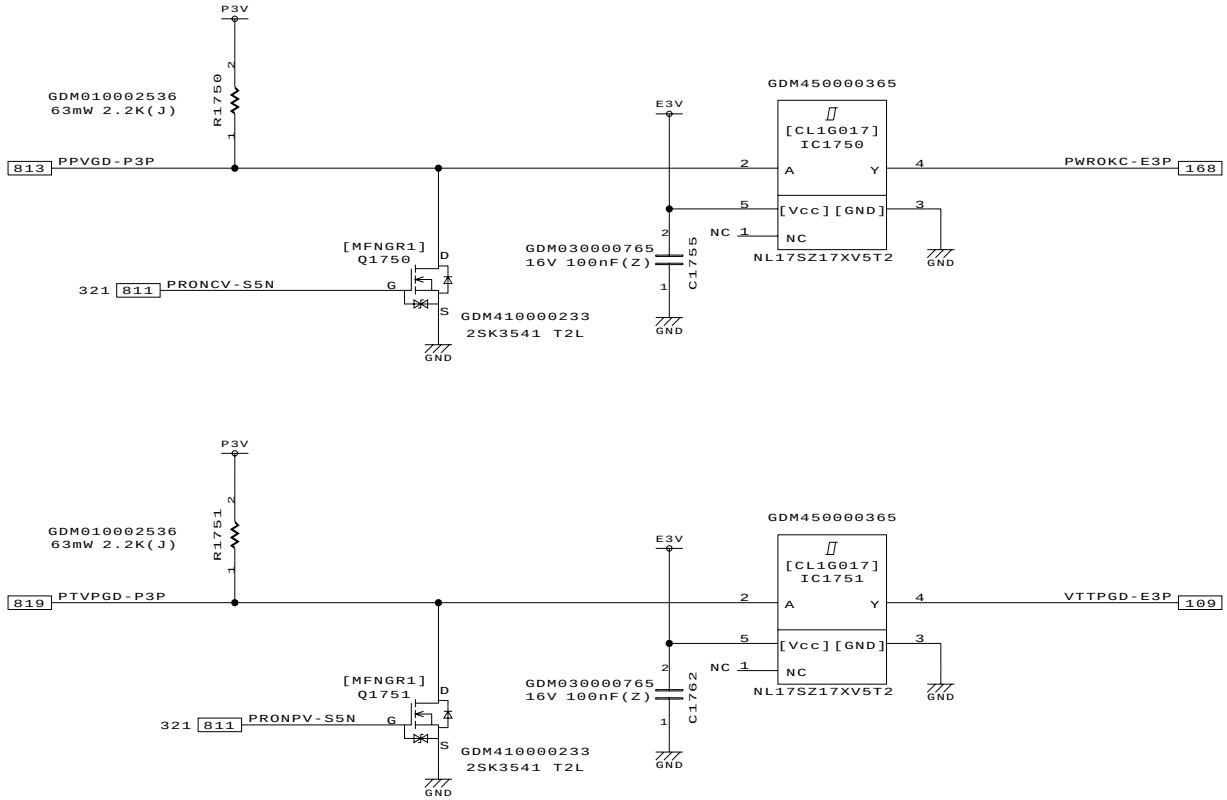
※TP必要

Henderson CS
Delete TP for NC/VSS_NCTF
■ 2009/7/2

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	Ibex Peak-M (15)	174	033	00	360069769

2009.10.15 17:09 G11



■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	IMVP-6.5 POWER OK	175	034	00	360069769

2009.10.15 17:09 G11

Calpella SFF TS
60pin→24pin不足信号に DP追加。
■ 2009/5/18

Calpella SFF TS
TP変更：貫通→表面
■ 2009/5/19

Henderson-CS
Delete DP1760-DP1777
■ 2009/07/09

Lillehammer Temp
XDP 60pin → XDP-SFF 24pin
■ 2009/3/4

Henderson-CS
CN1770:G-CODE化
■ 2009/08/04

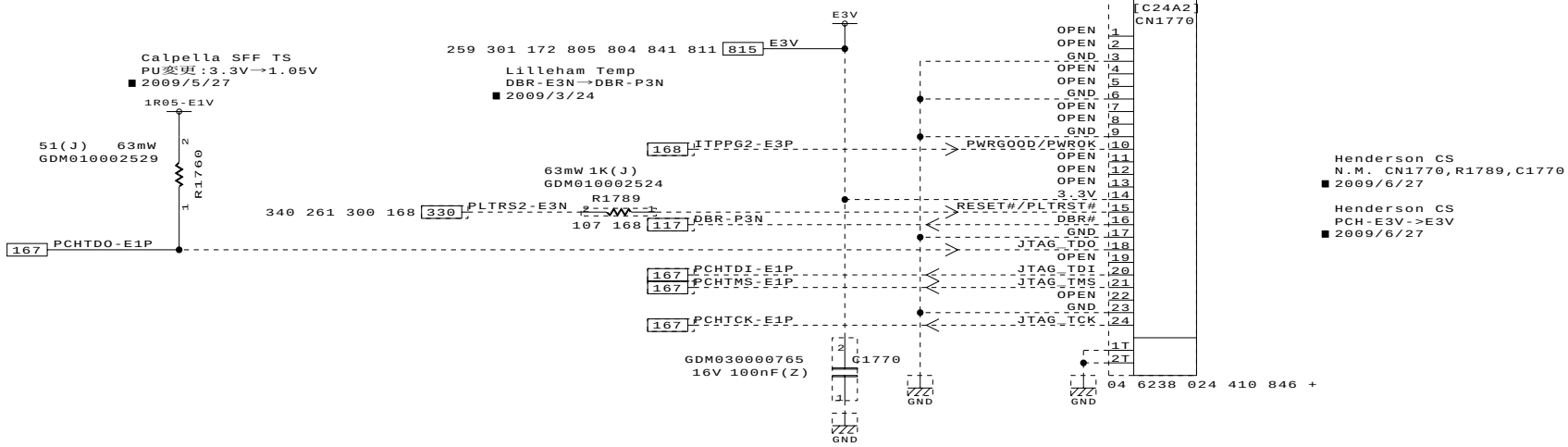
Henderson-CS
PLTRS* Re-assign:→PLTRS2#
■ 2009/07/13

Calpella SFF TS
PU変更:3.3V→1.05V
■ 2009/5/27

Lilleham Temp
DBR-E3N→DBR-P3N
■ 2009/3/24

Henderson CS
N.M. CN1770,R1789,C1770
■ 2009/6/27

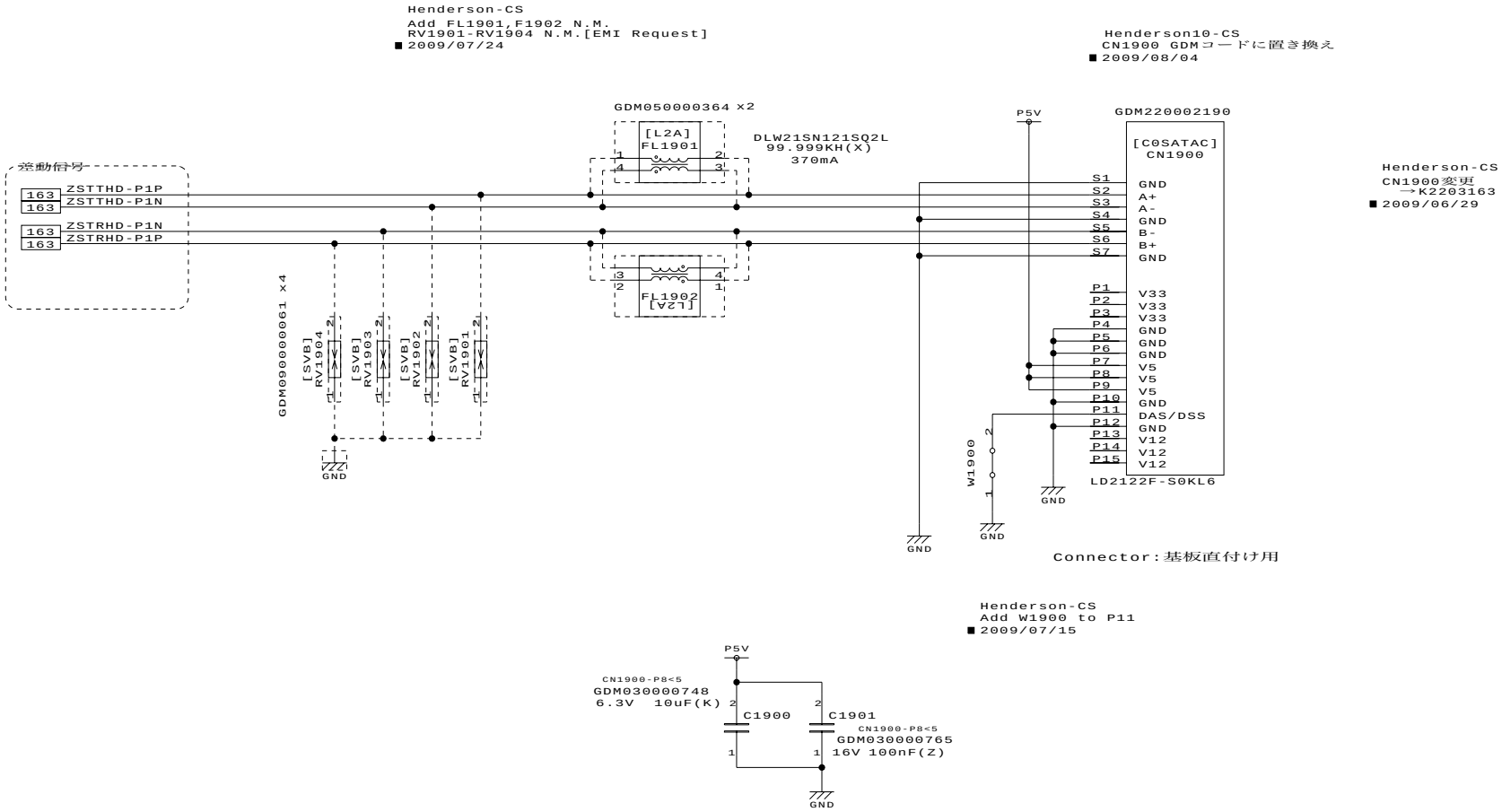
Henderson CS
PCH-E3V->E3V
■ 2009/6/27



■ REF: Calpella SFF TS. FCLST0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	PCH ITP-XDP 24pin	177	035	00	360069769

2009.10.15 17:09 G11



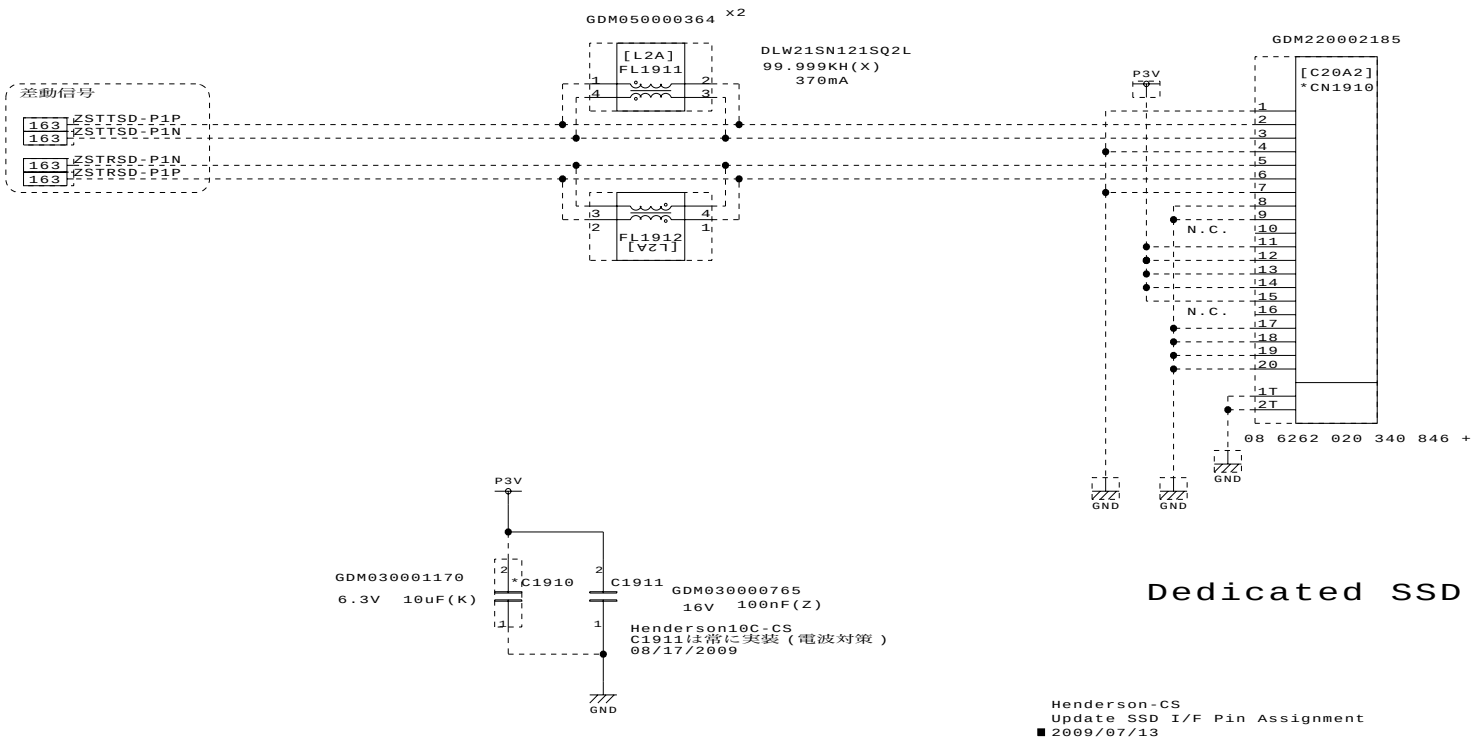
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	SATA HDD I/F	190	036	00	360069769

2009.10.15 17:09

仕向け設定有

Henderson-CS
Add FL1911,F1912 N.M.
[EMI Request]
■ 2009/07/24

SSDコネクタ：ハーネス経由用



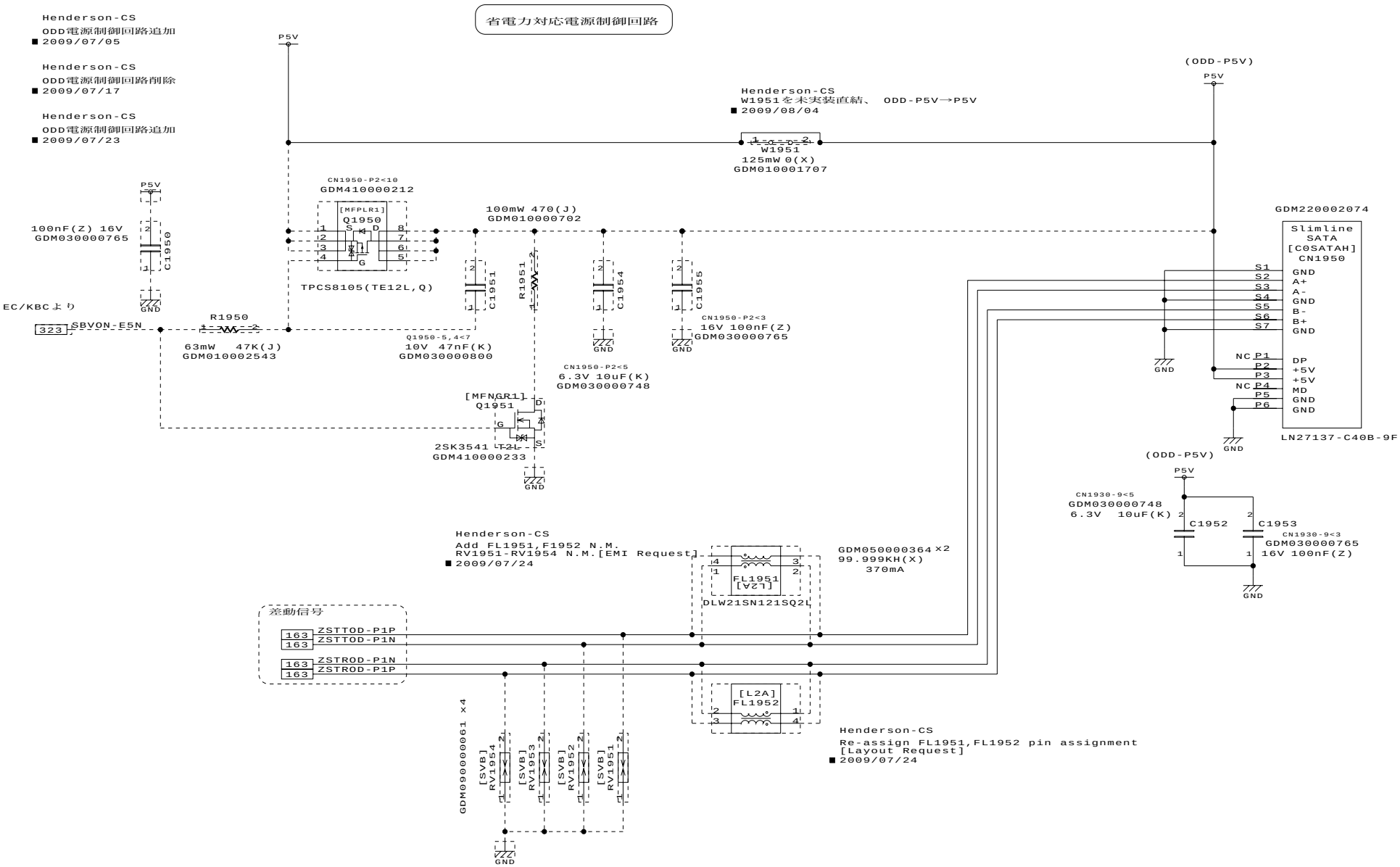
Dedicated SSD

Henderson-CS
Update SSD I/F Pin Assignment
■ 2009/07/13

■ REF:Duluth10(VP) . FDUSY2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	Dedicated SSD I/F	191	037	00	360069769

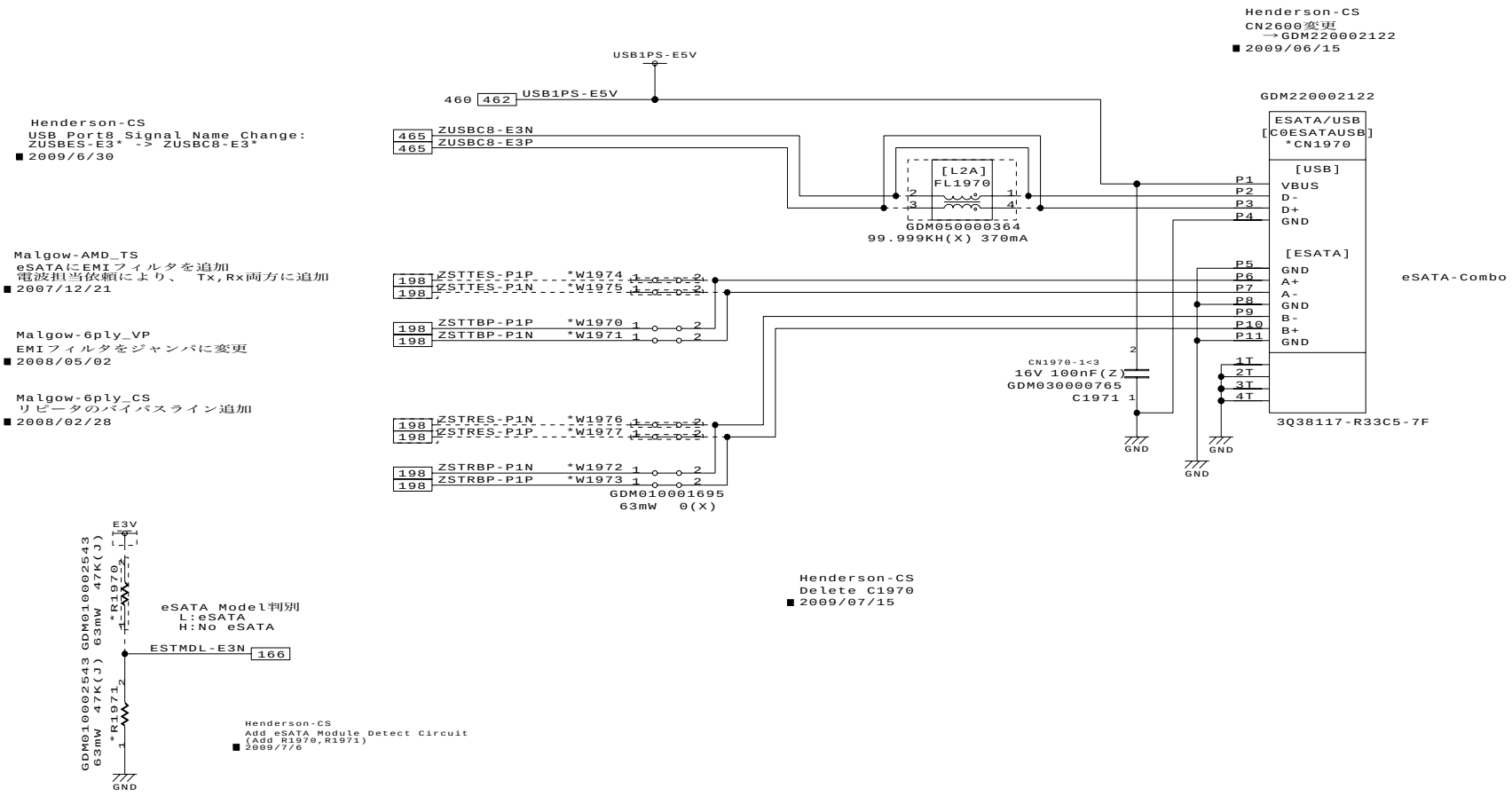
2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	SATA ODD I/F	195	038	00	360069769

2009.10.15 17:09

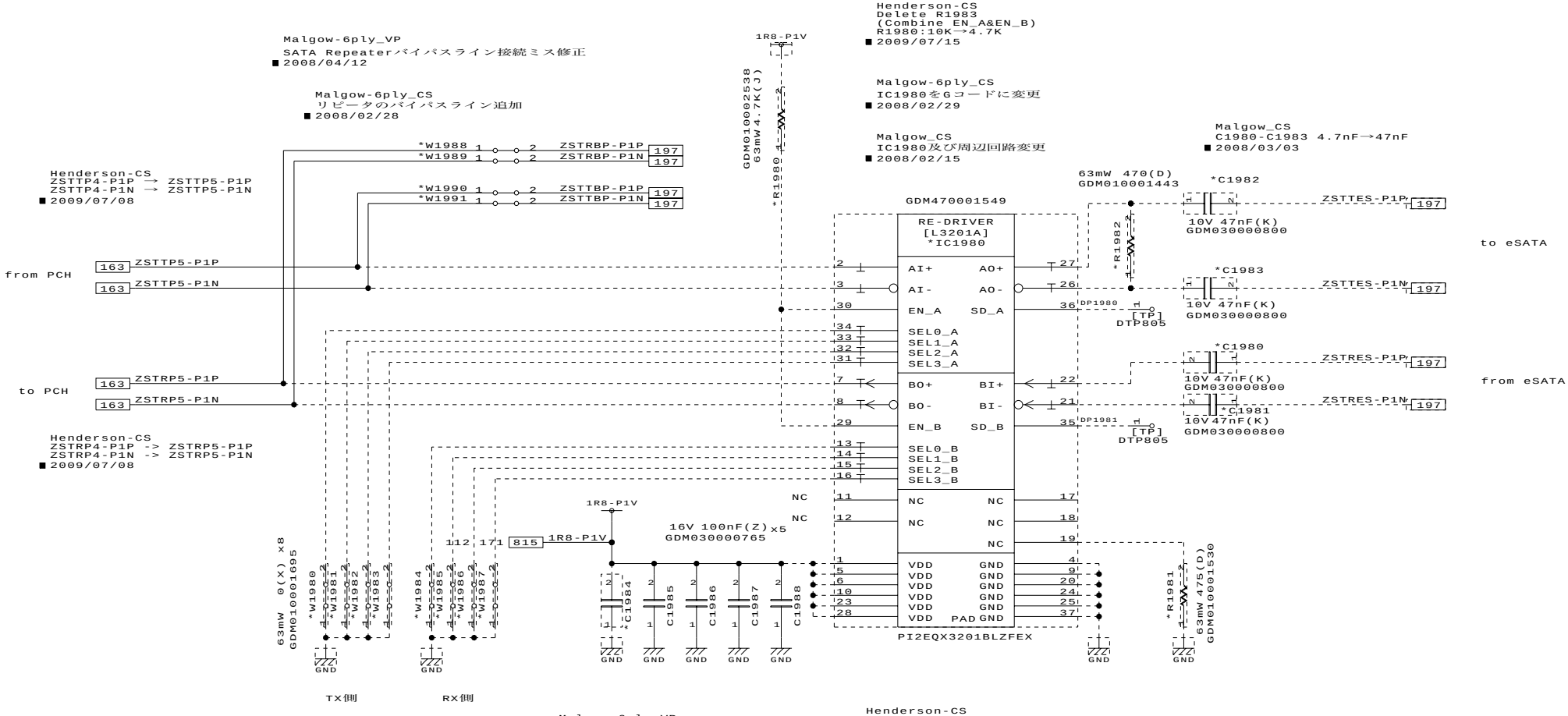
仕向け設定有



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	eSATA/USB I/F	197	039	00	360069769

2009.10.15 17:09

仕向け設定有



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	SATA REPEATER	198	040	00	360069769

2009.10.15 17:09 G11

仕向け設定有

GDM460002381

Henderson10-CS
change GDM Code.
■ 2009/07/28

PCI_OZ601 1/3
[U601A]
*IC2000

[PCI BUS I/F]
[PCI SYSTEM I/F]
[PWR CONTROL & GIP]

OZ601TN-A1-0

Henderson10-CS
Add C2001,IC2001,R2006,W2001
■ 2009/07/17

Henderson10-CS
N.M.C2001
■ 2009/07/21

Henderson10-CS
Change new PC-Card Cont.
■ 2009/07/13

Henderson10-CS
Add R2007 for Not support
PC card Gr.
■ 2009/07/19

*W2001
63mW 0(X)
GDM010001695

63mW 8.2K(J)
GDM010002539
R2006

[7SH14]
IC2001
[Vcc]
[Vss][NC]
GDM450000355
TC7SH14FU(TE85L,F)

C2001
16V 100nF(Z)
GDM030000765

*R2003
GDM010002543
47K(J) 63mW

*R2007
GDM010002543
47K(J) 63mW

PCBEEP-P3P 603

W2000
63mW 0(X)
GDM010001695
PME-E3N 165

63mW 8.2K(J)
GDM010002539
R2005

[MFNGR1]
Q2002
GDM410000233
2SK3541 T2L

[MFNGR1]
Q2001
GDM410000233
2SK3541 T2L

01Henderson10-CS
Q2001-Q2003,R2005 Not mount
■ 2009/08/17

[MFNGR1]
Q2003
2SK3541 T2L
GDM410000233

Henderson10-CS
Add Q2003 and PCNFPV-S3N
■ 2009/07/14

Henderson10-CS
Mount:Q2001-Q2003,R2004,R2005
Not mount;W2000
■ 2009/07/17

Henderson-CS
Re-assign RM2000 Pin Assignment
(layout request)
■ 2009/07/23

*RM2000
47K(J) 31mW
GDM010001731

63mW 8.2K(J)
GDM010002539
*R2004

PCNFPV-S3N 811

Def;PME#
Def;GPI0(MF0)
Def:Reserved(MF3)
Def:GPI3(MF4)
Def:Reserved(MF6)

Henderson10-CS
Delete PU of VCC3EN-P3N
■ 2009/07/17

[MFNGR1]
Q2000
GDM410000233
2SK3541 T2L
MCVCCA-PYV 210

省電力中の性能問題対策

63mW 100(J)
GDM010002530
*R2001

Ibex Peak allow to hide the PCI Devices.
B:0 D:30 F:0 Offset:40-41H
bit3:AD19(Device 3)
bit2:AD18(Device 2)
bit1:AD17(Device 1)
bit0:AD16(Device 0)

仕向け設定

PCMCIA	PC Card /CS-B	Express Card /NO
IC2000 Q2001 Q2002 Q2003 R2001 R2004 R2005 R2006 RM2000 W2001	Mount	Not mount
R2007	Not mount	Mount

VCC5#	VPP_PGM	IDSEL Connection
0	0	IDSEL Internally connected to AD18
0	1	IDSEL Internally connected to AD20
1	0	IDSEL Internally connected to AD25
1	1	IDSEL connected to Pin127

■ REF:New

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	PC CARD CONT(1)	200	041	00	360069769

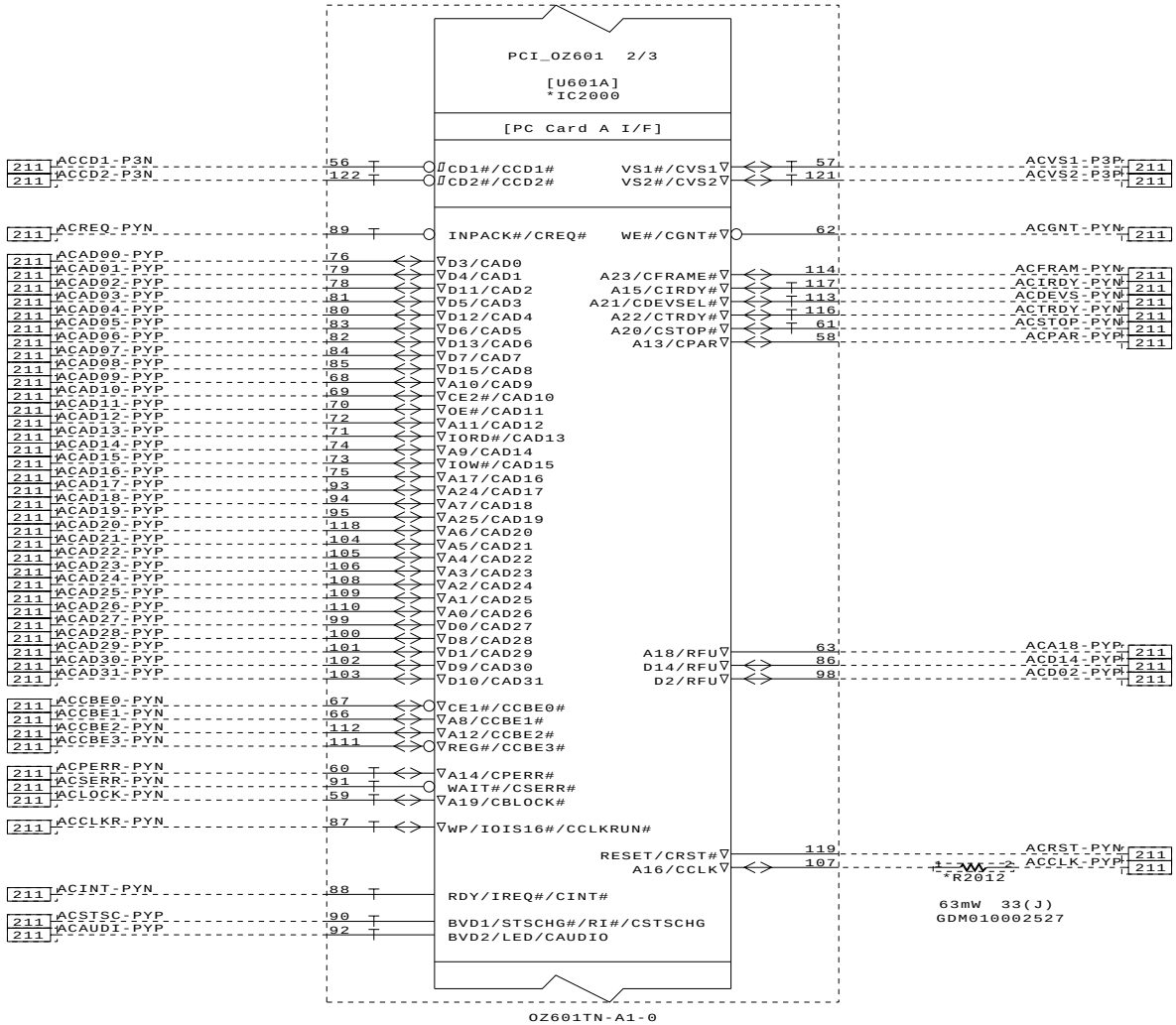
仕向け設定有

PCMCIA	PC Card /CS-B	Express Card /NO
IC2000 R2012	Mount	Not mount

Henderson10-CS
Change new PC-Card Cont.
■ 2009/07/13

Henderson10-CS
change GDM Code.
■ 2009/07/28

GDM460002381



OZ601TN-A1-0

■ REF:NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	PC CARD CONT(2)	201	042	00	360069769

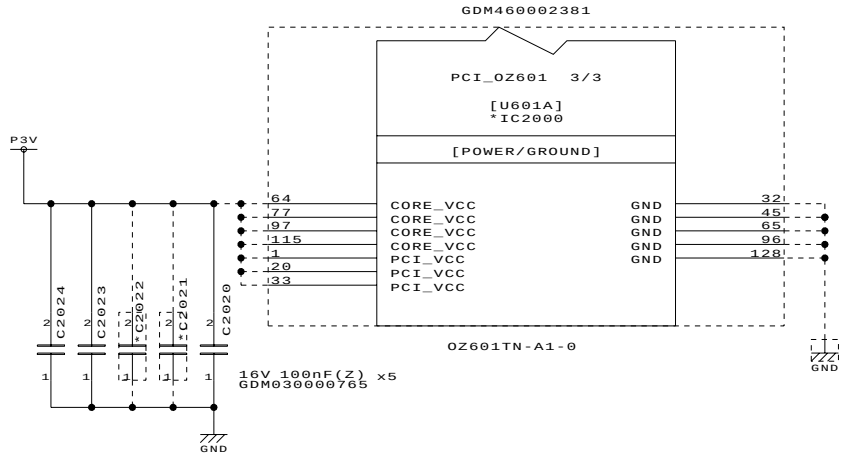
2009.10.15 17:09 G11

仕向け設定有

PCMCIA	PC Card /CS-B	Express Card /ND
IC2000	Mount	Not mount

Henderson10-CS
Change new PC-Card Cont.
■ 2009/07/13

Henderson10-CS
change GDM Code.
■ 2009/07/28



■ REF:NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	PC CARD CONT(3)	202	043	00	360069769

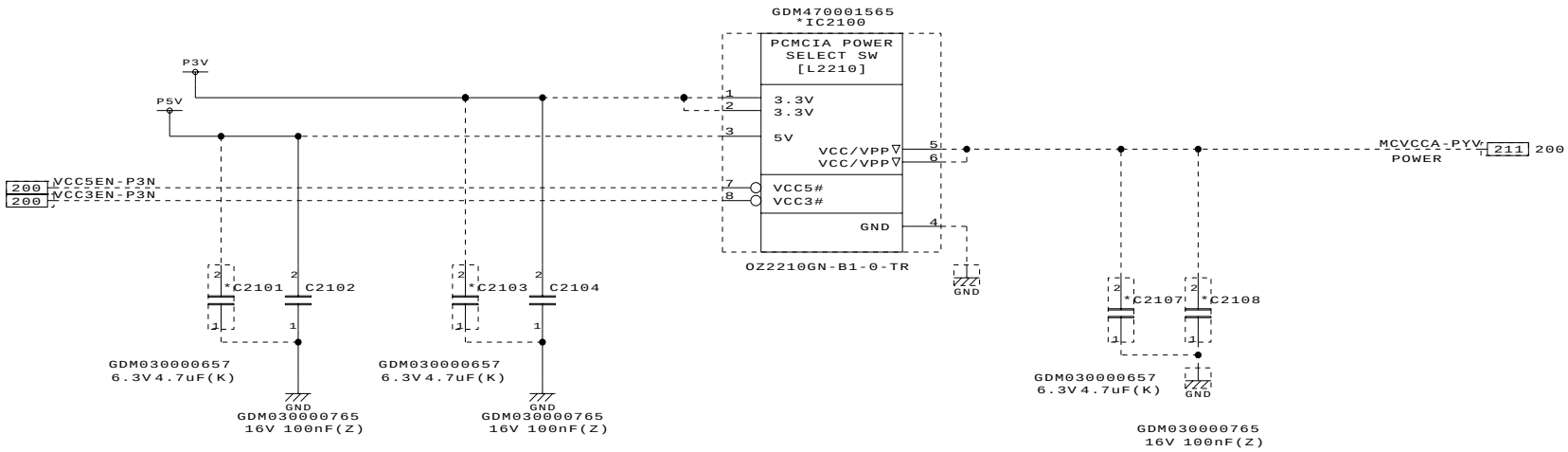
2009.10.15 17:09 G11

仕向け設定

PCMCIA	PC Card /CS-B	Express Card /NO
C2101 C2103 C2107 C2108 IC2100	Mount	Not mount

Henderson10-CS
Change new PC-Card Power SW
■ 2009/07/13

Henderson10-CS
change GDM Code.
■ 2009/07/28



■ REF:NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	PC-Card POWER	210	044	00	360069769

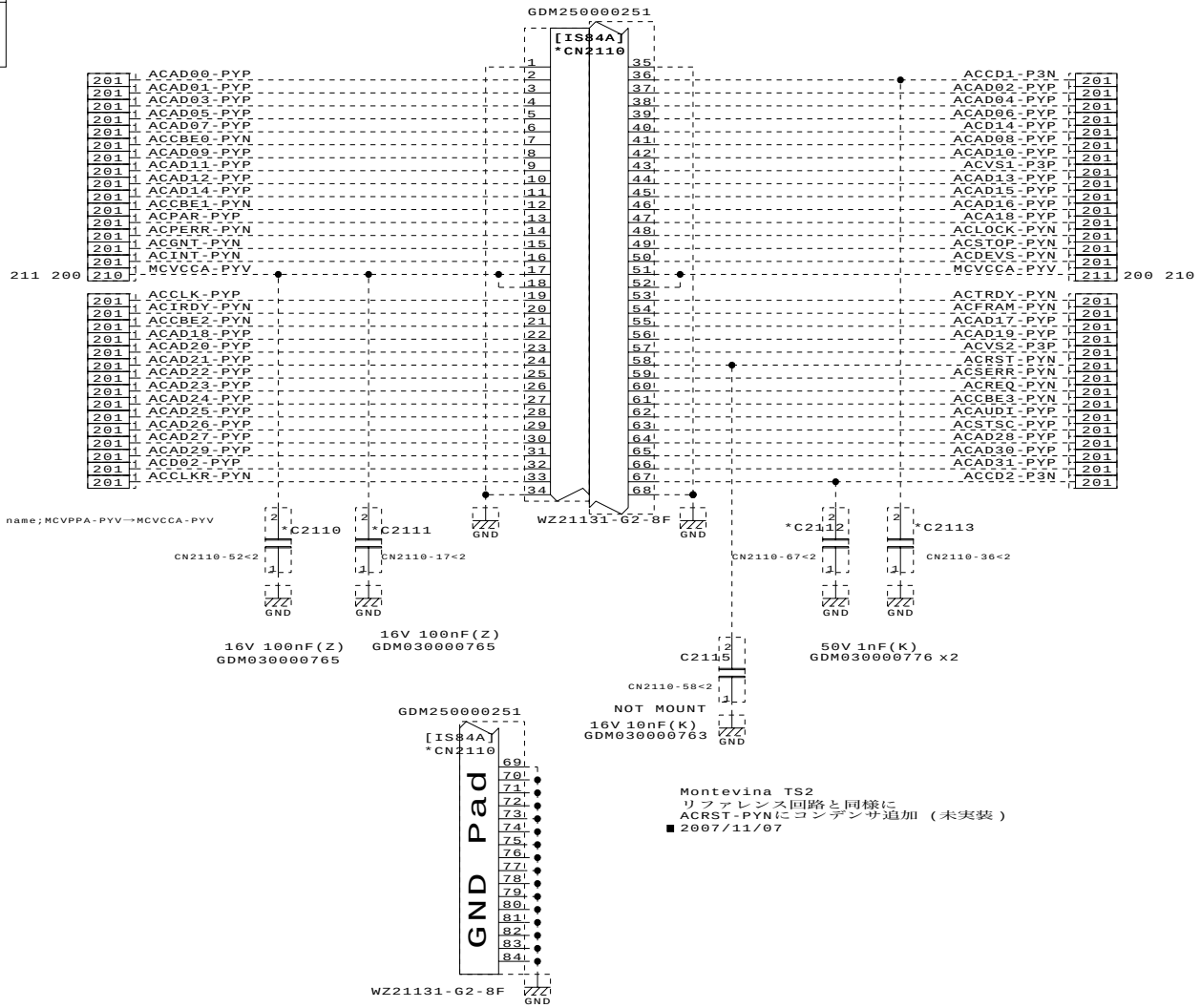
2009.10.15 17:09 G11

仕向け設定有

PCMCIA	PC Card /CS-B	Express Card /NO
C2110 C2111 C2112 C2113 CN2110	Mount	Not mount

Henderson-CS
CN2110変更
→K2500337
■ 2009/06/29

Henderson10-CS
CN2110 GDMコードに置き換え
■ 2009/08/04



Henderson-CS
Change Siganal name;MCVPPA-PYV→MCVCCA-PYV
■ 2009/7/13

Henderson10-CS
PC Card Headerを
Kコードに変更
■ 2009/03/30

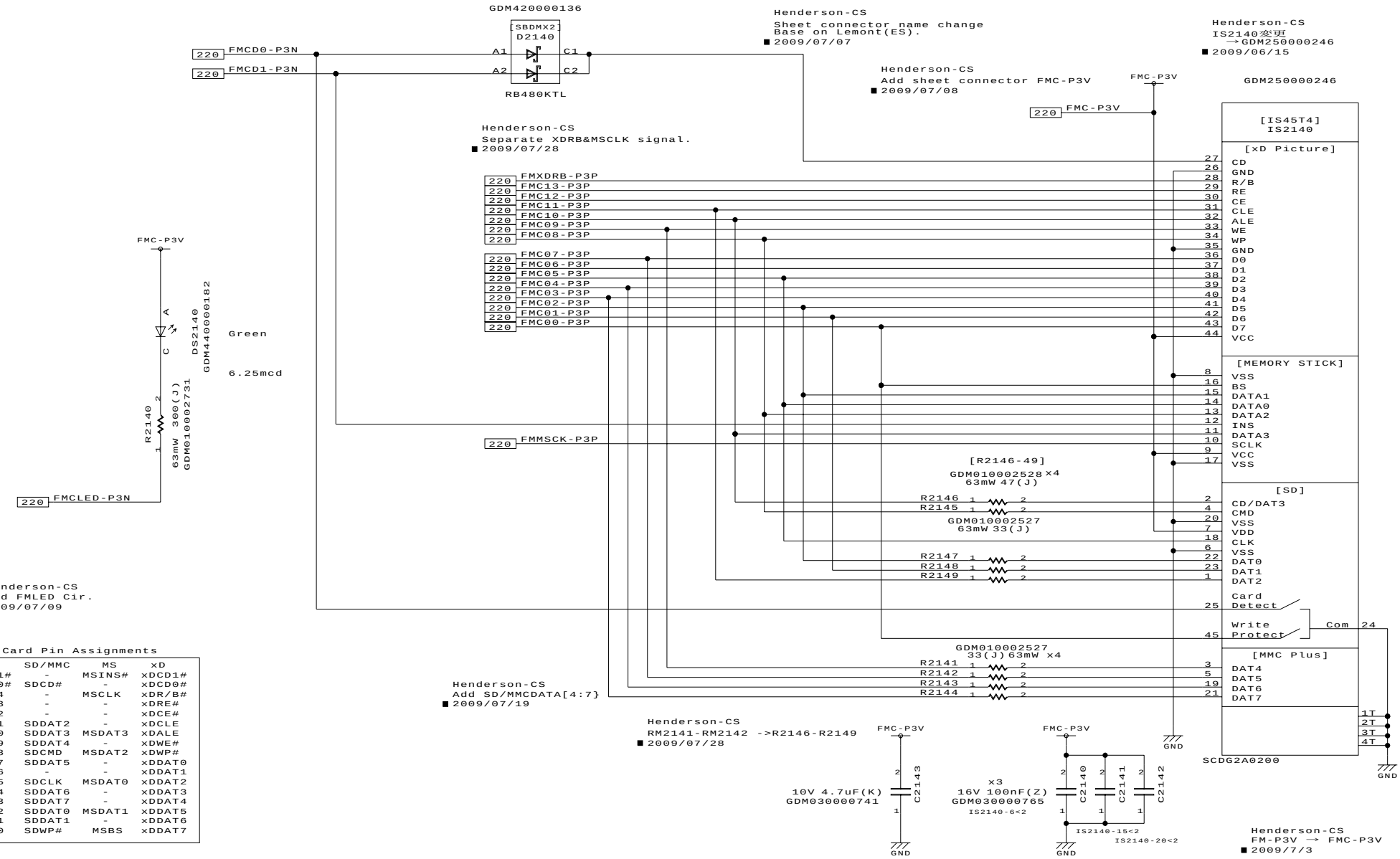
Montevina TS2
リファレンス回路と同様に
ACRST-PYNにコンデンサ追加 (未実装)
■ 2007/11/07

Henderson-CS
Add PC-Card/Ex-Card Module Detection Circuit
■ 2009/7/6

■ REF:Santa Rosa Base . FSRBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	PC-CARD I/F	211	045	00	360069769

2009.10.15 17:09 G11



Media Card Pin Assignments

	SD/MMC	MS	xD
MFCD1#	-	MSINS#	xDCD1#
MFCD0#	SDCD#	-	xDCD0#
MFC14	-	MSCLK	xDR/B#
MFC13	-	-	xDRE#
MFC12	-	-	xDCE#
MFC11	SDDAT2	-	xDCLE
MFC10	SDDAT3	MSDAT3	xDALE
MFC09	SDDAT4	-	xDWE#
MFC08	SDCMD	MSDAT2	xDWP#
MFC07	SDDAT5	-	xDDAT0
MFC06	-	-	xDDAT1
MFC05	SDCLK	MSDAT0	xDDAT2
MFC04	SDDAT6	-	xDDAT3
MFC03	SDDAT7	-	xDDAT4
MFC02	SDDAT0	MSDAT1	xDDAT5
MFC01	SDDAT1	-	xDDAT6
MFC00	SDWP#	MSBS	xDDAT7

■ REF:Lemont Base . FLNSY0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/JZ.Yu	FHNSY1	MEDIA BRIDGE I/F	214	046	00	360069769

2009.10.15 17:09 G11

仕向け設定有

Integrated Hub downstream ports

Port		Device removable
1	DP_DP/DN	Depend on MODE0/SC_LED
2	Smart Card reader	Non-removable
3	EGATE_DP/DN	Removable
4	Contactless Smart Card Reader	Non-removable

MODE0	Port 1
H	Non-removable
L	Removable

※Port 1未使用時はLに設定すること

Henderson-CS
PLTRS* Re-Assign:→PLTRS3#
■ 2009/07/13

63mW 0(X)
GDM010001695 x2

251 109 168 220 320 PLTRS3-E3N W2161 W2162-2<1
165 PCIRS1-E3N *W2162

Henderson-CS
X2160:→GDM100000382
■ 2009/08/01

Newport Smart_CS
PLTRS,PCIRSをレイアウトより波形に段が出ないように変更。接続は暫定
■ 2007/06/05

48MHz 30ppm
7X48080001
GDM100000382

[XMBA]
*X2160
OE OUT
VCC GND

Henderson-CS
Add Backup 48MHZ CLK Cir.
■ 2009/7/2

GDM010001695
0(X) 63mW x3

169 X48PCH-P3P W2170 *W2160
100 X48SCR-P3P *W2163

SMCRD-P3V
*C2160
6.3V 1uF(K)
GDM030000719
X2160-4<2
GND

63mW 470(J)
GDM010002534
217 SCDET-P3N R2177
C2169
50V 390pF(K)
GDM030000803
GND

SMCRD-P3V
*R2160
63mW 1.5K(J)
GDM010002732

GDM010001702 x4
0(X) 100mW
E5V W2169 SMCRD-P5V
P5V *W2164

(PCH)
164 ZUSBSF-E3P
164 ZUSBSF-E3N
*W2171 ZUSBFL-E3P 970
*W2172 ZUSBFL-E3N 970

E3V W2165 SMCRD-P3V
P3V *W2166

E系電源は Wake-Up要求対応の為、残しておく

Henderson-CS
Add Branch for Felica
(W2171,W2172)
■ 2009/07/21

3.3VCC:
OZ77CR6LN-B-0ではNCピン

GDM460001887
SMART CARD CONT.
[U77CR6LN]
*IC2160

RST#
48M_IN NC
MODE0/SC_LED
IOL=12mA(Max)

[SC I/F]
SC_VCC
SC_CLK
SC_RST#
SC_IO
SC_DET# SC_C4

[USB Hub/EGATE]
DP_DP
DP_DN
UP_DP EGATE_DP
UP_DN EGATE_DN

[CONTACTLESS]
RFIO_0
RFIO_1

[OTHERS]
MODE1 NC
MODE2 NC
NC

[POWER]
5.0VCC GND
5.0VCC GND
GND
3.3VCC NC
VR_CPR
VR_CPR
3V_CPR TH_PAD

OZ77CR6LN-B-0

LEDサポートしない場合シートコネクタ削除

Newport Smart_CS
LEDサポートしないのでSCLED-P3Nを削除
PD(R2165)は残す
■ 2007/06/06

*R2165 63mW 4.7K(J)
GDM010002538<- Do NOT remove.
GND SCVCC-PYV

5V/3.3V/1.8V Power & I/O

GDM010002527
*R2161 63mW 33(J) SCCLK-PYP 217
*R2162 63mW 220(J) SCRST-PYN 217
*R2163 63mW 220(J) SCIO-PYP 217
*R2164 63mW 220(J) SCC4-PYP 217
GDM010002532 x3

W2167
EGATE-P3N 217
W2168
NOT MOUNT
GDM010001695 x2
63mW 0(X)

Newport Smart_CS
EGATEに未実装ジャンパ追加
(W2167,W2168)
■ 2007/06/14

*R2178 *R2179 *R2166 *R2167 *R2168 *R2169
63mW 15K(J)
GDM010002718
x6
GND

*C2167 10V 100nF(K) GDM030000786
*C2168 10V 4.7uF(K) GDM030000840
IC2160-27<2
IC2160-27<5
GND

*C2161 10V 4.7uF(K) GDM030000840
*C2162 10V 100nF(K) GDM030000786
C2163 10V 4.7uF(K) GDM030000840
C2164 10V 100nF(K) GDM030000786
*C2165 16V 1uF(K) GDM030000733
*C2166 6.3V 1uF(K) GDM030000719
IC2160<2
IC2160-8<2
IC2160-29<2
IC2160-8<5
IC2160-6,10<2
IC2160<5

Newport Smart_CS
C2163,C2164を未実装に変更
■ 2007/06/21

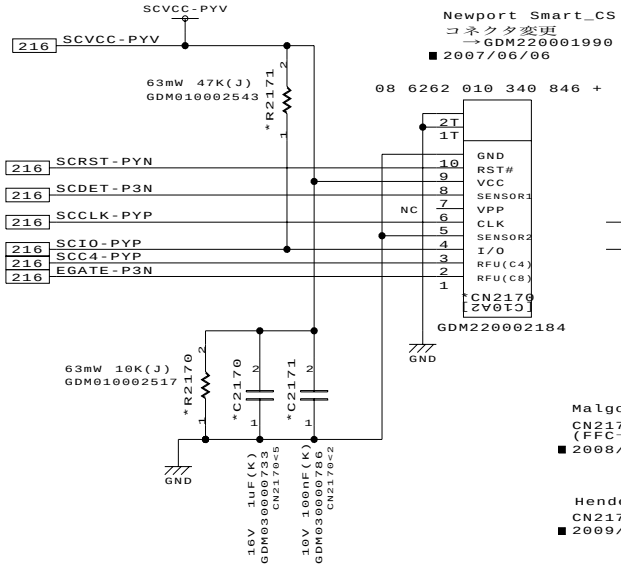
IC2160からSmart Cardコネクタまでの配線長は３インチ以内（コネクタモジュール内のフレキ配線も含む）

■ REF:NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	Smart Card Reader	216	047	00	360069769

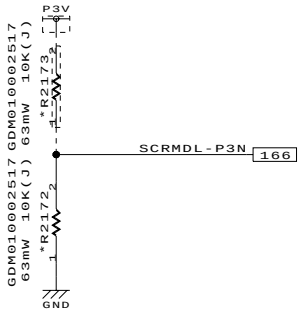
2009.10.15 17:09 G11

仕向け設定有



Smart Card Socket

Smartcard Reader
モデル判別
H:Not Support
L:Support



Malgow_VP
CN2170をGDM220002079に変更
(FFC→FPC変更対応)
■ 2008/03/24

Henderson-CS
CN2170をGDM220002184に変更
■ 2009/6/29

Henderson-CS
Change CN2170 Pin Assignment
■ 2009/07/02

Henderson-CS
Change CN2170 Pin Assignment
■ 2009/07/14

Henderson-CS
Reverse CN2170 Pin Assignment
■ 2009/07/15

Smart Card Socketのコンタクトのアサイン

Contact No.	Assignment	Contact No.	Assignment
C1	VCC	C5	GND
C2	RST	C6	VPP
C3	CLK	C7	I/O
C4	AUX1	C8	AUX2

AUX1 is used by function code (FCB) for type 2 synchronous cards (ISO/IEC 7816-10).

If an I/F device provides a USB I/F, VCC shall be used by VBUS, AUX1 by D+ and AUX2 by D-.

The use of AUX1 and AUX2 by the USB I/F does not preclude the use of these contacts by other protcols or other I/F devices defined in this International Standard series. Such use is reserved by ISO/IEC JTC1/SC17.

ISO/IEC 7816-2 AMENDMENT 1 (2004-06-01)より

■ REF:NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Smart Card I/F	217	048	00	360069769

2009.10.15 17:09 G11

IEEE1394	SUPPORT	NOT SUPPORT
W2000	MOUNT	NOT MOUNT
W2001	NOT MOUNT	MOUNT

FLNSY0 ES
IC2000 Gコードに置換え。
■ 2009/02/12
GDM460002286

FLESY0 ES
信号変更
ZERFMC-P1P/N -> ZETFMC-P1P/N
■ 2009/03/30

Henderson-CS
PLTRS* Re-Assign:→PLTRS3#
■ 2009/07/13

FLNSY0 ES
挿入。 R2005-R2017
GDM010002527
■ 2009/01/21

Henderson-CS
R2205削除
■ 2009/07/22

FLNSY0 ES
削除W2002,W2003
■ 2009/01/21

Henderson-CS
Separate the signal for XDRB&MSCLK.
Add R2218, R2219
■ 2009/07/28

Henderson-VP
XDRE#にR2220追加
■ 2009/10/15

Henderson-CS
24MHZ CLK Cir.削除
■ 2009/07/17

Henderson-CS
W2201 N.M.
XI connect to GND directly.
■ 2009/07/23

SINGLE CHIP HIGH PERFORMANCE MEDIA CONTROLLER
[U230]
IC2200

[PCI Express I/F]
RXP, RXN, TXP, TXN
REFCLKP, REFCLKN
PERST#
RREF, RXC, CPO

[Combo Slot for Media Card I/F]
MFCD0#(SDCD#/XDCD0#)
MFCD1#(MSINS#/XDCD1#)
MFIO00(SDWP#/MSBS/XDDAT7)
MFIO01(SDDAT1/XDDAT6)
MFIO02(SDDAT0/MSDAT1/XDDAT5)
MFIO03(SDDAT7/XDDAT4)
MFIO04(SDDAT6/MSDAT5/XDDAT3)
MFIO05(SDCLK/MSDAT0/XDDAT2)
MFIO06(XDDAT1)
MFIO07(SDDAT5/MSDAT4/XDDAT0)
MFIO08(SDCMD/MSDAT2/XDWP#)
MFIO09(SDDAT4/MSDAT6/XDWE#)
MFIO10(SDDAT3/MSDAT3/XDALE)
MFIO11(SDDAT2/XDCLE)
(XDCE#)MFIO12
MFIO13(MSDAT7/XDRE#)
MFIO14(MSCLK/XDR/B#)

[IEEE1394 Control/PHY I/F]
XI, XO
TPBIAS0, TPAP0, TPAN0, TPBP0, TPBN0

[User Difinable]
UDIO0, UDIO1, UDIO2, UDIO3

[Other]
TEST

[Power and GND]
VCC_3V, VCC_3V, AVCC_3V, AGND
MF_VOUT, PCIE_VOUT, PCIE_VOUT, PCIE_VIN, PCIE_VIN

[GND]
GND

R5U230-QFN48P

GDM030000786 X2 10V 100nF(K) C2200
GDM030000786 X2 10V 100nF(K) C2202
C2201
C2203
GDM10001468 63mW 5.1K(D)
GDM030000764 16V 22nF(K) C2205
GDM010002527 X12
GDM010002525X2 22(J) 63mW
GDM010001695 63mW 0(X) NOT MOUNT W2201
C2206 GDM030001116 50V 1.5nF(K)
GDM030001170 6.3V 10uF(K) C2207, C2209, C2210, C2211
GDM030000765 16V 100nF(Z) X3
GDM030001170 6.3V 10uF(K) C2208, C2212, C2213, C2214, C2215
GDM030000765 16V 100nF(Z) X4
GDM010002543 47K(J) 63mW R2202

ZETFMC-P1P, ZETFMC-P1N, XPEFMC-P0P, XPEFMC-P0N, PLTRS3-E3N
ZERFMC-P1P, ZERFMC-P1N
FMCD0-P3N, FMCD1-P3N
FMC00-P3P ～ FMC11-P3P
FMC06-P3P, FMC12-P3P, FMC13-P3P
FMXDRB-P3P, FMMSCK-P3P
FMCXRQ-P3N, FMCLED-P3N
R2206 33(J)63mW ～ R2216 33(J)63mW
R2217 33(J) 63mW
R2218, R2219
R2220 33(J) 63mW
P3V, FMC-P3V, GND, N.C.

差動信号 周波数:49.512/98.304/196.608MHz
差動特性インピーダンス :110Ω±5%
IC2000 ボール ～ 1394コネクタピン間
パターン長 :Max70mm
パターンは、基板の表層で引き、
スルーホールは使用しないこと。

Henderson-CS
Delete SRSMCK-P3P
SRSMDT-P3P Sheet connector
■ 2009/07/09

FLNSY0 ES
コメント追加
■ 2009/01/20

Henderson-CS
IEEE1394の接続を削除
■ 2009/07/09

Henderson-CS
R2204削除
■ 2009/07/15

Henderson-CS
RM2200,R2203削除
■ 2009/07/17

Henderson-CS
R2202 PU For Not used EEPROM strap.
■ 2009/07/23

(PU at PCH)

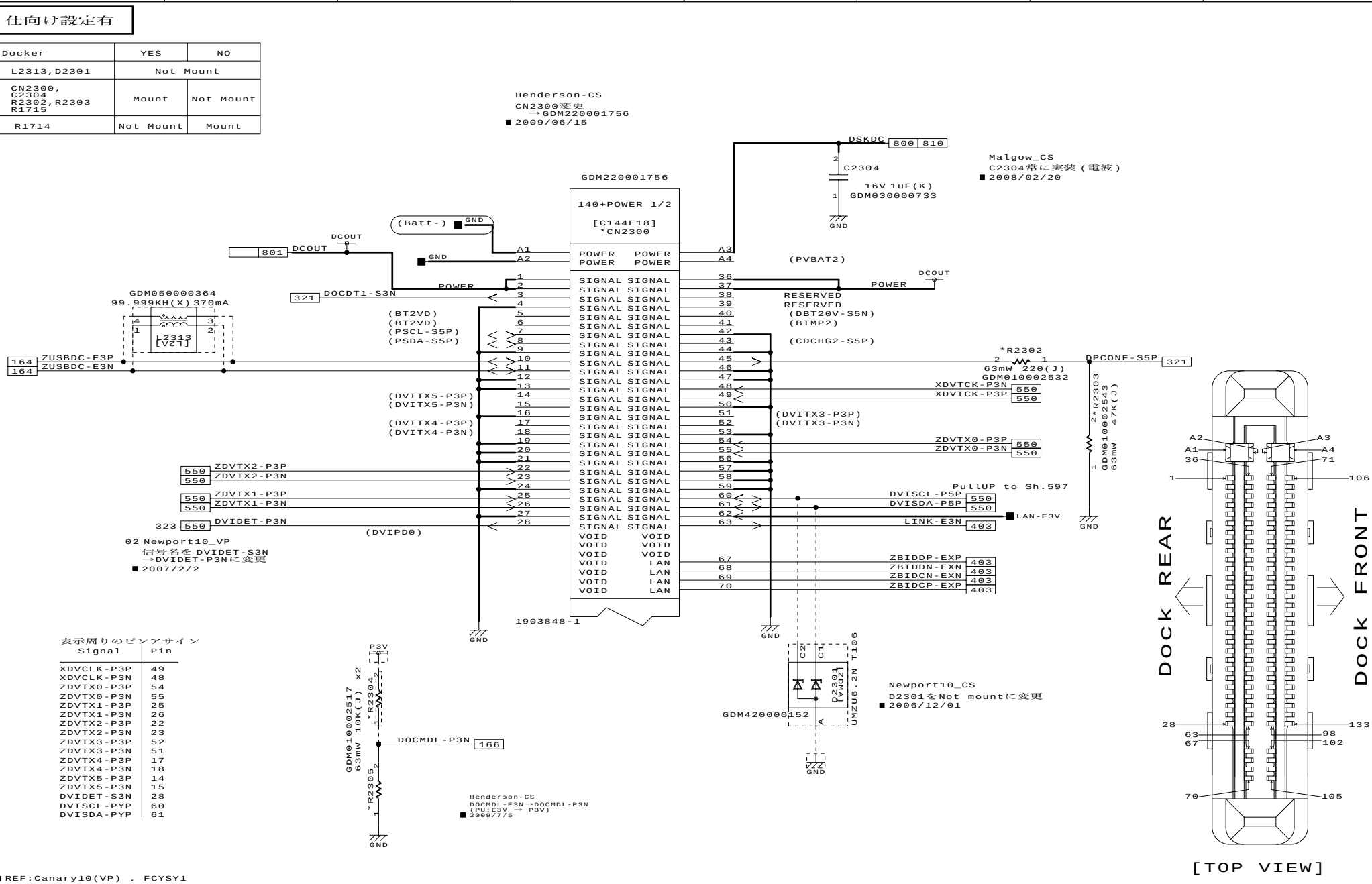
FLNSY0 ES
R2002,2003置換え
GDM010001756
-> GDM010002543
■ 2009/02/09

■ REF:Lenmont_C(ES) . FLESY0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	CARD CONT	220	049	00	360069769

仕向け設定有

Docker	YES	NO
L2313,D2301	Not Mount	
CN2300, C2304 R2302,R2303 R1715	Mount	Not Mount
R1714	Not Mount	Mount



表示周りのピンアサイン

Signal	Pin
XDVCLK-P3P	49
XDVCLK-P3N	48
ZDVTX0-P3P	54
ZDVTX0-P3N	55
ZDVTX1-P3P	25
ZDVTX1-P3N	26
ZDVTX2-P3P	22
ZDVTX2-P3N	23
ZDVTX3-P3P	52
ZDVTX3-P3N	51
ZDVTX4-P3P	17
ZDVTX4-P3N	18
ZDVTX5-P3P	14
ZDVTX5-P3N	15
DVIDET-S3N	28
DVISCL-PYP	60
DVISDA-PYP	61

■ REF:Canary10(VP) . FCYSY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/T.Naruse	FHNSY1	DOCKING I/F 1	230	050	00	360069769

2009.10.15 17:09 G11

1 2 3 4 5 6 7 8

A B C D E F

仕向け設定有

Docker	YES	NO
CN2300 C2310 R2314-R2316	Mount	Not Mount

	GM W/ Dock	GM/GL W/o Dock	PM W/ Dock
R2310-R2312	Mount	NotMount	NotMount
FL2310-FL2312	Mount	NotMount	Mount
IC2311	NotMount	NotMount	Mount

Aspen10
コネクタ GDMコードに変更
05/10/7

Canary10_CS:
コネクタ変更 GDM220001699→GDM220001756 06/2/23
コネクタ変更 GDM220001756→GDM220001699 06/2/28

[02] Canary10_VP:
コネクタ変更 GDM220001699→GDM220001756
(ピンスルー対応)TEMECO-0001096) 06/4/6

Henderson-CS
CN2300変更
→GDM220001756
■ 2009/06/15

表示周りのピンアサイン

Signal	pin
DRED-PXP	92
DBLUE-PXP	90
DGREEN-PXP	126
DHSYNC-P3P	113
DVSYNC-P3P	114
SCRTSC-P3P	96
SCRTSD-P3P	95

P3V
GDM450000354
[7SH08] IC2310
[Vcc]
[Vss]
SN74AHC1G08DCKR
C2311 GDM030000765 100nF(Z)16V
GND

323 DOCVON-S3P
*W2310
GDM010001695 0(X) 63mW
Henderson-CS Add DOCVON# Backup Cir. ■ 2009/07/22
Malgow_CS DOCVON# VA対応 反転ゲート削除、信号レベル・極性変更 S5N→S3P ■ 2008/02/14
Malgow_CS DOCVON#のシリーズ抵抗追加 (R2317) ■ 2008/03/03

950 322 PWRSW-S3N
*R2317 63mW 220(J) GDM010002532
Henderson_CS Add W2310 ■ 2009/07/23

800 810 DCOUT
GDM010002524 63mW 1K(J) *R2314

GDM220001756
140+POWER 2/2
[C144E18]
*CN2300
1903848-1

DILSON-S3P
PHYRST-E3N
Aspen10 1394無し 処理 05/10/7
RESERVED
RESERVED
(GND)
(GND)
(GND)
(GND)

541 160 IDSDA-P3P
541 160 IDSCL-P3P
403 ACT-E3N
403 ZBIDBN-EXN
403 ZBIDBP-EXP
403 ZBIDAN-EXN
403 ZBIDAP-EXP

GDM420000152 UMZU6.2N T106
D2310 [VDD2]

FCELLA-S5P
FCELLB-S5P
FCELLC-S5P
FCELLD-S5P

GDM010002524 63mW 1K(J) *R2315 EJCTRQ-S3N 321
M5V 809 802
*R2316 GDM010000963 47(J) 250mW
*C2310 GDM030000765 100nF(Z) 16V
各EMI Filterは Connector近くへ配置

DHSYNC-P3P 542
DVSYNC-P3P 542
ZDTPA-P3P
ZDTPA-P3N
ZDTPB-P3N
ZDTPB-P3P

GDM130000213 [FLA] *FL2311
DGREEN-PXP 540
SSCL-S5P 321
SSDA-S5P 321
DOCDT2-S3N 321

RGBGNDはDOCK Connectorのフレーム GNDと近いところで接続する。
Malmo_CS GM W/ Dockの仕向けをA-SW使用に変更 ■ 2008/2/15

540 DBLUE-PXP [FLA] *FL2312
GPU->DOCK Connectorまで、RGB信号の隣接層はGNDとする。
NFW31SP207X1E4L
GDM130000213 X2

540 DRED-PXP [FLA] *FL2310 GDM010001458 63mW 2K(D)

*R2310 *R2311

Filterのグランドパットから最短でGND層に接続。

GDM010001458 2K(D) 63mW *R2312

[ZDMA] D2311 UMZU6.2N T106 GDM420000152

Henderson-CS PSCL-S5P → SSCL-S5P PSDA-S5P → SSDA-S5P ■ 2009/07/21
Henderson-CS Add D2311(N.M.) ■ 2009/07/15
Henderson-VP Q2310,R2318を削除 ■ 2009/10/06
Henderson-CS IC2311をゲートから FETに変更 ■ 2009/07/15

■ REF:Malgow(VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/T.Naruse	FHNSY1	DOCKING I/F 2	231	051	00	360069769

2009.10.15 17:09 G11

Nur20MA:CRDSTB#に1ゲート追加，プルアップは削除
(内部プルアップ対策) 2004/9/1
Nur20MA:上記のゲート ICをNot Mountにする 2004/9/22

/SHDN制御
強制的にカード電源を
0Vにする(Discharge)

/STBY制御
S0 state中にD3 cold
にする
Primary power=off

GPO制御
デフォルト H

SPA20 CS:
SHDN#を制御する
7SH08でゲート
2004/6/11 落合

CRDSTB-P3NのPull up電源をE3VからP3V
に変更および抵抗値も 10K→47kに変更し、
MOUNTする。

R2520 47K(J)→4.7K(J)に変更，
IC2510 /SHDN,/STBY,.SYS_RESET
信号にPUの記号追加
2004/7/27 落合

SPA20 CS:
IC2512追加 2004/6/11 落合

63mW 47K(J) S3V
GDM010002543
*R2510

SN74AHC1G08DCKR
GDM450000354

CPPE-E3N
CPUSB-E3N
[7SH08]
*IC2512
[Vcc]
[Vss]
S3V
GND
CRDCPA-S3N 323
to ECKBC

E3V→S3Vに変更
2004/6/21 落合

Henderson-CS
Add R2510(PU to S3V)
■ 2009/07/15

Madeira10:CRDSHD-S3N → CRDSHD-E3N ゲートを削除して直結 05/5/11

Henderson-CS
PLTRS* Re-Assign:→PLTRS3#
■ 2009/07/13

ExpressCard Spec.
Pull up:100k-200k
TPS2231 Internal Pull up
150K typ. 110k min.

TC7SH14FU(TE85L,F)
GDM450000355
[7SH14]
*IC2514
RCLKEN-E3P
RCLKEN-E3N
E3V
GND

SN74AHC1G32DCKR
GDM450000356
[7SH32]
*IC2511
CRDCRQ-E3N
P3V
CRDXRQ-P3N 169
to CLKGEN

GDM470000633

Dubai10:CRDSTB-P3N削除 05/8/11

TPS2231
[L2231]
*IC2510

167 CRDSHD-E3N
251 252 CPPE-E3N
251 252 CPUSB-E3N
216 109 168 220 320 PLTRS3-E3N
1R5-P1V
P3V
E3V

/SHDN
/STBY
/CPPE
/CPUSB
/SYSRST
/OC
RCLKEN
/PERST
1.5VIN
1.5VOUT
3.3VIN
3.3VOUT
AUXIN
AUXOUT
[NC]
[GND]
[TH_PAD]

CRDRST-E3N 252
POWER CRD1R5-P1V 252
POWER CRD-P3V 252
POWER CRD-E3V 252

Express Card Spec (MAX)	Current Limit IC Spec(MAX)
650mA	1300mA
1300mA	2500mA
275mA	600mA

シートコネクタのコメントに記入

Newport10 CS
信号名変更
PLTRS2-E3N→PLTRS3-E3N
■ 2006/10/23

*C2515 *C2516 *C2517
6.3V4.7uF(K)
GDM030000657X3
TPS2231PWPR

*C2518 *C2519 *C2520 6.3V4.7uF(K)
GDM030000657X3

Madeira10:
PLTRS0-E3Nにプルダウン抵抗追加 (内臓プルアップによる電圧浮き上がり対策) 05/7/4
※ICH6のVCCSUS3_3の立ち上がりから VCCSUS1_5の立ち上がりまでの間に浮く
立ち上がりが同時なら必要なし

Dubai10:プルダウンを削除 05/8/11

正規シンボルに変更、 R2520追加 2004/4/27 落合

引き抜き抵抗削除

仕向け設定有

PCMCIA	PC Card	Express Card
IC2510 IC2511 IC2512 IC2514 C2515 C2516 C2517 C2518 C2519 C2520	Not mount	Mount

■ REF:Maderia10GC(VP), FDRSY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	EXPRESSCARD	251	052	00	360069769

2009.10.15 17:09 G11

仕向け設定有

PCMCIA	PC Card	Express Card	NO	CS-B
R2520 R2522 RM2520 C2521 C2523 C2525 C2526 C2527 C2528	Not mount	Mount	Not mount	Mount
R2521	Mount	Not mount	Mount	Not mount
IS2520	Not mount	Mount	Not mount	Not mount

Henderson-CS
RM2520分解
■ 2009/08/03

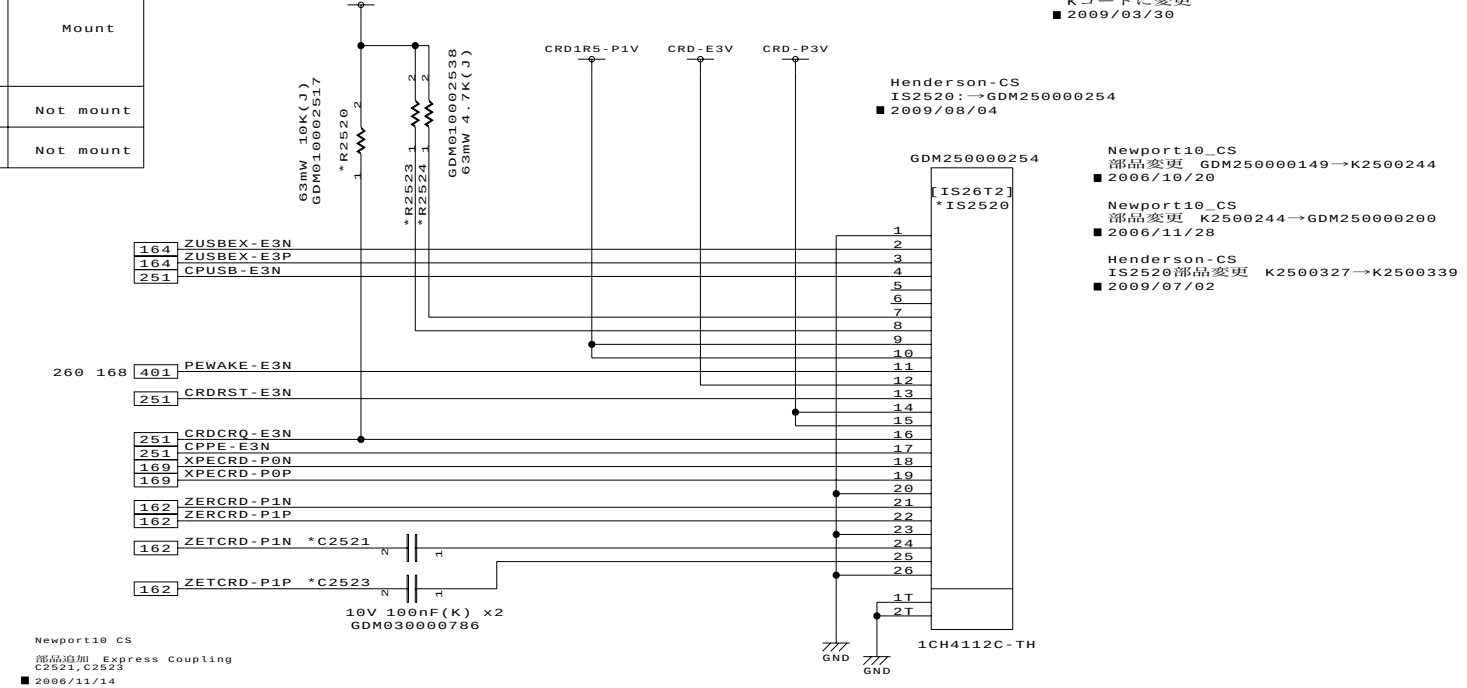
Henderson10-CS
Express Card Headerを
Kコードに変更
■ 2009/03/30

Henderson-CS
IS2520:→GDM250000254
■ 2009/08/04

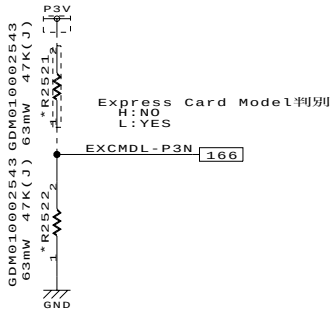
Newport10_CS
部品変更 GDM250000149→K2500244
■ 2006/10/20

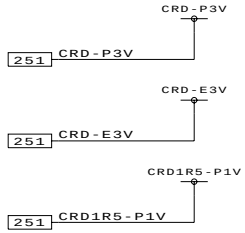
Newport10_CS
部品変更 K2500244→GDM250000200
■ 2006/11/28

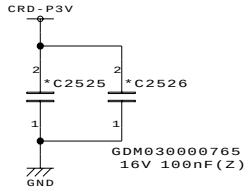
Henderson-CS
IS2520部品変更 K2500327→K2500339
■ 2009/07/02

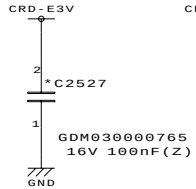


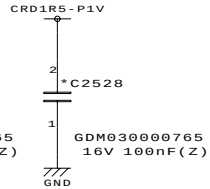
Newport10 CS
部品追加 Express Coupling
C2521,C2523
■ 2006/11/14











ExpressCard サポート時は本 Sheetの
全ての部品を Mountする
関連Sheet:162,169,251

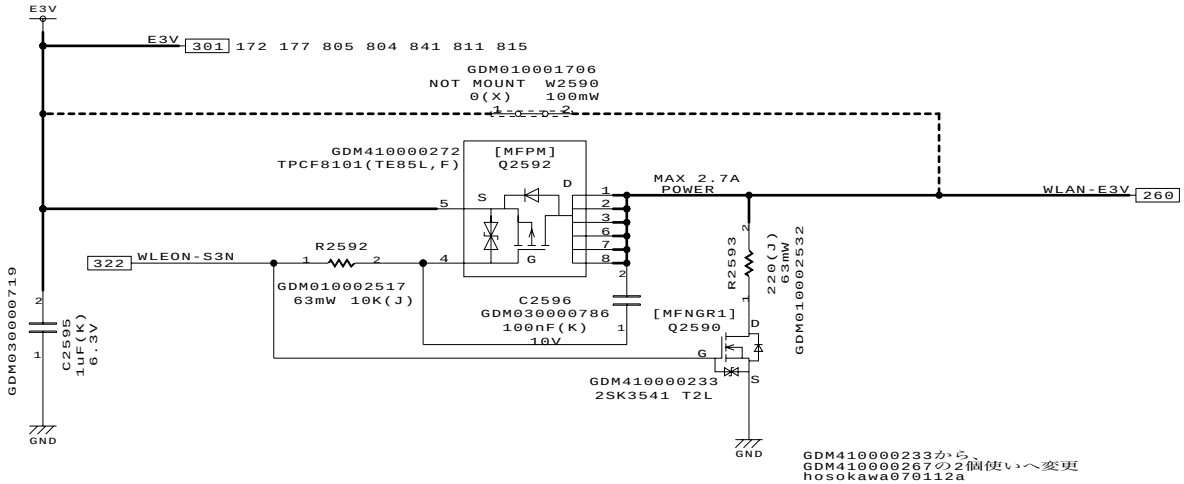
■ REF:Utah10 (VP) . FUTS81

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	EXPRESSCARD I/F	252	053	00	360069769

2009.10.15 17:09 G11

WLAN/WiMAX電源

TOSHIBA CONFIDENTIAL



PCI Express(TM) Mini Card Electromechanical Specification
Rev 1.0(June 2,2003)より

Power Rail	Voltage Tolerance	D0-D2 D3 hot Power(*)		Auxiliary Pow(**)	
		Peak(max)mA	Normal(max)mA	Peak(max)mA	Normal(max)mA
3.3Vaux	±9%	2750	1100	1750 (wake enabled)	250(wake enabled) 5(not wake enabled)
+1.5V	±5%	500	375	N/A	N/A

*:When available,the total power drawn by a PCI Express Mini Card function for the sum of +3.3V and 3.3Vaux shall not exceed 750mA(Normal max) and 1,000mA(Peak max)

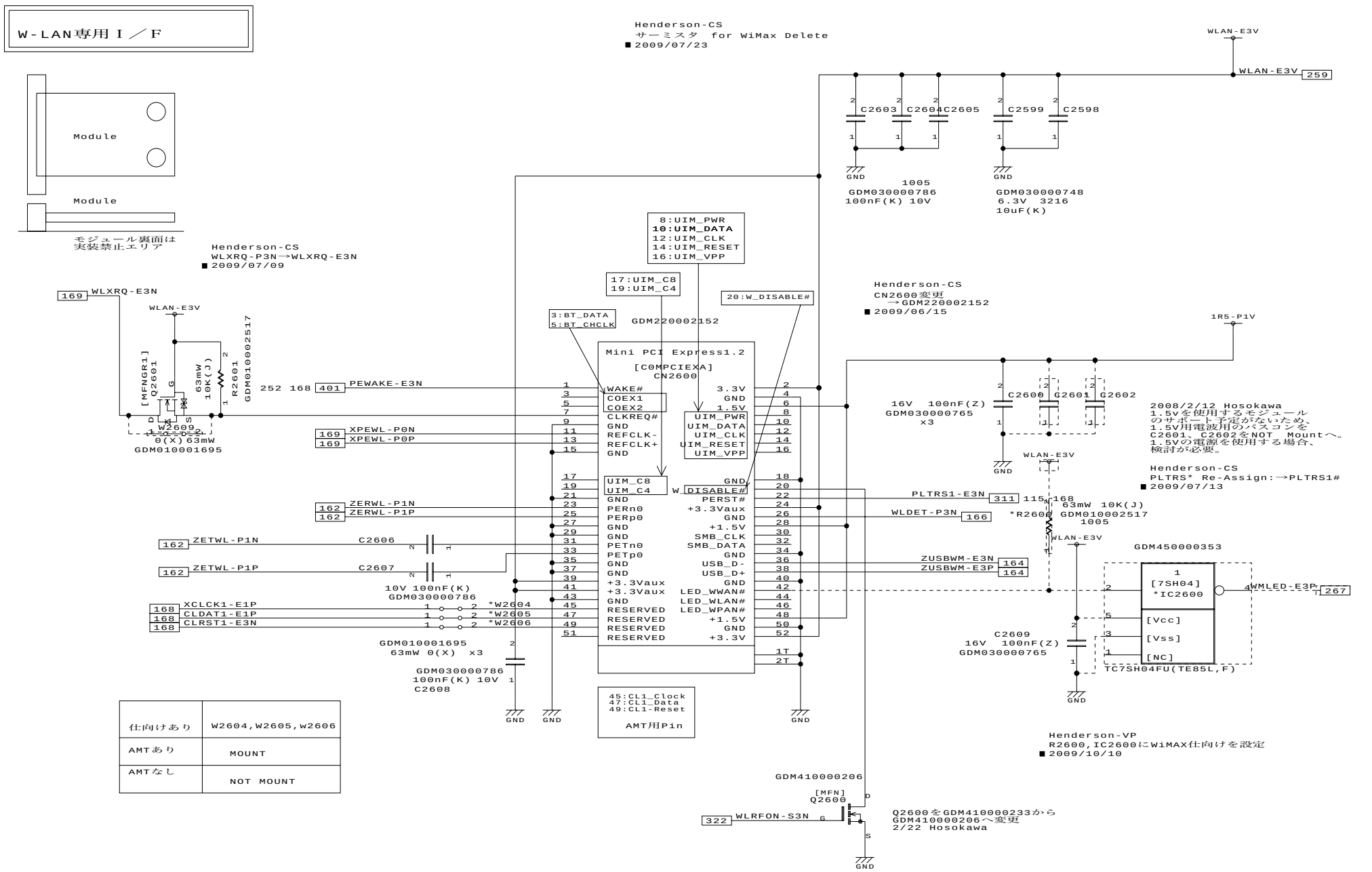
**:The auxiliary current limit only applies when the primary +3.3V and +1.5V Vvoltage sources are not available; i.e.,the card is in a low power D3 state.

Definitions:
Peak - The highest averaged current value over any 10-millisconds period.
Normal - The highest averaged current value over any 1-second period.

■ REF:Moldau10(CS) . FMUSY0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Hosokawa	FHNSY1	WLAN／WiMAX Power	259	054	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Hosokawa	FHNSY1	PCI-E MINI CARD	260	055	00	360069769

2009.10.15 17:09

3G Module

TOSHIBA CONFIDENTIAL

仕向け設定有

3G/Robson	YES	NO
R2610	Mount	Mount
W2614	Not Mount	Not Mount
上記部品以外全て	Mount	Not Mount

3G MODULE
消費電流 Peak 2.5A (実測値)
電流変動 0.3A～2.5A(実測値)

GDM010001706
NOT MOUNT W2614
0(X) 100mW

GDM410000272
TPCF8101(TE85L,F)
[MFPM]
*Q2611

*R2611
GDM010002517
63mW 10K(J)

*C2619
GDM030000786
100nF(K)
10V
[MFNGR1]

*R2612
220(J)
63mW
GDM010002532

*Q2612
GDM410000233
2SK3541 T2L

261 323 3GON-S3N

GDM030000719
*C2618
1uF(K)
6.3V

Canary10 CS:
C7700(パスコン) 追加
2006/2/24

POWER

Malmo_CS
C7700削除
■ 2007/11/20

*C2616 *C2617 *C2613 *C2614

GDM030000748
10uF(K) 6.3V x2

GDM030000765
100nF(Z) 16V
x3

UIMPWR-E3V 957
UIMDAT-E3P 957
UIMCLK-E3P 957
UIMRST-E3P 957

PLTRS2-E3N 340 300 168 177 330
MINDET-P3N 166

Henderson-CS
PLTRS* Re-Assign:→PLTRS2#
■ 2009/07/13

Malmo_CS
UIM-GNDを15pinから18pinに変更
■ 2007/11/20

ZUSB3G-E3N 164
ZUSB3G-E3P 164
UIM-GND

UIM-GNDを通常のGNDにしました (アンワル)
■ 2008/1/22

UIM-CLK&UIM-Data
UIM-CLKとUIM-Dataの信号はできるだけ(信号ライン2分以上)離して(間にGNDパターンを挟んで)引いて頂きたい。(アンワル)
■ 2008/1/22

Henderson-VP
アナログGND分離用ジャンパ削除による信号名変更
■ 2009/10/15

NCP15WF104F03RC
GDM090000050
100mW
[THM]
*MT2611

SENSE LINE
TMP3G-SXP 322
GND 310 335 115 144 322 336

ペアで平行に引く

サーミスタの実装位置は 3Gコネクタと同一面。
また、(ＩＰ設８) 殿の指示に従うこと。

Malgow_VP
サーミスタ GNDをGNDに変更
■ 2008/03/19

Henderson-CS
GND -> TMP3G-GND
■ 2009/07/08

Henderson-CS
CN2610変更
→GDM220002152
■ 2009/06/15

8:UIM_PWR
10:UIM_DATA
12:UIM_CLK
14:UIM_RESET
16:UIM_VPP

17:UIM_C8
19:UIM_C4

20:W_DISABLE#

3:BT_DATA
5:BT_CHCLK

GDM220002152

Mini PCI Express1.2
[COMPCIEXA]
*CN2610

1	WAKE#	3.3V	2
NC 3	COEX1	GND	4
NC 5	COEX2	1.5V	6
7	CLKREQ#	UIM_PWR	8
9	GND	UIM_DATA	10
11	REFCLK-	UIM_CLK	12
13	REFCLK+	UIM_RESET	14
15	GND	UIM_VPP	16
NC 17	UIM_C8	GND	18
NC 19	UIM_C4	W_DISABLE#	20
21	GND	PERST#	22
23	PERn0	+3.3Vaux	24
25	PERp0	GND	26
27	GND	+1.5V	28
29	GND	SMB_CLK	30 NC
31	PETn0	SMB_DATA	32 NC
33	PETp0	GND	34
35	GND	USB_D-	36
37	GND	USB_D+	38
39	+3.3Vaux	GND	40
41	+3.3Vaux	LED_WWAN#	42 NC
43	GND	LED_WLAN#	44 NC
NC 45	RESERVED	LED_WPAN#	46 NC
NC 47	RESERVED	+1.5V	48
NC 49	RESERVED	GND	50
NC 51	RESERVED	+3.3V	52
			1T
			2T

AS0B226-S40T-7H

Henderson-CS
Robson I/F削除
■ 2009/7/3

Gr.		
3G	有	無
C2613, C2614 C2616, C2617 C2619, C7700 CN2610 MT2611, Q2610 Q2611, Q2612 R2611, R2612	MOUNT	NOT MOUNT

MOUNT
DTP805
DP9301

322 RFCANT-S3N

Ultimate
リコンフィグアンテナ制御コネクタ
■ 2008/07/03

Malgow_CS
Q2613を未実装
(G-S逆転、VPで修正)
■ 2008/03/06

37,43pinをGNDに追加
06/04/07

Malgow_CS
C2611常に実装(電波)
■ 2008/02/20

■ REF:Newport10(VP) . FNPSY1

E3V
GDM410000206
*Q2610
323 3GRFON-S3N
*C2615
GDM030000765
FETをGDM410000233-->GDM410000206に変更
20071213 Anwar

*R2613
GDM010002517
63mW 10K(J)
1005
261 323 3GON-S3N

GDM450000391
[7SH02]
*IC2610
[Vcc]
[Vss]
3GLED-E3P 267

*C2612
GDM030000765
16V 100nF(Z)

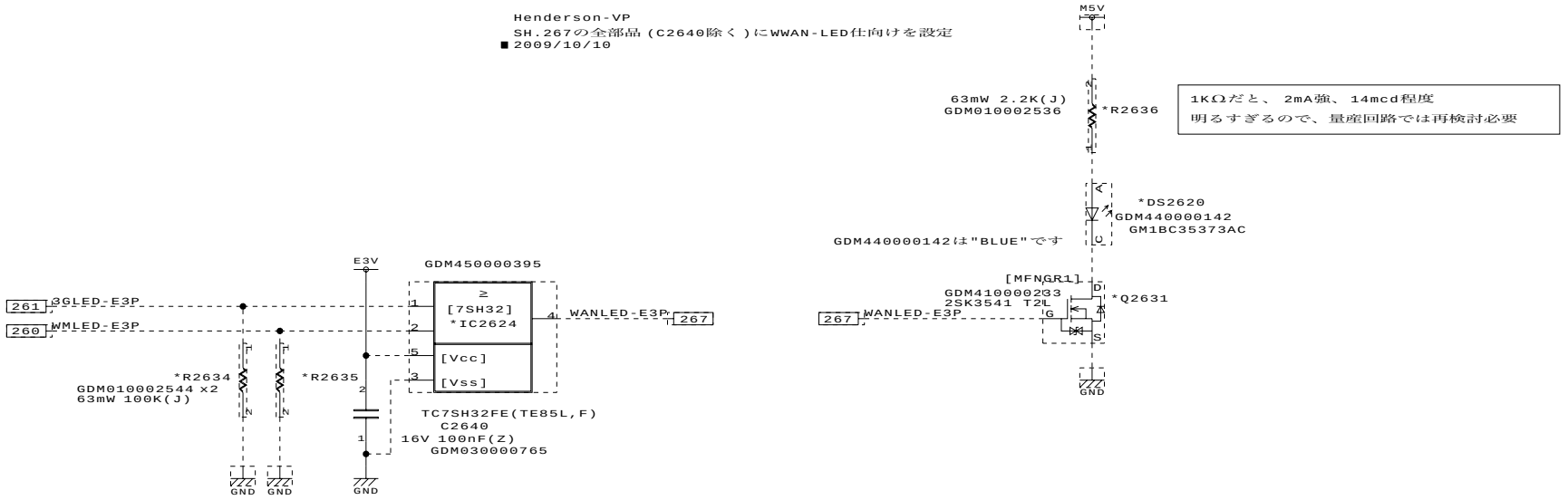
3G用PCI-E Mini Cardソケットと
SIM CARD ソケット間のライン長は２００mm以内として下さい

Henderson-CS
リファレンス変更 :C2620→C2612(重複)
■ 2009/08/04

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Anwar/T.OCHIAI	FHNSY1	PCI-E MINI Card(3G)	261	056	00	360069769

2009.10.15 17:09 G11

Henderson-VP
SH.267の全部品（C2640除く）にWWAN-LED仕向けを設定
■ 2009/10/10



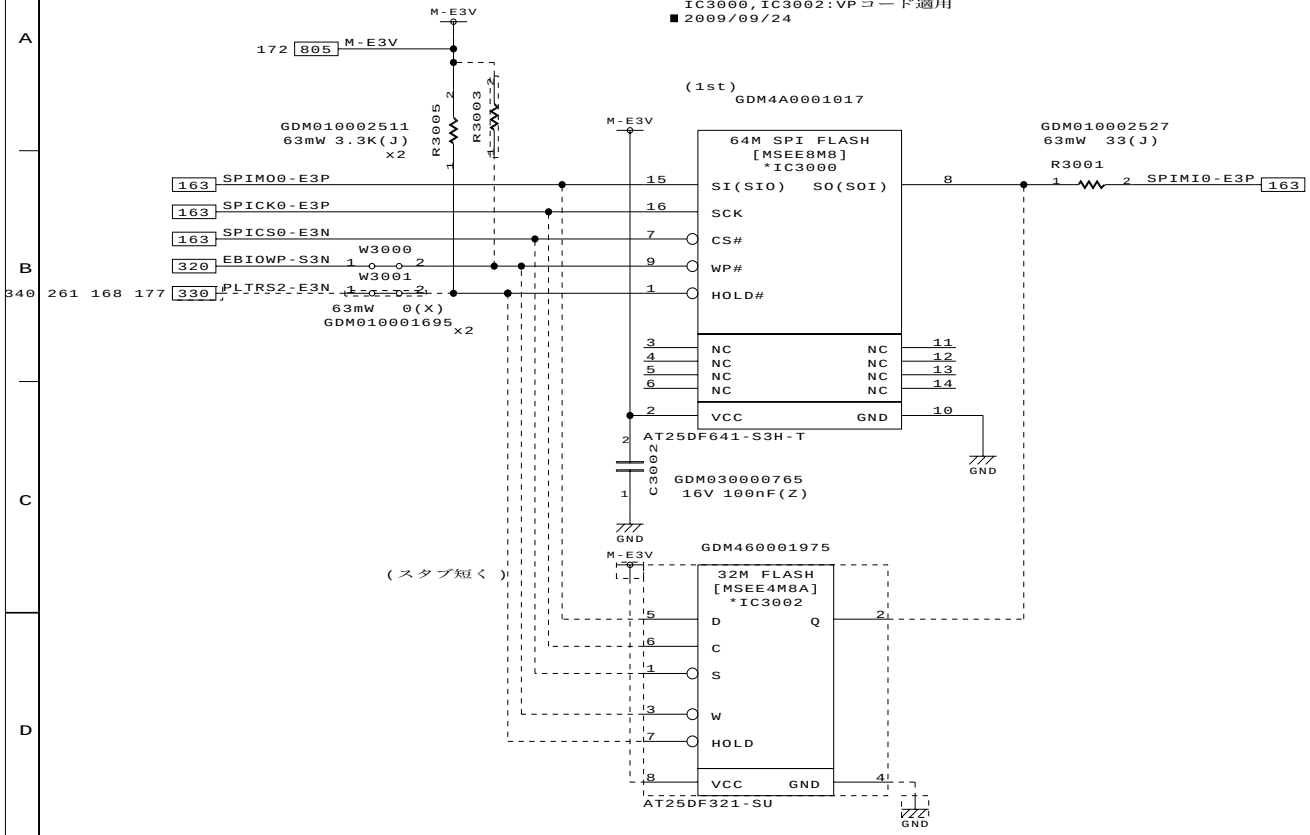
Ref:Wengen CS

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 H.Gushiken	FHNSY1	3G(WWAN) LED	267	057	00	360069769

2009.10.15 17:09 G11

仕向け設定有

Henderson-VP
IC3000,IC3002:VPコード適用
■ 2009/09/24



Henderson10/10C F/W Code

	AMT	NonAMT-Intel	NonAMT-Realtek
CS	GDM4A0001009 F0100901 FP	GDM4A0001013 F0101301 FT	GDM4A0001014 F0101401 FU
VP	GDM4A0001017 F0101701 FV	GDM4A0001018 F0101801 FW	GDM4A0001019 F0101901 FX

(SPI_Part No:64Mbit/GDM460002384
32Mbit/GDM460001975)

64Mbit: 16pin

32M/16Mbit: 8pin

~~Dual Footprint要検討~~

Henderson-CS
Delete Backup 2nd SPI Circuit
■ 2009/07/21

Henderson CS
Add Backup 2nd SPI circuit
■ 2009/6/27

Henderson-CS
PLTRS* Re-Assign:→PLTRS2#
■ 2009/07/13

Henderson-CS
IC3000をGコード(生ROM)に変更
■ 2009/08/01

	Ibex Peak	ME Function
16Mbit	PM55	(ME Ignition FW)
32Mbit	HM55,PM57,HM57	PAVP1.5,AT-p,Braidwood
64Mbit	PM57,HM57	PAVP1.5,AT-p,Braidwood,RPAT for Consumer
	QM57	PAVP1.5,AT-p,Braidwood,vPro 6.0 with RPAT,AT-d

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	FLASH ROM	300	058	00	360069769

2009.10.15 17:09

仕向け設定有

MDC	有り	無し
CN3010 R3010,R3011 C3011,C3012 IC3010	Mount	Not Mount
L3010 C3010	Not Mount	

Henderson10GC-VP
WebCam用フィルタのレイアウト・スペース確保の為、　L3010を削除
■ 2009/10/10

Hamilton10 CS
CN3010をコネクタリスト
Rev01にあわせる
GDM220001530→GDM220001498
■ 2006/9/26

Hamilton10 CS
CN3010をコネクタリスト
Rev02にあわせる
GDM220001498→GDM220001721
■ 2006/10/24

Hamilton10 CS
CN3010をコネクタリスト
Rev03にあわせる
GDM220001721→GDM220001530
■ 2006/11/2

Hamilton10 CS
CN3010をコネクタリスト
Rev07にあわせる
GDM220001721→GDM220001530
■ 2006/11/24

Hamilton10 CS
CN3010をコネクタリスト
Rev06にあわせる
GDM220001530→GDM220001721
■ 2006/11/16

Hamilton10 CS
CN3010をコネクタリスト
Rev08にあわせる
GDM220001530→GDM220001721
■ 2006/12/4

259 172 177 805 804 841 811 815 E3V

E3V

C3010

Henderson-CS
CN3010変更
→GDM220001498
■ 2009/06/15

Hamilton10 CS
CN3010をコネクタリスト
Rev10にあわせる
GDM220001721→GDM220001530
■ 2006/12/7
ECO-0002028

GDM220001498

C3011

6.3V 10uF(K)
GDM030000748

16V 100nF(Z)
GDM030000765

MDC1.5 CONNECTOR
[C0MDCB]
*CN3010

1	GND	RESERVED	2
3	SDO	RESERVED	4
5	GND3.3Vmain/aux		6
7	SYNC	GND	8
9	SDI	GND	10
11	RST#	BCLK	12
			1T
			2T
			3T
			4T
			5T
			6T

164 AZSDO1-E3P
164 AZSYC1-E3P
164 AZRST1-E3N
*R3010
XAZBC1-E3P 164

63mW 39(J)
GDM010002725

*R3011

63mW 47K(J)
GDM010002543

1-179397-2

GND GND GND

GDM470001567

Henderson10-CS
IC3010 GDMコードに置き換え
■ 2009/08/04

ANALOG SW
[L66A]
*IC3010

(PCH_GPIO)
167 MDMEN-E3P

A B OE VCC GND

AZSDI1-E3P 164

TC7SBL66CFU(T5L,F)

Henderson-CS
GDM470001211->K4702296
■ 2009/07/23

16V 100nF(Z)
GDM030000765

C3012

GND [Modem Device Lock]

Henderson-CS
Add sheet connector:
MDMRNG-E3P & MDMTIP-E3P
■ 2009/07/08

GDM220000952

[CN2]
*CN3011

1 MDMRNG-E3P 301
2 MDMTIP-E3P 301

SM02B-SRSS-G-TB(LF)

Henderson10-CS
J3010 GDM210000161→GDM210000247
■ 2009/08/04

GDM210000247

[J2A2]
*J3010

301 MDMTIP-E3P 1
301 MDMRNG-E3P 2
1T
2T

JM34615-N2051-7F

GND

■ REF:Santa Rosa Base . FSRBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	MDC I/F	301	059	00	360069769

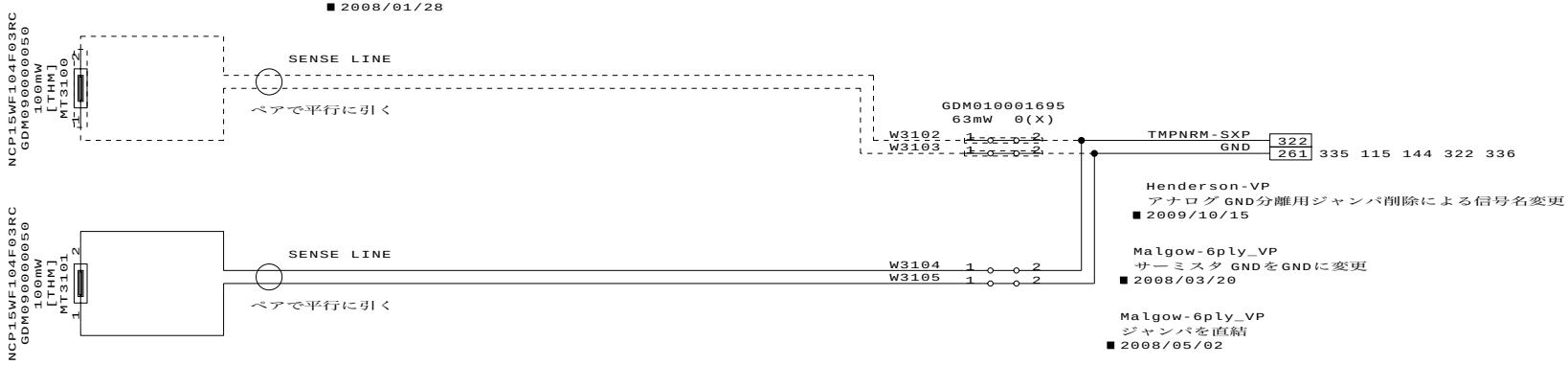
2009.10.15 17:09 G11

故障予兆

Malgow-6ply_VP
未使用サーミスタを未実装
■ 2008/05/02

Malgow_CS
故障予兆用回路追加
■ 2008/01/28

常温測定用



配置候補地 3

サーミスタの実装位置は（ PC開1）の指示に従うこと。

Henderson-VP
アナログGND分離用ジャンパ削除による信号名変更
■ 2009/10/15

Malgow-6ply_VP
サーミスタ GNDをGNDに変更
■ 2008/03/20

Malgow-6ply_VP
ジャンパを直結
■ 2008/05/02

Henderson-CS
ジャンパ直結 Delete
■ 2009/07/09

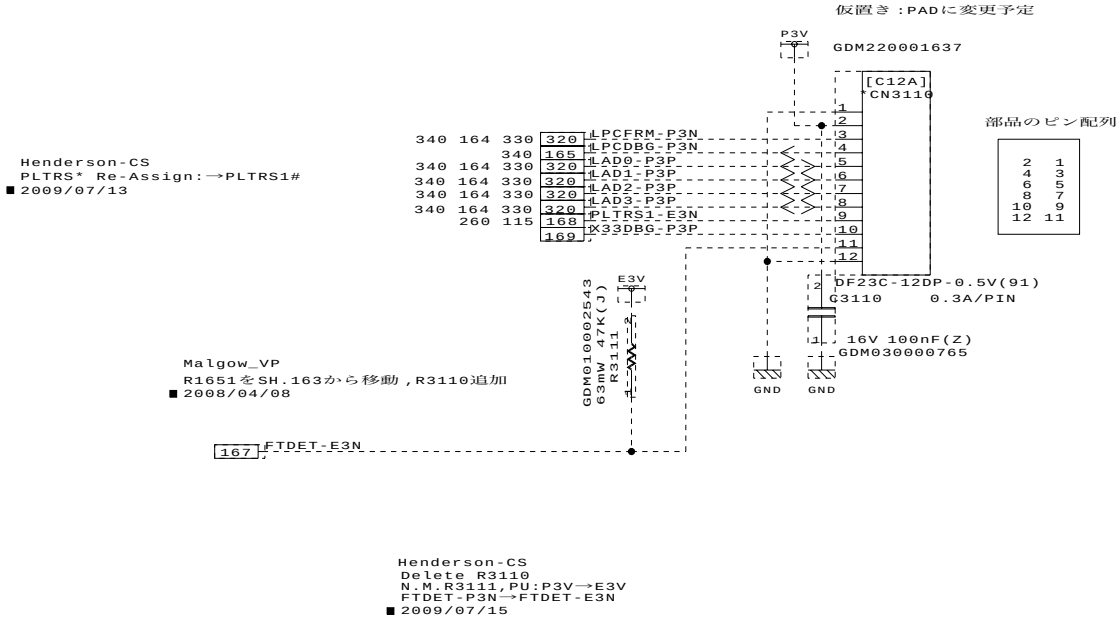
Henderson-CS
High Temp Thermistor Delete
■ 2009/07/22

■ REF:New

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	PC Health PJ	310	060	00	360069769

2009.10.15 17:09 G11

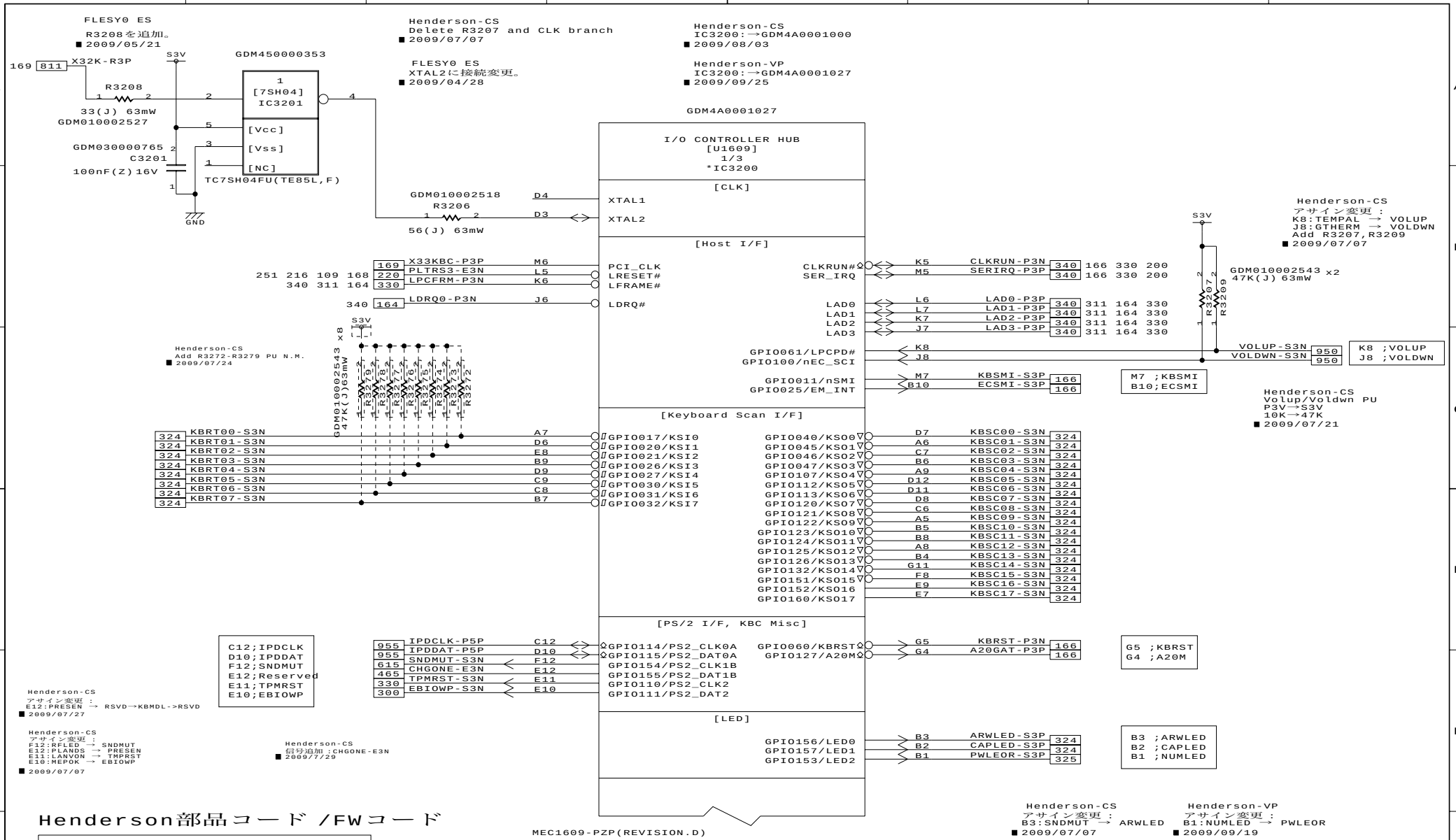
仕向け設定有



■ REF:Malgow(VP) . FG6IN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Iwaki/T.Ichimura	FHNSY1	F/T PJ	311	061	00	360069769

2009.10.15 17:09 G11



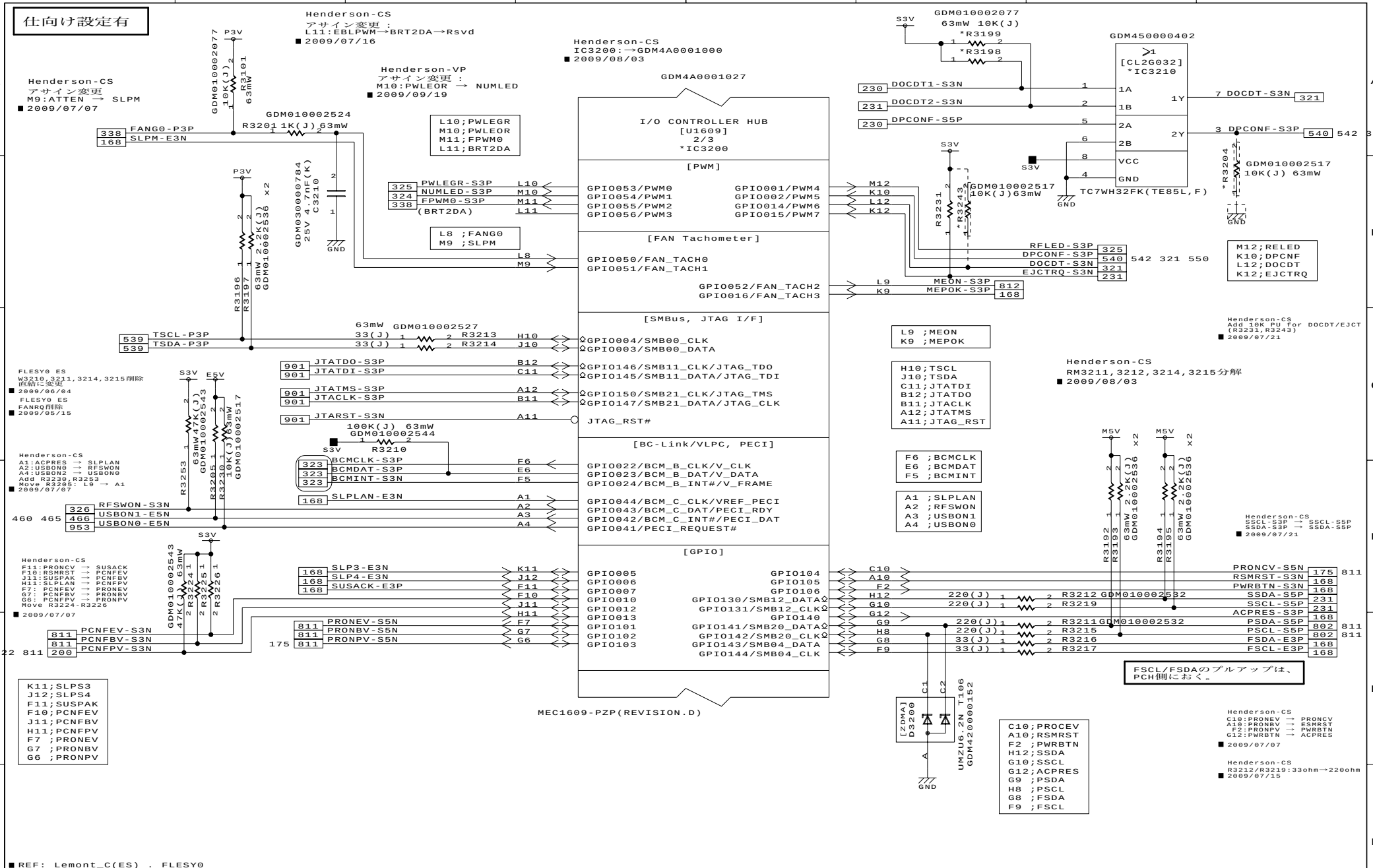
Henderson部品コード /FWコード

CS	GDM4A0001000	F0100001
VP	GDM4A0001027	F0102701
生ROM	GDM460002376	

■ REF: Lemont_C(ES) . FLESY0

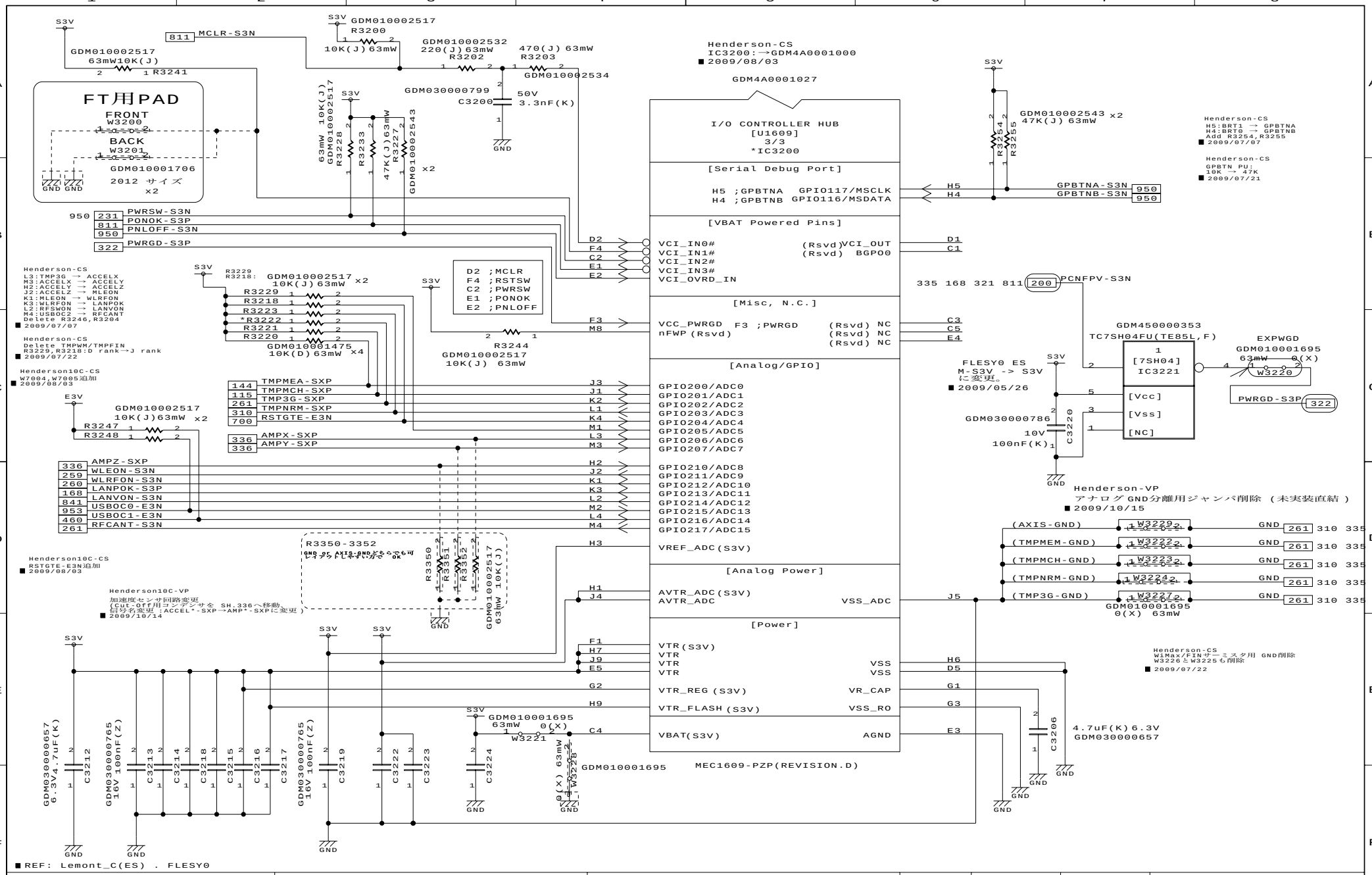
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	EC/KBC(1)	320	062	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	EC/KBC(2)	321	063	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	EC/KBC(3)	322	064	00	360069769

2009.10.15 17:09 G11

1 2 3 4 5 6 7 8

A B C D E F

ヘッダピンコネクタにはリファレンスのシルク印刷を付けて下さい。また、ピン配置がわかるようにして下さい

SMB_ADDR設定

PJ3230 JP SEL	SMBUS ADDRESS[7:1]
1-2Pin	0111 001b
2-3Pin	0111 000b

GPIO Expander I/F設定

JP Gr.	JP	BC-Link使用時	SMBUS使用時
EXSCM	W3230 W3231 R3237	Mount	Not Mount
EXSMB1	W3312 W3313	Not Mount	Mount
EXSMB2	W3232 W3233	Not Mount	Mount

FLESY0 ES
SMB_ADDRを[L]固定。
■ 2009/04/08

BC-Linkで使用

Henderson10-CS
R3237 Not mountに変更
■ 2009/08/04

FLESY0 ES
BC-Link時は、実装が正しい。
R3237実装とし、コメントも修正。
■ 2009/06/04

BC-Link使用時は NOT MOUNTにする。

FLESY0 ES
BTRST用に47Kプルアップ。
■ 2009/05/11

Henderson-CS
DPMODE-P3P->HPDDPE-P3P
■ 2009/07/23

Henderson-CS
HPDDPG-P3P->HPDDPG-S3P
■ 2009/7/23

Henderson-CS
GPIO00:3GON→ HPDETI
GPIO01:3GRFON → DVIDET
GPIO02:UWBON → HPDDPE
GPIO03:UWBRFON → PNLOBS
GPIO07:FELIEN → BLOBS
GPIO13:CIRWAKE→ CRDCPA
GPIO14:HPDETI → GTHERM
GPIO15:CRDCPA → TEMPAL
GPIO16:BTRST → DCPWEN
GPIO17:TPADON → DOCVON
Move R3240,R3242,R3232,R3234,R3231
■ 2009/07/07

Henderson-CS
GPIO00:HPDETI → DNGLDT
■ 2009/07/08

Henderson-CS
GPIO00:DNGLDT->DPMODE
■ 2009/07/09

Henderson-CS
HPDDPE-S3P->HPDDPE-P3P
■ 2009/07/09

Henderson-CS
HPDDPE-P3P->HPDDPG-P3P
■ 2009/07/16

Henderson-CS
GPIO20:MC → 3GON
GPIO24:TOCHEN → 3GRFON
GPIO25:MCHHOT → BTRST
GPIO26:SPCKLW → RLANDS
GPIO27:SALERT → FELIEN
GPIO30:N.C. → NORMKB
GPIO31:PNLOBS → SBVON
GPIO32:BLOBS → MCHHOT
GPIO36:DSPRST → KBLEON
GPIO37:LDOCNT → FGSPON
Add R3243
■ 2009/07/07

(PU at SH443)

Henderson-CS
Add GPIO31:SBVON
Add R3243
■ 2009/07/23

Henderson-CS
GPIO31:SBVON→Rsvd
Delete R3243
■ 2009/07/21

Henderson-CS
GPIO25:BTRST → BTPWON
Delete R3231
■ 2009/07/08

Henderson-CS
GPIO30:CBRST# → Rsvd
■ 2009/07/16

Henderson-VP
Change NORMKB-S3N to DPPWOC-P3
and KBLEON-P5N to DPPWON-P3N.
Add R3280,R3281
■ 2009/10/06

Henderson-CS
Add GPIO12:NORMKB
■ 2009/07/24

Henderson-CS
R3256追加
■ 2009/07/09

Mini-DP

R3256	Yes	No
	NotMount	Mount

JPチップGr	JPチップ
EXSCM (EX配置エリア)	W3230 W3231 R3237
EXSMB2 (EX配置エリア)	W3232 W3233

JPチップはできるだけ EXGrJPチップ配置エリアに、Grごとにかためて配置して下さい

■ REF: Lemont_C(ES) . FLESY0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	GPIO Expander	323	065	00	360069769

2009.10.15 17:09 G11

Newport10 Vista用KB

Newport10 CS
CN3230部品変更
GDM220001155→GDM220001203
■ 2006/9/29

Henderson-CS
CN3230部品変更
GDM220001155→GDM220001203
■ 2009/6/29

Henderson-CS
CN3230 Pin Assign Reversed
■ 2009/07/08

Newport10 CS
Visata対応KBに変更の為、
ピンアサイン変更
■ 2006/7/13

Newport10 CS
C3230を1uF→100nFに変更
■ 2006/11/17

	SP Support PCB	10Key Support PCB
R3249 R3250 C3242	Mount	Not Mount
W3244 W3245	Not Mount	Mount

Normal KBはどちらのPCBでもサポート可

04 6268 034 000 800 +

CN3230 [C34A2]
GDM220001203

NUMLED-P5N 324
ARWLED-P5N 324
CAPLED-P5N 324

Henderson-VP
W3240,3241削除
■ 2009/09/19

GDM010001726 x6
31mW 1K(J)
RM3245
RM3246
RM3240
RM3241
RM3242
RM3243

KBSC00-S3N 320
KBSC09-S3N 320
KBSC03-S3N 320
KBSC02-S3N 320
KBSC05-S3N 320
KBSC04-S3N 320
KBSC06-S3N 320
KBSC08-S3N 320
KBSC10-S3N 320
KBSC01-S3N 320
KBSC11-S3N 320
KBSC07-S3N 320
KBSC15-S3N 320
KBSC12-S3N 320
KBSC13-S3N 320
KBSC14-S3N 320

KBRT06-S3N 320
KBRT02-S3N 320
KBRT03-S3N 320
KBRT05-S3N 320
KBRT01-S3N 320
KBRT04-S3N 320
KBRT07-S3N 320
KBRT00-S3N 320

Henderson KB Gr, Setting 設定

(IPSP5V) SP-P5V 324 955
SPX-PXP 955
KBSC17-S3N 320
(IPSGND) SP-GND 324 955
SPY-PXP 955
KBSC16-S3N 320

*W3245
*R3250
*W3244
*R3249

Henderson-CS
KB Connリファレンス番号変更
→CN3230(リファレンス重複)
■ 2009/08/04

Henderson-CS
Add jumpers for Group Setting
of various KBs
■ 2009/7/1

Henderson-CS
Add I/F for KB LED
■ 2009/07/02

コメント
短く引く
＊周りにノイズのある信号は置かない

Henderson-CS
W3242,3243削除
■ 2009/08/04

Henderson-CS
Q3243:G-CODE化
■ 2009/08/04

Malgow_CS
アキュポイントのコンデンサを未実装にする
VPでは実装に戻す
■ 2008/02/07

Henderson-CS
GDM410000213->K4100695
■ 2009/07/24

SP-P5V 324 955
*C3242
GDM030000733
1uF(K) 16V
SP-GND 324 955

Henderson-CS
R3252 PU P5V->E5V
■ 2009/07/23

GDM410000470
SSM3J129TU(TE85L)
[Q3243 MEK]

GDM010002517
63mW 10K(J)
R3252

*C3243
GDM030000765
100nF(Z) 16V

GDM010002524
63mW 1K(J) 08 6262 004 940 846 +
R3257

*CN3241
[C4A2]
GDM220002199

Henderson-CS
CN3401:→GDM220002199
■ 2009/08/04

Henderson-CS
リファレンス変更 :C3241→C3242
■ 2009/08/04

Henderson-VP
R3252:→Not Mount
■ 2009/10/09

Henderson-CS
Delete KB Type Detect Cir.
■ 2009/07/27

from EC/KBC

Henderson-VP
Delete KBLEON-P5N
■ 2009/10/06

166 LEKBID-P3N

To PCH GPIO(PU at SH166)

C3240
16V 100nF(Z)
GDM030000765

Henderson-CS
Re-assign D3242,D3244 Pin Assignment
(layout request)
■ 2009/07/23

[ZDMA] D3245
[ZDMA] D3243
D3242 [VMD2]
D3244 [VMD2]

D3242-D3245

UMZU6.2N T106 x4
GDM420000152

GDM010001725
31mW 330(J)
RM3244
CAPLED-P5N 324
ARWLED-P5N 324
NUMLED-P5N 324

DTC143ZE TL
GDM410000230 x3

[TRNC] Q3240
[TRNC] Q3241
[TRNC] Q3242

NUMLED-S3P 321
ARWLED-S3P 320
CAPLED-S3P 320

Henderson-VP
Q3241仕向け設定削除
■ 2009/09/19

Normal KB Type判別
H:A4+10Key
L:A4(Normal)

SPサポート基板 /10Keyサポート基板
共にノーマル Keyサポートとする
10Keyの有無は HWSCの値で判定する
判別用 GPIはバックアップとして残す

Henderson-VP
Delete KB detect circuit
■ 2009/10/06

Henderson-CS
Add Normal KB Detect Cir.
■ 2009/07/24

Henderson-CS
R3280->R3245,R3281->R3246
■ 2009/07/24

仕向け設定有

■ REF:Malgow (VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	KB I/F	324	066	00	360069769

2009.10.15 17:09 @Date

1 2 3 4 5 6 7 8

A B C D E F

Henderson LED配置

LED 電流値
Green 6mA
Orange 9mA

Henderson-CS
R3262:G010002723->GDM010002713
■ 2009/07/27

DC-IN Power Battery W-LAN SATA

M5V
C3250
GDM030000765
16V 100nF(Z)
GND

GDM010002537
680(J) 63mW
R3258
R3259
R3260
RM3251
RM3251
RM3251
RM3251
31mW 2.2K(J)
GDM010001217

ORANGE GREEN ORANGE GREEN ORANGE GREEN ORANGE GREEN

SML-D12Y8WT86 GDM4400000183 DS3250
SML-D12M8WT86 GDM4400000182 DS3251
SML-D12Y8WT86 GDM4400000183 DS3252
SML-D12M8WT86 GDM4400000182 DS3253
SML-D12Y8WT86 GDM4400000183 DS3254
SML-D12M8WT86 GDM4400000182 DS3255
SML-D12Y8WT86 GDM4400000183 DS3260
SML-D12M8WT86 GDM4400000182 DS3257

1.1mA(5mcd)

Malgow_VP
LED電流制限抵抗変更 ：システム課指示
Orange:1.5K, Green:680
■ 2008/04/29

Henderson-CS
RM3250 Divided into R3258-R3260
■ 2009/07/22

P3V
R3262
GDM010002731
300(J) 63mW
C3251
GDM030000765
16V 100nF(Z)
GND

811 DCINOR-S5N
811 DCINGR-S5N
811 BAT1OR-S5N
811 BAT1GR-S5N

[MFNGR1]
Q3250
2SK3541 T2L
GDM410000233

320 PWLEOR-S3P
321 PWLEGR-S3P

GDM010002544
63mW 100K(J)
R3251

[TRNC]
Q3251

DTC143ZE TL
GDM410000230

[TRNC]
Q3260

321 RFLED-S3P
166 SATLED-P3N

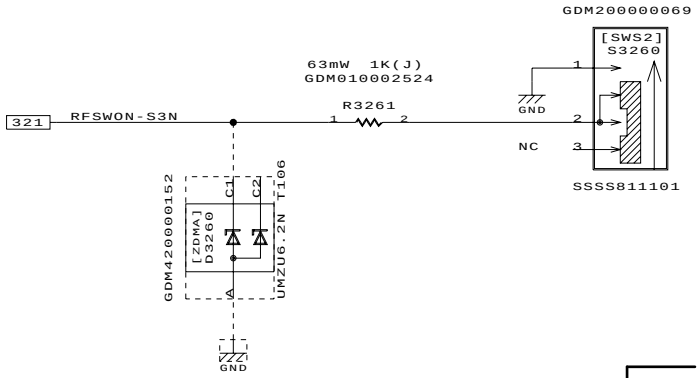
Henderson-CS
Delete Q3253
PU:P5V→P3V
Series Resistor:680ohm→200ohm
■ 2009/07/22

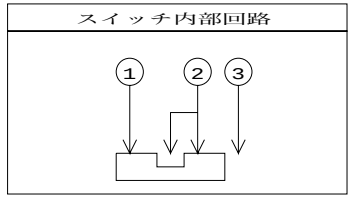
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	LED	325	067	00	360069769

2009.10.15 17:09

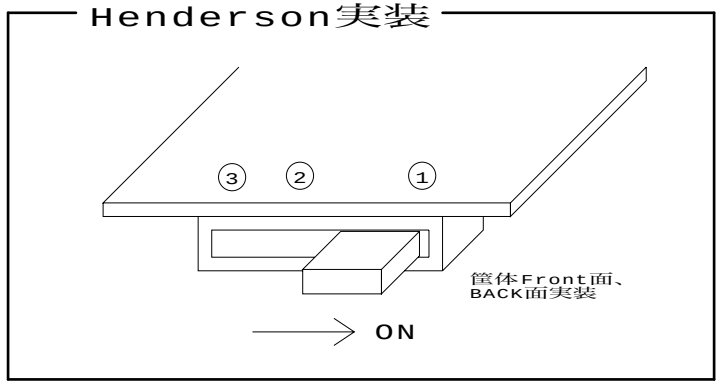
Malgow_CS
実装位置により
1:NC, 3:GNDに変更
■ 2008/02/15

Henderson-CS
実装位置により
1:GND,3:N.C.に変更
■ 2009/07/08





接点の切換えタイミング：規定なし
(Non shortingではない)

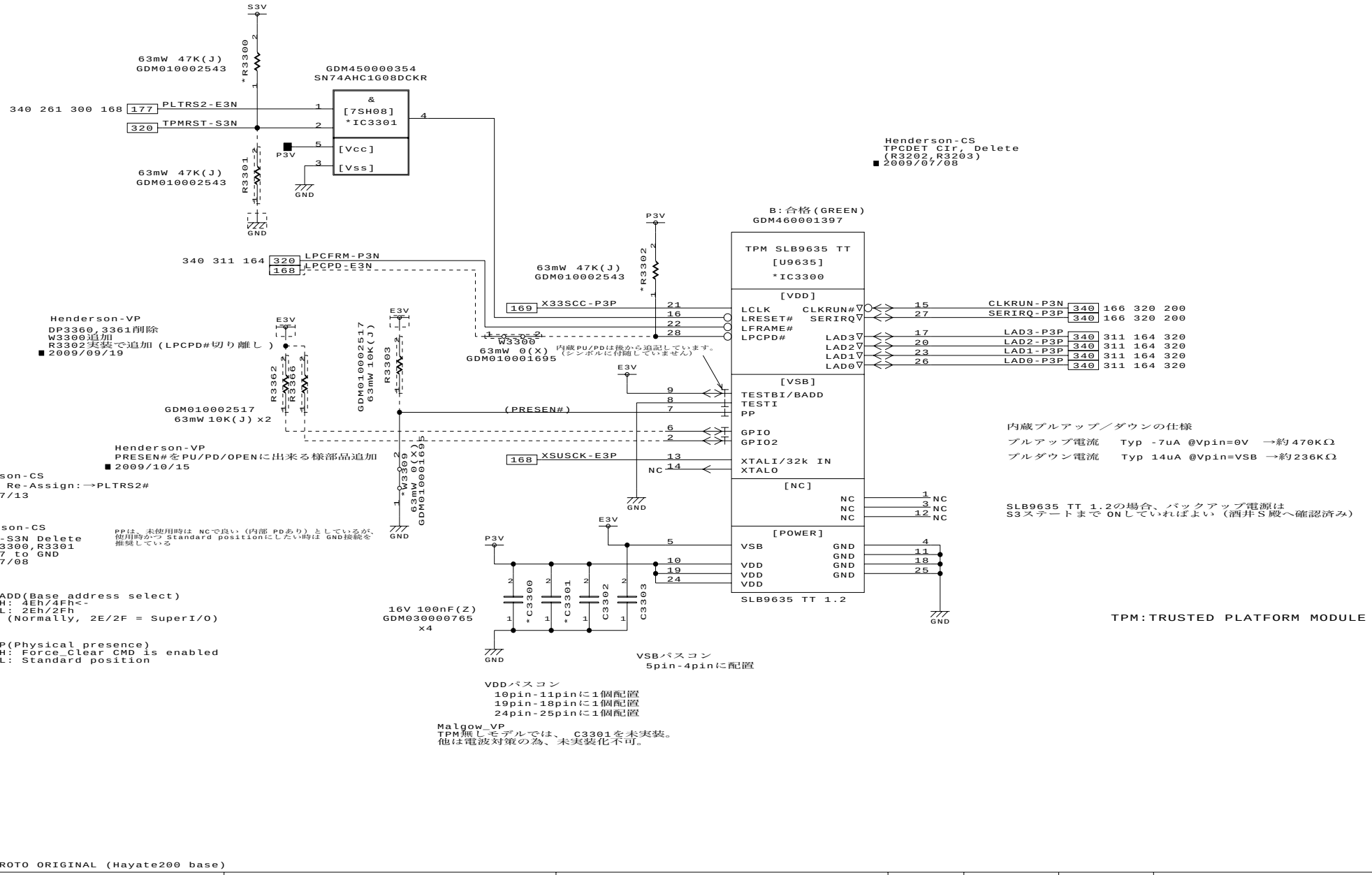


REF:Nurburgring20L VP(FNN2J4)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	WIRELESS SW,LED	326	068	00	360069769

2009.10.15 17:09

仕向け設定有



ROME-PROTO ORIGINAL (Hayate200 base)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	TPM CONT	330	069	00	360069769

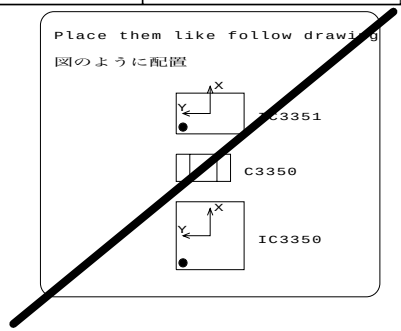
2009.10.15 17:09 G11

仕向け設定有

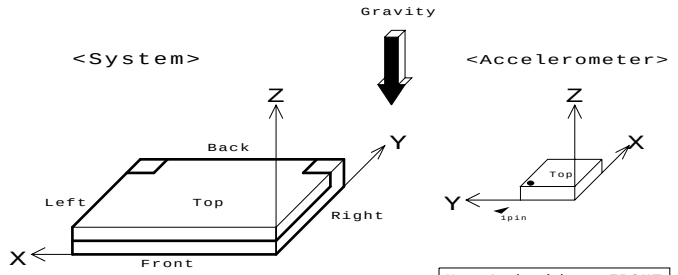
Plase FRONT side
Spot and direction of accelerometer depend on (1P-SETSU8)
Don't place nearby exothermic parts for avoid thermal drift

ＦＲＯＮＴ面に実装。
配置する場所／向きは (1P設8)指示に従うこと。
温度ドリフトし易い部品なので、発熱部品の近くへ配置しないこと。

Place them like follow drawing
図のように配置



静的加速度の座標軸



Mounted side = FRONT

Wegenen20 2008/03/20

Malgow-DDR3_CS
Kionixを削除し、 FreescaleをSH.336に追加
■ 2009/01/30

Malgow-Andalusia_CS
加速度センサ判別信号追加
L:STMicro
H:Kionix
■ 2008/04/12

Malgow-Andalusia_CS
AXIS-GND→GNDに変更
■ 2008/03/31

Henderson-CS
加速度センサ Delete.
■ 2009/07/08

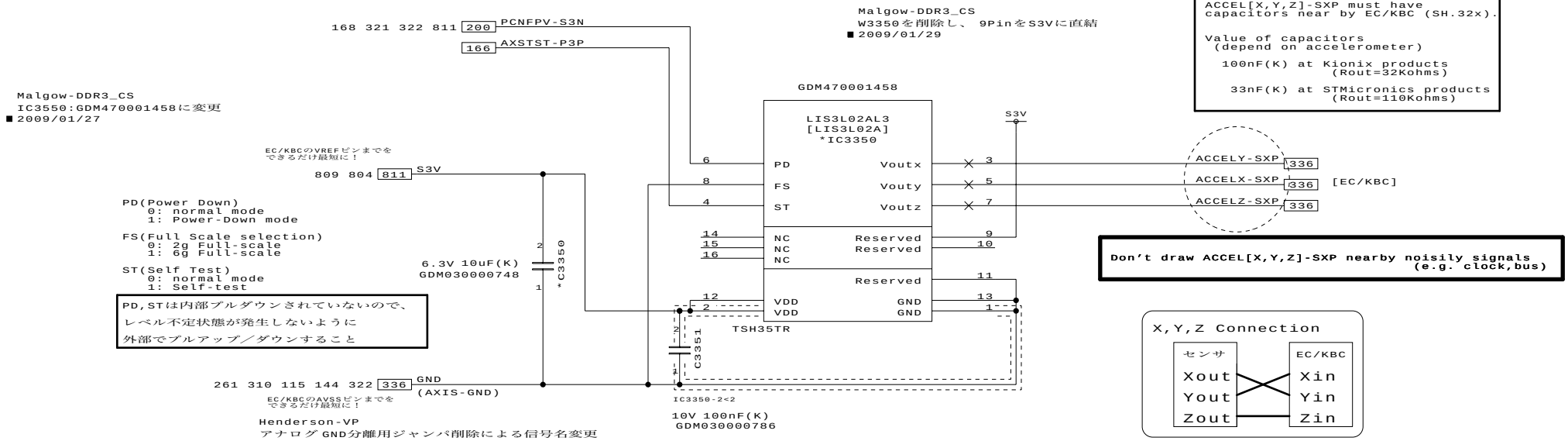
Malgow-DDR3_CS
IC3550:GDM470001458に変更
■ 2009/01/27

Malgow-DDR3_CS
W3350を削除し、 9PinをS3Vに直結
■ 2009/01/29

ACCEL[X,Y,Z]-SXPは、EC/KBC側のシートにコンデンサを設置すること

CAUTION (Circuit)
ACCEL[X,Y,Z]-SXP must have capacitors near by EC/KBC (SH.32x).

Value of capacitors
(depend on accelerometer)
100nF(K) at Kionix products (Rout=32Kohms)
33nF(K) at STMicronics products (Rout=110Kohms)



Don't draw ACCEL[X,Y,Z]-SXP nearby noisily signals (e.g. clock,bus)

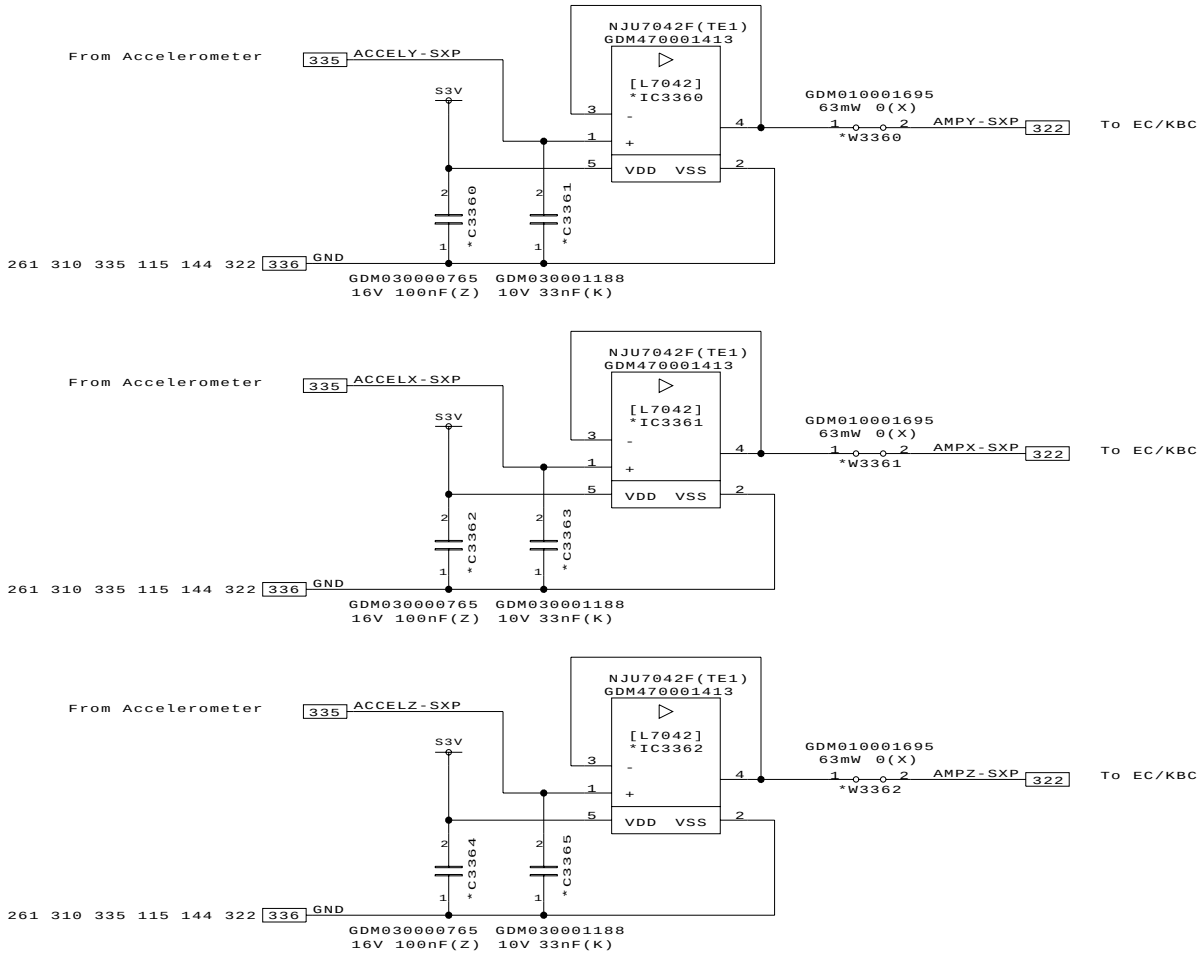
X,Y,Z Connection

Henderson-VP
アナログGND分離用ジャンパ削除による信号名変更
■ 2009/10/15

■ REF:MCP79-TS(FNTBS0)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
H.Nishioka/T.Ichimura	FHNSY1	ACCELEROMETER (FRONT)	335	070	00	360069769

Henderson10C-VP
SH.336追加：加速度センサ対応
■ 2009/10/14



Henderson-VP
アナログGND分離用ジャンパ削除による信号名変更
■ 2009/10/15

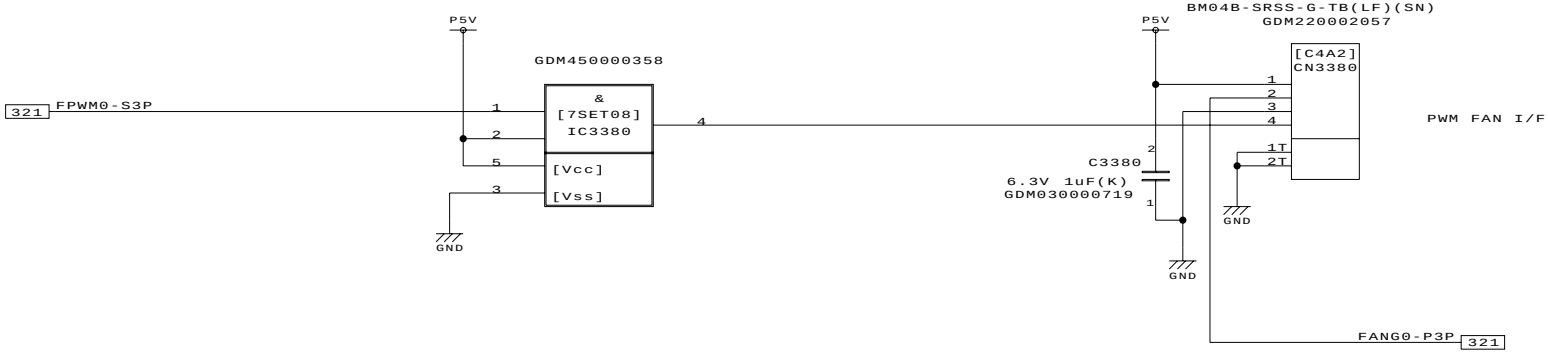
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	AMP for AXIS	336	071	00	360069769

2009.10.15 17:09 G11

~~仕向け設定あり~~

Henderson-CS
Delete Power rail:E5V
Delete W3380-W3381
■ 2009/07/08

Henderson-CS
CN3380部品変更
GDM220000951→GDM220002057
■ 2009/6/29



～チェック～
□ FAN電圧 [5.0V]
□ コネクタ
□ PWM信号電圧

~~FPWM~~	3.3V	~~5.0V~~
R8771	~~6.8k(D)~~	10k(D)
~~R8772~~	22k(D)	~~10k(D)~~

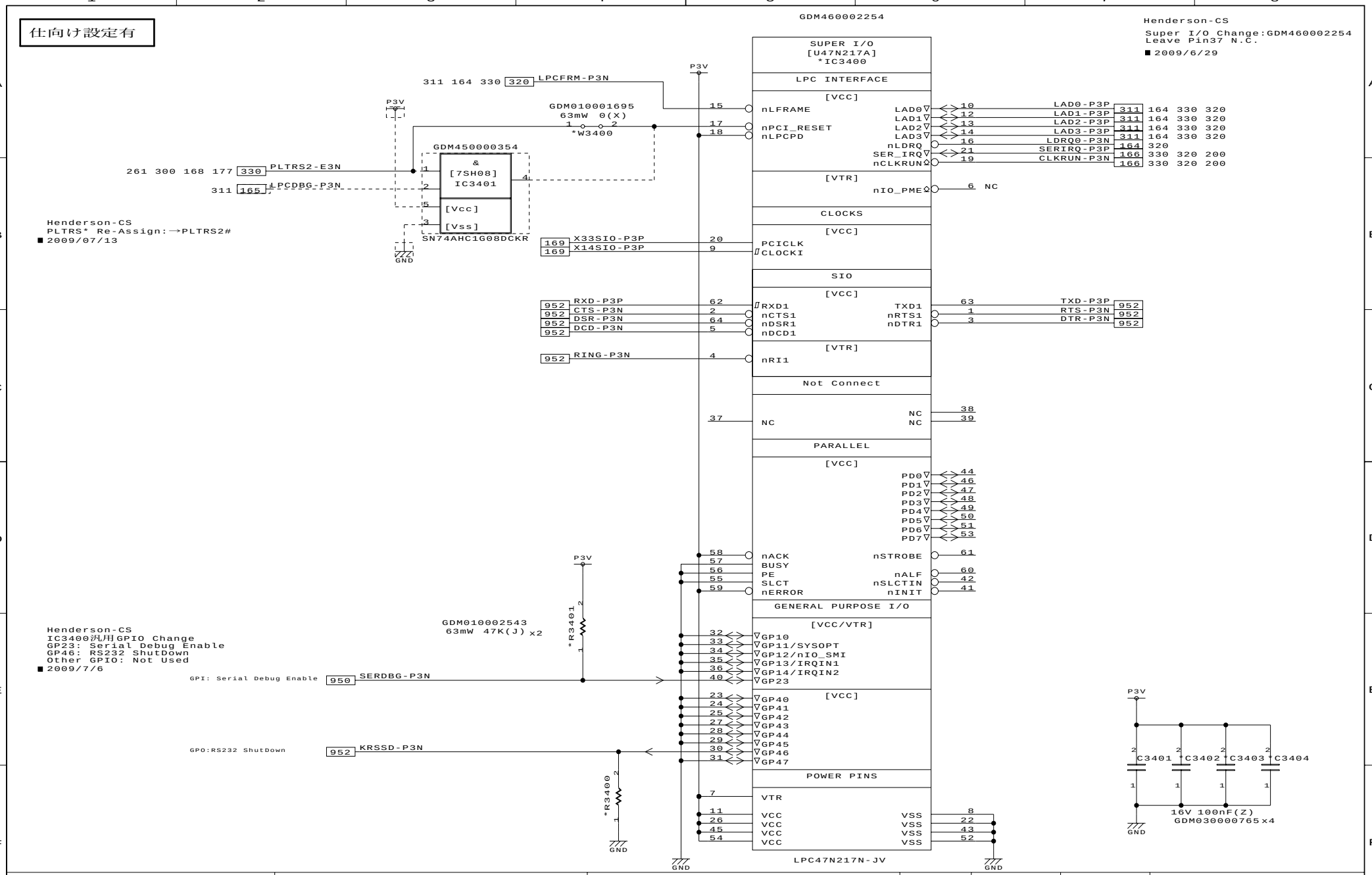
仕向け設定

	AMTサポート	AMT非サポート
Group		
W3380	Mount	Not Mount
W3381	Not Mount	Mount

■ REF:Montevina Platform TS (TS2) . FPC9T1

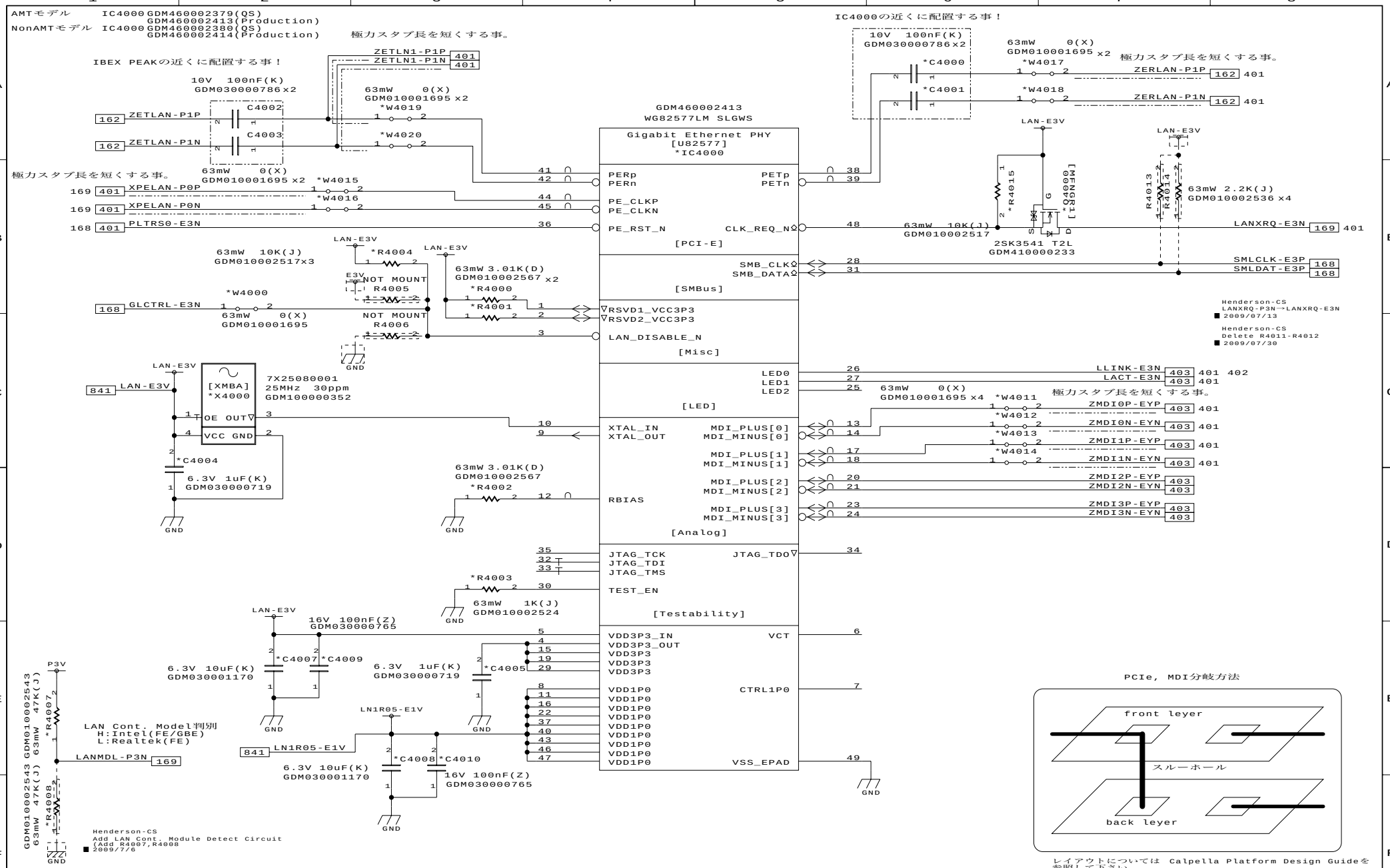
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	FAN I/F	338	072	00	360069769

2009.10.15 17:09 G11



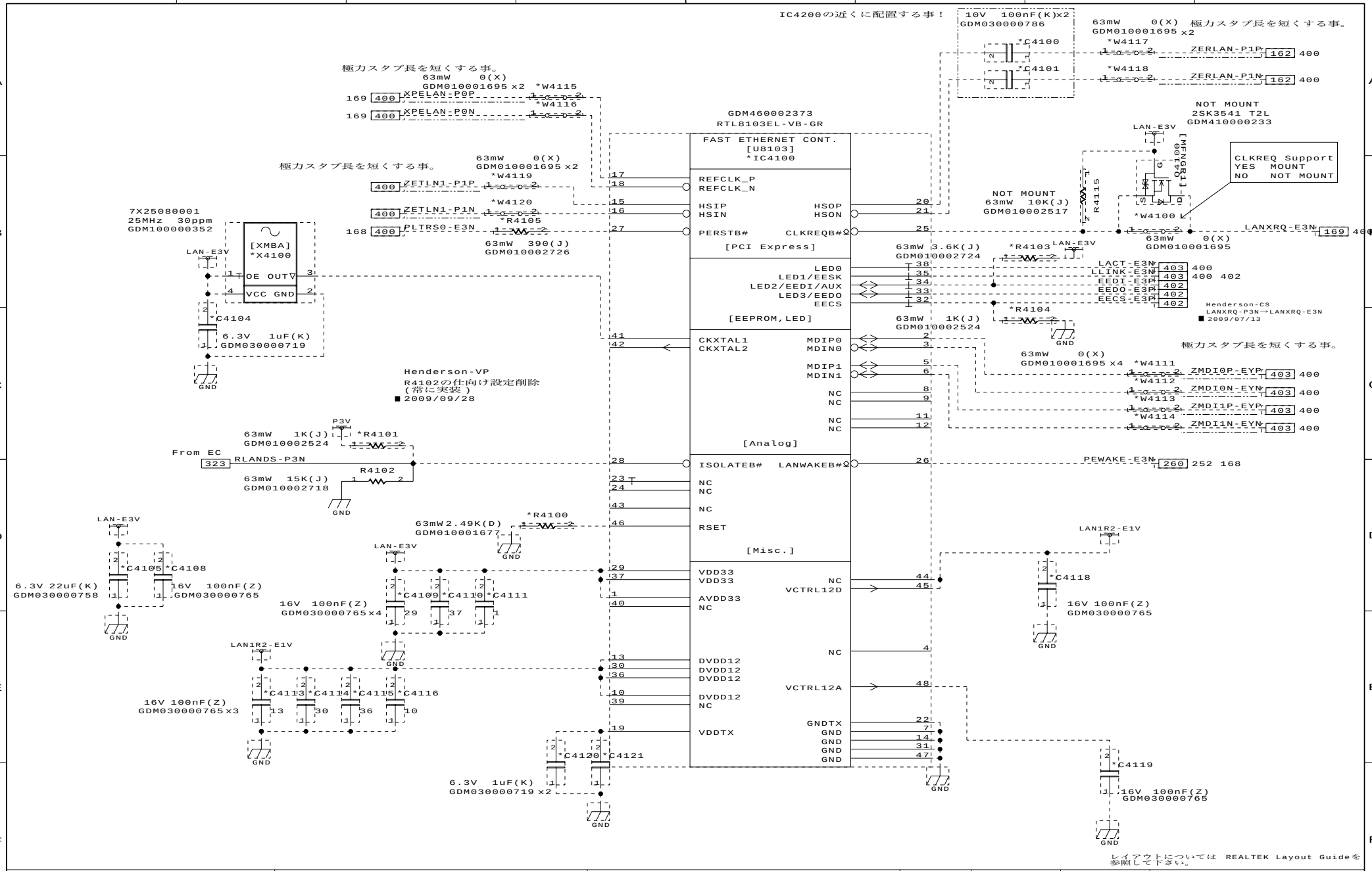
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	SUPER I/O	340	073	00	360069769

2009.10.15 17:09



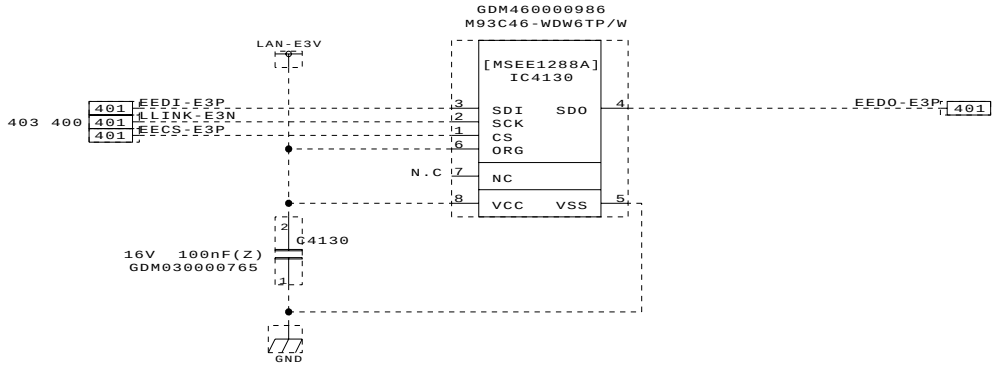
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Horibe	FHNSY1	Ethernet PHY(Hanksville)	400	074	00	360069769

2009.10.15 17:09



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 K.Horibe	FHNSY1	LAN CONTROLLER(8103EL)	401	075	00	360069769

2009.10.15 17:09

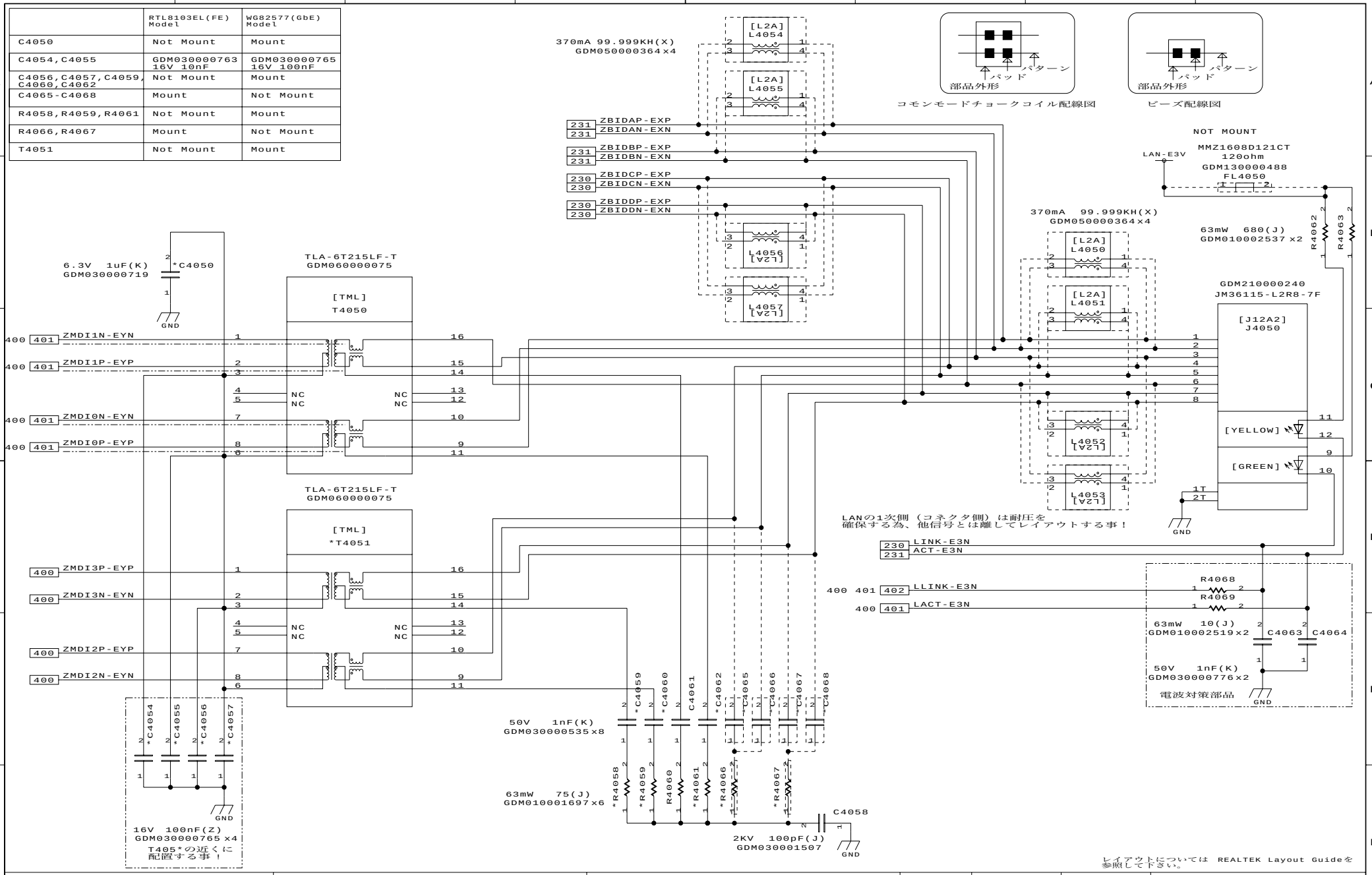


レイアウトについては REALTEK Layout Guideを
参照して下さい。

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 K.Horibe	FHNSY1	LAN EEPROM(8103EL)	402	076	00	360069769

2009.10.15 17:09

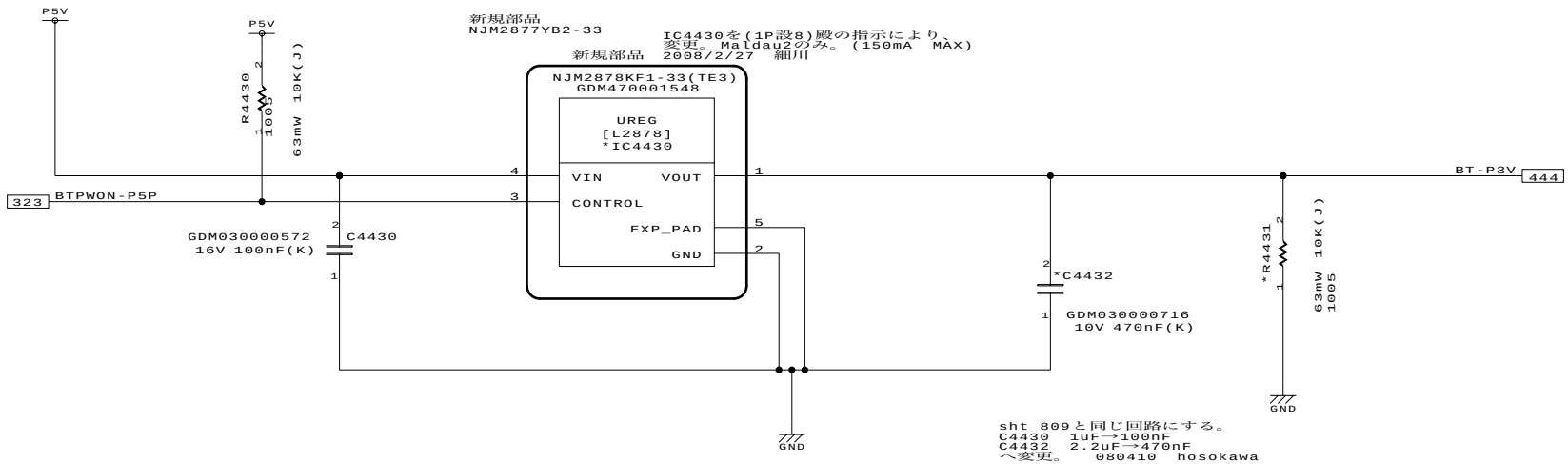
	RTL8103EL(FE) Model	WG82577(GbE) Model
C4050	Not Mount	Mount
C4054,C4055	GDM030000763 16V 10nF	GDM030000765 16V 100nF
C4056,C4057,C4059, C4060,C4062	Not Mount	Mount
C4065-C4068	Mount	Not Mount
R4058,R4059,R4061	Not Mount	Mount
R4066,R4067	Mount	Not Mount
T4051	Not Mount	Mount



レイアウトについては REALTEK Layout Guideを参照して下さい。

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 K.Horibe	FHNSY1	LAN I/F&LED(8111DL/8103EL)	403	077	00	360069769

2009.10.15 17:09



sht 809と同じ回路にする。
C4430 1uF→100nF
C4432 2.2uF→470nF
へ変更。 080410 hosokawa

BTはリセット信号のみで制御する

■ REF:Moldau2/Sierra10(VP) . FMTSY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Hosokawa	FHNSY1	Bluetooth Power	443	078	00	360069769

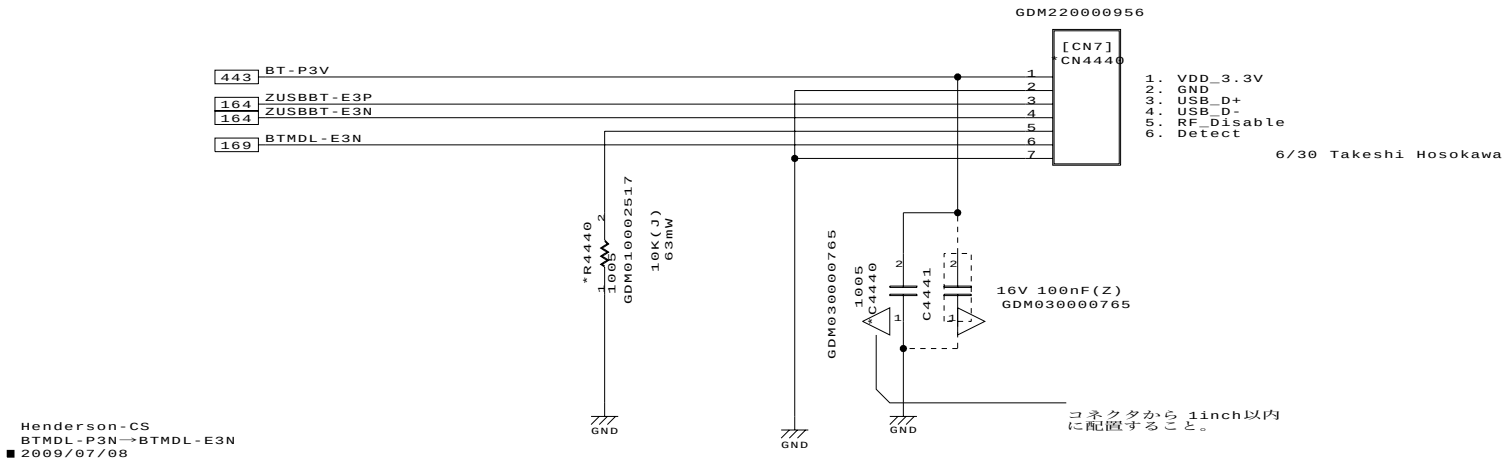
2009.10.15 17:09 G11

1 2 3 4 5 6 7 8

A B C D E F

BTMDL＃信号

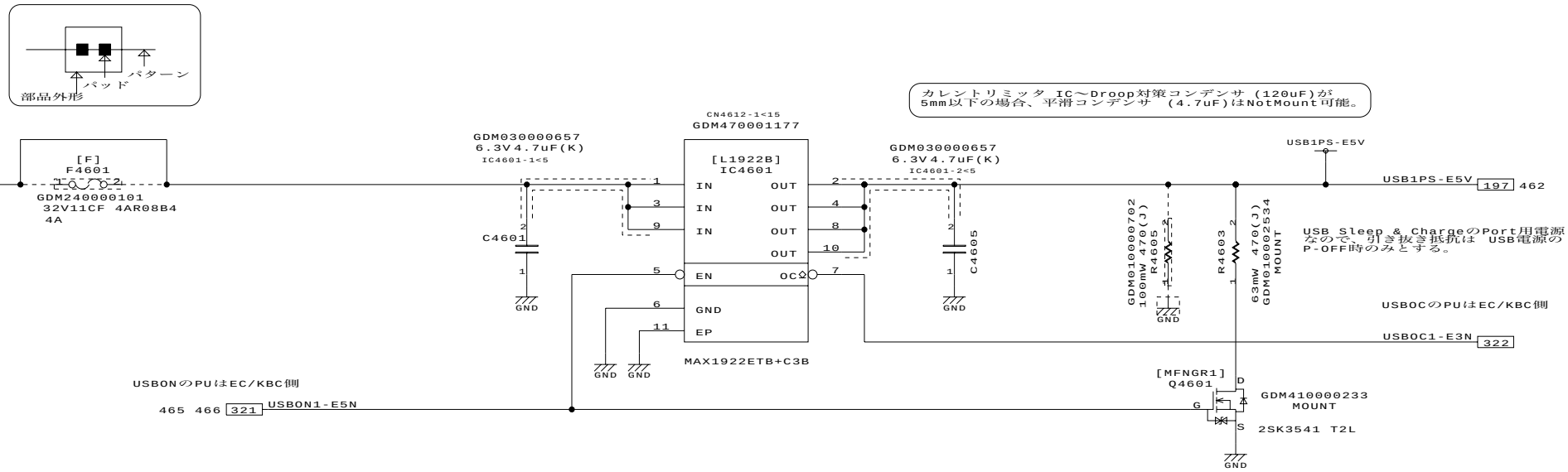
L	Bluetooth基板あり
H	Bluetooth基板無し



Henderson-CS
BTMDL-P3N→BTMDL-E3N
■ 2009/07/08

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Hosokawa	FHNSY1	Bluetooth I/F	444	079	00	360069769

2009.10.15 17:09 G11



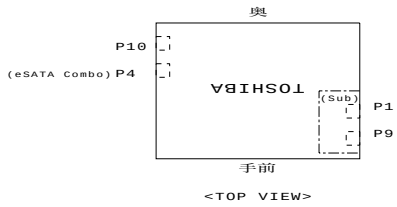
Henderson-CS
Delete C4609
■ 2009/07/15

Henderson-CS
Delete 2 USB Current Limit IC related Circuit
Change F4601 to N.M.
Add Bypass Pattern
■ 2009/6/30

■ REF:Malgow(VP) . FG6IN2

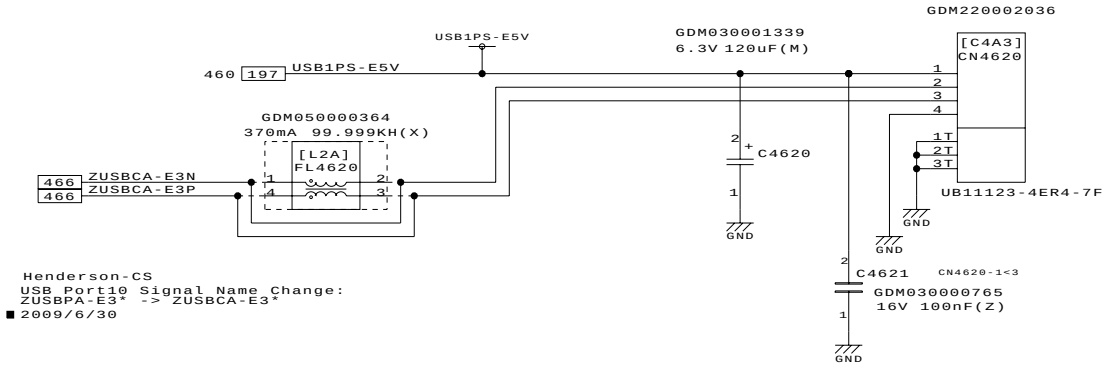
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	USB POWER	460	080	00	360069769

2009.10.15 17:09 G11

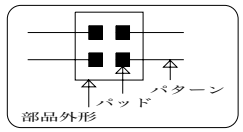


Henderson-CS
Delete CN4621,CN4622 related circuit
■ 2009/6/29

02 Malgow-Andalusia_CS
Droop対策コンデンサ
(C4620)追加
■ 2008/03/17



Henderson-CS
USB Port10 Signal Name Change:
ZUSBPA-E3* -> ZUSBCA-E3*
■ 2009/6/30



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	USB I/F	462	081	00	360069769

仕向け設定有

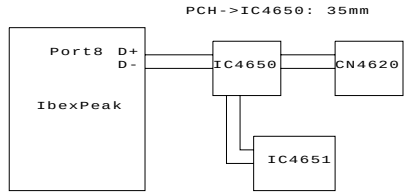
USB Port8 特定Device用 or Dedicated Charger

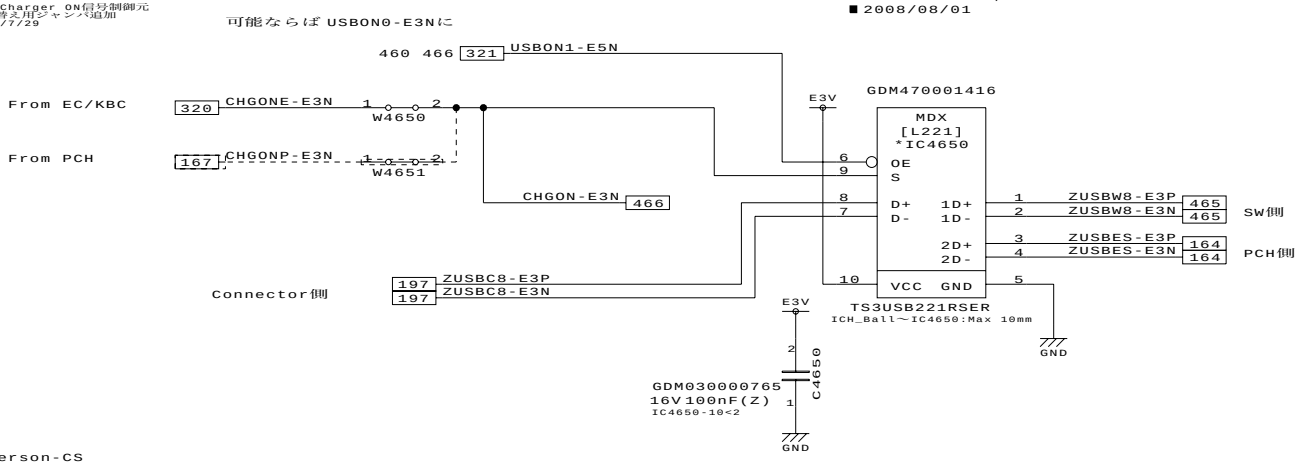
Henderson-CS
信号名変更：
USBSC-E3N→CHGONP-E3N
USBDC-E3N→MOD3EN-E3N
USBIP-E3N→MOD4EN-E3N
■ 2009/7/29

Henderson-CS
USB Charger ON信号制御元
切り替え用ジャンパ追加
■ 2009/7/29

Henderson USB Charge Circuit Layout Guide

Cassiopeia10 CS
Delete W4650,W4651
■ 2008/08/01

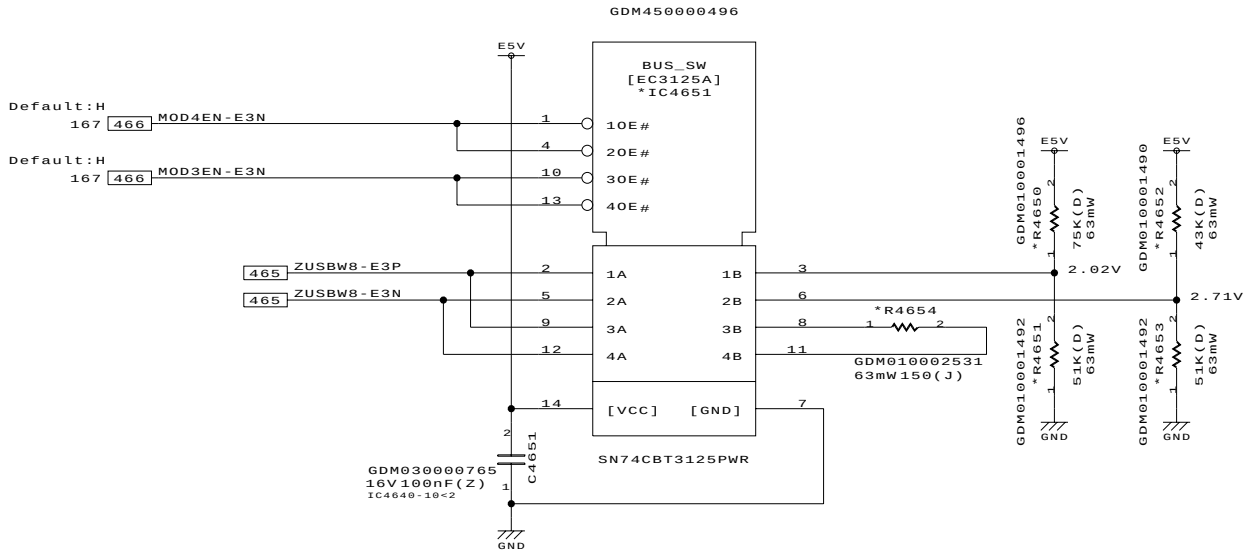




S	OE#	Function
X	H	Disconnect
L	L	D=1D
H	L	D=2D

Henderson-CS
USB Port4 Signal Name Change
■ 2009/7/3

Cassiopeia10 CS
USB Port0 Signal Name Change
■ 2008/07/28



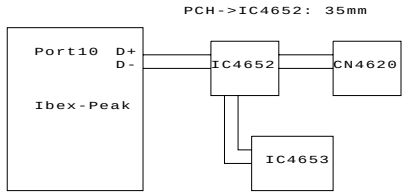
■ REF:Cassiopeia10(VP) . FCASY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	USB CHARGER 3/4(1)	465	082	00	360069769

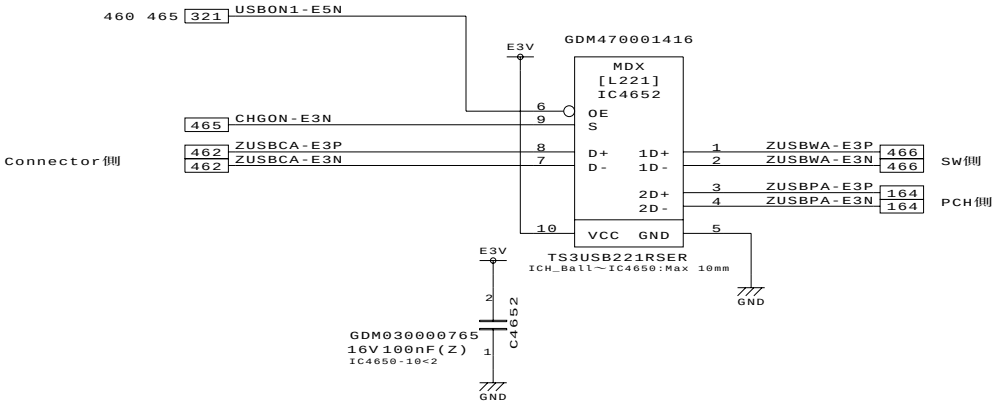
2009.10.15 17:09

USB Port10 特定Device用 or Dedicated Charger

Henderson USB Charge Circuit Layout Guide

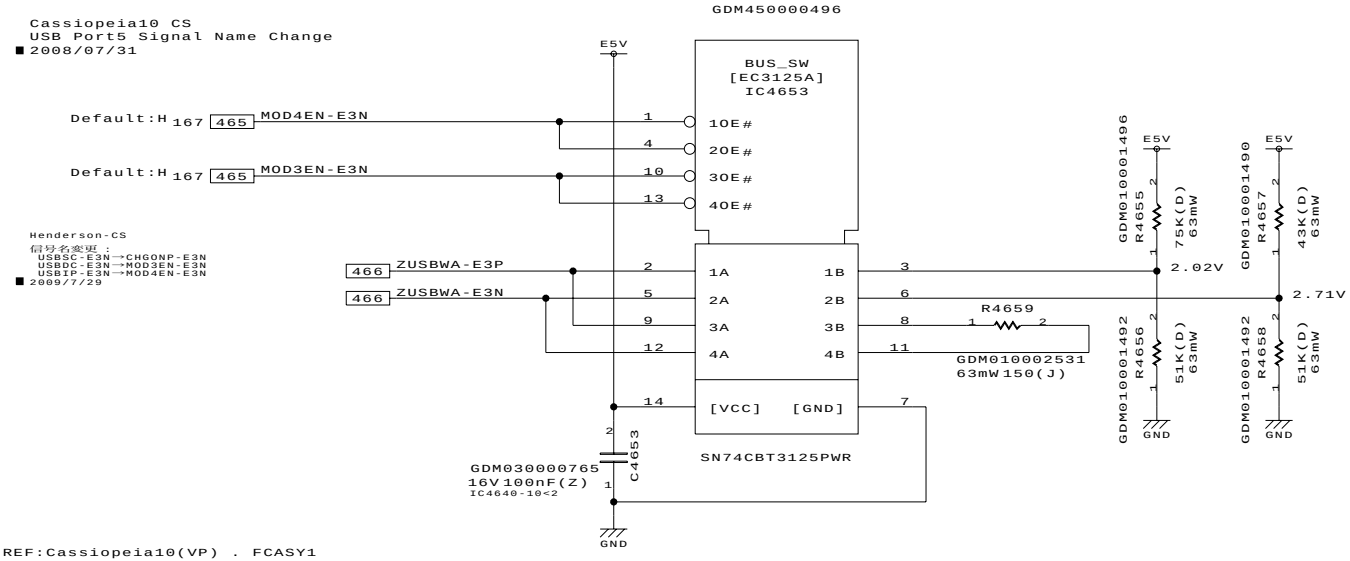


Cassiopeia10 CS
Delete W4652,W4653
■ 2008/08/01



S	OE#	Function
X	H	Disconnect
L	L	D=1D
H	L	D=2D

Cassiopeia10 CS
USB Port5 Signal Name Change
■ 2008/07/31



■ REF:Cassiopeia10(VP) . FCASY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	USB CHARGER 3/4(2)	466	083	00	360069769

2009.10.15 17:09

R8857-Q5300間の
パターン抵抗は　30mΩ以下

Henderson10　CS

Load SWのドロップ削減のために
E3V-P3Vに変更
■ 2009/7/1

GDM410000229
[MFNX2C]
Q5300

E3V
C5300
GDM030000765
100nF(Z) 16V
GND

PNL-P3V
POWER
539

GDM010002524
1K(J)　63mW
R5304

TPCS8204(T2LOMEQ,M

Henderson-CS
FLEN-E3P→FLEN-P3P
(PWR Well:Core)
■ 2009/7/6

E3V
GDM450000354
[7SH08]
IC5301
[Vcc]
[Vss]
SN74AHC1G08DCKR

PCH(GPIO39) 166 FLEN-P3P
GMCH 160 BLON-P3P

GDM010002544
63mW 100K(J)
R3100

BLEN-E3P 539 323
BLOBS-E3N
故障予兆 :Back Light_OBSERVE

PNLOBS-E3N 323
故障予兆 :Panel_OBSERVE

10-EAV
GDM010002524
63mW　1K(J)
R5303
R5302
GDM010002544
63mW 100K(J)

GDM410000233
[MFNGR1]
Q5303
2SK3541 T2L
RDS=7ohm(max)
at VGS=2.5V
VGS=1.5V

C5303
GDM030000572
100nF(K) 16V

806 10-EAV

E3V
R5307
GDM010002517
63mW 10K(J)

RDS ON=7Ω(max)
at(VGS=2.5V)

GDM410000233
[MFNGR1]
Q5304

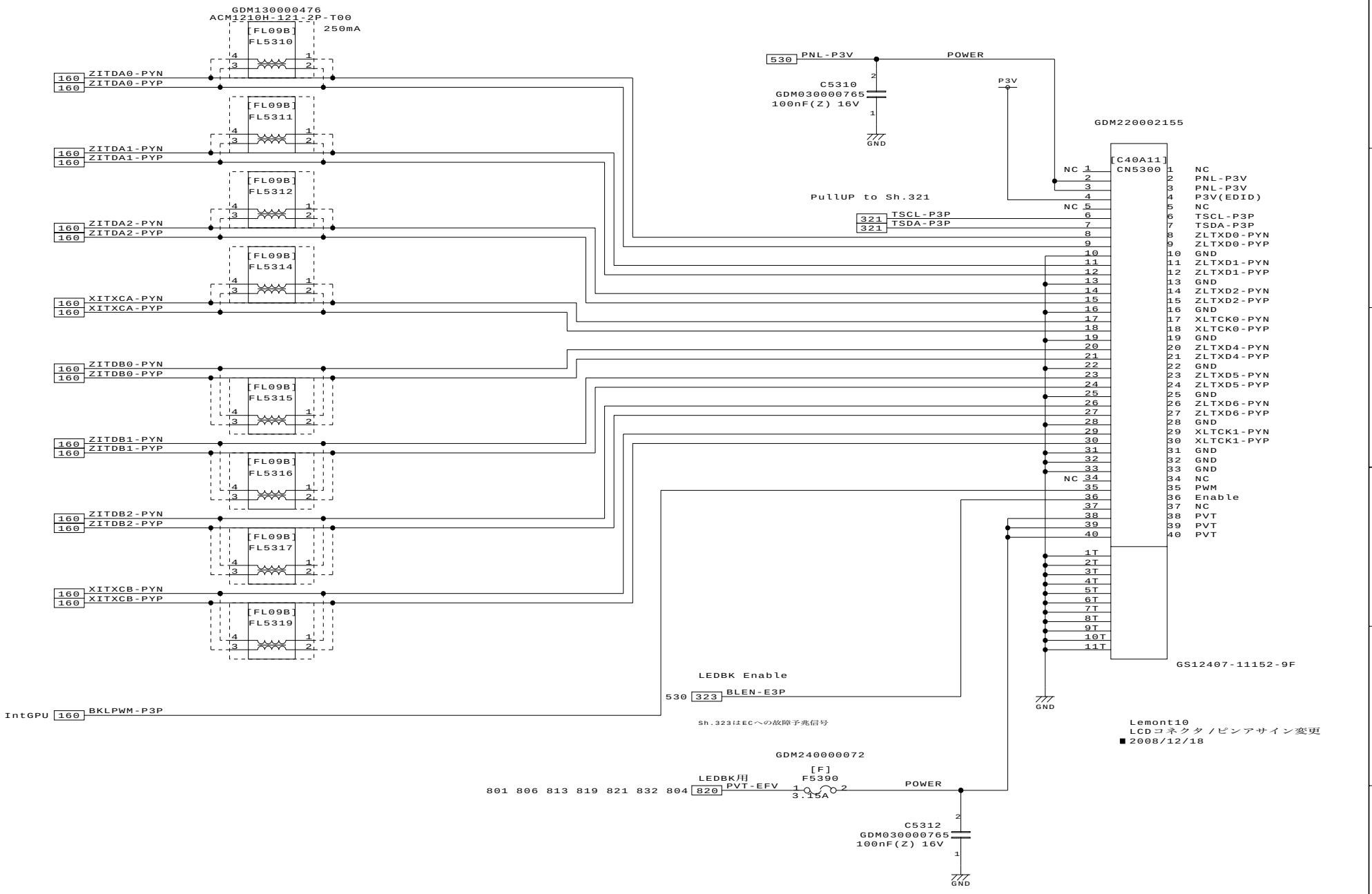
GMCH
160 LCDON-P3P

GDM010002544
100K(J) 63mW
R5306
VGS=1.5V
2SK3541 T2L

■ REF:Newport10C(VP)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	LCD PS	530	084	00	360069769

2009.10.15　17:09　G11

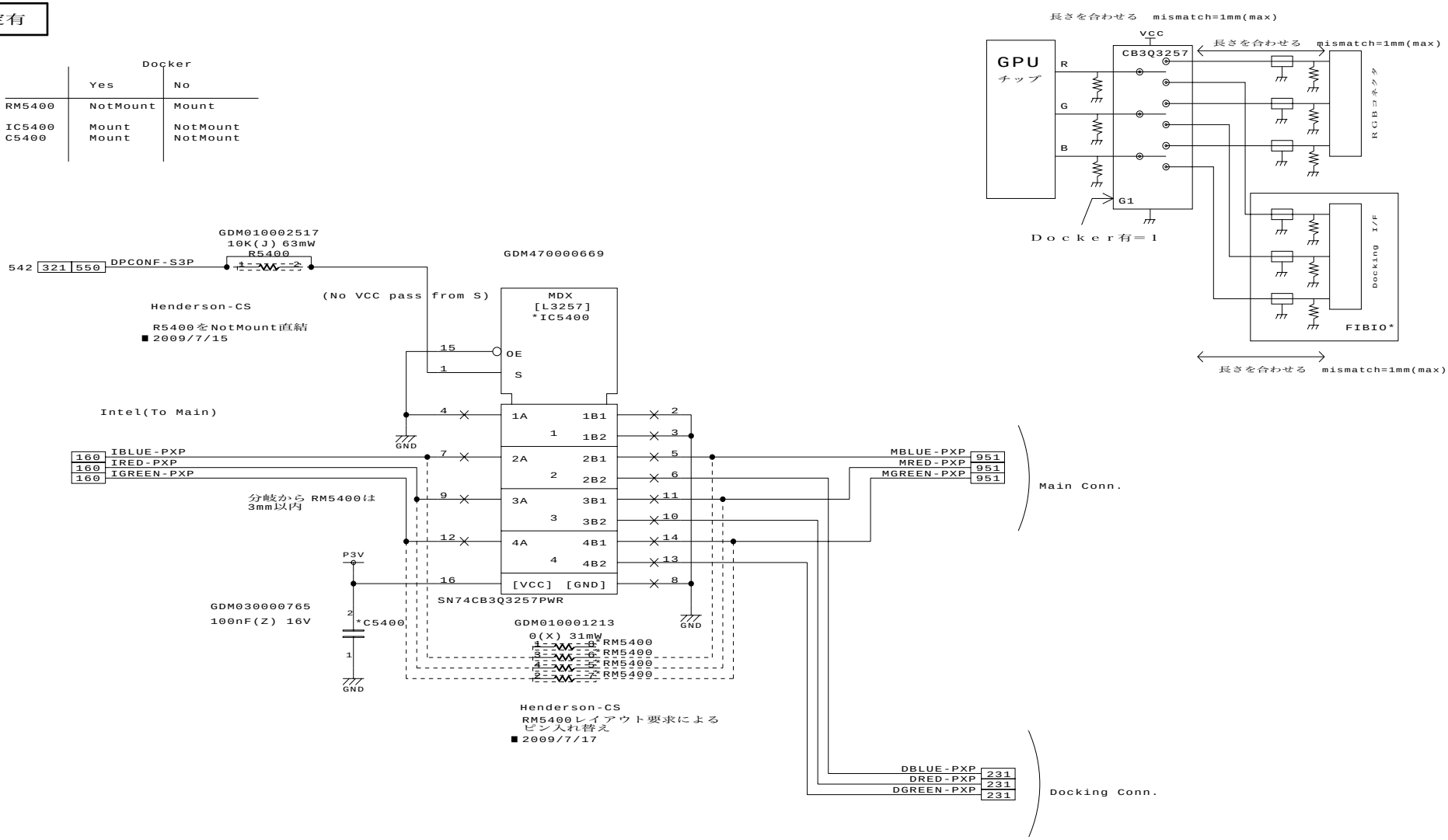


DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	LCD I/F	539	085	00	360069769

2009.10.15 17:09 G11

仕向け設定有

	Docker Yes	Docker No
RM5400	NotMount	Mount
IC5400	Mount	NotMount
C5400	Mount	NotMount

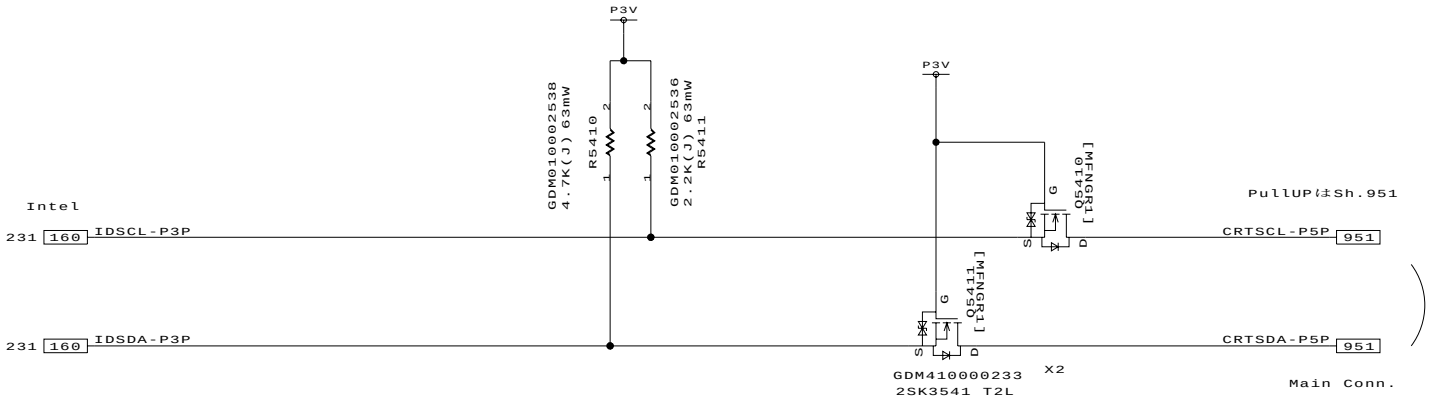


S(DPCONF)	L	H
CB3Q3257(S1)	ON	OFF
CB3Q3257(S2)	OFF	ON

TOSHIBA&NVIDIA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	CRT RGB	540	086	00	360069769

2009.10.15 17:09 G11

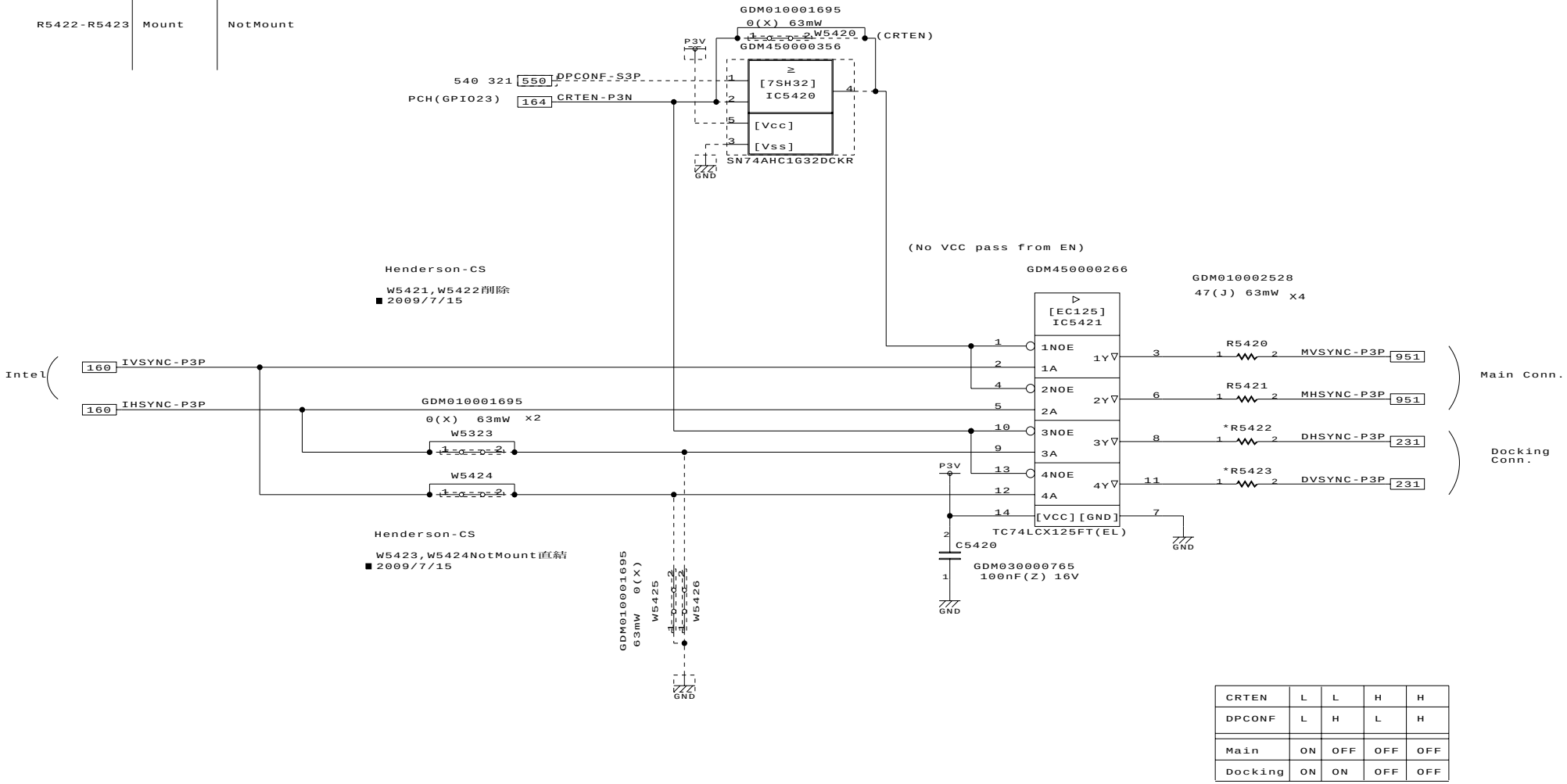


DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	CRT DDC	541	087	00	360069769

2009.10.15 17:09 G11

仕向け設定有

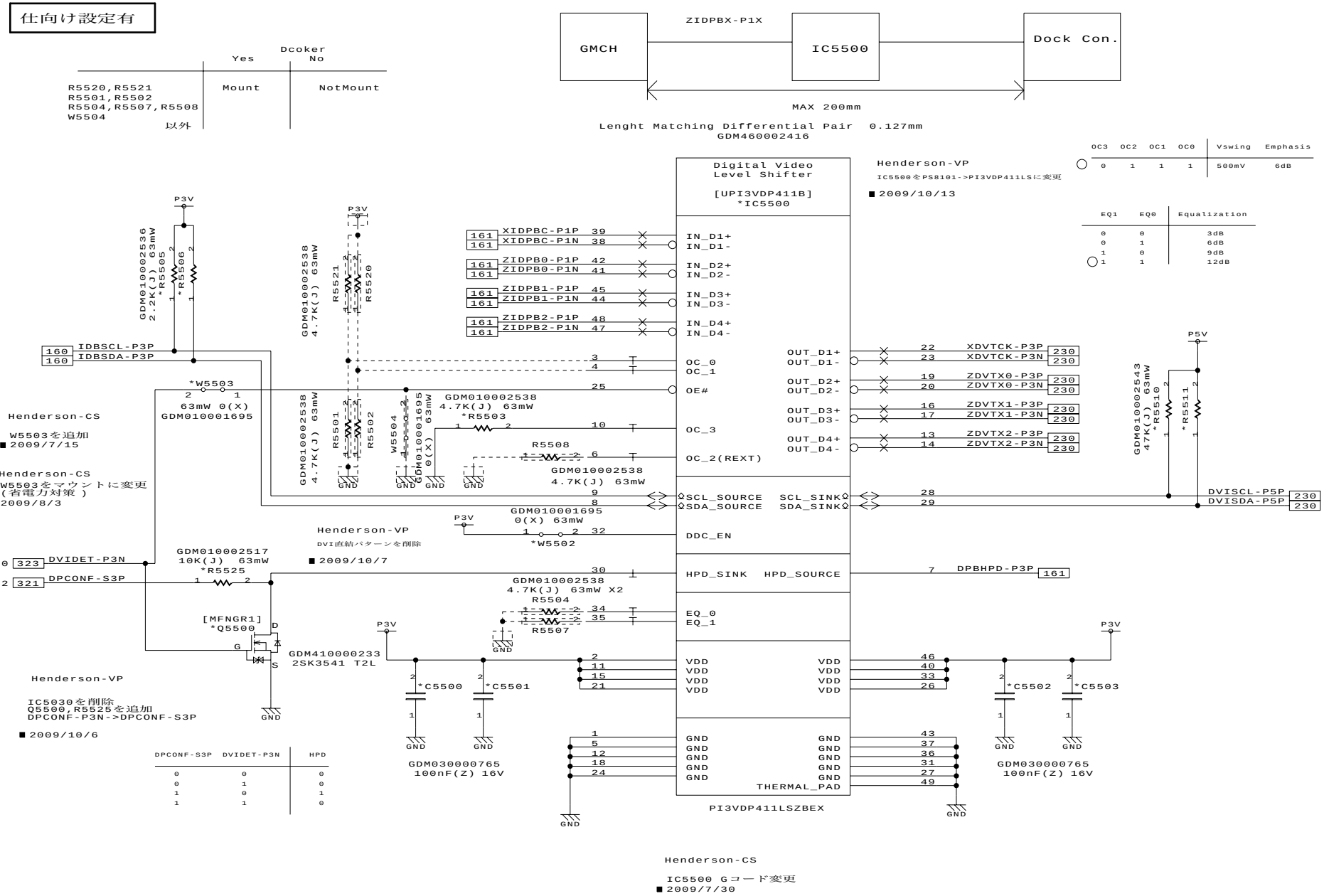
	Docker	
	Yes	No
R5422-R5423	Mount	NotMount



CRTEN	L	L	H	H
DPCONF	L	H	L	H
Main	ON	OFF	OFF	OFF
Docking	ON	ON	OFF	OFF

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	CRT H/VSync	542	088	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	DVI Level Shifter	550	089	00	360069769

2009.10.15 17:09 G11

1 2 3 4 5 6 7 8

A B C D E F

Henderson-VP

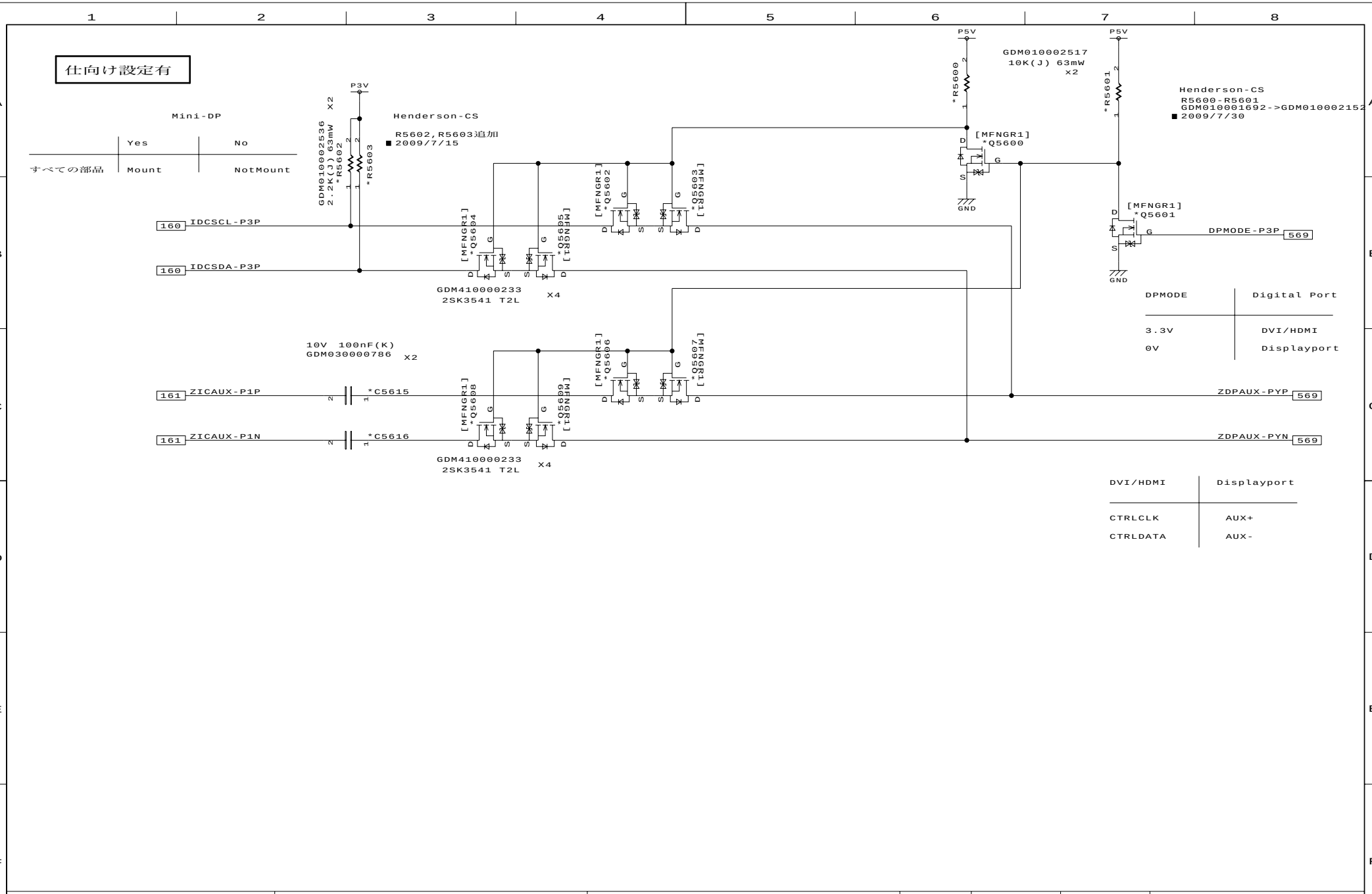
DVI直結パターンを削除

■ 2009/10/7

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	DVI Jumper	551	090	00	360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse	FHNSY1	Displayport Dongle	560	091	00	360069769

2009.10.15 17:09 G11

仕向け設定有

Mini-DP

	Yes	No
D5690以外 R5699,R5700 Q5690,Q5691	Mount	NotMount
R5705	NotMount	Mount
R5706	Mount	NotMount

Henderson-CS
Add DPMDL#,R5705,R5706
For DP Detection
■ 2009/7/6

GDM220002196
[C20A4]
*CN5690
1 GND
2 HPD
3 ML_LANE0+
4 DP_MODE
5 ML_LANE0-
6 CEC
7 GND
8 GND
9 ML_LANE1+
10 ML_LANE3+
11 ML_LANE1-
12 ML_LANE3-
13 GND
14 GND
15 ML_LANE2+
16 AUX_CH+
17 ML_LANE2-
18 AUX_CH-
19 GND
20 DP_PWR
3V112M3-RH3TTB-7H

Henderson-CS
K2203168->GDM220002196
■ 2009/7/24

ZDPTX0-P1P
ZDPTX0-P1N
ZDPTX1-P1P
ZDPTX1-P1N
ZDPTX2-P1P
ZDPTX2-P1N
ZDPTX3-P1P
ZDPTX3-P1N
DPMODE-P3P
*R5698 GDM010002530 100(J)63mW
*R5691 GDM010002719 1M(J) 63mW
Henderson-CS
R5698追加
■ 2009/7/15

DPMDL-P3N
*R5705 GDM010002543 47K(J) 63mW X2
*R5706
GDM010002544 100K(J) 63mW *R5695
ZDPAUX-PYP
ZDPAUX-PYN
GDM010002544 100K(J) 63mW *R5696

Henderson-VP
DP_PWR回路変更
■ 2009/10/06

Henderson-VP
DP_PWR回路変更
■ 2009/09/24

GDM470001177
[L1922B]
*IC5600
GDM030000657 4.7uF(K)6.3V *C5690
IN IN IN OUT OUT OUT OUT EN OC GND EP
MAX1922ETB+C3B
POWER
500mA
GDM010002534 *R5694 470(J) 63mW
DPPWOC-P3N
DPPWOCのPUは、 EC側
DPPWONのPUは、 EC側
DPPWON-P3N

Henderson-VP
Diode削除
■ 2009/09/29
*C5699
GDM030001170 10uF(K)6.3V

GDM010002524 1K(J) 63mW *R5692
[MFNGR1] *Q5693
GDM410000233 2SK3541 T2L
*R5693 GDM010002526 470K(J) 63mW

[MFNGR1] Q5690
GDM410000233 2SK3541 T2L
[MFNGR1] Q5691
VGS=1.5V
*W5690
GDM010001695 0(X) 63mW

Henderson-CS
W5690を追加
■ 2009/7/23

GDM010002517 10K(J) 63mW x2
R5699 R5700

Henderson-CS
R5699,R5700,Q5690,Q5691をNotMount
■ 2009/7/23

GPU
HPDDPG-S3P 161 323
EC

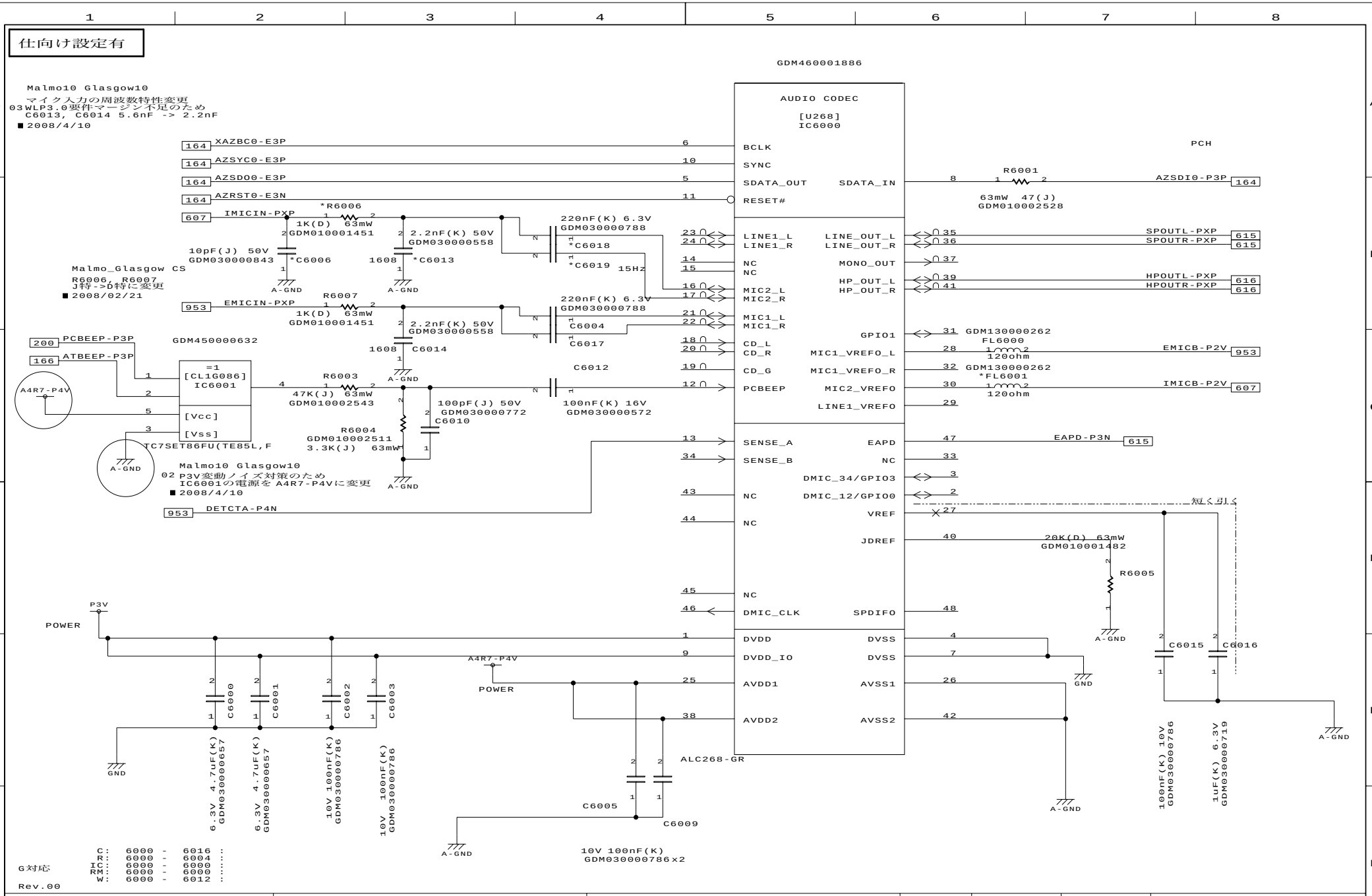
Henderson-CS
R5694,R5697を削除
■ 2009/7/16

Henderson-CS
HPDDPG-P3P->HPDDPG-S3P
■ 2009/7/23

Henderson-CS
IC5691をシュミットから FETに変更
■ 2009/7/15

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Endo/T.Naruse	FHNSY1	Displayport I/F	569	092	00	360069769

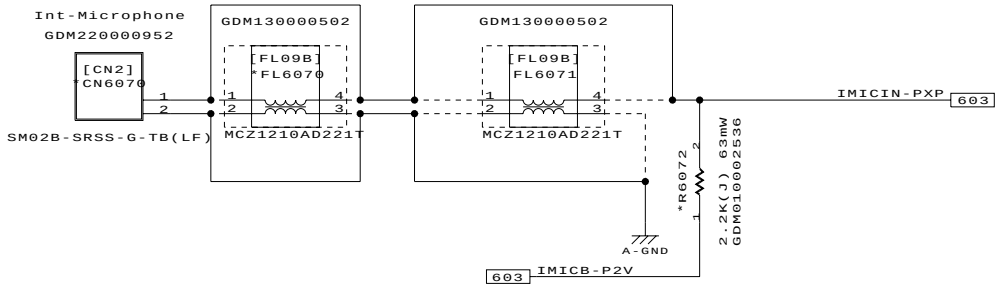
2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	ALC268	603	093	00	360069769

2009.10.15 17:09 G11

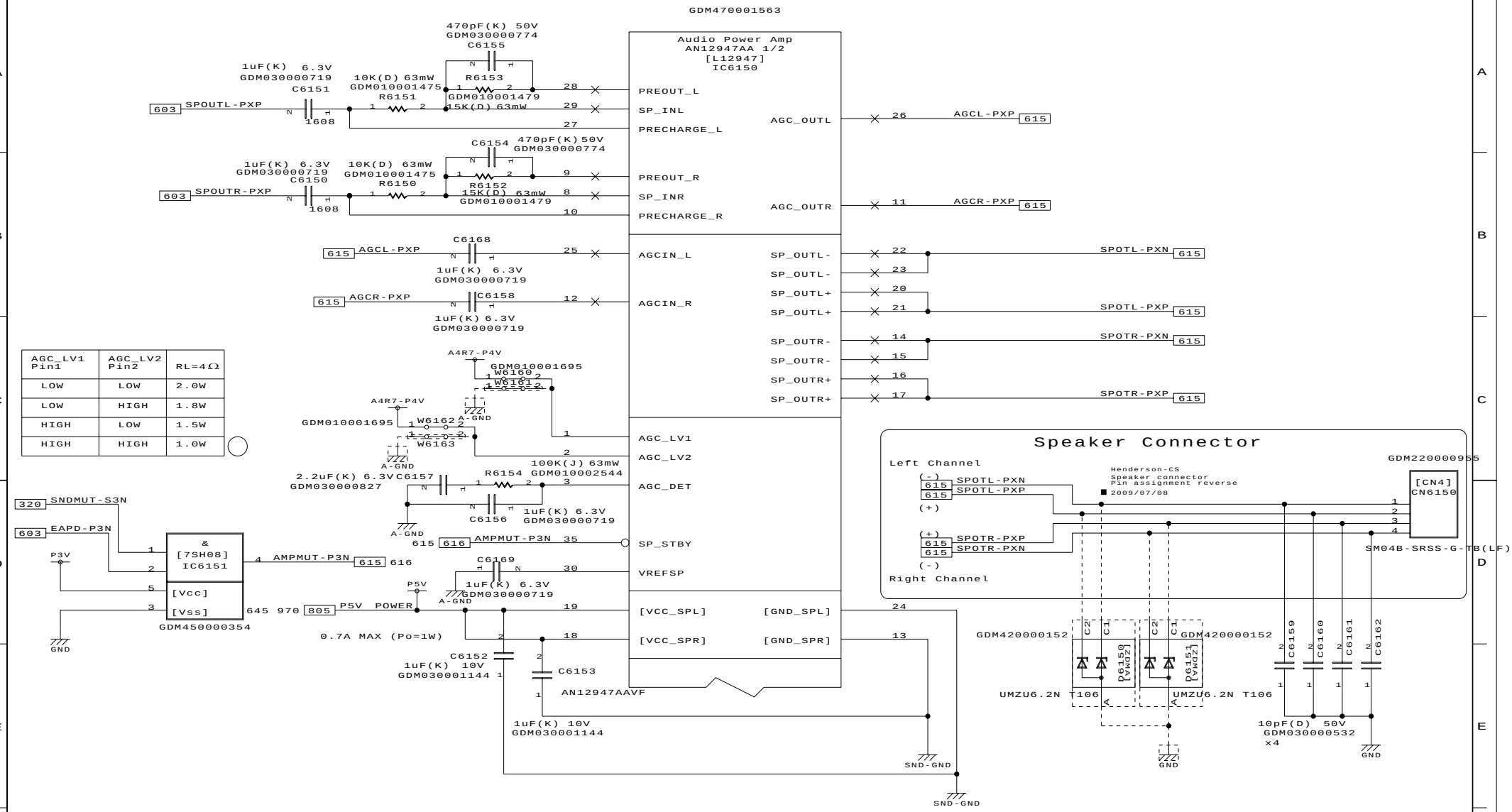
仕向け設定有



FL: 6050 - 6050 :
C: 6050 - 6052 :
R: 6050 - 6053 :
IC: 6050 - 6050 :
W: 6050 - 6050 :
PJ: 6050 - 6051 :

REV.01 鉛フリー済み

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	Int-MIC	607	094	00	360069769

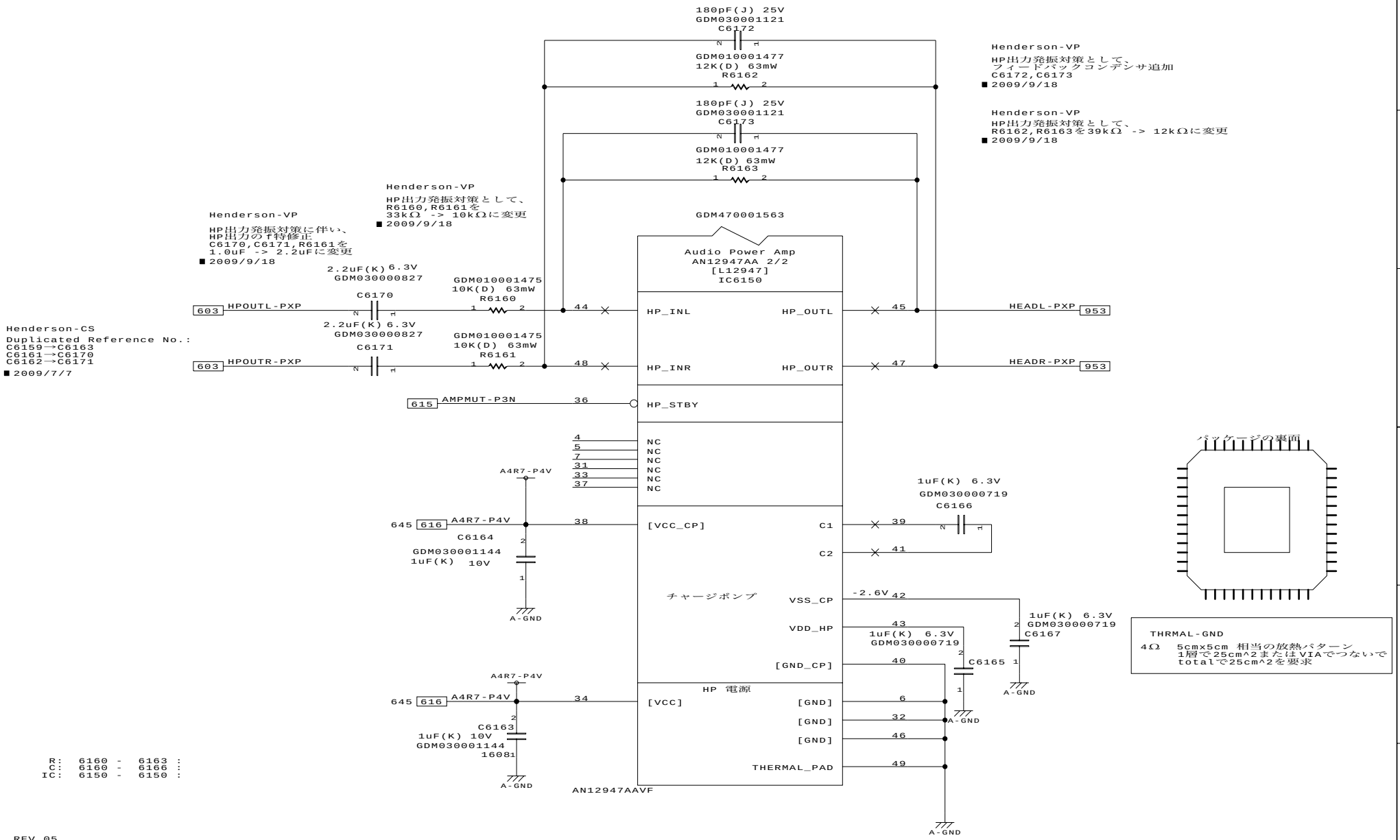


AGC_LV1 Pin1	AGC_LV2 Pin2	RL=4Ω
LOW	LOW	2.0W
LOW	HIGH	1.8W
HIGH	LOW	1.5W
HIGH	HIGH	1.0W

R: 6150 - 6153 :
C: 6150 - 6156 :
IC: 6150 - 6150 :

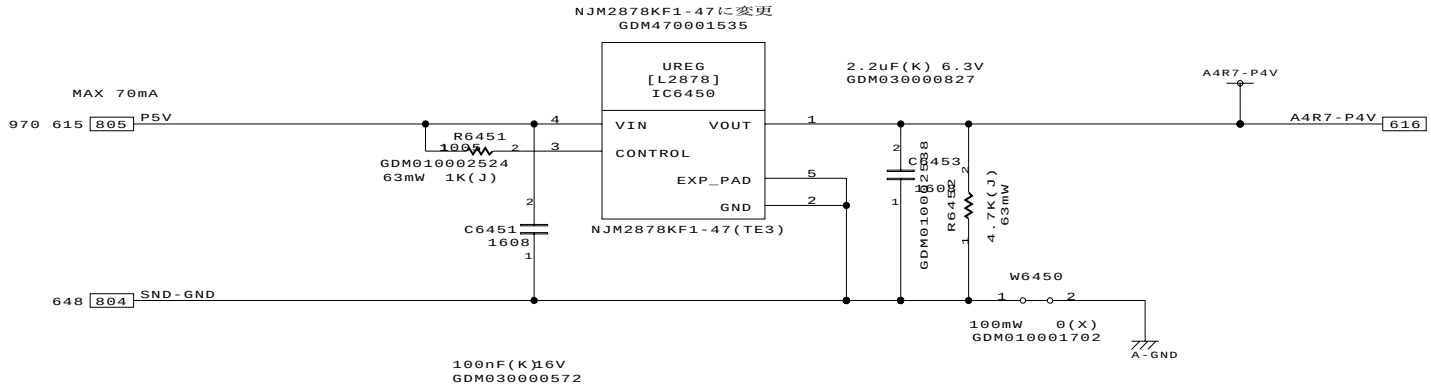
REV.05

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	AN12947AA(1)	615	095	00	360069769



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	AN12947AA(2)	616	096	00	360069769

2009.10.15 17:09 G11

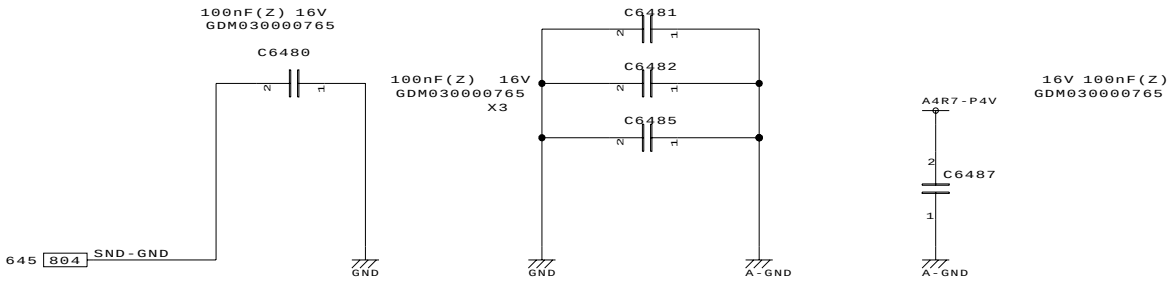


W: 6450 - 6450 :
C: 6451 - 6453 :
R: 6450 - 6451 :
IC: 6450 - 6450 :

REV.01 G対応済み

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	ANALOG POWER	645	097	00	360069769

Henderson-CS
リファレンス変更 :C6483→C6481、C6484→C6482(重複)
■2009/08/04

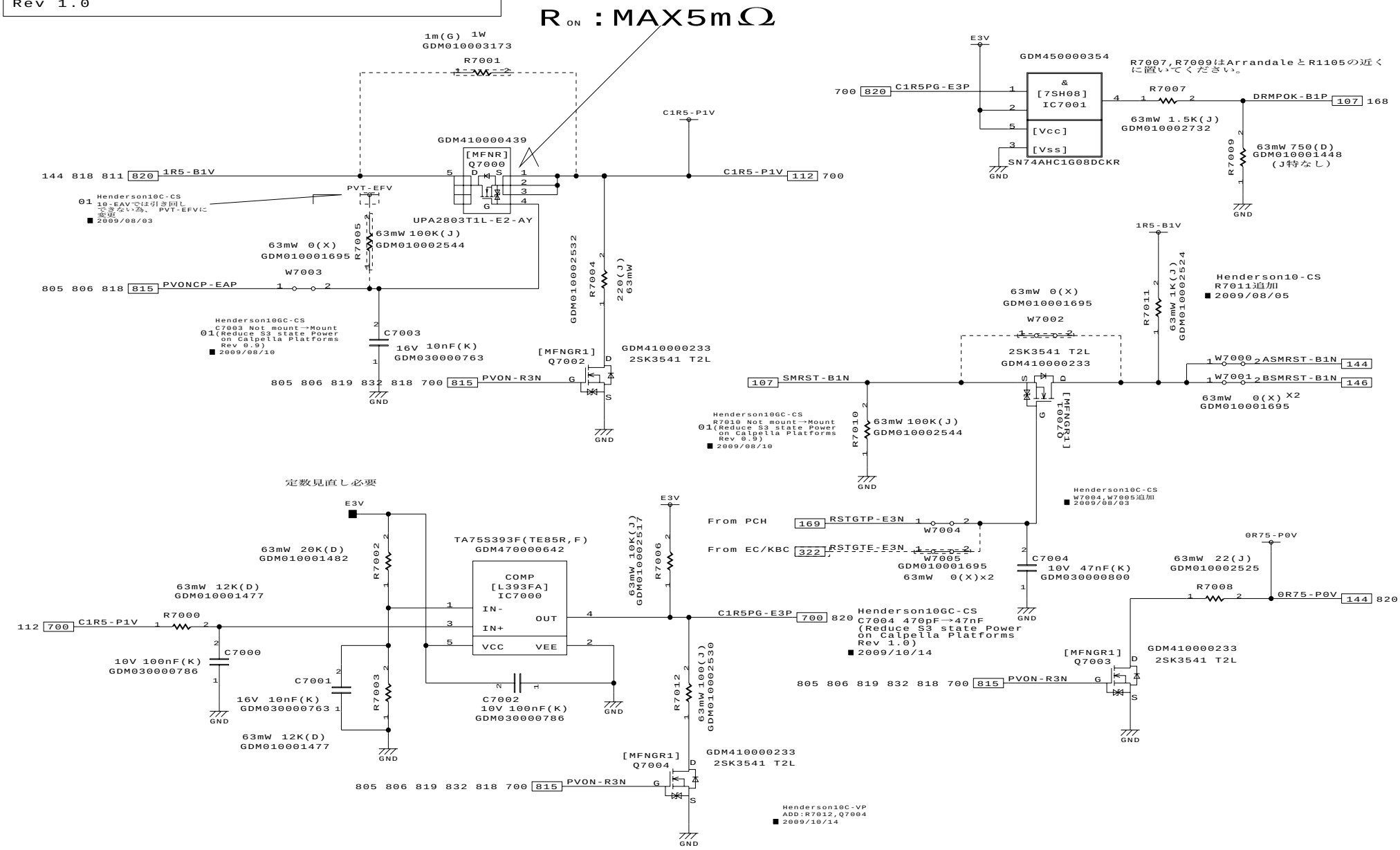


C: 6480 - 6490 :

REV.01 G対応済み

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	Pass-con	648	098	00	360069769

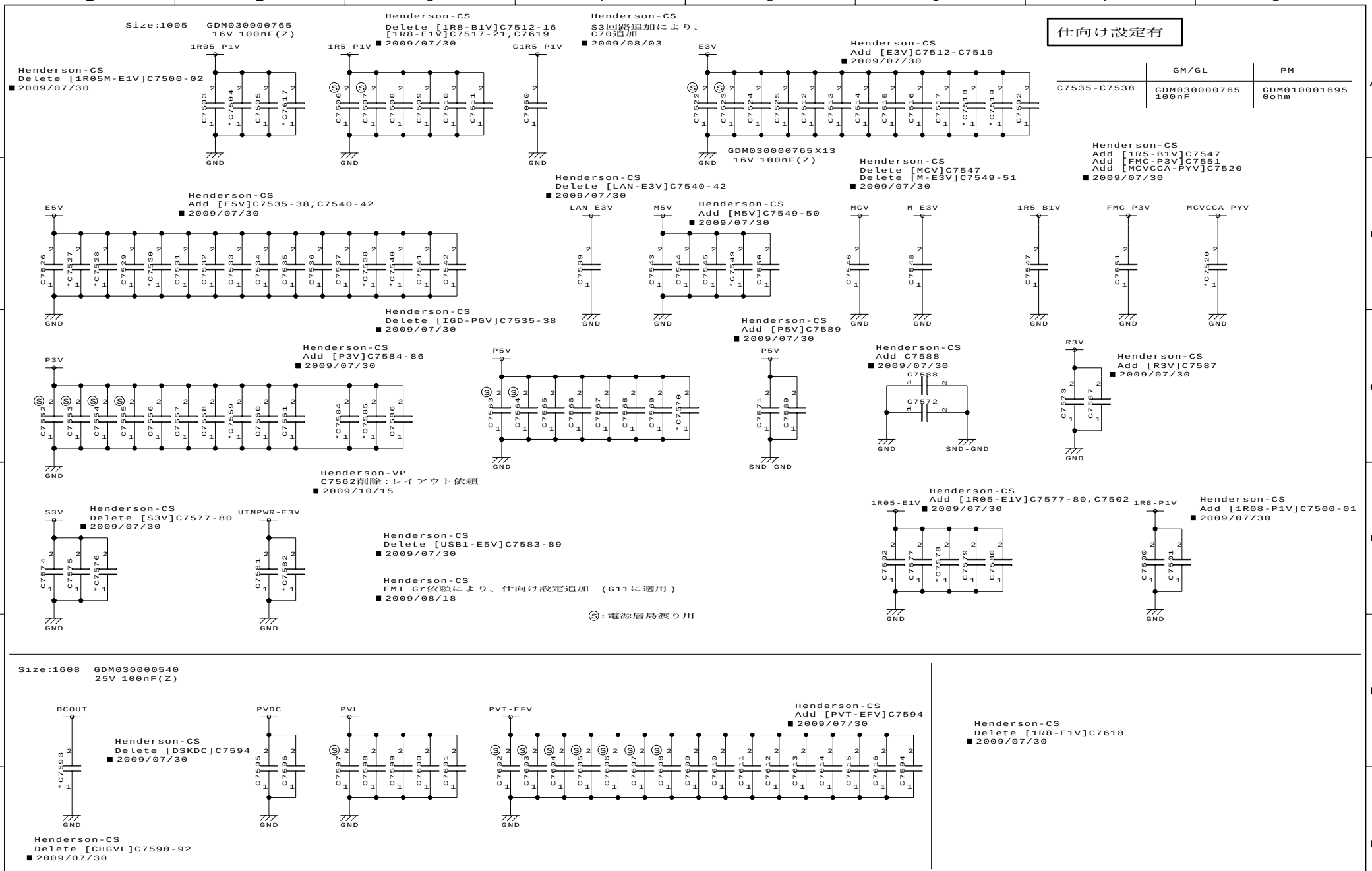
Reduce S3 state Power on Calpella Platforms Rev 1.0



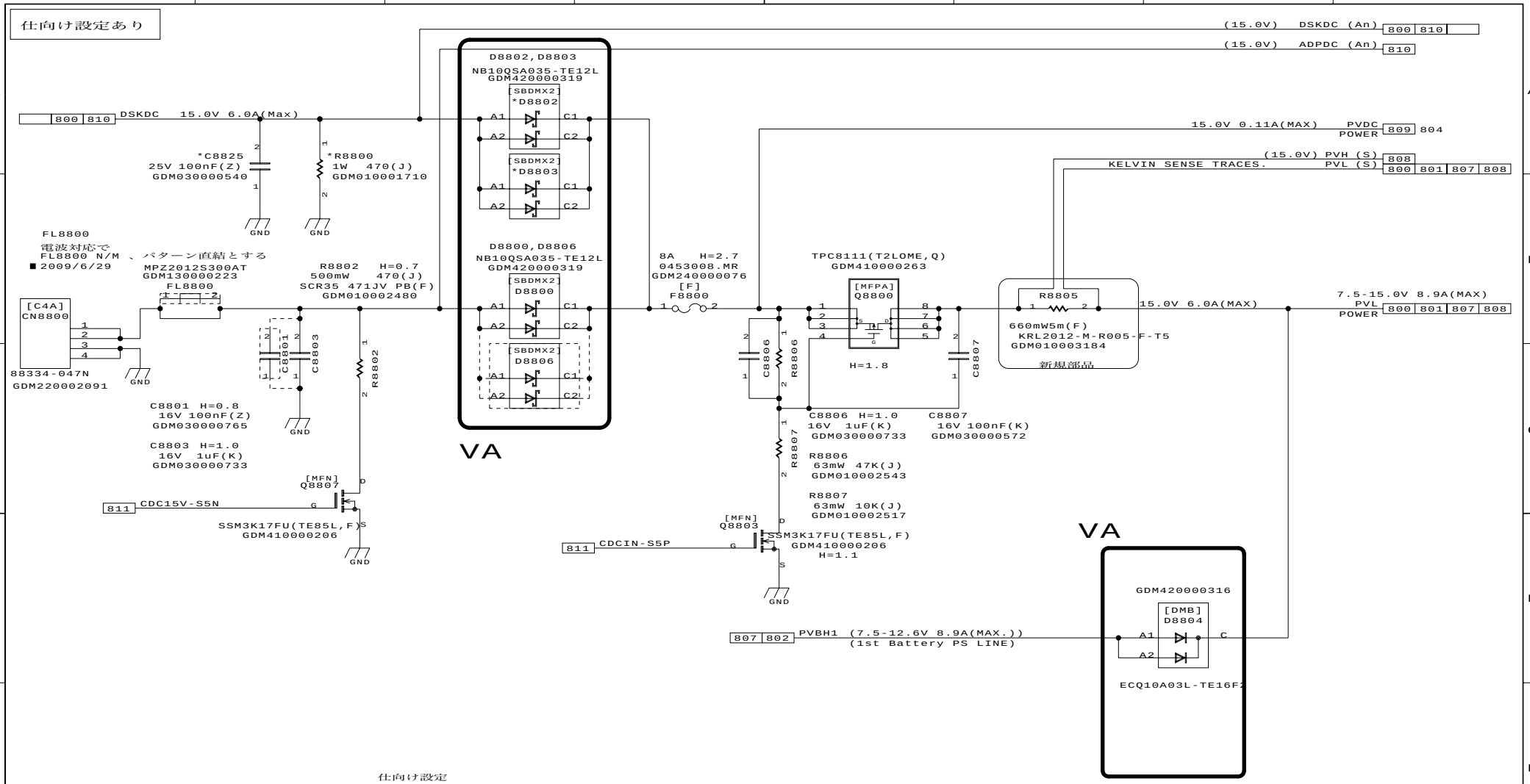
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Iwai/Y.Horie /T.Ochiai/T.Ichimura	FHNSY1	Reduce S3 Power	700	099	00	360069769

2009.10.15 17:09

TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	EMI CAP.	750	100	00	360069769



仕向け設定

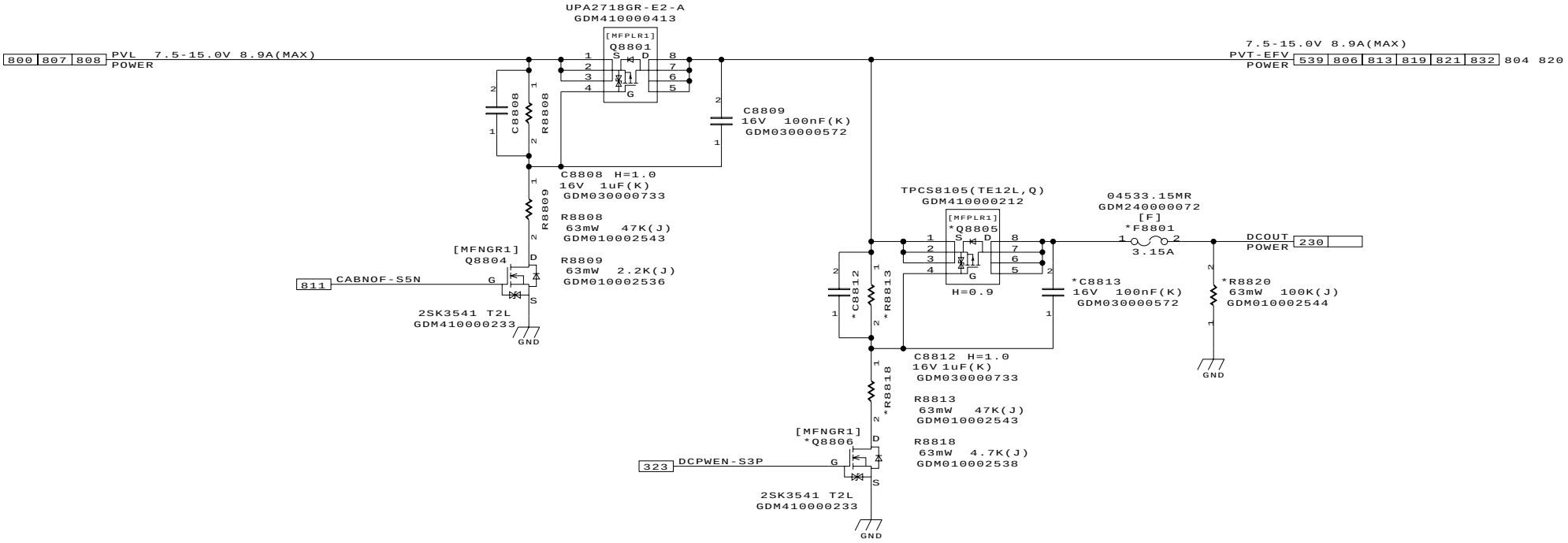
	Dockerサポート	Docker非サポート
Group		
C8825,R8800,D8802,D8803	Mount	Not Mount

～チェック・ポイント～
- □ 逆流防止ダイオード ,ヒューズの定格
- □ 引き抜き抵抗の定格、容量
- □ 電波、ハーネス等で発生する、パスコン
- □ ドッキング入力回路、出力回路
- □ バッテリー入力回路

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie 2009/8/17	FHNSY1	[PS]DC-IN	800	101	00	360069769

2009.10.15 17:09 G11

仕向け設定あり



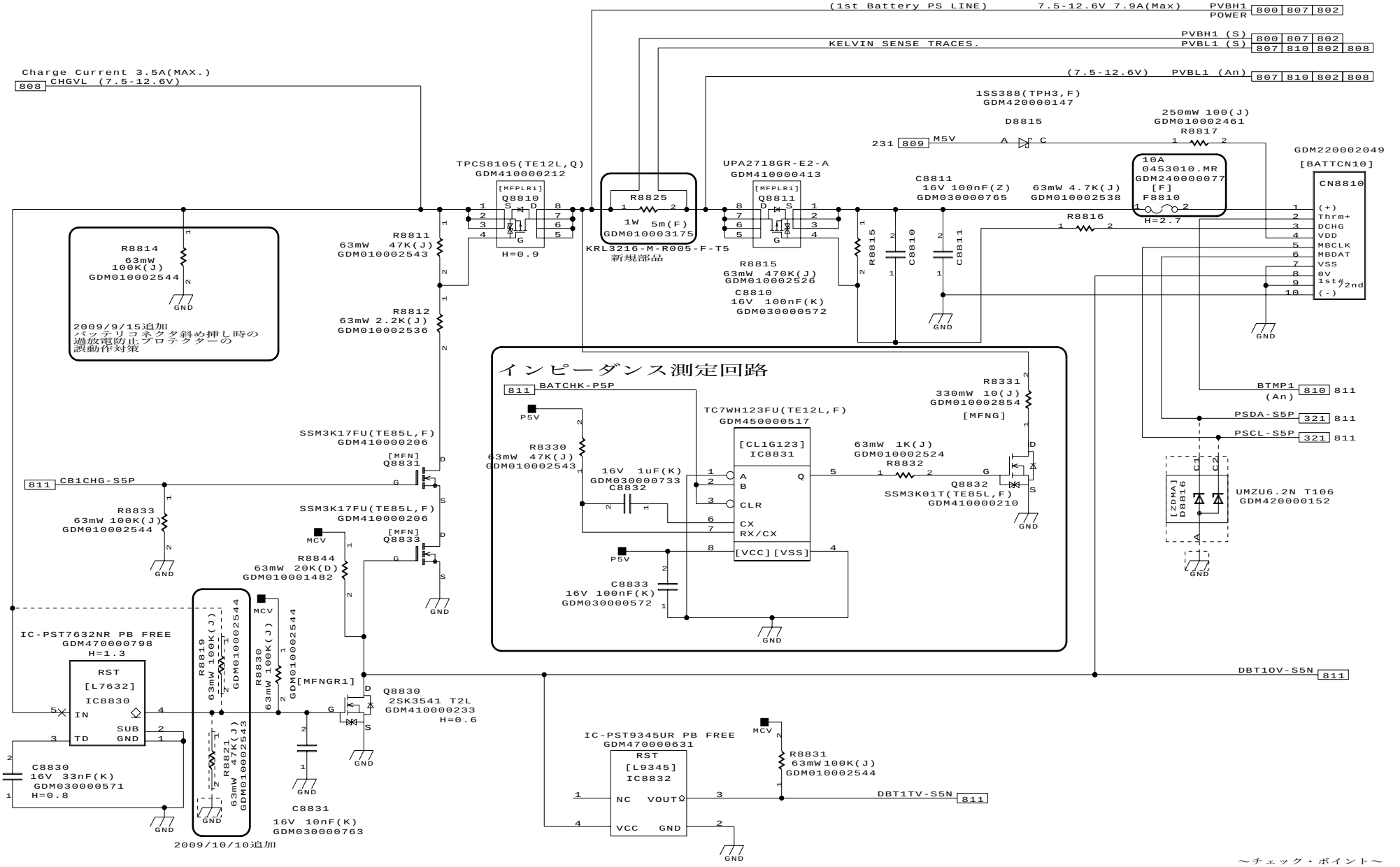
仕向け設定

Group	Dockerサポート	Docker非サポート
C8812,C8813,F8801,Q8805 Q8806,R8813,R8818,R8820	Mount	Not Mount

～チェック～
□ 部品定格
○FET
□ ドッキング出力回路

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]PVT-SW	801	102	00	360069769

2009.10.15 17:09 G11

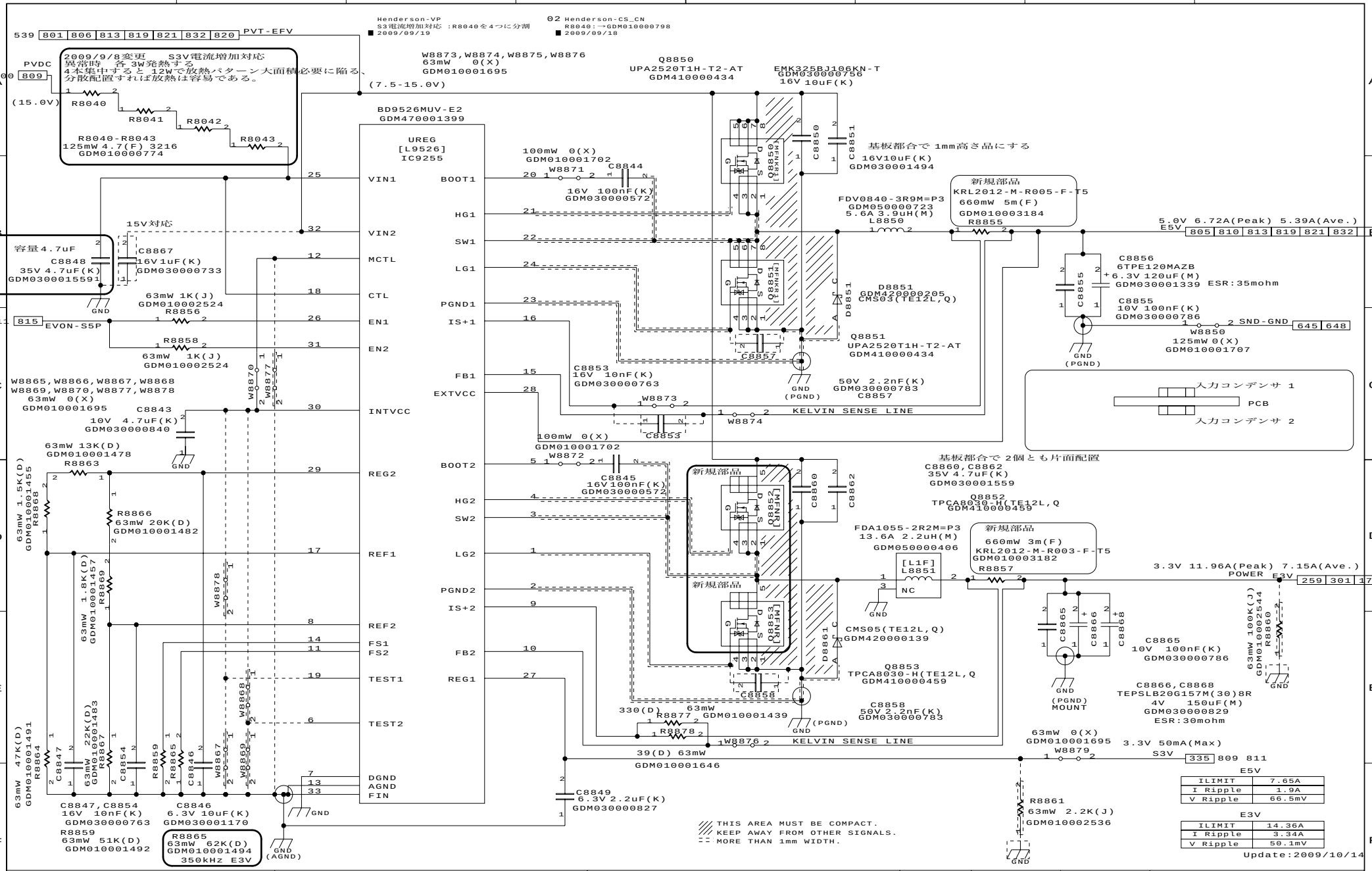


電安法 STEP2対応回路 2009/7/8

～チェック・ポイント～
□ 充電電流の定格
□ ヒューズの定格
□ バッテリーコネクタ

DESIGNED BY 2009/10/10	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]1st Battery	802	103	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/10 A.Kanou	FHNSY1	[PS]E5V/E3V	804	104	00	360069769

2009.10.15 17:09 G11

仕向け設定あり

NOT MOUNT
GDM410000439
UPA2803T1L-E2-AY
[MFNR]
Q9305

GDM410000298
TPCP8002(TE85L,F)
[MFNKR1]
Q9306

サウンド用 P5V はQ9306の根元から分岐してください

サウンド用 P5V P5V 5.0V, 0.23A
645 970 615 805

810 813 819 821 832 804 E5V

5.0V, 4.72A(max)

5.0V, 4.49A(max)
P5V
645 970 615 805
POWER

D8860
1SS388(TPH3,F)
GDM420000147

GDM410000439
UPA2803T1L-E2-AY
[MFNR]
Q9307
(2009/7/27変更)

(2009/7/27変更)
3.3V, 5.2A(max)
P3V
970 171 815
POWER

259 301 172 177 804 841 811 815 E3V

806 818 700 815 PVONCP-EAP (9.9V)

W8862 1005
63mW 0(X)
GDM010001695

TPC6004(TE85L,F)
GDM410000268
[MFNM]
*Q9309
Rdson
24[mohm](max) @Vgs=4.5[V]

3.3V 0.106A(max)
POWER M-E3V 300 172

2012 R8874
250mW 100(J)
GDM010002461

806 818 MEON-SAP

2009/7/16 VA
D8860追加して、引き抜き回路を削除した

[MFNGR1]
Q8867

806 819 832 818 700 815 PVON-R3N

2SK3541 T2L
GDM410000233

*R9309 1005
63mW 220(J)
GDM010002532

[MFNGR1]
*Q9310
2SK3541 T2L
GDM410000233

806 812 818 MEON-S3N

GND

～チェック・ポイント～
□ 入力電源ステータス
□ 出力電源ステータス
□ ＯＮ信号 (PVONCP-EAP,MEON-SAP)

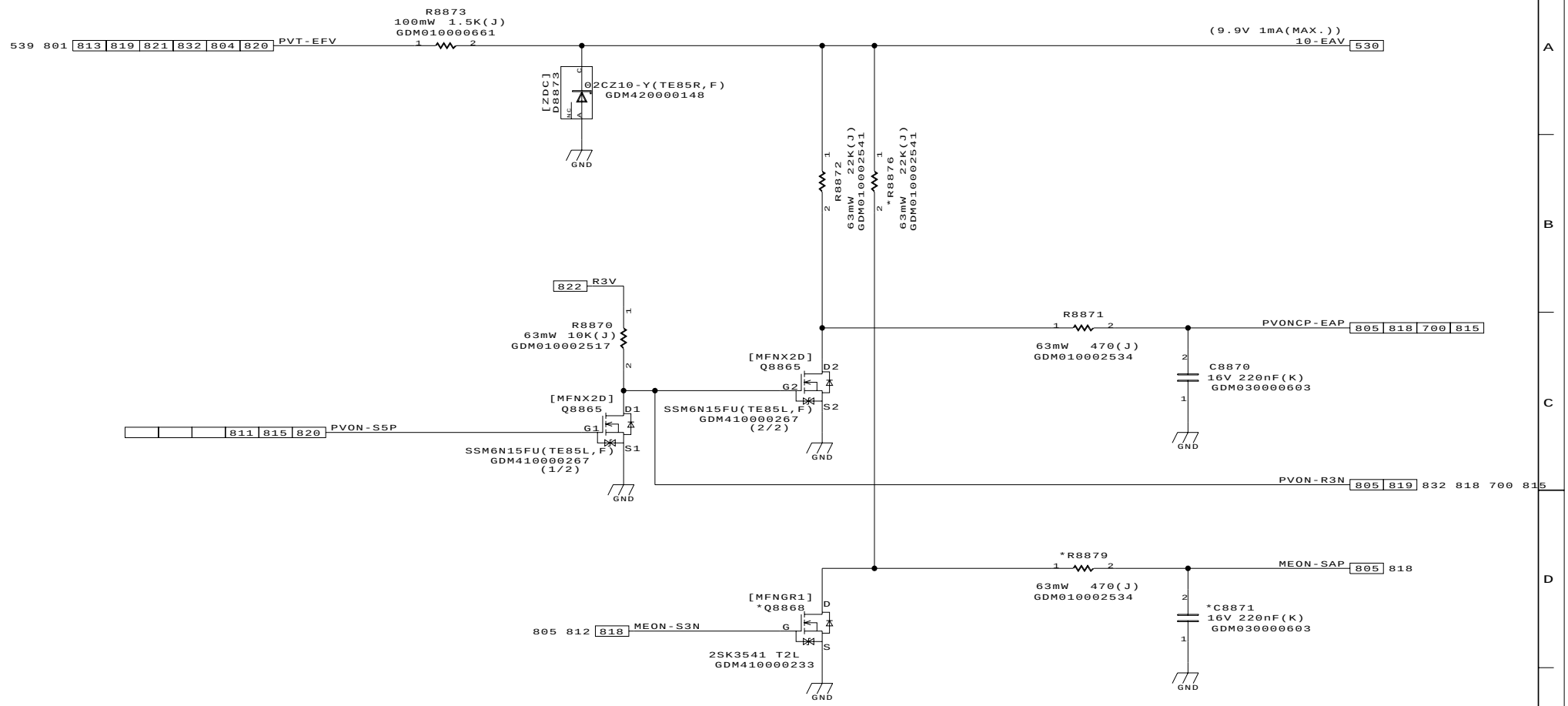
□ 電流に対する FETの損失定格
2SK3541 8Ω (max) (@VGS = 4.0V)
□引き抜き回路
○ P5V 100Ω + FET
○ P3V D + 100Ω + FET
○ M-E3V 220Ω + FET

仕向け設定

	AMTサポート	AMT非サポート
Q9309,Q9310 R9309	Mount	Not Mount
W8862	Not Mount	Mount

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/7/27 Y.Horie	FHNSY1	[PS] B*V/P*V	805	105	00	360069769

仕向け設定あり



仕向け設定

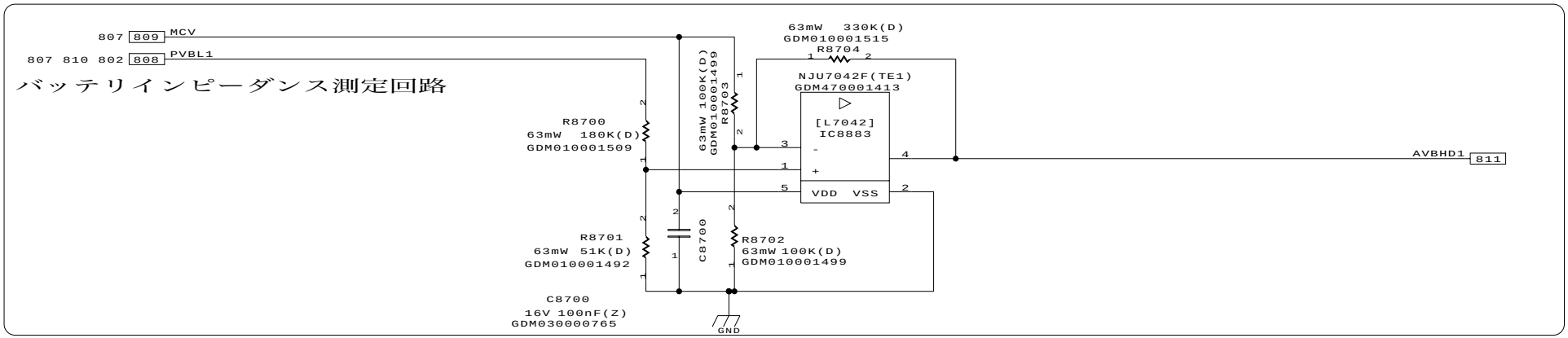
	AMTサポート	AMT 非サポート
R8876,R8879 C8871,Q8868	Mount	Not Mount

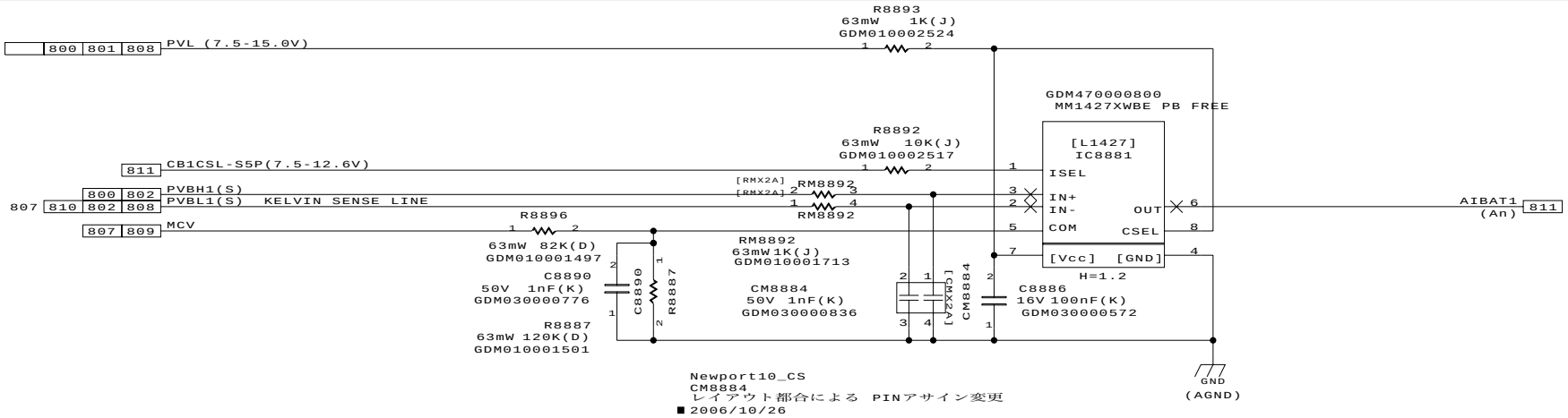
～チェック～

- □ 入力電圧 [PVT-EFV]
- □ 出力電圧 [10.0V]
- □ 負荷電流 [1mA Max]
- ○ 負荷抵抗
- ○ FETゲート漏れ電流
- □ 出力時定数 [ms]

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/7/3 Y.Horie	FHNSY1	[PS]E10V	806	106	00	360069769

2009.10.15 17:09 G11





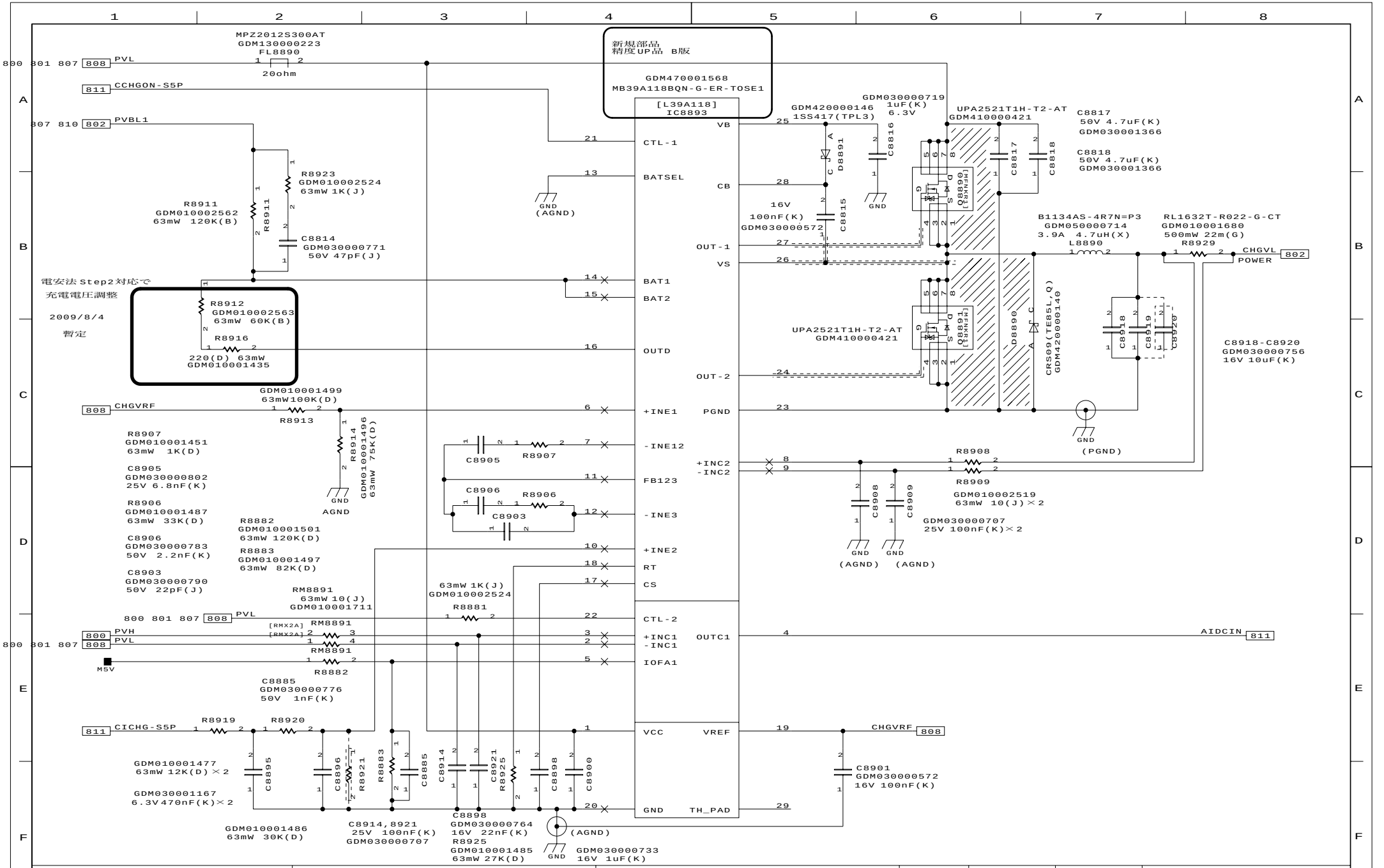
Newport10_CS
CM8884
レイアウト都合による PINアサイン変更
■ 2006/10/26

～チェック～
□ 2nd-Battery 有り/無し
□ 電流検出範囲
○ 電流検出抵抗値
○ 検出 ICのGain
○ PSC入力範囲

■ REF:NewPort10(CS) . fnpsy0_1102

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]CURRENT AMP	807	107	00	360069769

2009.10.15 17:09 G11



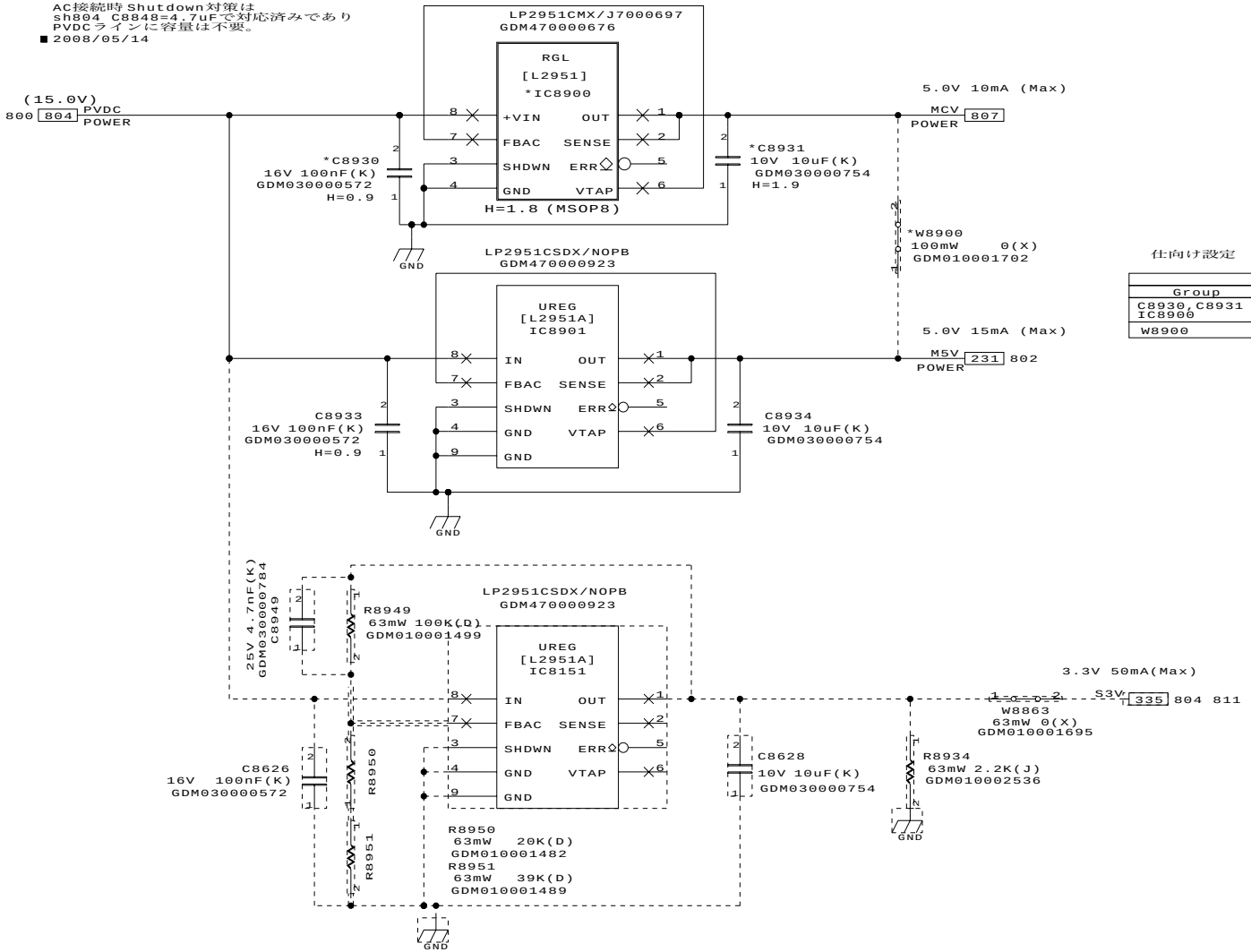
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie 2009/7/22	FHNSY1	[PS] CHARGE	808	108	00	360069769

2009.10.15 17:09 G11

仕向け設定あり

M5V Supply current
- LED : [10mA]
- Thermal : [1mA x 2 =2mA]
- IR Controller : [7.5mA]

AC接続時 Shutdown対策は
sh804 C8848=4.7uFで対応済みであり
PVDCラインに容量は不要。
■ 2008/05/14



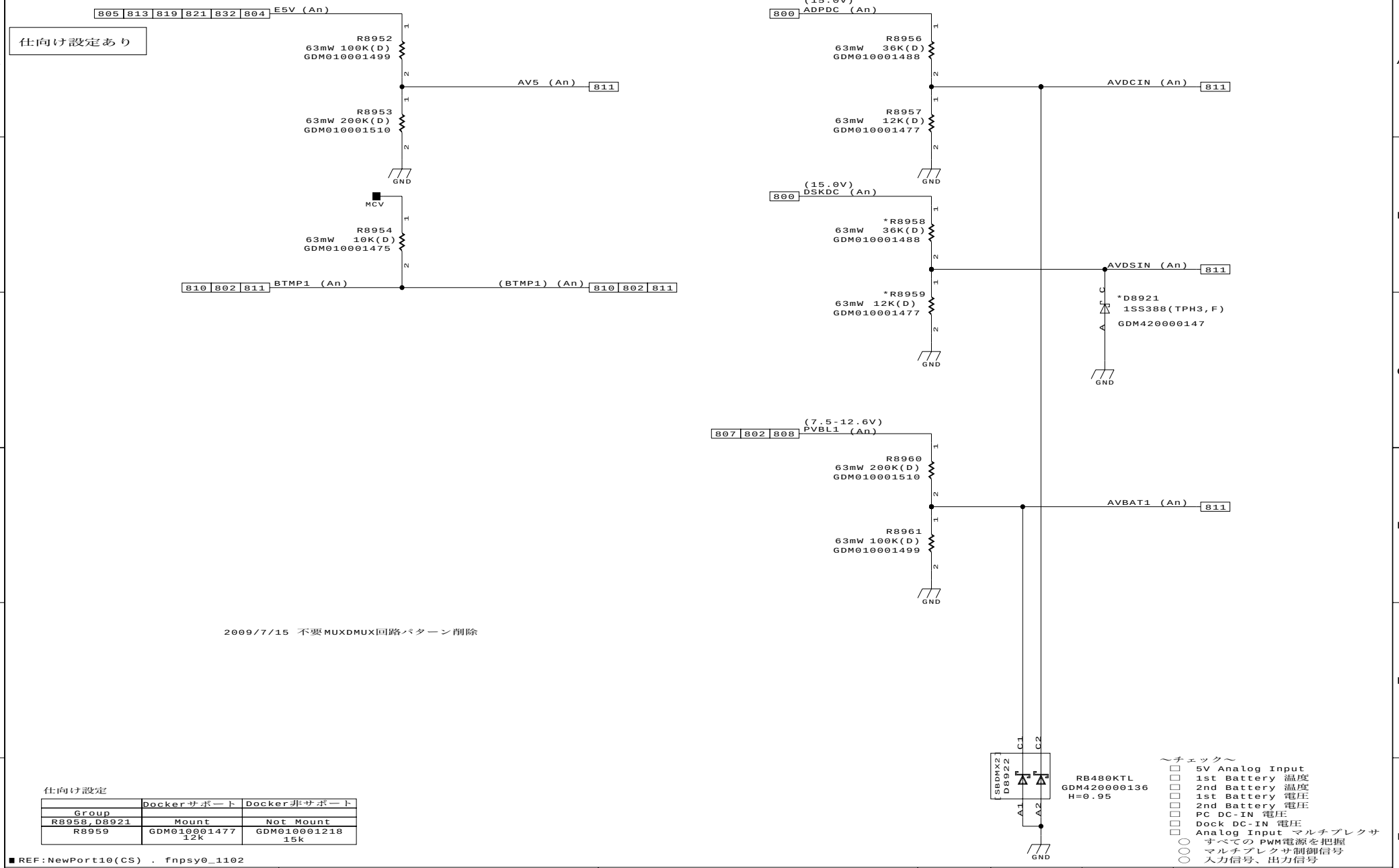
仕向け設定

	Dockerサポート	Docker非サポート
Group		
C8930,C8931 IC8900	Mount	Not Mount
W8900	Not Mount	Mount

～チェック～
- □ 出力電圧
- □ 出力電流定格

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie 2009/9/25	FHNSY1	[PS]S5V/S3V	809	109	00	360069769

2009.10.15 17:09 G11



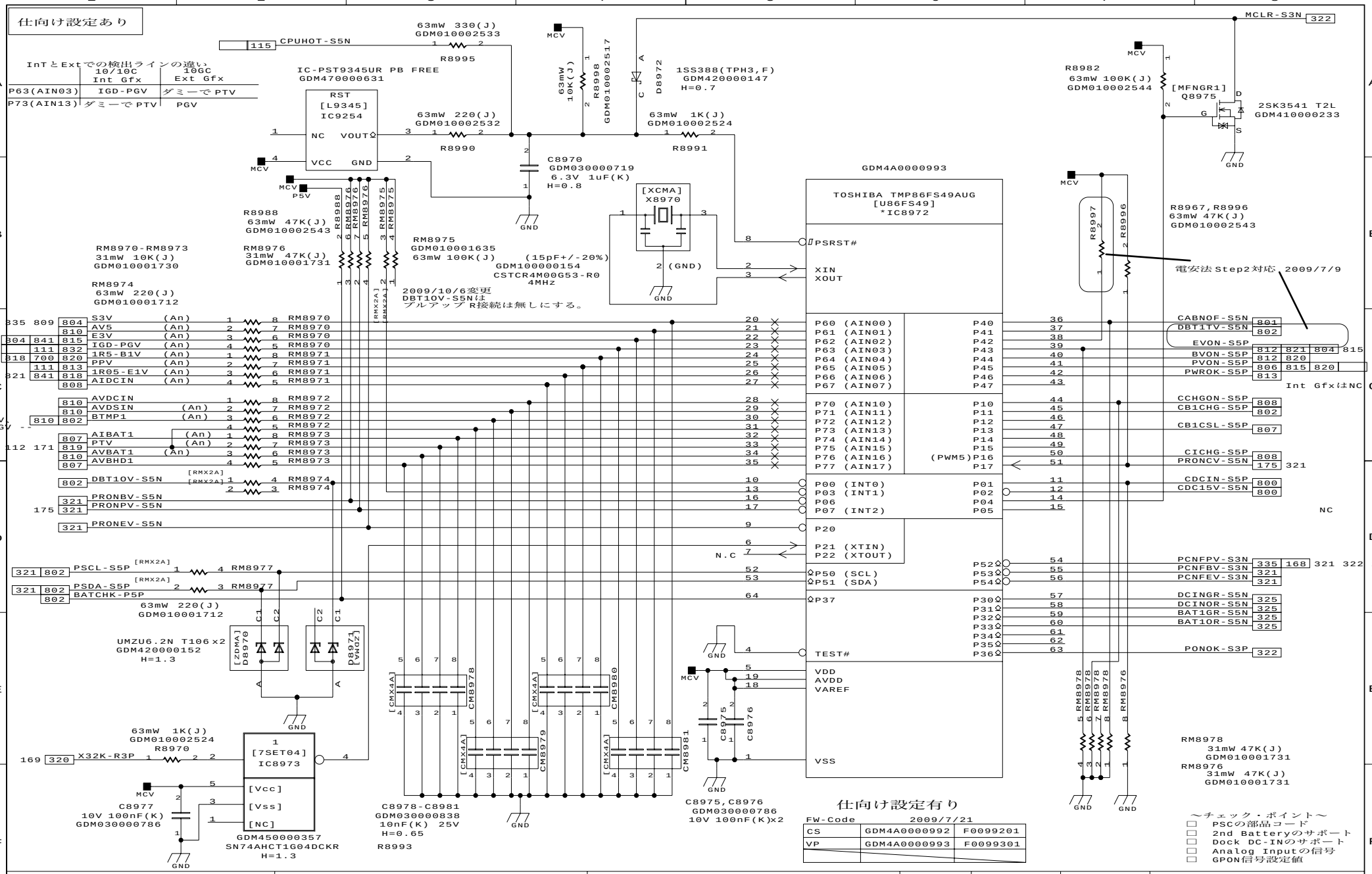
仕向け設定

	Dockerサポート	Docker非サポート
Group		
R8958,D8921	Mount	Not Mount
R8959	GDM010001477 12k	GDM010001218 15k

■ REF:NewPort10(CS) . fnpsy0_1102

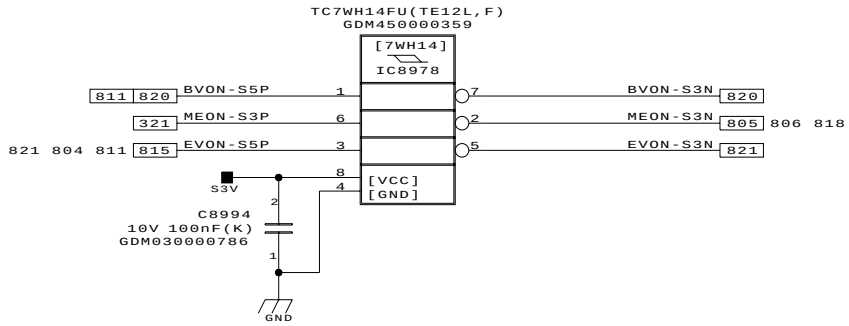
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie 2009/7/15	FHNSY1	[PS]AnalogInput	810	110	00	360069769

2009.10.15 17:09 G11



DESIGNED BY 2009/10/6	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]PSC(1)	811	111	00	360069769

2009.10.15 17:09 G11



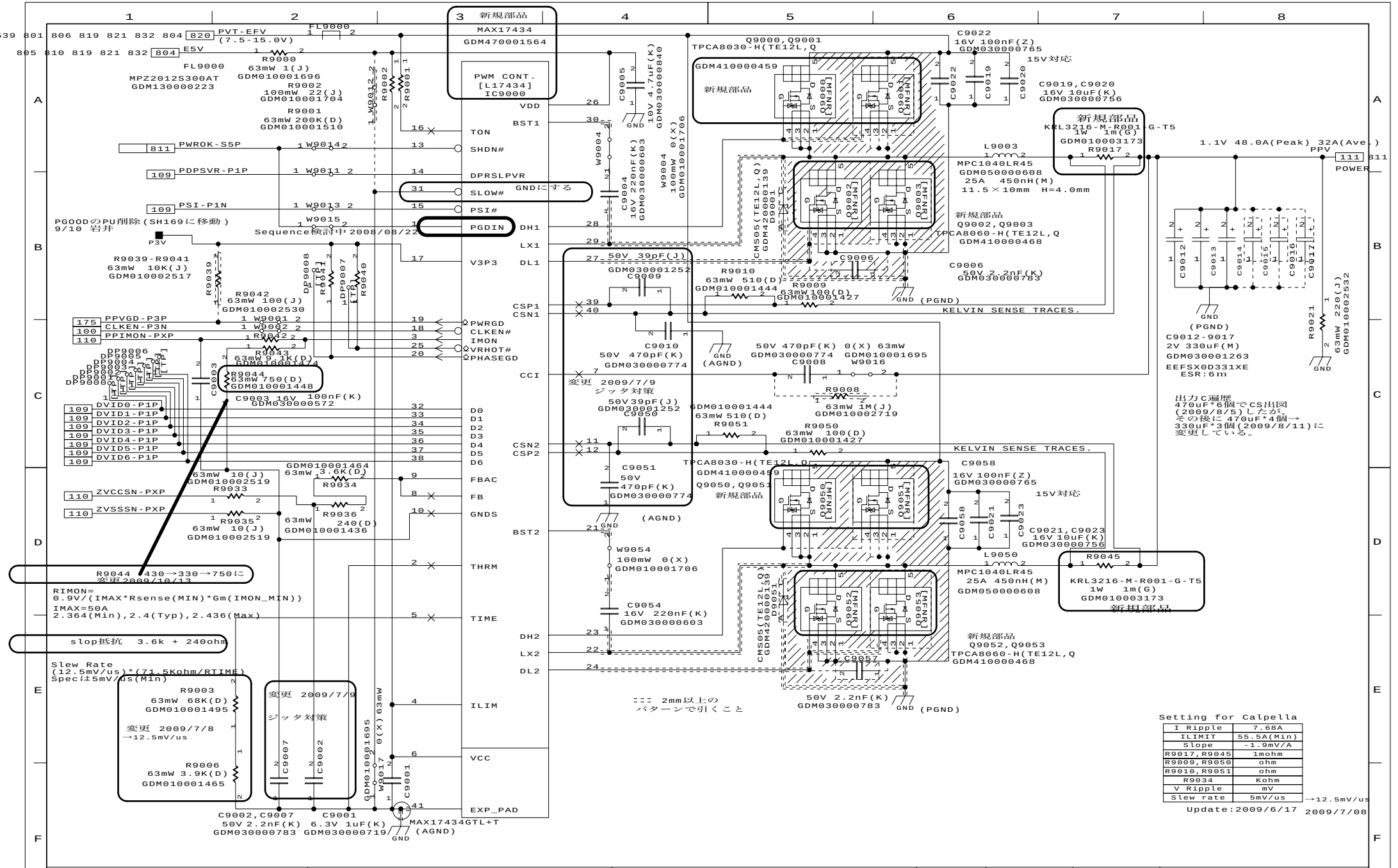
PPVGD-P3P回路削除　2009/7/3

Henderson-CS
Delete VRMPGD Cir.
(Move to SH100)
■2009/07/13

2009/7/3　2個→1個へ変更
このshに置く必要なし、システム側へ移動したい

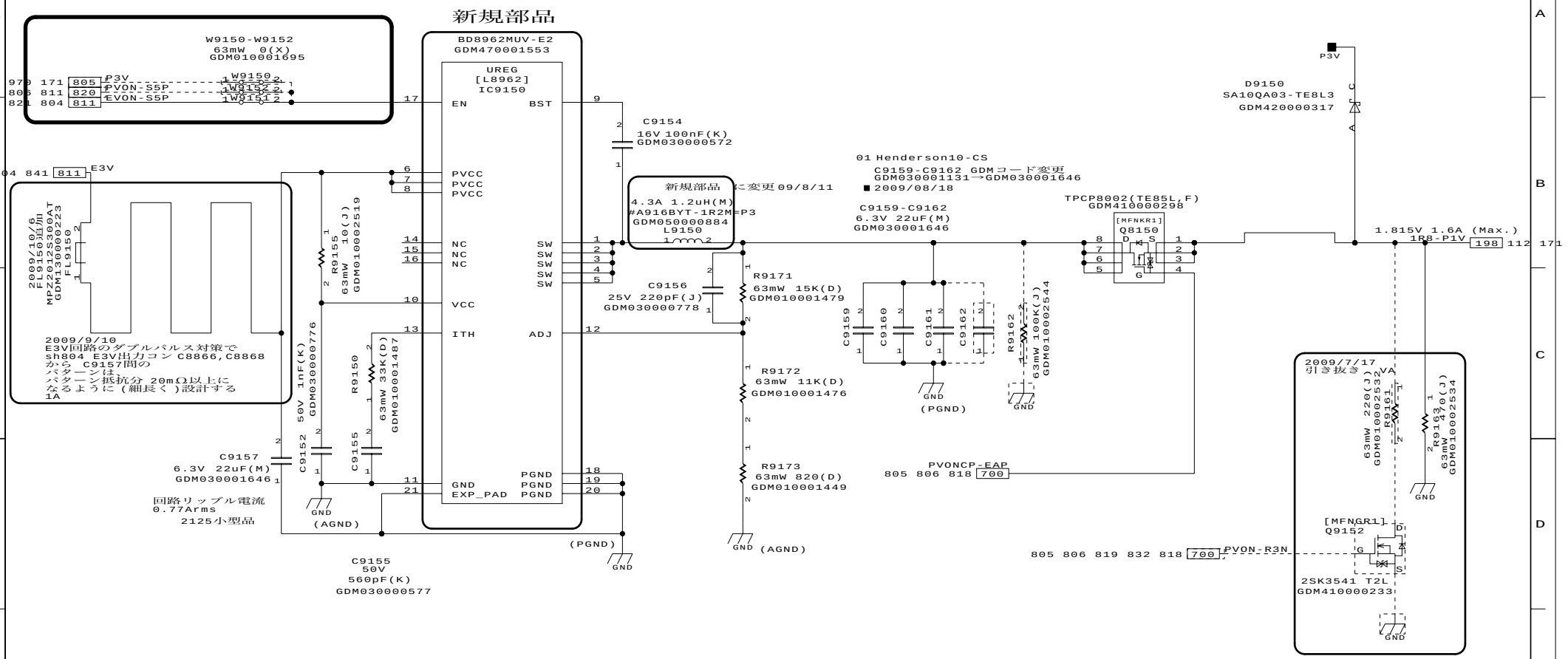
DESIGNED BY 2009/7/16	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]PSC(2)	812	112	00	360069769

2009.10.15　17:09　G11



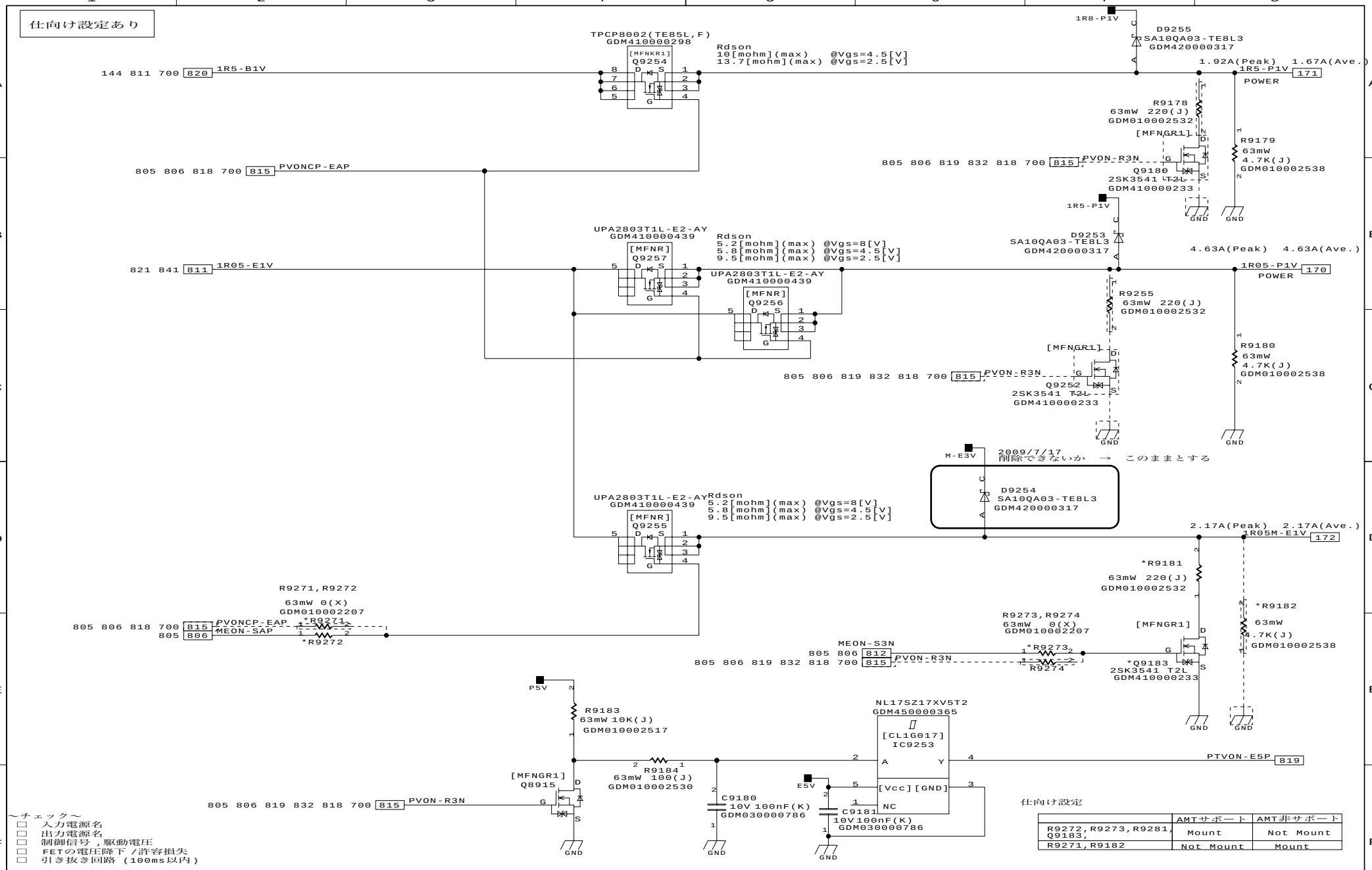
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/10 A.Kanou	FHNSY1	[PS]CPUVCC	813	113	00	360069769

2009.10.15 17:09 G11



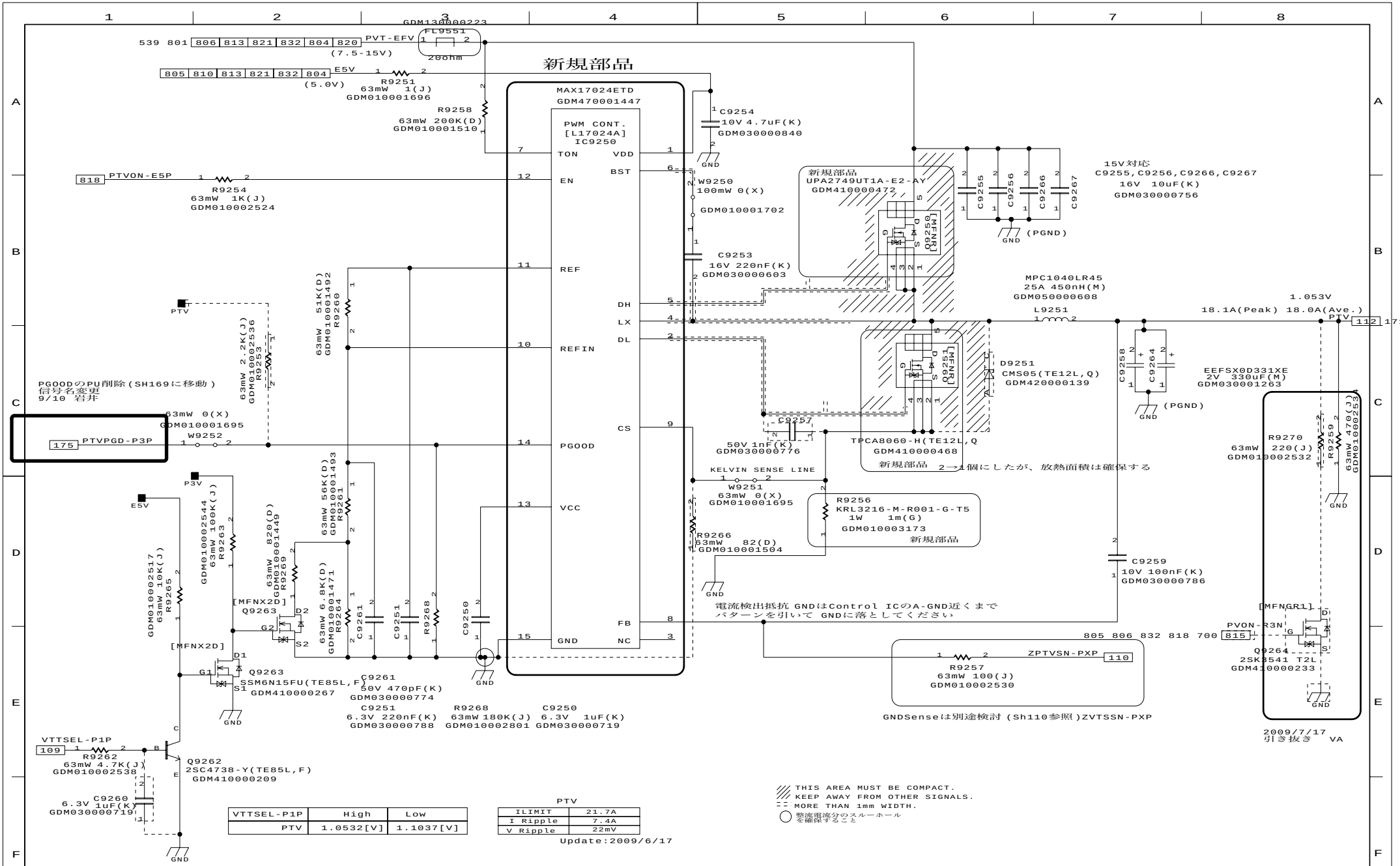
DESIGNED BY 2009/10/6	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
A.Kanou	FHNSY1	[PS]1R8-P1V	815	114	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie 2009/7/16	FHNSY1	[PS]LOAD SW2	818	115	00	360069769

2009.10.15 17:09 G11

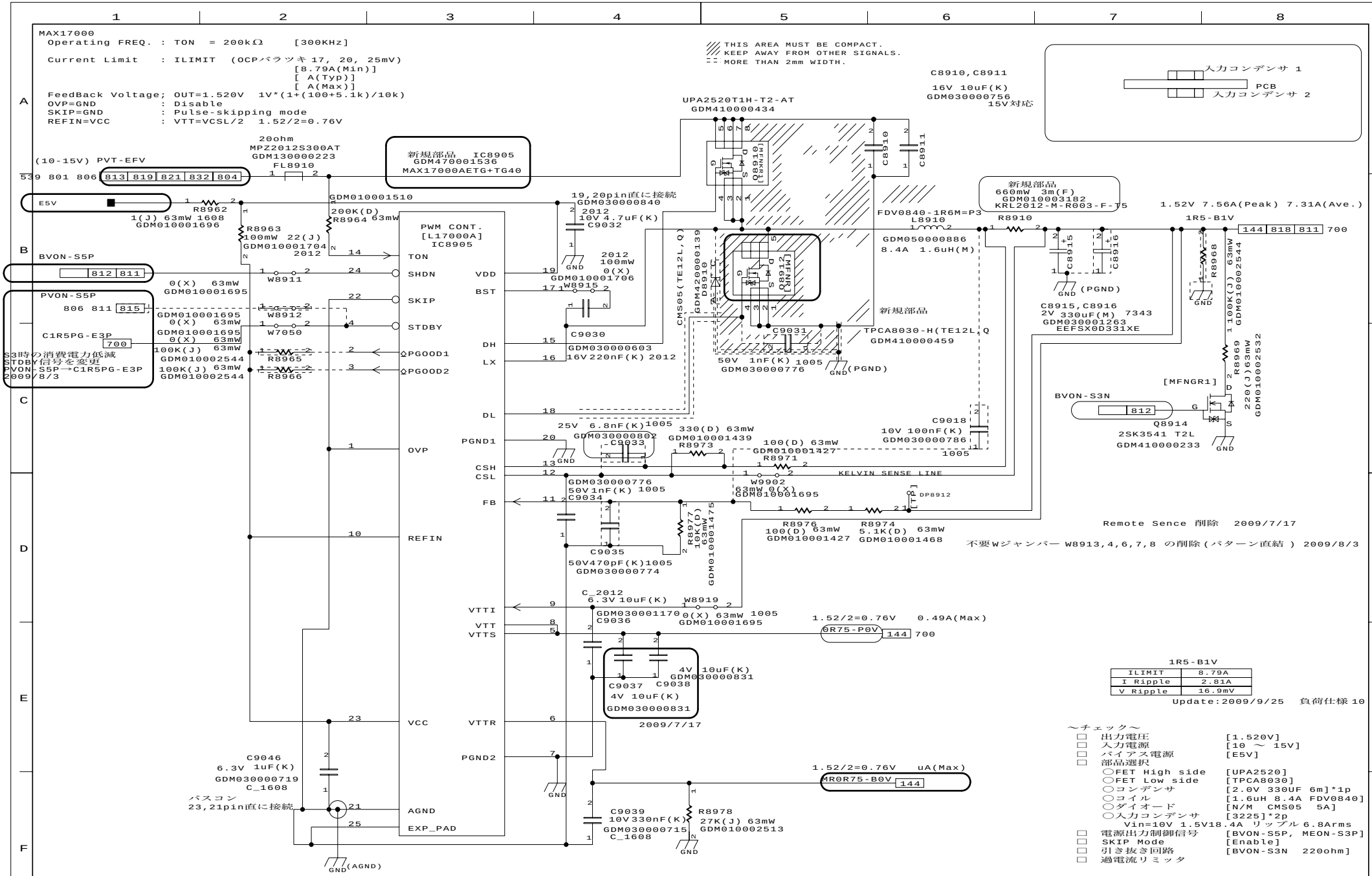


VTTSEL-P1P	High	Low
PTV	1.0532[V]	1.1037[V]

PTV	
ILIMIT	21.7A
I Ripple	7.4A
V Ripple	22mV

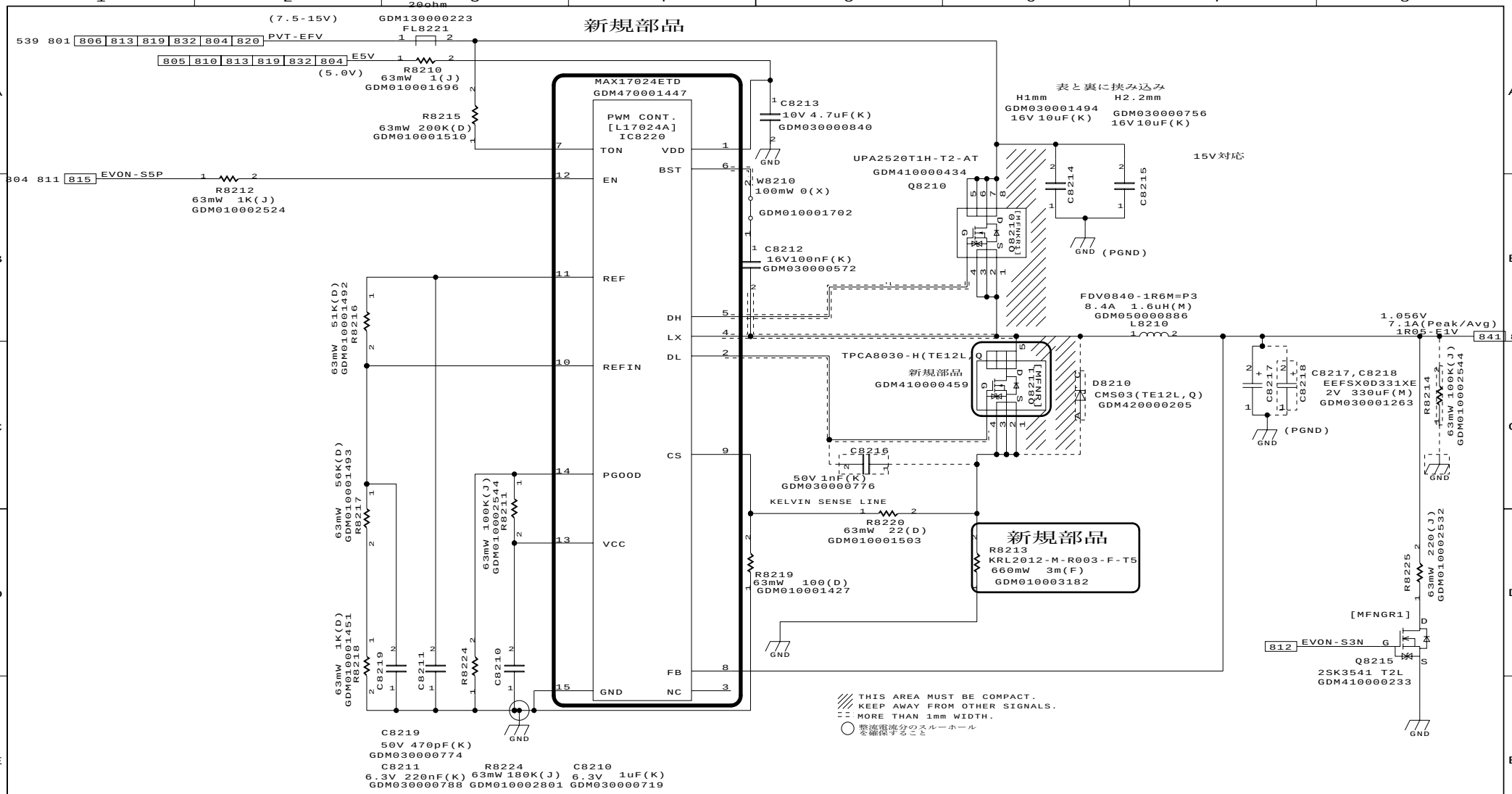
DESIGNED BY 2009/9/25	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
A.Kanou	FHNSY1	[PS]PTV	819	116	00	360069769

2009.10.15 17:09 G11



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
A.Kanou 2009/8/3	FHNSY1	[PS]1R5-B1V,0R75-P0V	820	117	00	360069769

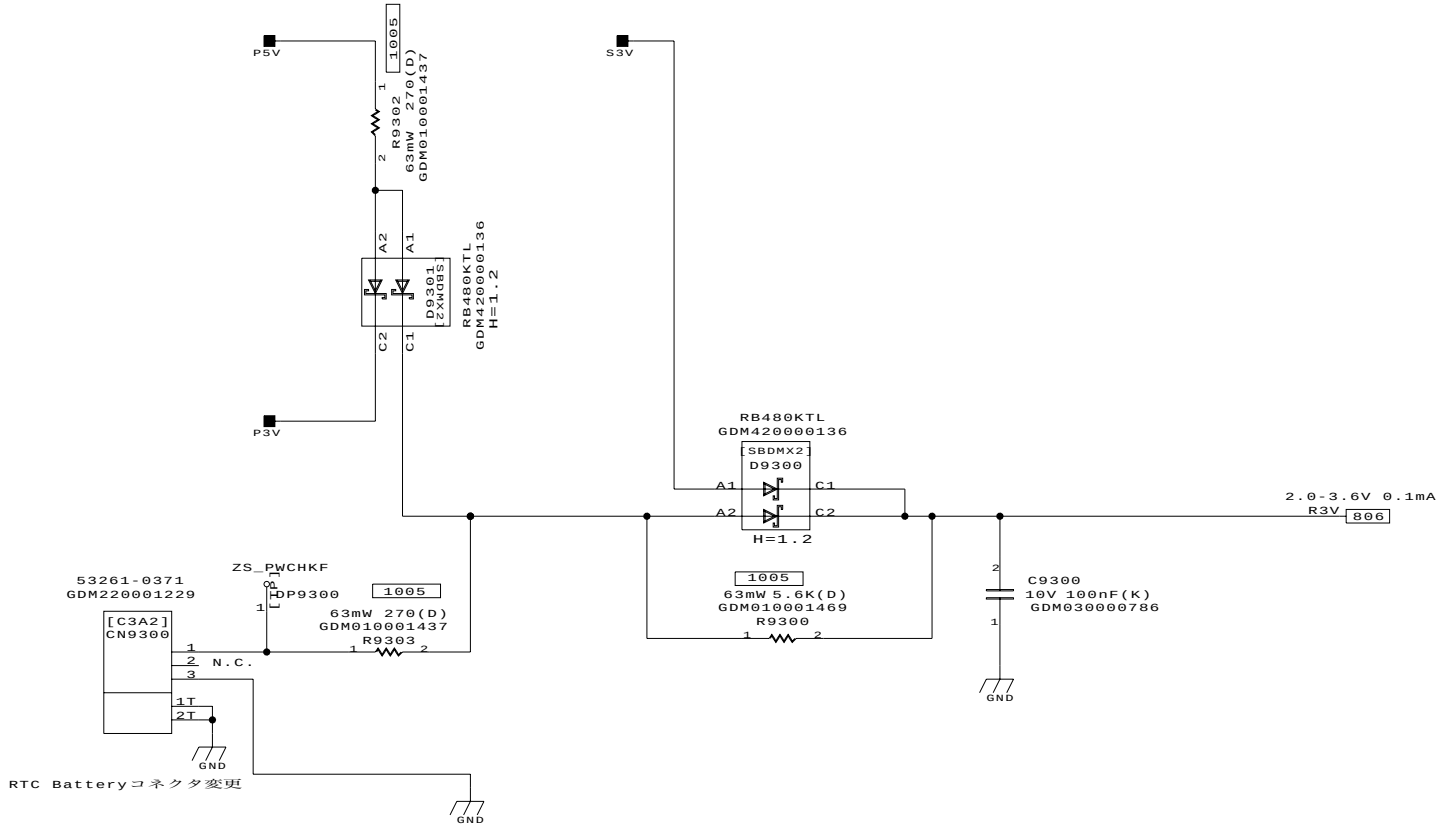
2008.08.21 17:09 G11



- □ 出力電圧 [1.0555V]
- □ 入力電源 [PTV-EFV]
- □ バイアス電源 [E5V]
- □ 部品選択
  - ○FET High Side [UPA2520]
    Low Side [TPCM8030]
  - ○コンデンサ [2.0V 330UF 6m]
  - ○コイル [1.6uH 8.4A FDV0840]
  - ○ダイオード [N/M]
- □ 電源出力制御信号 [EVON-S5P]
- □ SKIP Mode [Enable]
- □ 引き抜き回路 [220ohm]
- □ リップル電流 [2.03A p-p]
- □ 過電流リミッタ [8.34A(Min)]

DESIGNED BY 2009/9/25	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]1R05-E1V	821	118	00	360069769

2009.10.15 17:09 G11



～チェック・ポイント～
□ RTC Battery コネクタ
□ 使用RTC Battery (Hi-MH)

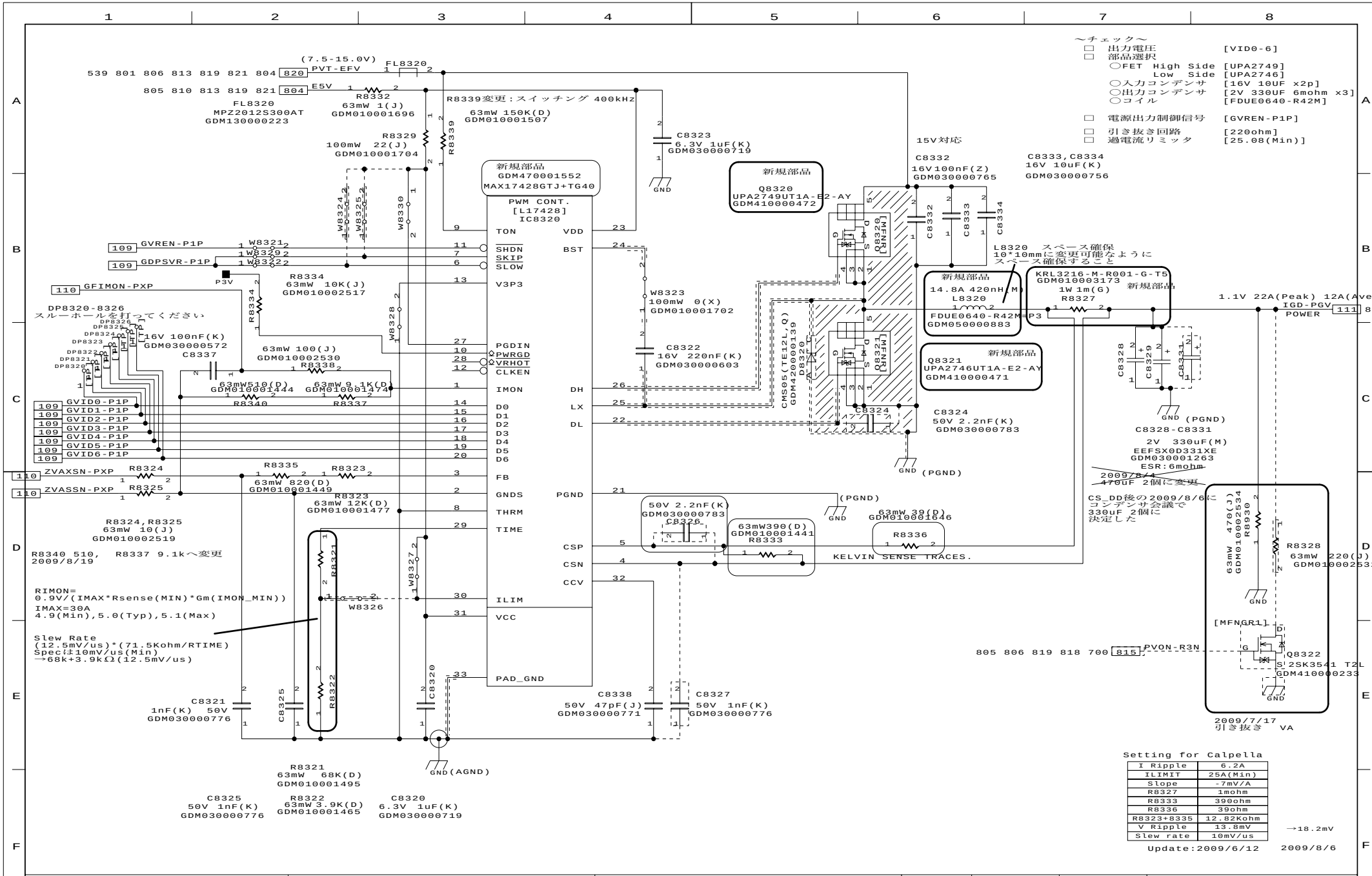
Ultimate よりコピーした 2009/7/13 RTC充電回路

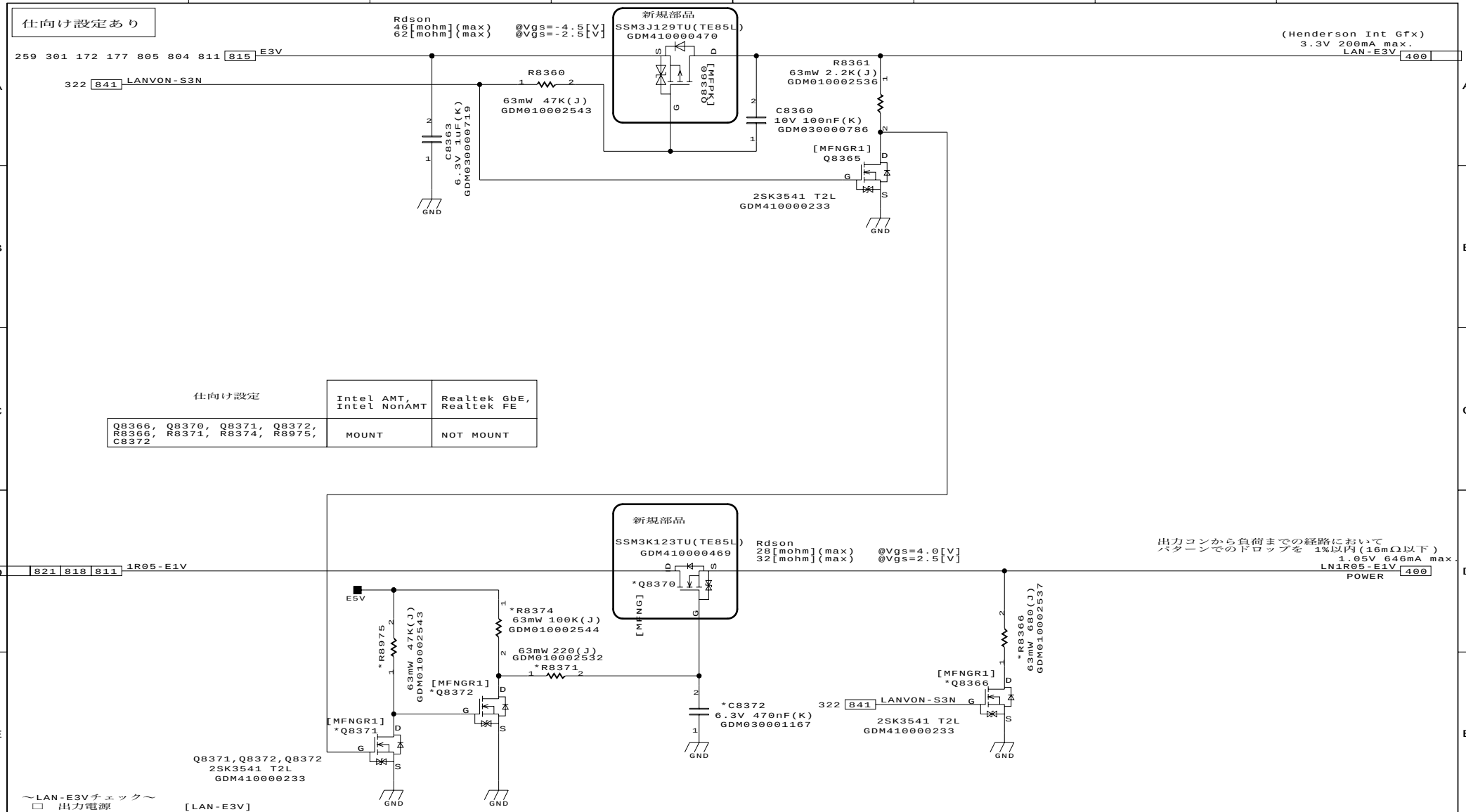
FROM SPA20

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/7/13 Y.Horie	FHNSY1	[PS]RTCVCC	822	119	00	360069769

2009.10.15 17:09

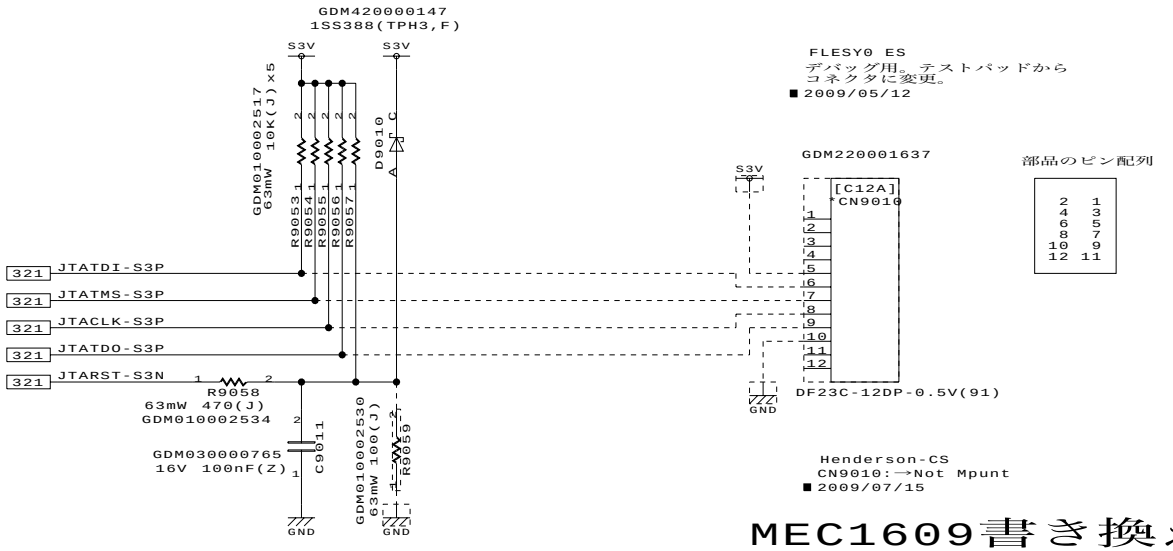
TOSHIBA CORPORATION





DESIGNED BY 2009/7/17	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]LAN-PS	841	121	00	360069769

2009.10.15 17:09 G11



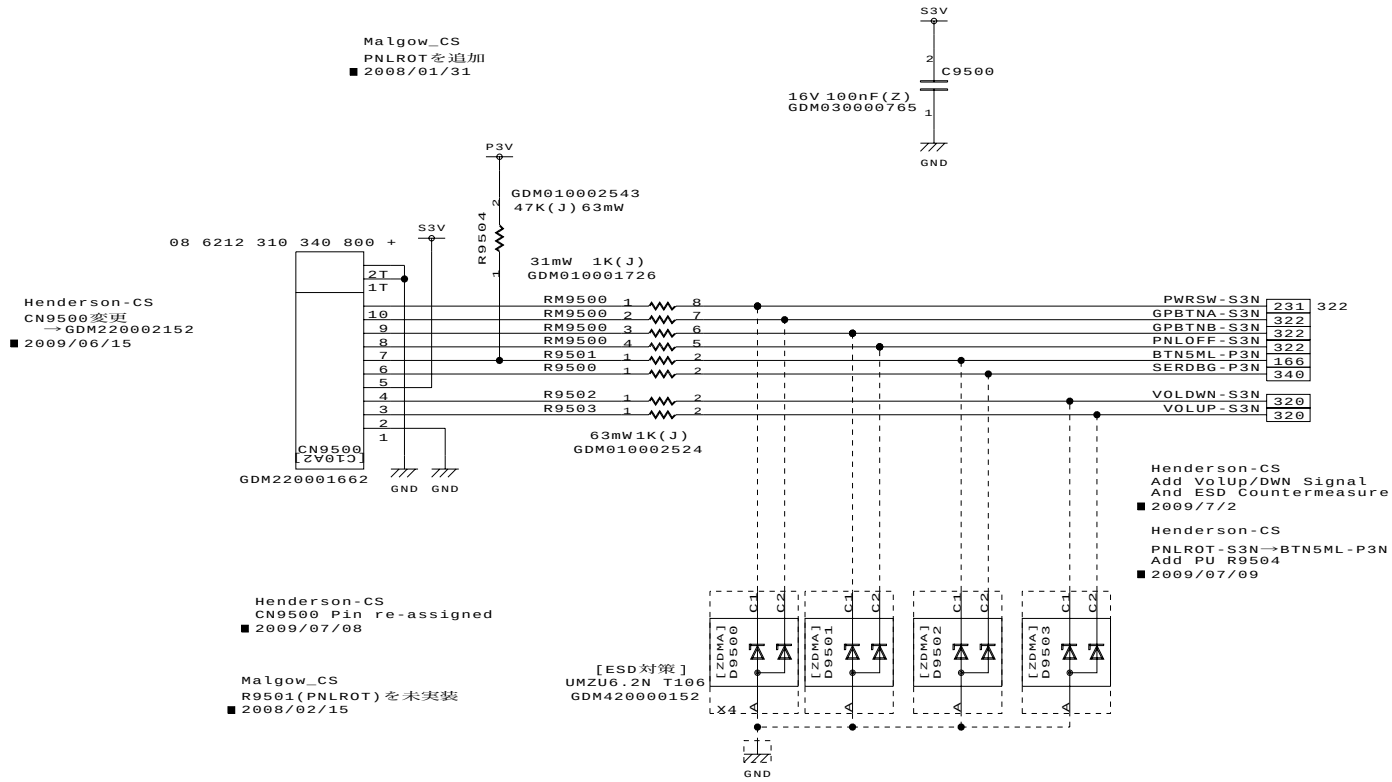
JTAG I/F

MEC1609書き換え用

■REF: Lemont_C(ES) . FLESY0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Midorikawa	FHNSY1	EC TEST PAD	901	122	00	360069769

2009.10.15 17:09 G11

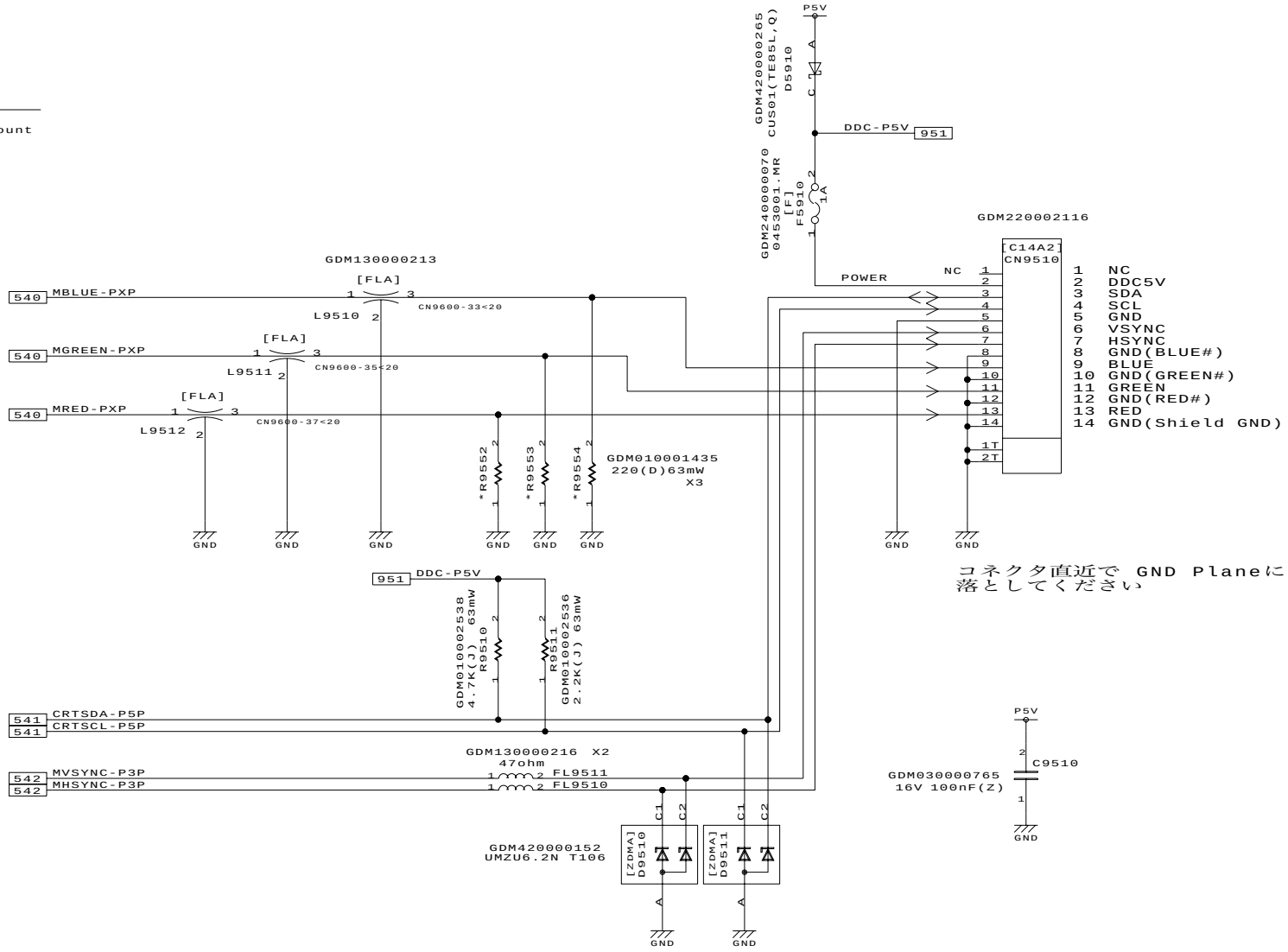


■ REF:Heidelberg10C(VP) . FHBIS2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	FHNSW* I/F	950	123	00	360069769

仕向け設定有

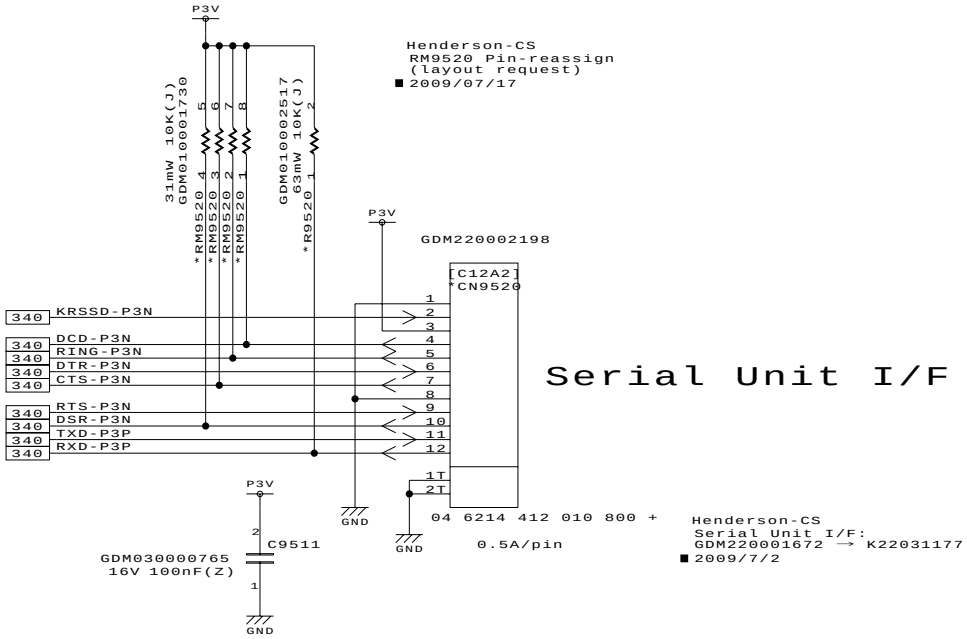
Docker	Yes	No
R9552-R9554	Mount	NotMount

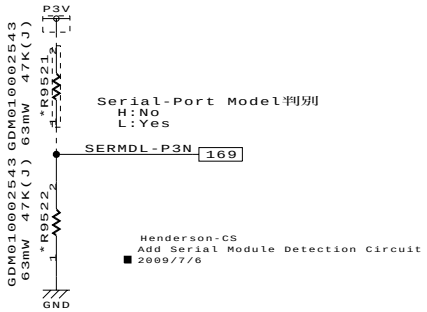


■ REF:Moscow10C (VP)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.OCHIAI	FHNSY1	RGB Unit I/F	951	124	00	360069769

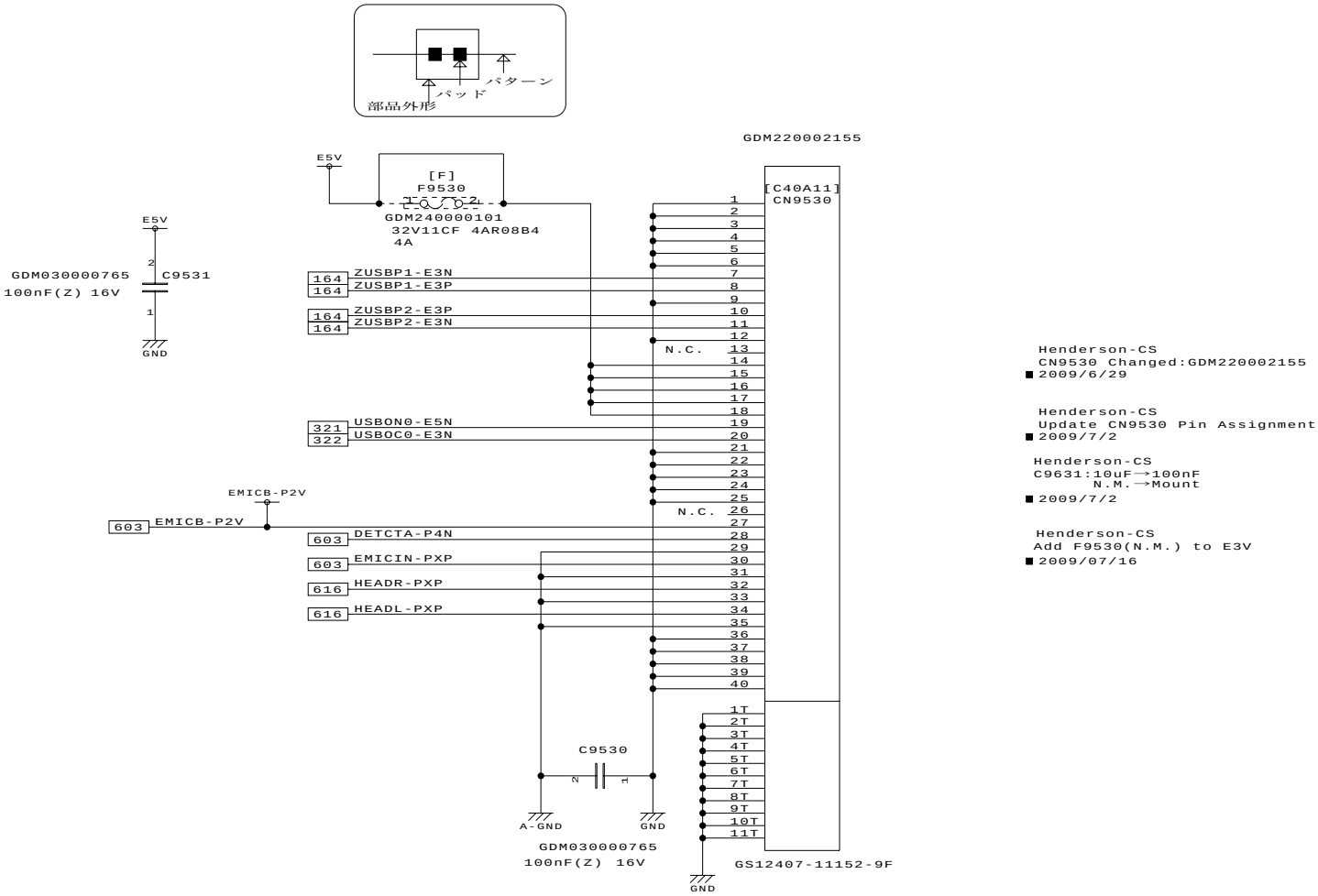
仕向け設定有





■ REF:Malgow(VP) . FG6IN1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 T.Ichimura	FHNSY1	Serial Unit I/F	952	125	00	360069769



Henderson-CS
CN9530 Changed:GDM220002155
■ 2009/6/29

Henderson-CS
Update CN9530 Pin Assignment
■ 2009/7/2

Henderson-CS
C9631:10uF→100nF
N.M.→Mount
■ 2009/7/2

Henderson-CS
Add F9530(N.M.) to E3V
■ 2009/07/16

■ REF:Duluth10(VP) . FDUSY2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	FHNJK* I/F	953	126	00	360069769

2009.10.15 17:09

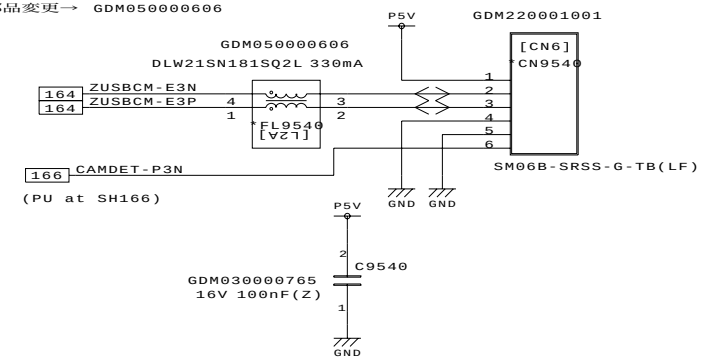
仕向け設定有

Malgow_CS
Camera検出GPI追加
■ 2008/02/14

Henderson-CS
Move CAMDETのPU to SH166
(Delete R9540)
■ 2009/07/21

Henderson10-VP
FL9540追加
■ 2009/10/13

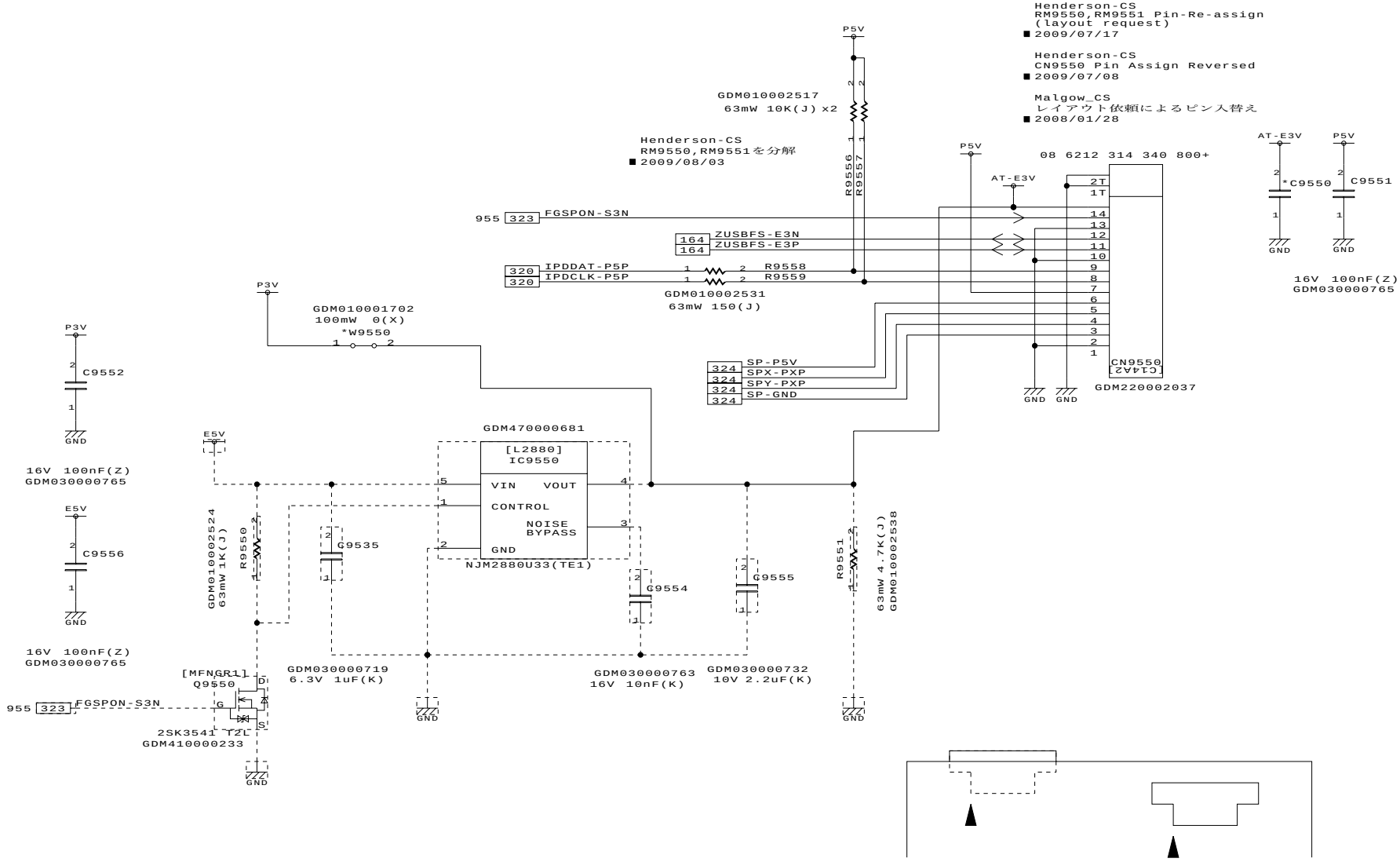
Henderson10-VP
FL9540ピン入替え、部品変更→ GDM050000606
■ 2009/10/15



■ REF:Heidelberg10C (VP) . FHBIS2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	WebCam I/F	954	127	00	360069769

仕向け設定有

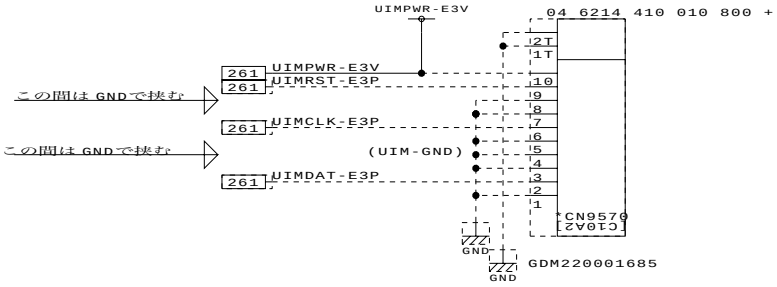


■ REF:Bolmio10 (VP) . FBMOS1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 T.Ichimura	FHNSY1	FHNFS* I/F	955	128	00	360069769

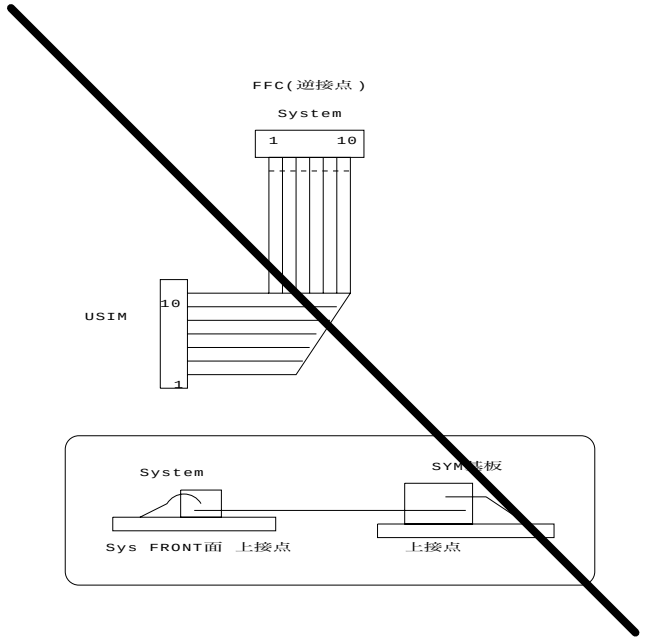
仕向け設定有

Henderson-CS
CN9570変更
→GDM220001685
■ 2009/06/15



Henderson-CS
CN9570 Pin Assign Reversed
■ 2009/07/08

pin2 pin6 pin7は不使用のため削除。
2005/09/30 五十嵐



■ REF:Malgow(VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Anwar/T.Ichimura	FHNSY1	FHNSM* I/F	957	129	00	360069769

2009.10.15 17:09 G11

1 2 3 4 5 6 7 8

A B C D E F

仕向け設定有

WLAN	YES	NO
C9702	Mount	Mount
W9570	Not Mount	Not Mount
IC9700 Q9700 CN9700 C9701 C9700 R9709 R9701	Mount	Not Mount

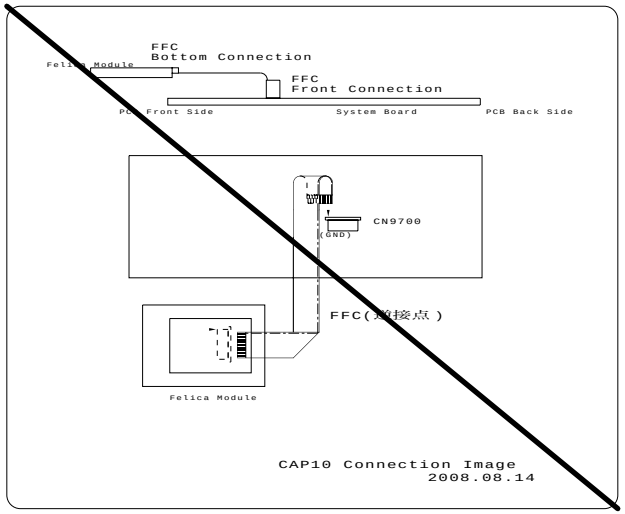
Tucson10G CS3
Felica用のE5VラインにＳＷを追加。
■ 2007/02/14

セットで配置してください。
GDM010001702
W9570
0(X) 100mW
GDM410000226
[MFPI]
*Q9700
TPC6104(TE85L,F)

Henderson10GC-CS
電源切り替え用ジャンパ追加、電源を P系に変更
■ 2009/08/03

P3V
P5V
GDM010001702 x2
100mW 0(X)
645 615 805 P5V
W9701
171 805 815 P3V
*W9700

GDM450000462
323 FELIEN-S3N
&
[7SET08]
*IC9700
[Vcc]
[Vss]
TC7SET08FU(TE85L,F
GND
*R9700
GDM010002542
33K(J) 63mW
*C9700
GDM030000764
22nF(K)16V



CAP10 Connection Image
2008.08.14

[FeliCa I/F]

08 6262 006 340 846+
GDM220002041
Henderson
FeliCa I/F Unit
Pin Assignment

#	Name
1	VDD
2	USB-
3	USB+
4	GND
5	N.C.
6	GND

216 ZUSBFL-E3P
216 ZUSBFL-E3N
(FCLED) N.C.
*CN9700 [C6A2]
GDM010002538
4.7K(J) 63mW
*R9701
*C9701
*C9702
4.7uF(K)6.3V
GDM030000657
100nF(Z) 16V
GDM030000765

FDUSY0
Duluth用に信号名変更。
■ 2008/01/05

FDUSY0
パススイッチの未実装パターンを削除。
■ 2008/01/05

Henderson-CS
Felica/SmartCard用に信号名変更
■ 2009/6/29

Henderson-CS
Felica用に信号名変更
■ 2009/07/21

Henderson-CS
CN9700変更
→GDM220002041
■ 2009/06/15

Henderson-CS
CN9700:接続変更
■ 2009/07/09

Duluth10 CS
CN9570のピンアサインを反転
■ 2008/01/19

Cassiopeia10 CS
CN9700:GDM220002041->GDM220002127
■ 2008/01/19

Cassiopeia10 CS
CN9700:接続変更
■ 2008/08/11

P3V
GDM010002543
63mW 47K(J)
*R9572
Felica Model判別
H:No
L:Yes
FELMDL-P3N 166
GDM010002543
63mW 47K(J)
*R9571
GND

Henderson-CS
Add Felica Module Detection Circuit
■ 2009/7/6

Tucson10G CS3
R9570,C9571を追加。
■ 2007/02/14

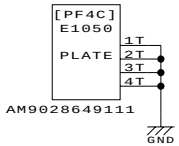
■ REF:Cassiopeia10(VP) . FCASY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2009/10/15 Ling.Yu	FHNSY1	FELICA I/F	970	130	00	360069769

2009.10.15 17:09

Proc.Back-Up Plate

GDMF20001232



Henderson10-VP
E1050 VPコードに置き換え
■ 2009/10/15

Henderson-CS
E1050変更
→KF201380
■ 2009/06/15

Henderson10-CS
E1050 GDMコードに置き換え
■ 2009/08/04

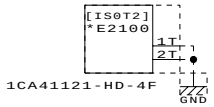
GPU Back-Up Plate

PC Card Ejector

Henderson10-CS
PC/Express Card EjectorをKコードに変更
■ 2009/03/30

Henderson10-CS
PC/Express Card EjectorをKコードに変更
■ 2009/06/29

GDM250000257



Henderson10-CS
Add ExCard Ejector for 10GC
■ 2009/06/29

Henderson10-VP
E2100:→GDM250000257
■ 2009/10/14

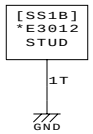
MDC/SIM Stud

T-353331LF2S-P03M2
GDM640000293

HendersonVP-CS
MDC Studの仕向けをMDC-SIMに変更
■ 2009/10/12

MDC Harness Holder Stud

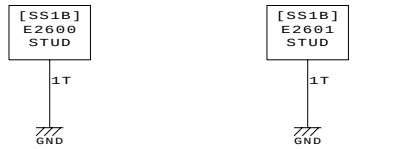
T-353331LF2S-P03M2
GDM640000293



Henderson-CS
Add Stud for MDC Harness Holder
■ 2009/06/29

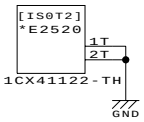
Mini Card Stud (WLAN/WiMax)

T-353331LF2S-P01.5M2
GDM640000292



Express Card Ejector

GDM250000252



For 10/10C

Henderson-CS
E2100,2520をGコード化
■ 2009/08/04

For 10GC

GPU Daughter Card Stud

Henderson-CS
GPU Daughter Card Stud削除
■ 2009/07/05

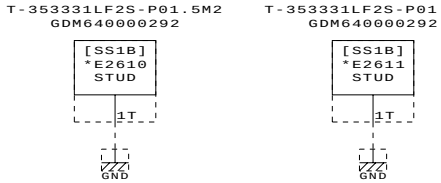
PCMCIA	PC Card	Express Card
E2110	Mount	Not mount
E2520	Not mount	Mount

3G/ ROBSON	有	無
E2610, E2611	Mount	Not mount

MDC	有	無
E3010, E3011	Mount	Not mount

Mini Card Stud (3G Module)

T-353331LF2S-P01.5M2
GDM640000292

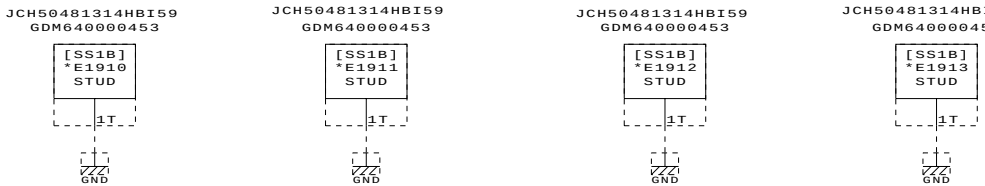


SSD Stud(temp)

Henderson-CS
Add 4Stud for SSD: GDM640000293
■ 2009/7/3

Henderson10-CS
E1910-E1913 GDM640000293→GDM640000453
■ 2009/08/04

JCH50481314HBI59
GDM640000453



Newport10
Stud類追加(Rev.17)
■ 2006/10/30

■ REF:Bormio10 (VP) . FBMOS1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Maeshima/T.Ichimura	FHNSY1	Accessories	990	131	00	360069769

2009.10.15 17:09