Model Name: GA-H81M-S2PH

Revision 1.01

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	ITE 8620 LPC IO
16	COM,KB_MS,USB PWR
17	IT8892E
18	PCI SLOT 1,2
19	PCI EXPRESS*1 SLOT.LPT
20	HWM,FAN CTRL,OV,-PROCHOT
21	DUAL BIOS
22	FP,F_USB,SPK,SATALED
23	Realtek ALC887-VD2
24	REAR AUDIO JACK
25	REALTEK RTL8111F
26	DISCRETE POWER
27	ATX,DUMMY LOAD

SHEET	TITLE
28	RT8120_DDR POWER
29	VCORE ISL95812_1
30	VCORE ISL95812_2
31	HDMI

Gigabyte Technology

Title: Cover Sheet

Size: Custom | Document Number: GA-H81M-S2PH | Rev: 1.01

Date: Monday, July 15, 2013 | Sheet 1 of 31

Model Name: GA-H81M-S2PH

Revision 1.01

Component value change history

2013/07/15

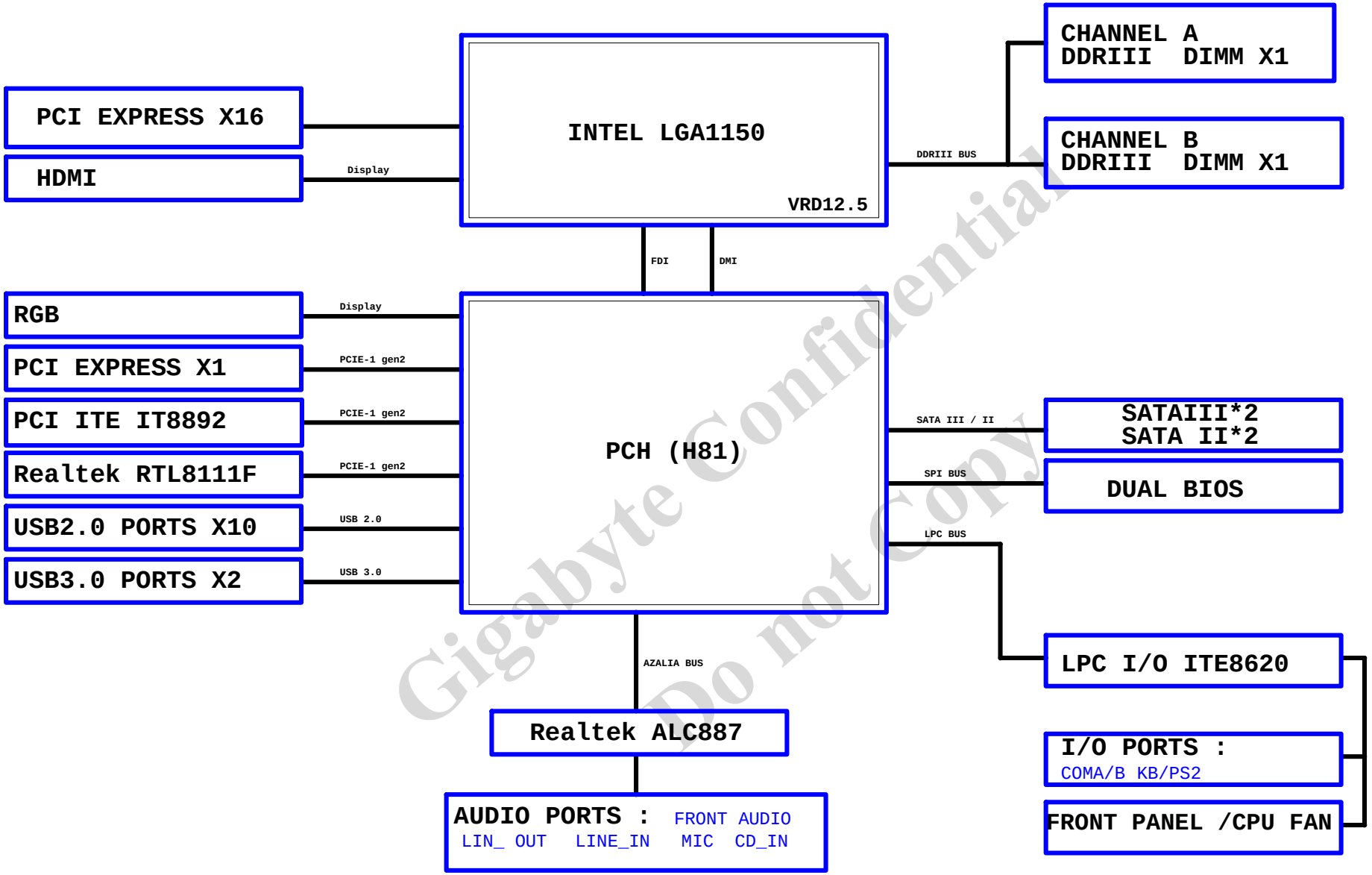
Data	Change Item	Reason
2013/05/17	BACK PANEL FOR EMI KB_MS,COMA,LPT -->AGND1.	
2013/06/18	R1.0 .MODIFY PROCHOT,5VSB CIRCUIT.BOM CHANGE R_USB30/HDMI.	
2013/06/28	R1.01.FIX RSMRST TIMMING.ADD 5VSB OVP PROTECTION.	

Circuit or PCB layout change

DATE	Change Item	Reason

Gigabyte Technology		
Title	BOM & PCB MODIFY HISTORY	
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 2 of 31	

BLOCK DIAGRAM



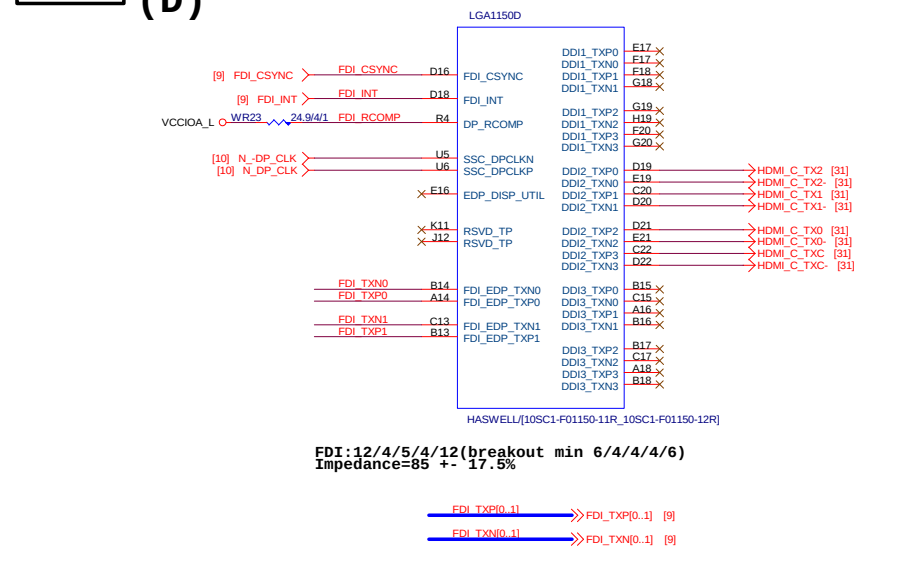
Gigabyte Technology		
Title: BLOCK DIAGRAM		
Size: C	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 3 of 31	

LGA1150 (E)

CFG	H	L	NOTE
0	RSVD	RSVD	RSVD
1	RSVD	RSVD	RSVD
2	**NORM**	**Reverse**	LANE REVERSAL[0],x16
3	RSVD	RSVD	RSVD
4	**Disable**	**Enable**	eDP Enable
7	RSVD	RSVD	RSVD
8	RSVD	RSVD	RSVD
9	RSVD	RSVD	RSVD
10	RSVD	RSVD	RSVD
11	RSVD	RSVD	RSVD
12	RSVD	RSVD	RSVD
13	RSVD	RSVD	RSVD
14	RSVD	RSVD	RSVD
15	RSVD	RSVD	RSVD
16	RSVD	RSVD	RSVD
17	RSVD	RSVD	RSVD

CFG6	CFG5	PCIE CONFIG
1	1	1X16 , Default
1	0	2X8
0	1	RSVD
0	0	X8,X4,X4

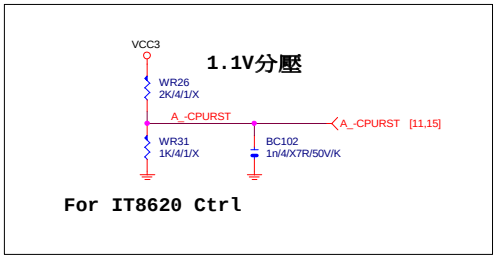
LGA1150 (D)



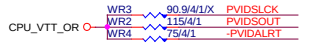
LGA1155 (C)

-CPURST

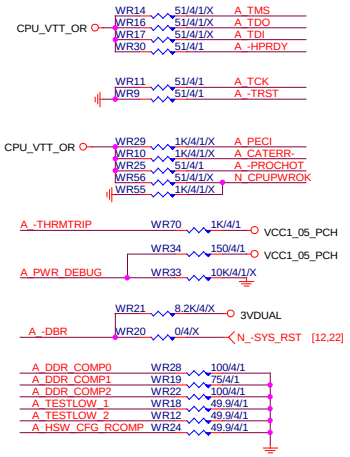
FOR IT8620 Ctrl



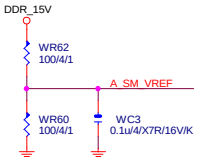
CPU SVID



CPU PU/PD



SM REF



THRMTRIP DISABLE

Gigabyte Technology			
Title: CPU LGA1150-A			
Size: Custom	Document Number: GA-H81M-S2PH		Rev: 1.01
Date: Monday, July 15, 2013	Sheet 4 of 31		

LGA1150 (A)

LGA1150A

MAAA0	AU13	DDR0_MA0	DDR0_DQ0	AD38	MDA0
MAAA1	AV16	DDR0_MA1	DDR0_DQ1	AD39	MDA1
MAAA2	AU16	DDR0_MA2	DDR0_DQ2	AF38	MDA2
MAAA3	AW17	DDR0_MA3	DDR0_DQ3	AF39	MDA3
MAAA4	AU17	DDR0_MA4	DDR0_DQ4	AD37	MDA4
MAAA5	AW18	DDR0_MA5	DDR0_DQ5	AD40	MDA5
MAAA6	AV17	DDR0_MA6	DDR0_DQ6	AE37	MDA6
MAAA7	AT18	DDR0_MA7	DDR0_DQ7	AE40	MDA7
MAAA8	AU18	DDR0_MA8	DDR0_DQ8	AH40	MDA9
MAAA9	AT19	DDR0_MA9	DDR0_DQ9	AH39	MDA13
MAAA10	AW11	DDR0_MA10	DDR0_DQ10	AK38	MDA10
MAAA11	AV19	DDR0_MA11	DDR0_DQ11	AK39	MDA11
MAAA12	AU19	DDR0_MA12	DDR0_DQ12	AH37	MDA12
MAAA13	AY10	DDR0_MA13	DDR0_DQ13	AH38	MDA8
MAAA14	AT20	DDR0_MA14	DDR0_DQ14	AK37	MDA14
MAAA15	AU21	DDR0_MA15	DDR0_DQ15	AK40	MDA15
			DDR0_DQ16	AM40	MDA17
MODT_A0	AW10	DDR0_ODT0	DDR0_DQ17	AM39	MDA21
MODT_A1	AY8	DDR0_ODT1	DDR0_DQ18	AP38	MDA18
	AW9	DDR0_ODT2	DDR0_DQ19	AP39	MDA19
	AU8	DDR0_ODT3	DDR0_DQ20	AM37	MDA20
			DDR0_DQ21	AM38	MDA16
			DDR0_DQ22	AP37	MDA22
	AW33	DDR0_ECC0	DDR0_DQ23	AP40	MDA23
	AV33	DDR0_ECC1	DDR0_DQ24	AV37	MDA25
	AU31	DDR0_ECC2	DDR0_DQ25	AW37	MDA29
	AV31	DDR0_ECC3	DDR0_DQ26	AU35	MDA26
	AT33	DDR0_ECC4	DDR0_DQ27	AV35	MDA27
	AU33	DDR0_ECC5	DDR0_DQ28	AT37	MDA28
	AT31	DDR0_ECC6	DDR0_DQ29	AU37	MDA24
	AW31	DDR0_ECC7	DDR0_DQ30	AT35	MDA30
			DDR0_DQ31	AW35	MDA31
[7] SBAA0	SBAA0 AV12	DDR0_BA0	DDR0_DQ32	AY6	MDA33
[7] SBAA1	SBAA1 AY11	DDR0_BA1	DDR0_DQ33	AU6	MDA37
[7] SBAA2	SBAA2 AT21	DDR0_BA2	DDR0_DQ34	AV4	MDA34
			DDR0_DQ35	AU4	MDA35
[7] CKEA0	CKEA0 AV22	DDR0_CKE0	DDR0_DQ36	AW6	MDA36
[7] CKEA1	CKEA1 AT23	DDR0_CKE1	DDR0_DQ37	AV6	MDA32
	AU22	DDR0_CKE2	DDR0_DQ38	AW4	MDA38
	AU23	DDR0_CKE3	DDR0_DQ39	AY4	MDA39
			DDR0_DQ40	AR1	MDA41
[7] -CSA0	-CSA0 AU14	DDR0_CS_N0	DDR0_DQ41	AR4	MDA45
[7] -CSA1	-CSA1 AV9	DDR0_CS_N1	DDR0_DQ42	AN3	MDA42
	AU10	DDR0_CS_N2	DDR0_DQ43	AN4	MDA43
	AW8	DDR0_CS_N3	DDR0_DQ44	AR2	MDA44
			DDR0_DQ45	AR3	MDA40
[7] DCLKA0	DCLKA0 AY15	DDR0_CLK_P0	DDR0_DQ46	AN2	MDA46
[7] -DCLKA0	-DCLKA0 AY16	DDR0_CLK_N0	DDR0_DQ47	AN1	MDA47
[7] DCLKA1	DCLKA1 AW15	DDR0_CLK_P1	DDR0_DQ48	AL1	MDA49
[7] -DCLKA1	-DCLKA1 AW15	DDR0_CLK_N1	DDR0_DQ49	AL4	MDA53
	AY14	DDR0_CLK_P2	DDR0_DQ50	AJ3	MDA50
	AW14	DDR0_CLK_N2	DDR0_DQ51	AJ4	MDA51
	AW13	DDR0_CLK_P3	DDR0_DQ52	AL2	MDA52
	AY13	DDR0_CLK_N3	DDR0_DQ53	AL3	MDA48
			DDR0_DQ54	AJ2	MDA54
	AW12	RSVD	DDR0_DQ55	AJ1	MDA55
			DDR0_DQ56	AG1	MDA57
			DDR0_DQ57	AG4	MDA61
			DDR0_DQ58	AE3	MDA58
			DDR0_DQ59	AE4	MDA59
			DDR0_DQ60	AG2	MDA60
			DDR0_DQ61	AG3	MDA56
[7] -SRASA	-SRASA AU12	DDR0_RAS*	DDR0_DQ62	AE2	MDA62
			DDR0_DQ63	AE1	MDA63
[7] -SWEA	-SWEA AU11	DDR0_WE*	DDR0_DQS_P0	AE39	DQSA0
			DDR0_DQS_P1	AJ39	DQSA1
	AV20	RSVD	DDR0_DQS_P2	AN39	DQSA2
			DDR0_DQS_P3	AV36	DQSA3
	AW27	RSVD	DDR0_DQS_P4	AV5	DQSA4
			DDR0_DQS_P5	AP3	DQSA5
[7] -SCASA	-SCASA AU9	DDR0_CAS*	DDR0_DQS_P6	AK3	DQSA6
			DDR0_DQS_P7	AE3	DQSA7
[7,8] -DDR3_RST	WR61 0/4/SHT/MX AK22	DDR_RESET*	DDR0_DQS_P8	AV32	
			DDR0_DQS_N0	AE38	-DQSA0
			DDR0_DQS_N1	AJ38	-DQSA1
			DDR0_DQS_N2	AN38	-DQSA2
			DDR0_DQS_N3	AU36	-DQSA3
			DDR0_DQS_N4	AW5	-DQSA4
			DDR0_DQS_N5	AP2	-DQSA5
			DDR0_DQS_N6	AK2	-DQSA6
			DDR0_DQS_N7	AF2	-DQSA7
			DDR0_DQS_N8	AU32	

WC4 0.1u/4/X7R/16V/K/X

HASWELL/[10SC1-F01150-11R_10SC1-F01150-12R]

LGA1150 (B)

LGA1150B

MAAB0	AL19	DDR1_MA0	DDR1_DQ0	AE34	MDB0
MAAB1	AK23	DDR1_MA1	DDR1_DQ1	AE35	MDB1
MAAB2	AM22	DDR1_MA2	DDR1_DQ2	AG35	MDB2
MAAB3	AM23	DDR1_MA3	DDR1_DQ3	AH35	MDB3
MAAB4	AP23	DDR1_MA4	DDR1_DQ4	AD34	MDB4
MAAB5	AL23	DDR1_MA5	DDR1_DQ5	AD35	MDB5
MAAB6	AY24	DDR1_MA6	DDR1_DQ6	AG34	MDB6
MAAB7	AY25	DDR1_MA7	DDR1_DQ7	AH34	MDB7
MAAB8	AU26	DDR1_MA8	DDR1_DQ8	AL34	MDB8
MAAB9	AW25	DDR1_MA9	DDR1_DQ9	AL35	MDB9
MAAB10	AP18	DDR1_MA10	DDR1_DQ10	AK31	MDB10
MAAB11	AY25	DDR1_MA11	DDR1_DQ11	AL31	MDB11
MAAB12	AY26	DDR1_MA12	DDR1_DQ12	AK34	MDB12
MAAB13	AR15	DDR1_MA13	DDR1_DQ13	AK35	MDB13
MAAB14	AY27	DDR1_MA14	DDR1_DQ14	AK32	MDB14
MAAB15	AY28	DDR1_MA15	DDR1_DQ15	AL32	MDB15
			DDR1_DQ16	AN34	MDB17
MODT_B0	AM17	DDR1_ODT0	DDR1_DQ17	AP34	MDB21
MODT_B1	AL16	DDR1_ODT1	DDR1_DQ18	AN31	MDB19
	AM16	DDR1_ODT2	DDR1_DQ19	AP31	MDB23
	AK15	DDR1_ODT3	DDR1_DQ20	AN35	MDB20
			DDR1_DQ21	AP35	MDB16
	AM26	DDR1_ECC0	DDR1_DQ22	AN32	MDB18
	AM25	DDR1_ECC1	DDR1_DQ23	AP32	MDB22
	AP25	DDR1_ECC2	DDR1_DQ24	AM29	MDB25
	AP26	DDR1_ECC3	DDR1_DQ25	AM28	MDB28
	AL26	DDR1_ECC4	DDR1_DQ26	AR29	MDB27
	AL25	DDR1_ECC5	DDR1_DQ27	AR28	MDB30
	AR26	DDR1_ECC6	DDR1_DQ28	AL29	MDB24
	AR25	DDR1_ECC7	DDR1_DQ29	AL28	MDB29
			DDR1_DQ30	AP29	MDB26
			DDR1_DQ31	AP28	MDB31
[8] SBAB0	SBAB0 AK17	DDR1_BA0	DDR1_DQ32	AR12	MDB32
[8] SBAB1	SBAB1 AL18	DDR1_BA1	DDR1_DQ33	AP12	MDB33
[8] SBAB2	SBAB2 AW28	DDR1_BA2	DDR1_DQ34	AL13	MDB34
			DDR1_DQ35	AL12	MDB35
[8] CKEB0	CKEB0 AW29	DDR1_CKE0	DDR1_DQ36	AR13	MDB36
[8] CKEB1	CKEB1 AY29	DDR1_CKE1	DDR1_DQ37	AP13	MDB37
	AU28	DDR1_CKE2	DDR1_DQ38	AM13	MDB38
	AU29	DDR1_CKE3	DDR1_DQ39	AM12	MDB39
			DDR1_DQ40	AR9	MDB45
[8] -CSB0	-CSB0 AP17	DDR1_CS_N0	DDR1_DQ41	AP9	MDB41
[8] -CSB1	-CSB1 AN15	DDR1_CS_N1	DDR1_DQ42	AR6	MDB47
	AN17	DDR1_CS_N2	DDR1_DQ43	AP6	MDB43
	AL15	DDR1_CS_N3	DDR1_DQ44	AR10	MDB44
			DDR1_DQ45	AP10	MDB40
			DDR1_DQ46	AR7	MDB46
			DDR1_DQ47	AP7	MDB42
[8] DCLKB0	DCLKB0 AM20	DDR1_CLK_P0	DDR1_DQ48	AM9	MDB52
[8] -DCLKB0	-DCLKB0 AM21	DDR1_CLK_N0	DDR1_DQ49	AL9	MDB53
[8] DCLKB1	DCLKB1 AP22	DDR1_CLK_P1	DDR1_DQ50	AL6	MDB50
[8] -DCLKB1	-DCLKB1 AP21	DDR1_CLK_N1	DDR1_DQ51	AL7	MDB55
			DDR1_DQ52	AM10	MDB48
	AN20	DDR1_CLK_P2	DDR1_DQ53	AL10	MDB49
	AN21	DDR1_CLK_N2	DDR1_DQ54	AM6	MDB54
	AP19	DDR1_CLK_P3	DDR1_DQ55	AM7	MDB51
	AP20	DDR1_CLK_N3	DDR1_DQ56	AH6	MDB61
			DDR1_DQ57	AH7	MDB60
[8] -SCASB	-SCASB AP16	DDR1_CAS*	DDR1_DQ58	AE6	MDB59
	AL20	RSVD	DDR1_DQ59	AE7	MDB63
[8] -SRASB	-SRASB AM18	DDR1_RAS*	DDR1_DQ60	AJ6	MDB56
[8] -SWEB	-SWEB AK16	DDR1_WE*	DDR1_DQ61	AJ7	MDB57
			DDR1_DQ62	AF6	MDB58
[7] VREF_DQA	VREF_DQA AB39	DDR_VREF_DQ0	DDR1_DQ63	AF7	MDB62
[8] VREF_DQB	VREF_DQB AB40	DDR_VREF_DQ1	DDR1_DQS_P0	AF35	DQSB0
			DDR1_DQS_P1	AL33	DQSB1
			DDR1_DQS_P2	AP33	DQSB2
			DDR1_DQS_P3	AN28	DQSB3
			DDR1_DQS_P4	AN12	DQSB4
			DDR1_DQS_P5	AP8	DQSB5
			DDR1_DQS_P6	AL8	DQSB6
			DDR1_DQS_P7	AG7	DQSB7
			DDR1_DQS_P8	AN25	
			DDR1_DQS_N0	AF34	-DQSB0
			DDR1_DQS_N1	AK33	-DQSB1
			DDR1_DQS_N2	AN33	-DQSB2
			DDR1_DQS_N3	AP29	-DQSB3
			DDR1_DQS_N4	AN13	-DQSB4
			DDR1_DQS_N5	AR8	-DQSB5
			DDR1_DQS_N6	AM8	-DQSB6
			DDR1_DQS_N7	AG6	-DQSB7
			DDR1_DQS_N8	AN26	

HASWELL/[10SC1-F01150-11R_10SC1-F01150-12R]

Place in CPU bottom side

VREF_DQA
VREF_DQB
WBC34 0.1u/4/X7R/16V/K
WBC33 0.1u/4/X7R/16V/K

LGA1150 (CR)

CR
CPU RETAINTION/X

LGA1150_P

ILM_BP/1156/CSP/ILM_BP/1156/CSP/[12KRC-0F0001-52R_12KRC-0F0001-51R]

DDR BUS

[7] MODT_A[0..1] MODT_A[0..1]
[8] MODT_B[0..1] MODT_B[0..1]
[7] MDA[0..63] MDA[0..63]
[8] MDB[0..63] MDB[0..63]
[7] DQSA[0..7] DQSA[0..7]
[7] -DQSA[0..7] -DQSA[0..7]
[7] MAAA[0..15] MAAA[0..15]
[8] MAAB[0..15] MAAB[0..15]
[8] DQSB[0..7] DQSB[0..7]
[8] -DQSB[0..7] -DQSB[0..7]

Gigabyte Technology			
Title: CPU LGA1156-B			
Size: Custom	Document Number: GA-H81M-S2PH		Rev: 1.01
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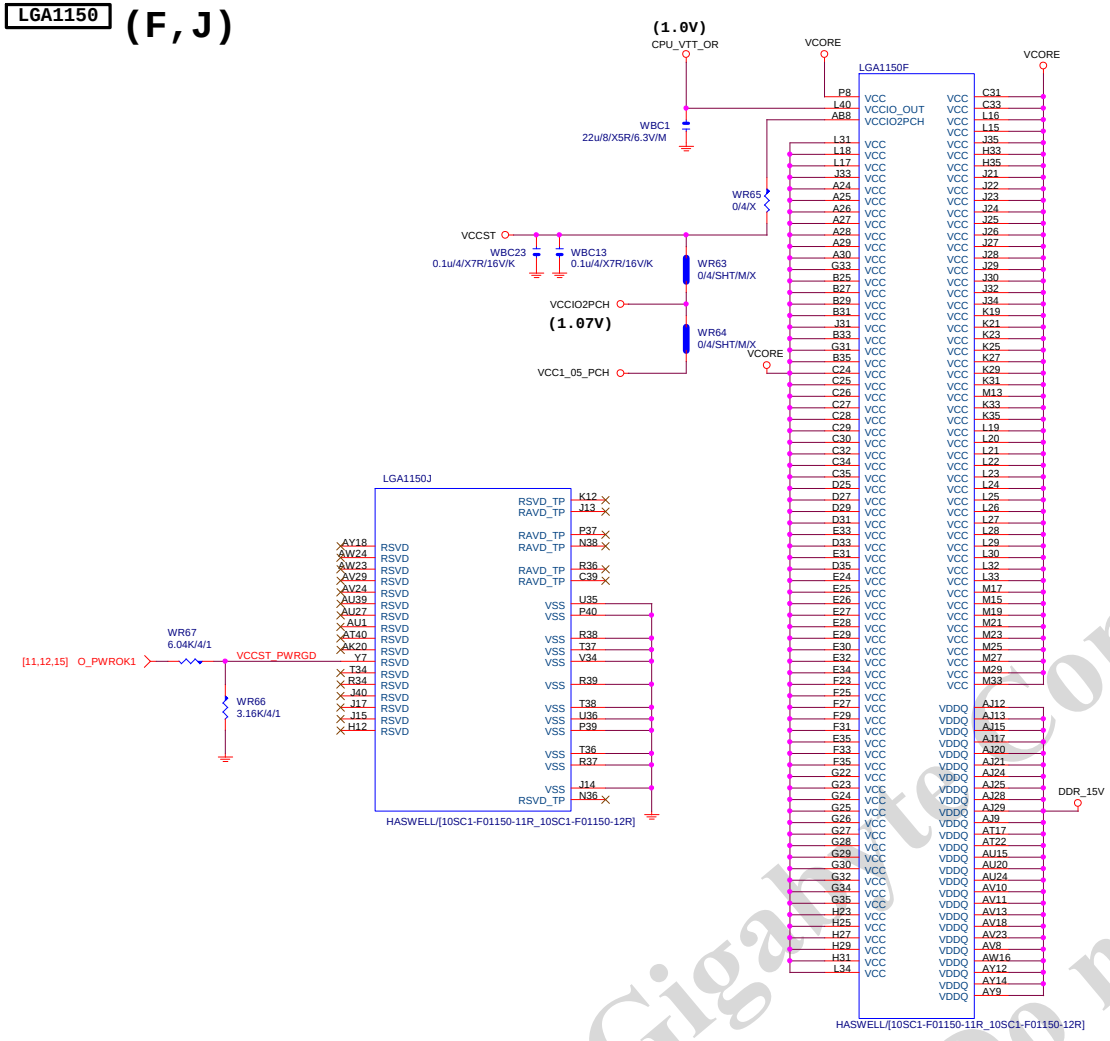
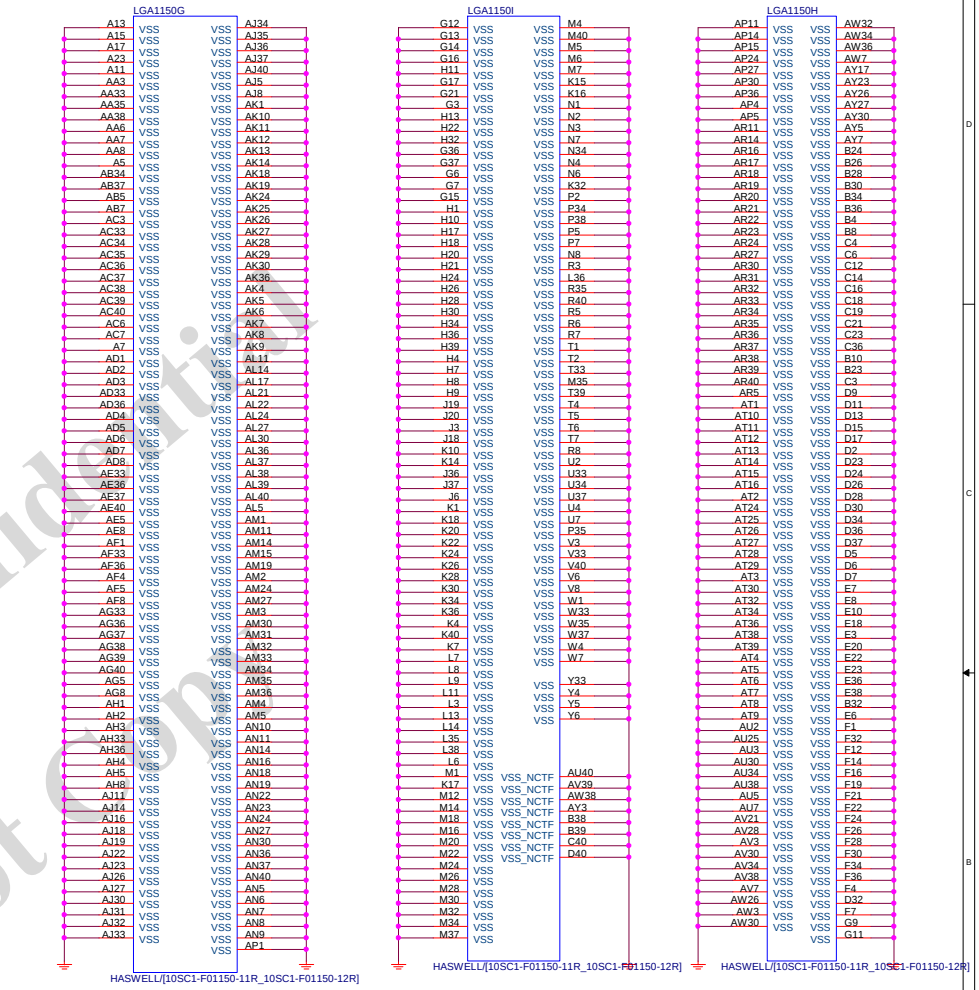
LGA1150 (F,J)

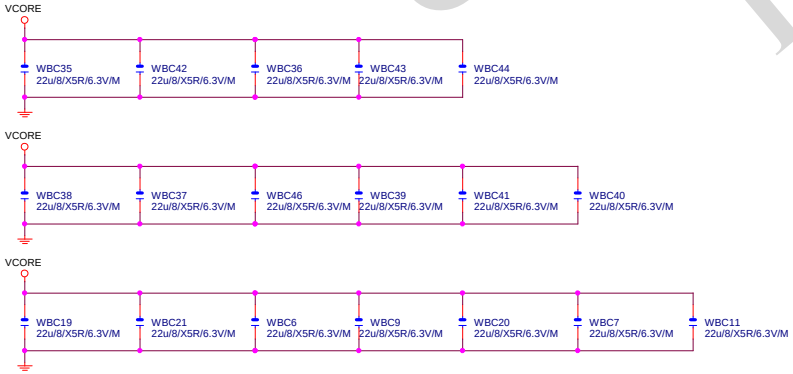


LGA1155 (G,H,I)



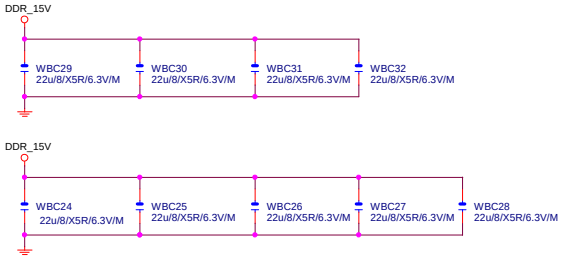
VCore CAP

(X18)

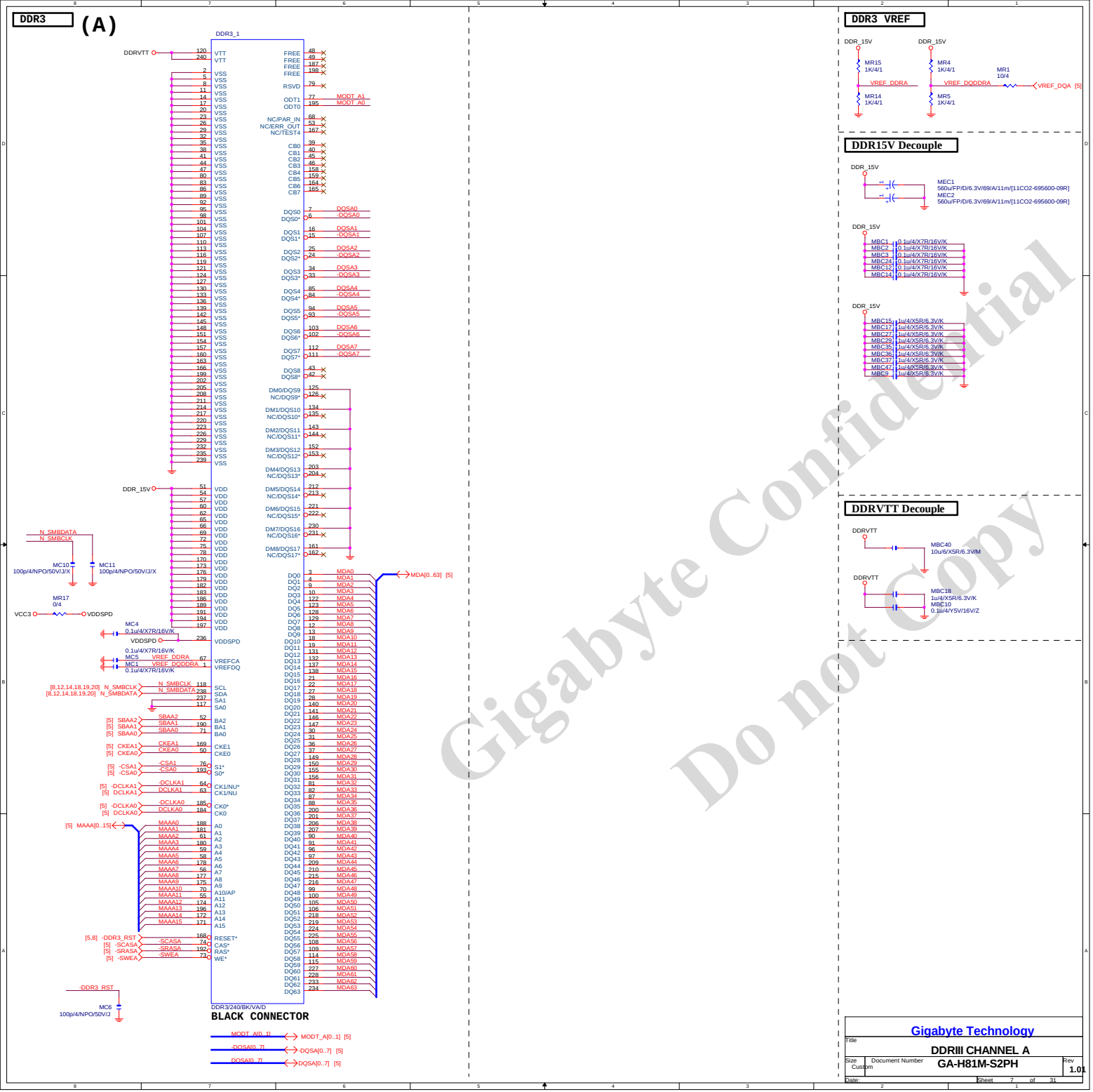


DDR CAP

(X9)



Gigabyte Technology		
Title: CPU LGA1150-C		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 6 of 31	

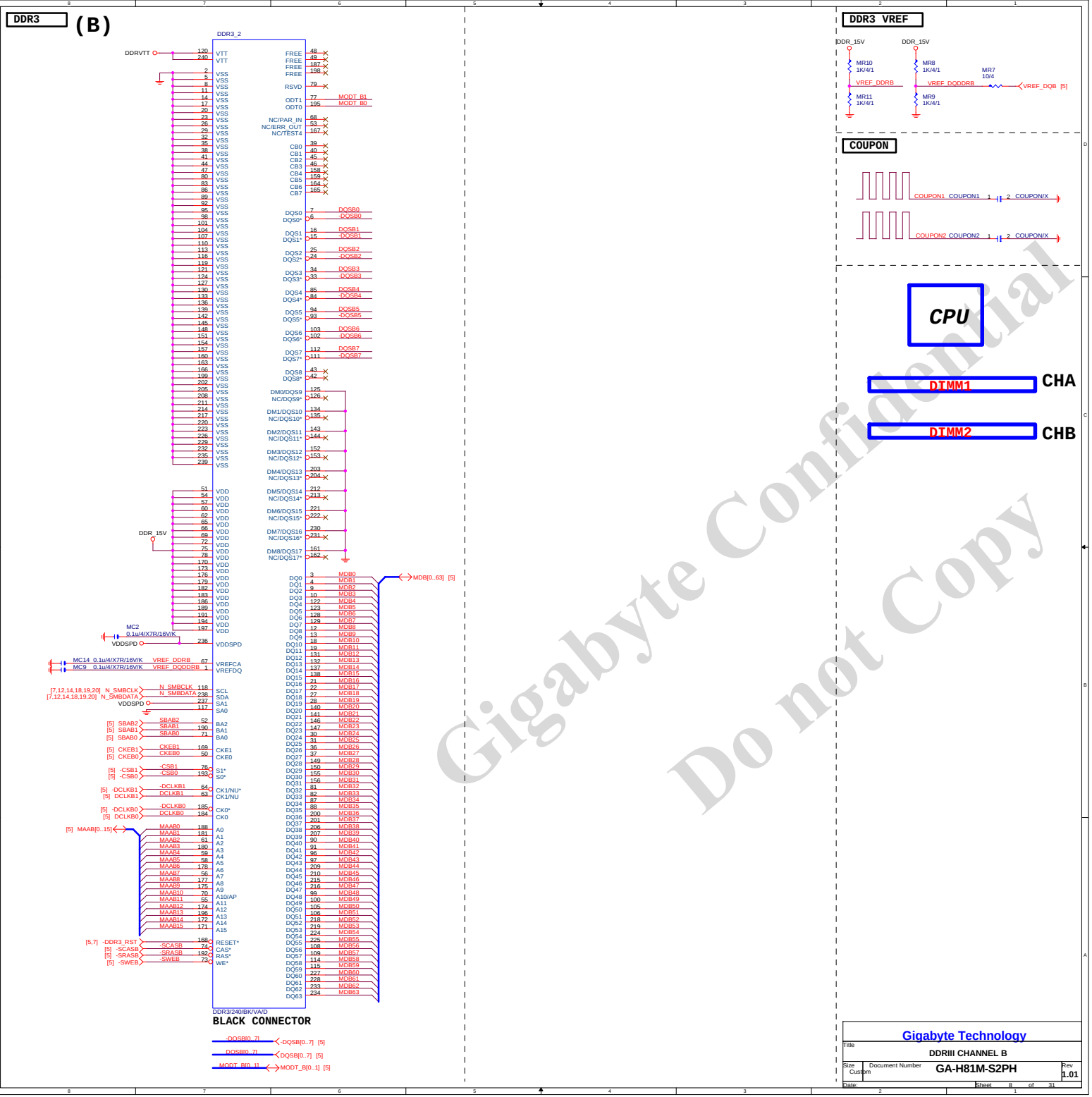


Gigabyte Technology

Title: DDRIII CHANNEL A

Size: Custom | Document Number: GA-H81M-S2PH | Rev: 1.01

Date: | Sheet 7 of 31



Gigabyte Technology

Title: DDRIII CHANNEL B

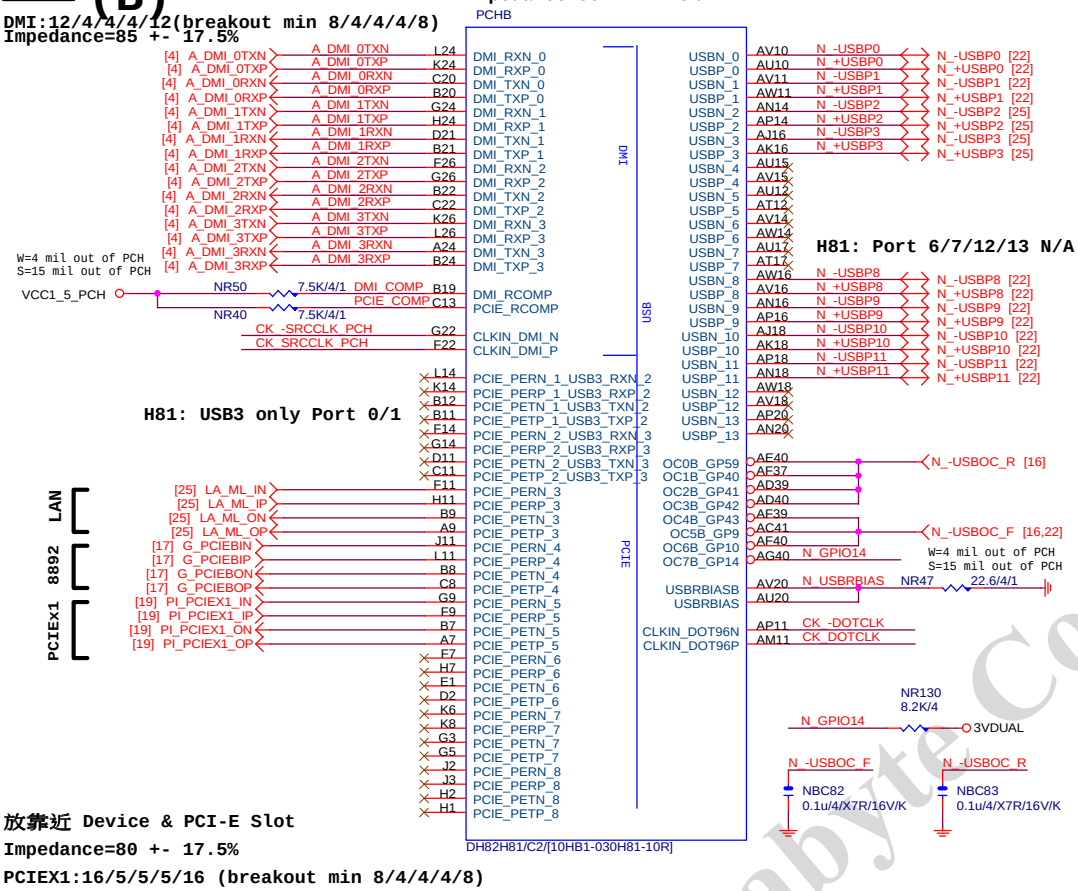
Size: Custom | Document Number: GA-H81M-S2PH | Rev: 1.01

Date: | Sheet 8 of 31

PCH (B)

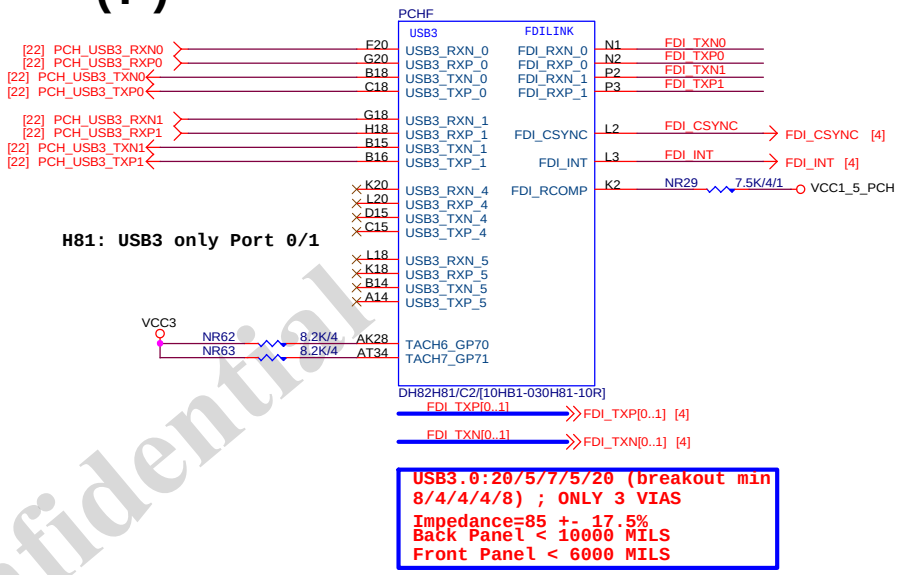
DMI:12/4/4/4/12(breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

USB2.0 : 12/4.5/7.5/4.5/12 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%



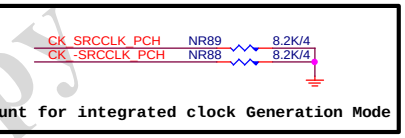
放靠近 Device & PCI-E Slot
Impedance=80 +- 17.5%
PCIEX1:16/5/5/5/16 (breakout min 8/4/4/4/8)

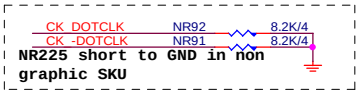
PCH (F)



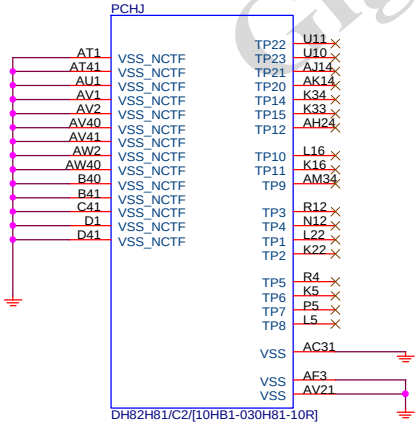
USB3.0:20/5/7/5/20 (breakout min 8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

PCH CLK PD



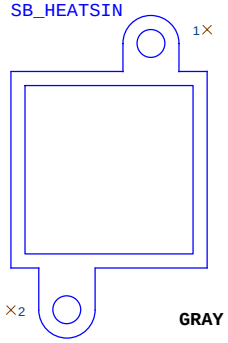


PCH (J)



PCH H/S

LOW COST ICH7 HEATSINK



GRAY HS

PCH_HS
PCH_HS/[12SP2-030005-43R_12SP2-030005-41R_12SP2-030005-42R]

USB TABLE

OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

USB OC#	Configure
OC0#	USB30_HDMI
OC1#	R_USB
OC2#	N/A
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	N/A
OC7#	Not Use

Gigabyte Technology

Title	PCH FDI,DMI,USB ,PCIE,NVRAM	
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
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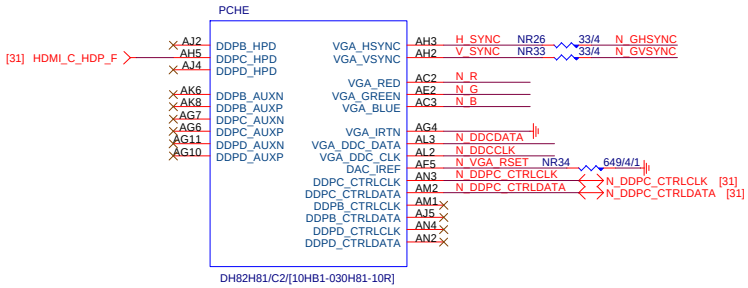
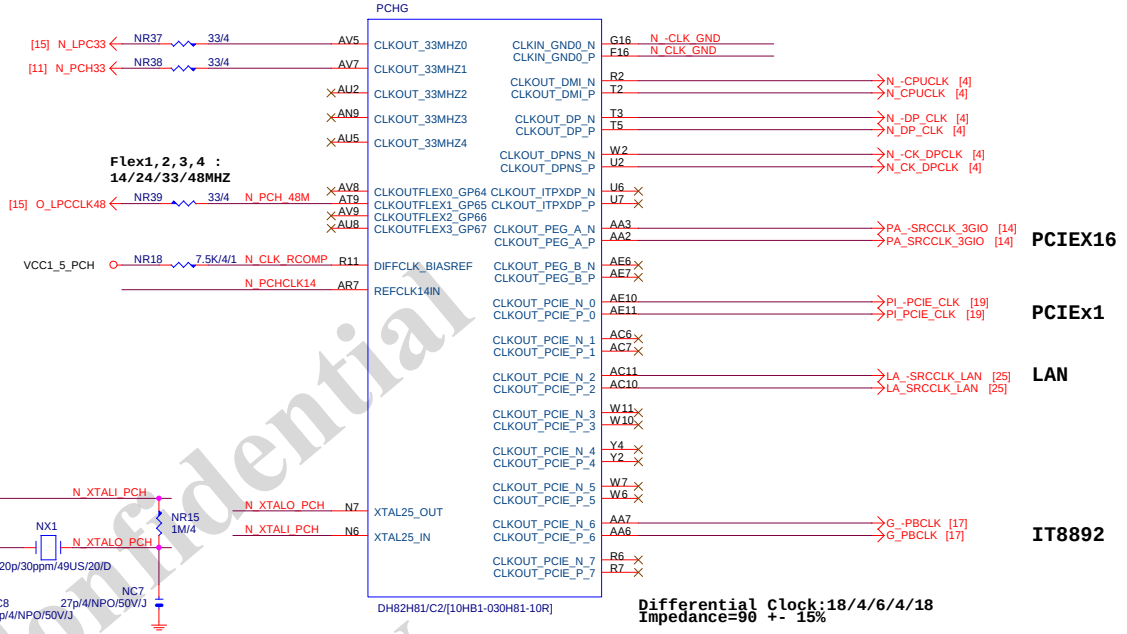
PCH (E)

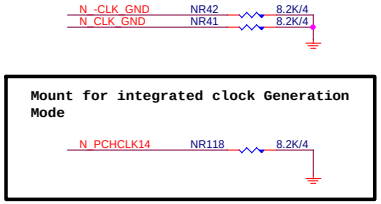


PCH (G)

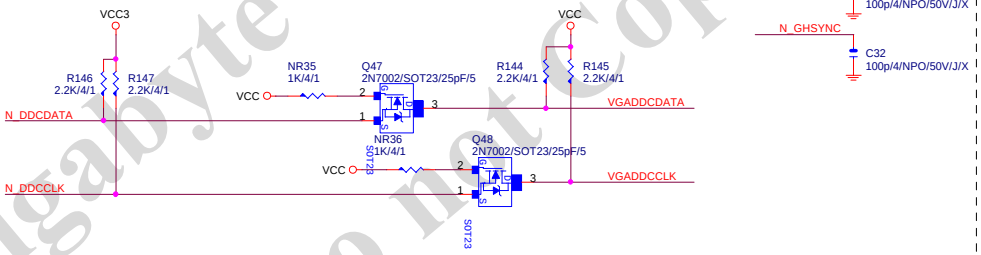


Differential Clock:18/4/6/4/18
Impedance=90 +- 15%

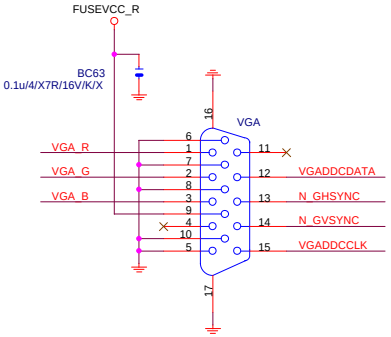
PCH CLK PD



VGA DDC



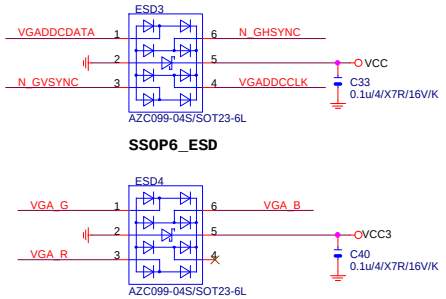
VGA CONNECTOR



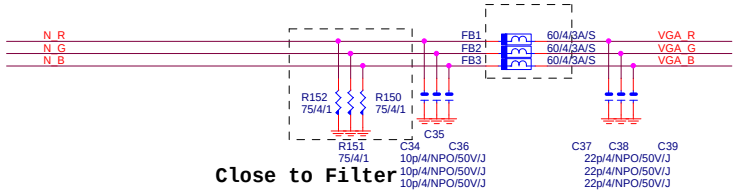
VGA/BK/SC-11/RA/D/L/[11NR6-103015-21R_11NR6-103015-22R]

BLACK D-SUB

VGA ESD

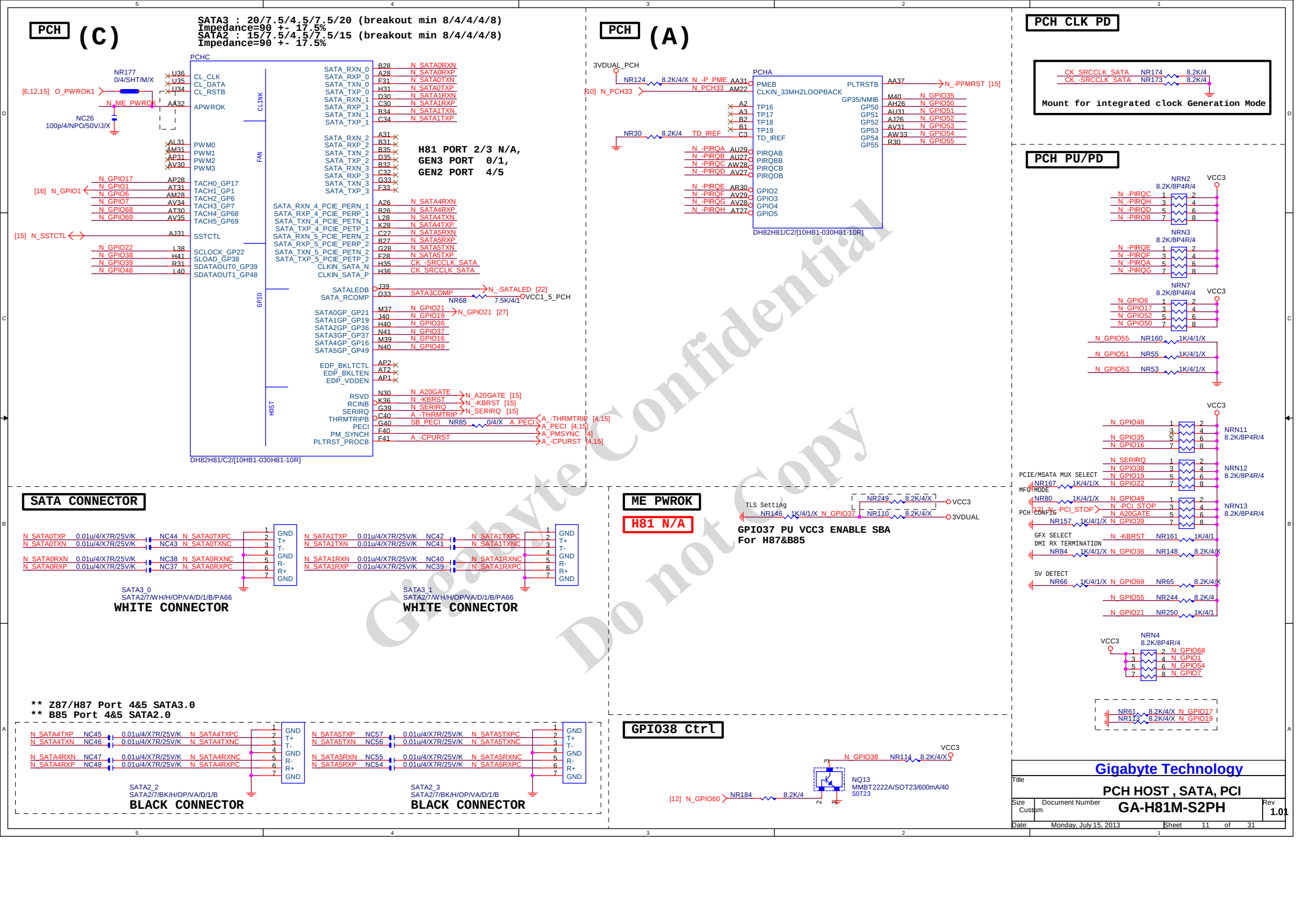


VGA DDC



Close to Filter

Gigabyte Technology		
Title: PCH DISPLAY ,CLK BUFFER		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 10 of 31	


PCH (C)
SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%
PCHC
CL_CLK
CL_DATA
CL_RSTB
APWROK
PWM0
PWM1
PWM2
PWM3
TACH0_GP17
TACH1_GP1
TACH2_GP6
TACH3_GP7
TACH4_GP68
TACH5_GP69
SSTCTL
SCLOCK_GP22
SLOAD_GP38
SDATAOUT0_GP39
SDATAOUT1_GP48
CLINK
FAN
GPIO
HOST
H81 PORT 2/3 N/A,
GEN3 PORT 0/1,
GEN2 PORT 4/5
SATALEDB
SATA_RCOMP
SATA0GP_GP21
SATA1GP_GP19
SATA2GP_GP36
SATA3GP_GP37
SATA4GP_GP16
SATA5GP_GP49
EDP_BKLTCTL
EDP_BKLTEN
EDP_VDDEN
RSVD
RCINB
SERIRQ
THRMTRIPB
PECI
PM_SYNCH
PLTRST_PROCB
DH82H81/C2/[10HB1-030H81-10R]
PCH (A)
3VDUAL_PCH
PCHA
PMEB
CLKIN_33MHZLOOPBACK
TP16
TP17
TP18
TP19
TD_IREF
PIRQAB
PIRQBB
PIRQCB
PIRQDB
GPIO2
GPIO3
GPIO4
GPIO5
PLTRSTB
GP35/NMIB
GP50
GP51
GP52
GP53
GP54
GP55
DH82H81/C2/[10HB1-030H81-10R]
PCH CLK PD
Mount for integrated clock Generation Mode
PCH PU/PD
SATA CONNECTOR
WHITE CONNECTOR
SATA3_0
SATA2/7/WH/H/OP/VA/D/1/B/PA66
SATA3_1
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
BLACK CONNECTOR
SATA2_2
SATA2/7/BK/H/OP/VA/D/1/B
SATA2_3
ME PWROK
H81 N/A
GPIO37 PU VCC3 ENABLE SBA
For H87&B85
GPIO38 Ctrl
NQ13
MMBT2222A/SOT23/600mA/40
SOT23
Gigabyte Technology
PCH HOST , SATA, PCI
GA-H81M-S2PH
Rev 1.01
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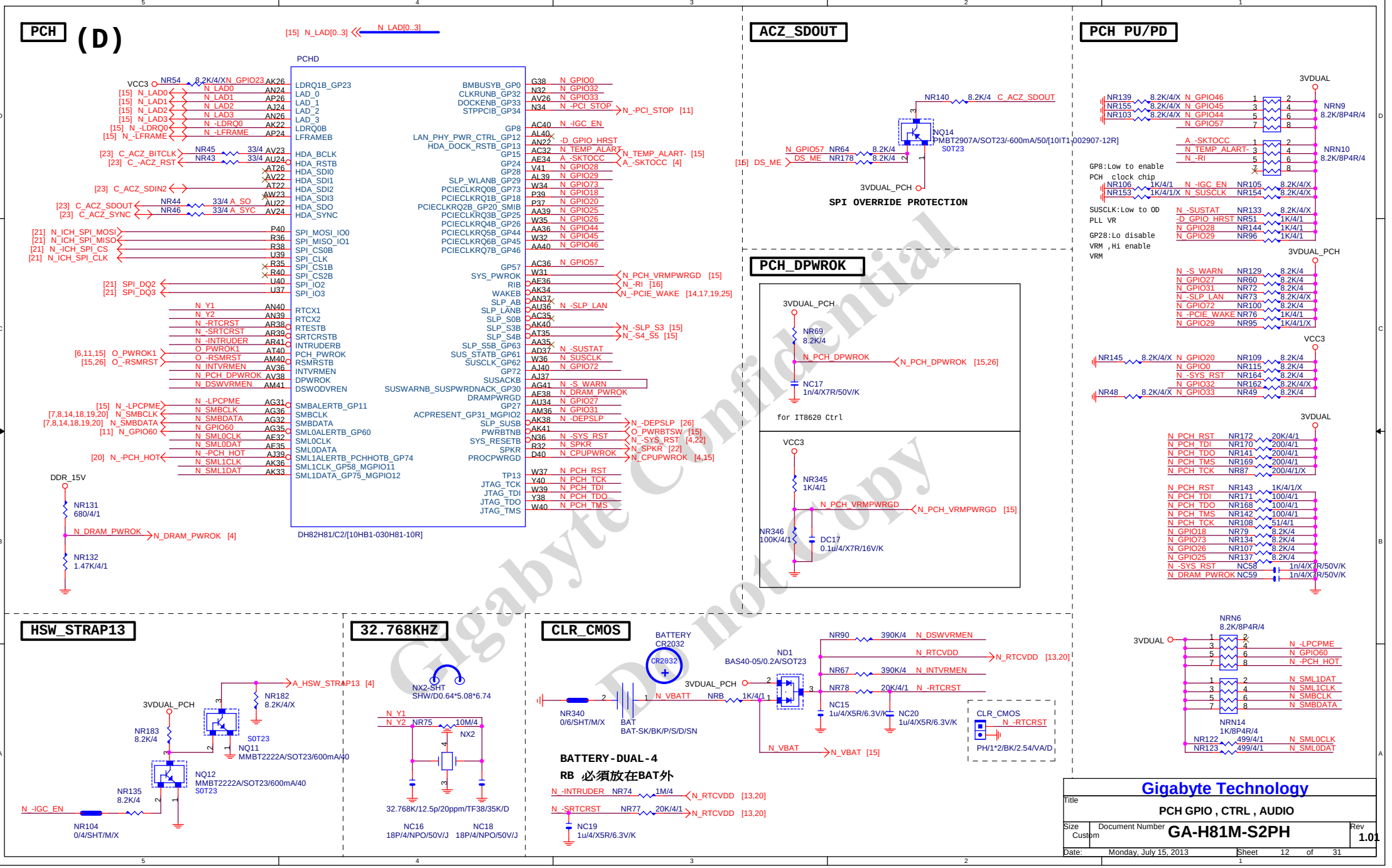
Gigabyte Confidential
Do not Copy


PCH (D)
PCHD
DH82H81/C2/[10HB1-030H81-10R]
ACZ_SDOUT
SPI OVERRIDE PROTECTION
PCH_DPWROK
PCH PU/PD
HSW_STRAP13
32.768KHZ
CLR_CMOS
BATTERY-DUAL-4
RB 必須放在BAT外
Gigabyte Technology
PCH GPIO , CTRL , AUDIO
GA-H81M-S2PH
1.01
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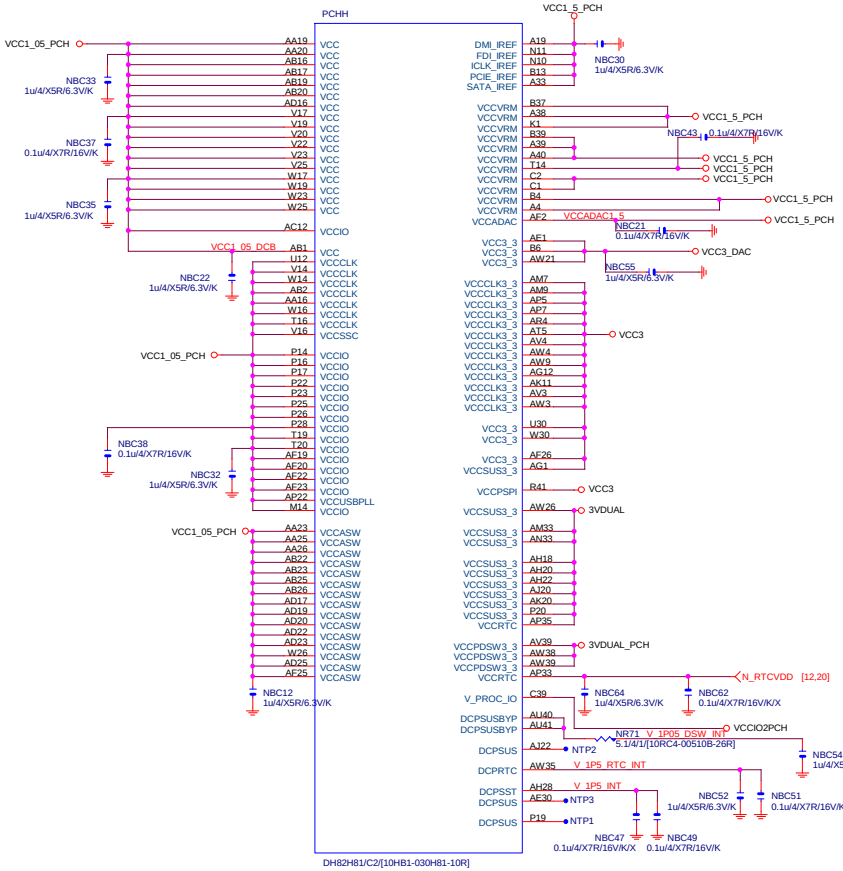
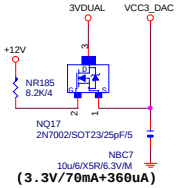
Do not Copy

PCH (H)



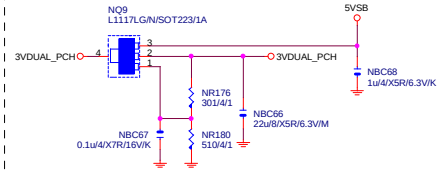
VCC3_DAC

CLOSE北橋(注意震盪水波紋)



(3.3V/70mA+360uA)

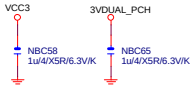
3VDUAL_PCH



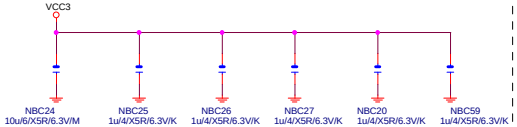
SHT PWR

M3 POWER

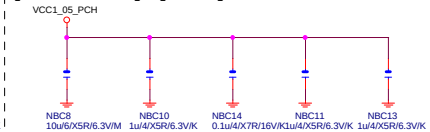
CAP



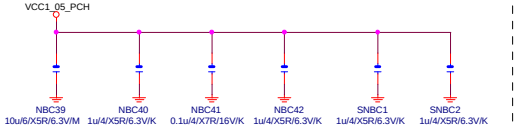
(3.3V)(X6)



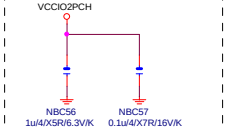
(1.05V)(X5)



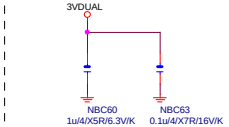
(1.05V)(X6)



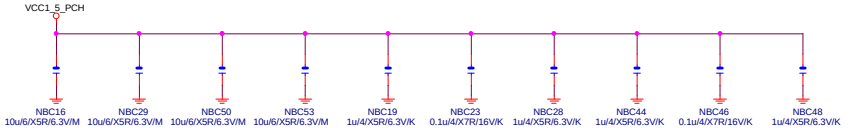
(1.05V)(X2)



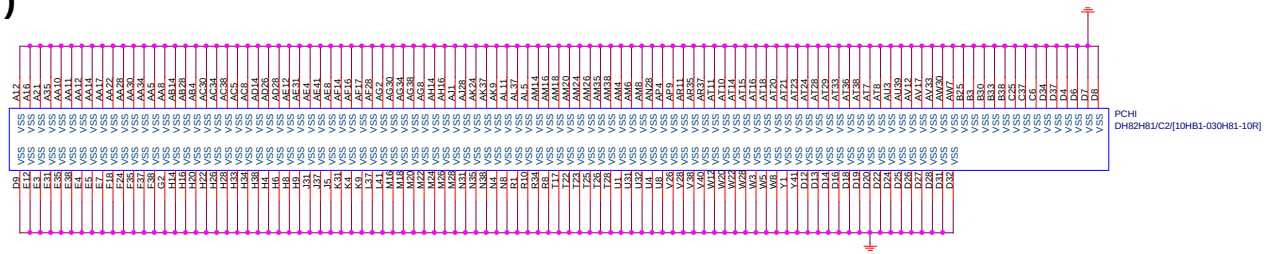
(3.3V)(X2)



(1.05V)(X10)



PCH (I)



Gigabyte Technology			
Title: PCH PWR ,GND			
Size: Custom	Document Number: GA-H81M-S2PH		Rev: 1.01
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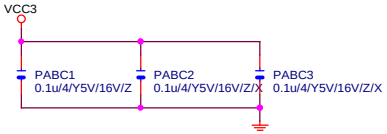
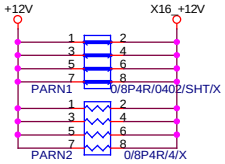
PCIEX16 CAP



PCIEX16 PROTECT SHT



PCIEX16 AC CAP

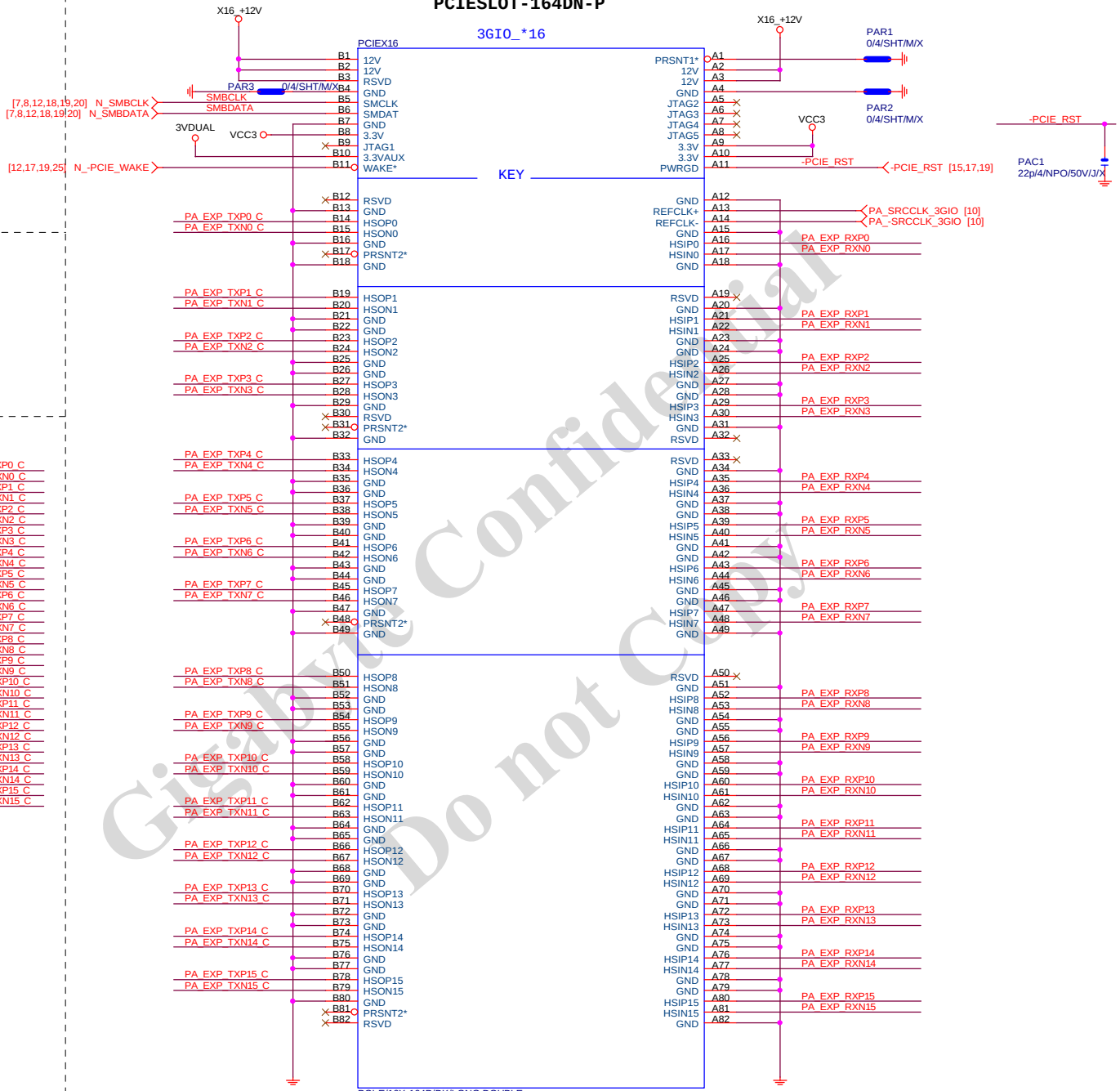
PA_EXP_TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA_EXP_TXP0_C
PA_EXP_TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA_EXP_TXN0_C
PA_EXP_TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA_EXP_TXP1_C
PA_EXP_TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA_EXP_TXN1_C
PA_EXP_TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA_EXP_TXP2_C
PA_EXP_TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA_EXP_TXN2_C
PA_EXP_TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA_EXP_TXP3_C
PA_EXP_TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA_EXP_TXN3_C
PA_EXP_TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA_EXP_TXP4_C
PA_EXP_TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA_EXP_TXN4_C
PA_EXP_TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA_EXP_TXP5_C
PA_EXP_TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA_EXP_TXN5_C
PA_EXP_TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA_EXP_TXP6_C
PA_EXP_TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA_EXP_TXN6_C
PA_EXP_TXP7	PAC19	0.22u/4/X5R/6.3V/K	PA_EXP_TXP7_C
PA_EXP_TXN7	PAC18	0.22u/4/X5R/6.3V/K	PA_EXP_TXN7_C
PA_EXP_TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA_EXP_TXP8_C
PA_EXP_TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA_EXP_TXN8_C
PA_EXP_TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA_EXP_TXP9_C
PA_EXP_TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA_EXP_TXN9_C
PA_EXP_TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA_EXP_TXP10_C
PA_EXP_TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA_EXP_TXN10_C
PA_EXP_TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA_EXP_TXP11_C
PA_EXP_TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA_EXP_TXN11_C
PA_EXP_TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA_EXP_TXP12_C
PA_EXP_TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA_EXP_TXN12_C
PA_EXP_TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA_EXP_TXP13_C
PA_EXP_TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA_EXP_TXN13_C
PA_EXP_TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA_EXP_TXP14_C
PA_EXP_TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA_EXP_TXN14_C
PA_EXP_TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA_EXP_TXP15_C
PA_EXP_TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA_EXP_TXN15_C

PA_EXP_RXP[0..15] >> PA_EXP_RXP[0..15] [4]
PA_EXP_RXN[0..15] >> PA_EXP_RXN[0..15] [4]
PA_EXP_TXP[0..15] >> PA_EXP_TXP[0..15] [4]
PA_EXP_TXN[0..15] >> PA_EXP_TXN[0..15] [4]

PCIEX16 SLOT

PCIESLOT-164DN-P

3GIO_*16



BLACK CONNECTOR

Company	Gigabyte Technology
Title	PCI EXPRESS * 16
Size	Custom
Document Number	GA-H81M-S2PH
Rev	1.01
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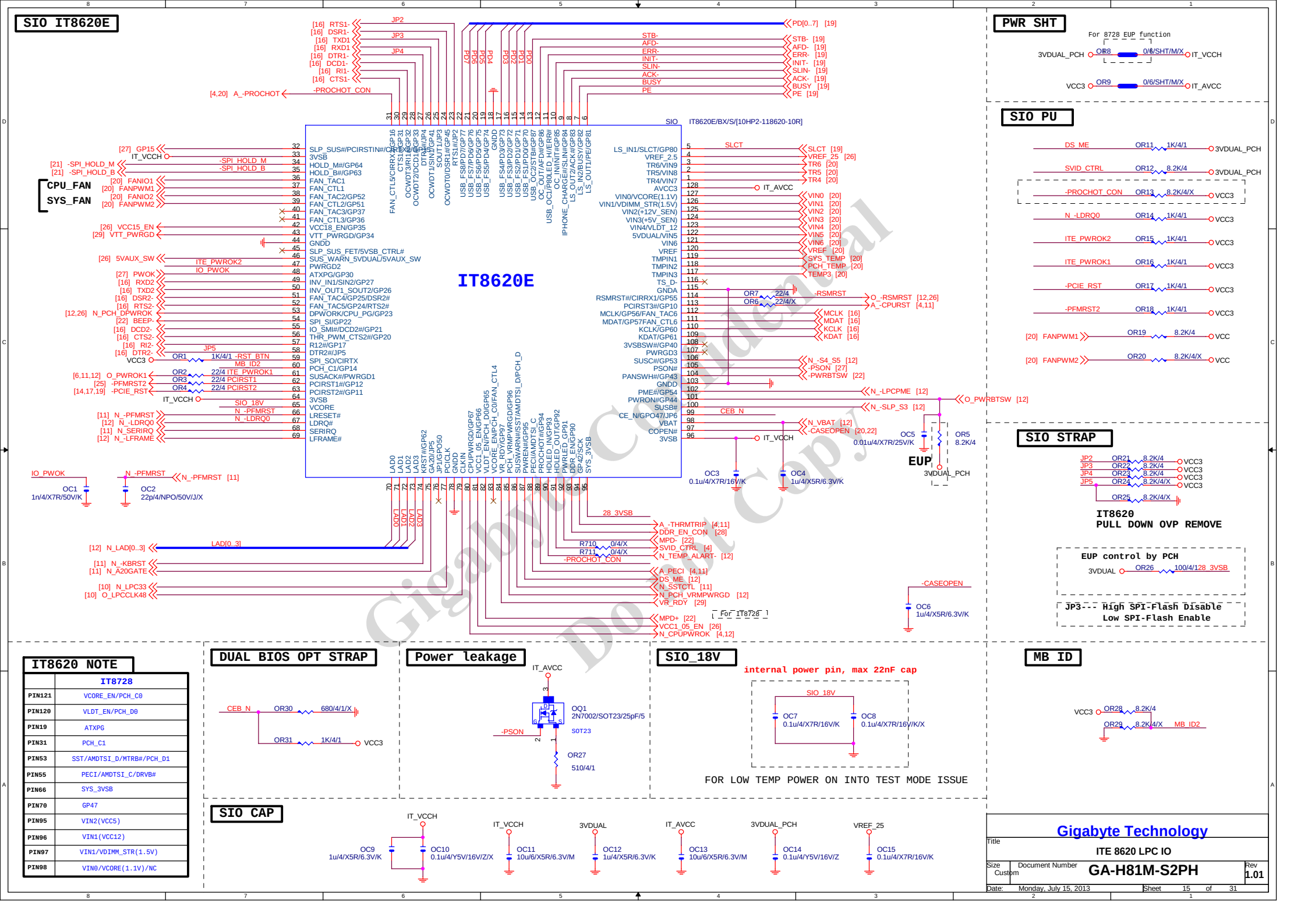


IT8620 NOTE	IT8728
PIN121	VCORE_EN/PCH_C0
PIN120	VLDT_EN/PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRVB#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2(VCC5)
PIN96	VIN1(VCC12)
PIN97	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0/VCORE(1.1V)/NC

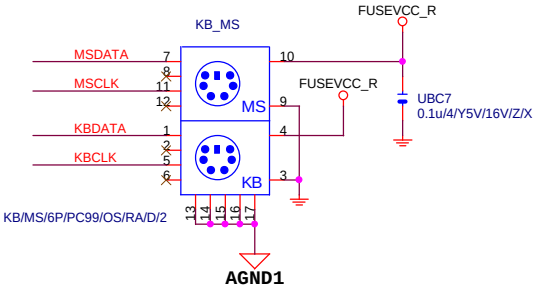
Gigabyte Technology

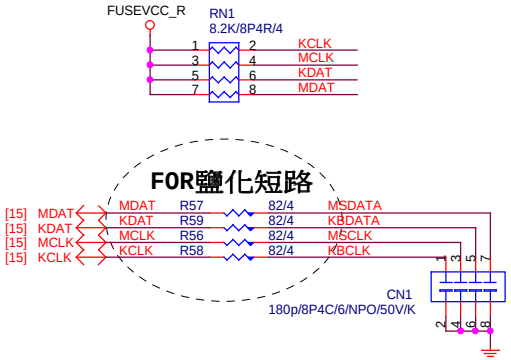
Title: ITE 8620 LPC IO

Size: Custom | Document Number: GA-H81M-S2PH | Rev: 1.01

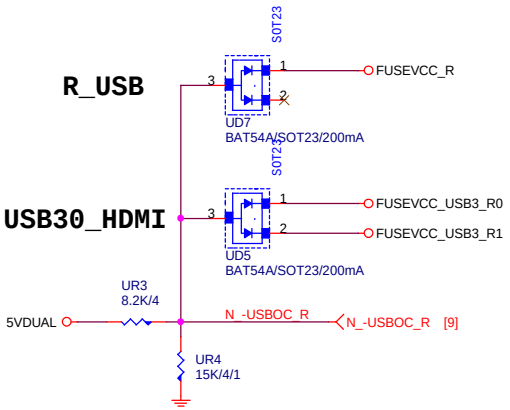
Date: Monday, July 15, 2013 | Sheet 15 of 31

KB/MS

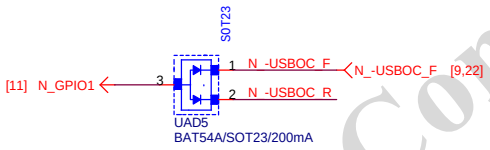




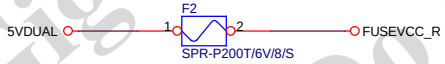
-USBOC_R



USB POWER PROTECT

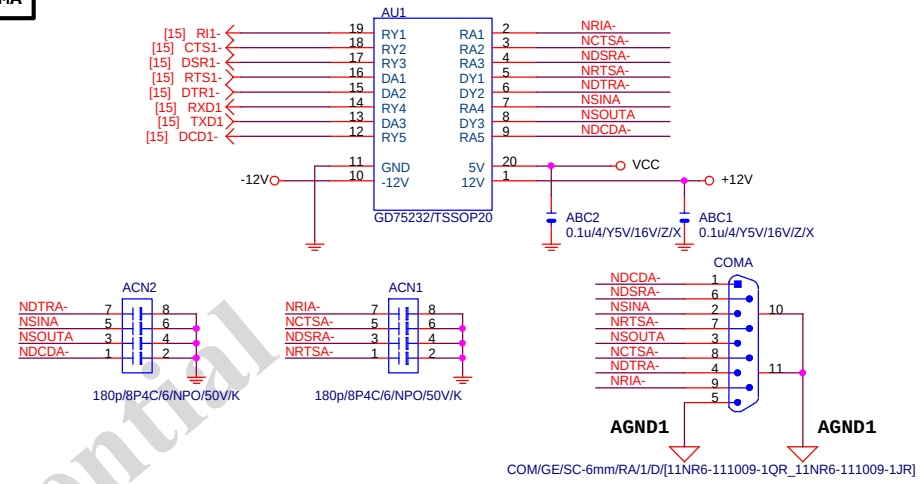


R_USB PWR

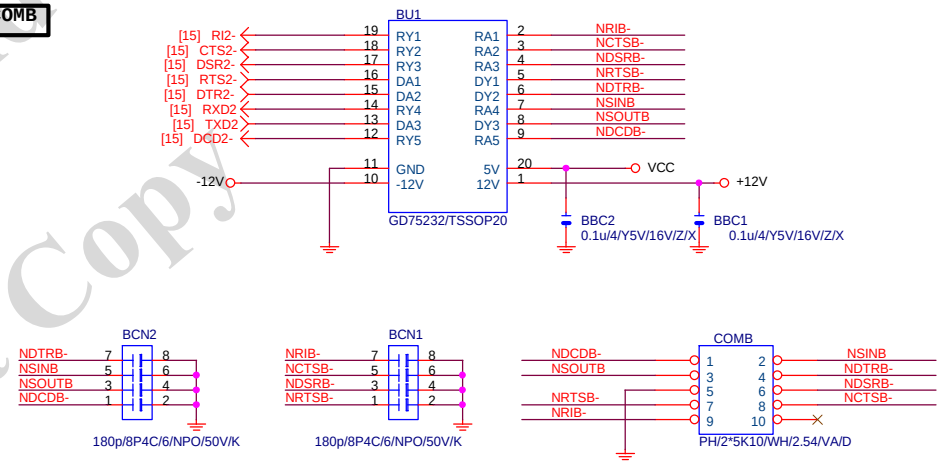


Close to connector
KB_MS_USB 2-Port 2.0A

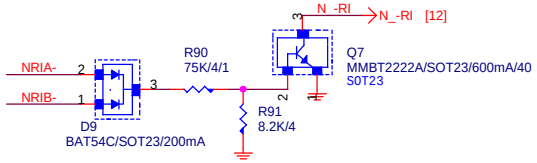
COMA



COMB



COM RI



Gigabyte Technology		
Title: COM,-RI,KB_MS		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 16 of 31	

PCIE TO PCI

PCI:5/4/5 Impedance=50 +- 15%

BA_D[0..31] BA_D[0..31] [18]

-BC_BE0 -BC_BE0 [18]
-BC_BE1 -BC_BE1 [18]
-BC_BE2 -BC_BE2 [18]
-BC_BE3 -BC_BE3 [18]

-BPERR -BPERR [18]
-BSERR -BSERR [18]

BPAR BPAR [18]
-BPLOCK -BPLOCK [18]
-BDEVSEL -BDEVSEL [18]
-BSTOP -BSTOP [18]
-BTRDY -BTRDY [18]
-BIRDY -BIRDY [18]
-BFRAME -BFRAME [18]

-PCIE_RST -PCIE_RST [14,15,19]

-BPCIRST -BPCIRST [18]

-BREQ0 -BREQ0 [18]
-BREQ1 -BREQ1 [18]
-BGNT0 -BGNT0 [18]
-BGNT1 -BGNT1 [18]

-BPCIPME1 -BPCIPME1 [18]

[18] BPCLK0 PR24 47/4/1 CLKOUT0
[18] BPCLK1 PR19 47/4/1 CLKOUT1

RREF PR13 12K/4/1
TEST_EN PR21 10K/4/1
EXT_ARB PR22 10K/4/1
RST_SEL PR7 10K/4/1

M66EN
PR42 1K/4/1

High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

PCICLK_SEL
PR37 10K/4/1

High: PCICLK INTPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip

VCC3

PU1

IT8892E/BX LQFP128

IT8892E/FX/S/[10HP2-698892-40R]

[10] G_-PBCLK
[10] G_PBCLK

[9] G_PCIEBOP
[9] G_PCIEBON
[9] G_PCIEBIN
[9] G_PCIEBIP

PBC61 0.1u/4/X7R/16V/K G_PCIEBOP_C
PBC62 0.1u/4/X7R/16V/K G_PCIEBON_C
PBC43 0.1u/4/X7R/16V/K G_PCIEBIN_C
PBC44 0.1u/4/X7R/16V/K G_PCIEBIP_C

IT8892

PCI slot

PRN14 0/8P4R/0402/SHT/X
-BPIRQA -BPIRQA1 [18]
-BPIRQD -BPIRQD1 [18]
-BPIRQB -BPIRQB1 [18]
-BPIRQC -BPIRQC1 [18]

VCCP PR26 0/4/SHT/M/X 3VDUAL

PCI slot

-BPCIPME1 PR27 0/4/SHT/M/X N_-PCIE_WAKE [12,14,19,25]

chipset side

3VDUAL
PR2 0/4/SHT/M/X
PCIEWAKE PR34 10K/4/1
-BPCIPME PR43 10K/4/1

PRN1 2.2K/8P4R/4
-BFRAME
-BTRDY
-BSTOP
-BDEVSEL

PRN2 2.2K/8P4R/4
-BPERR
-BSERR
-BPLOCK
-BIRDY

PRN15 2.2K/8P4R/4
-BPIRQA
-BPIRQD
-BPIRQB
-BPIRQC

PRN4 2.2K/8P4R/4
-BGNT3
-BREQ3
-BGNT1
-BREQ1

PRN5 2.2K/8P4R/4
-BGNT2
-BREQ2
-BGNT0
-BREQ0

BPAR PR41 2.2K/4/1/X

LDO_18V PFB1 0/6/SHT/M/X 1.8VA
PFB2 0/6/SHT/M/X 1.8VD
LDOAUX_18V PFB3 0/6/SHT/M/X 1.8V_AUX
PFB4 0/6/SHT/M/X 1.8V_AUXA

VCC3 PBC60 10u/6/X5R/6.3V/M PBC21 1u/4/X5R/6.3V/K PBC22 0.1u/4/X7R/16V/K PBC33 1u/4/X5R/6.3V/K PBC34 0.1u/4/X7R/16V/K PBC35 0.1u/4/X7R/16V/K

3VDUAL PBC29 0.1u/4/X7R/16V/K/X
1.8V_AUX PBC30 0.1u/4/X7R/16V/K
1.8V_AUXA PBC31 0.1u/4/X7R/16V/K

1.8VD PBC23 10u/6/X5R/6.3V/M PBC24 1u/4/X5R/6.3V/K PBC25 0.1u/4/X7R/16V/K

1.8VD PBC59 1u/4/X5R/6.3V/K PBC38 0.1u/4/X7R/16V/K PBC39 0.01u/4/X7R/25V/K

PCB layout note:
Close to chip

1.8VA PBC26 10u/6/X5R/6.3V/M PBC27 1u/4/X5R/6.3V/K PBC28 0.1u/4/X7R/16V/K

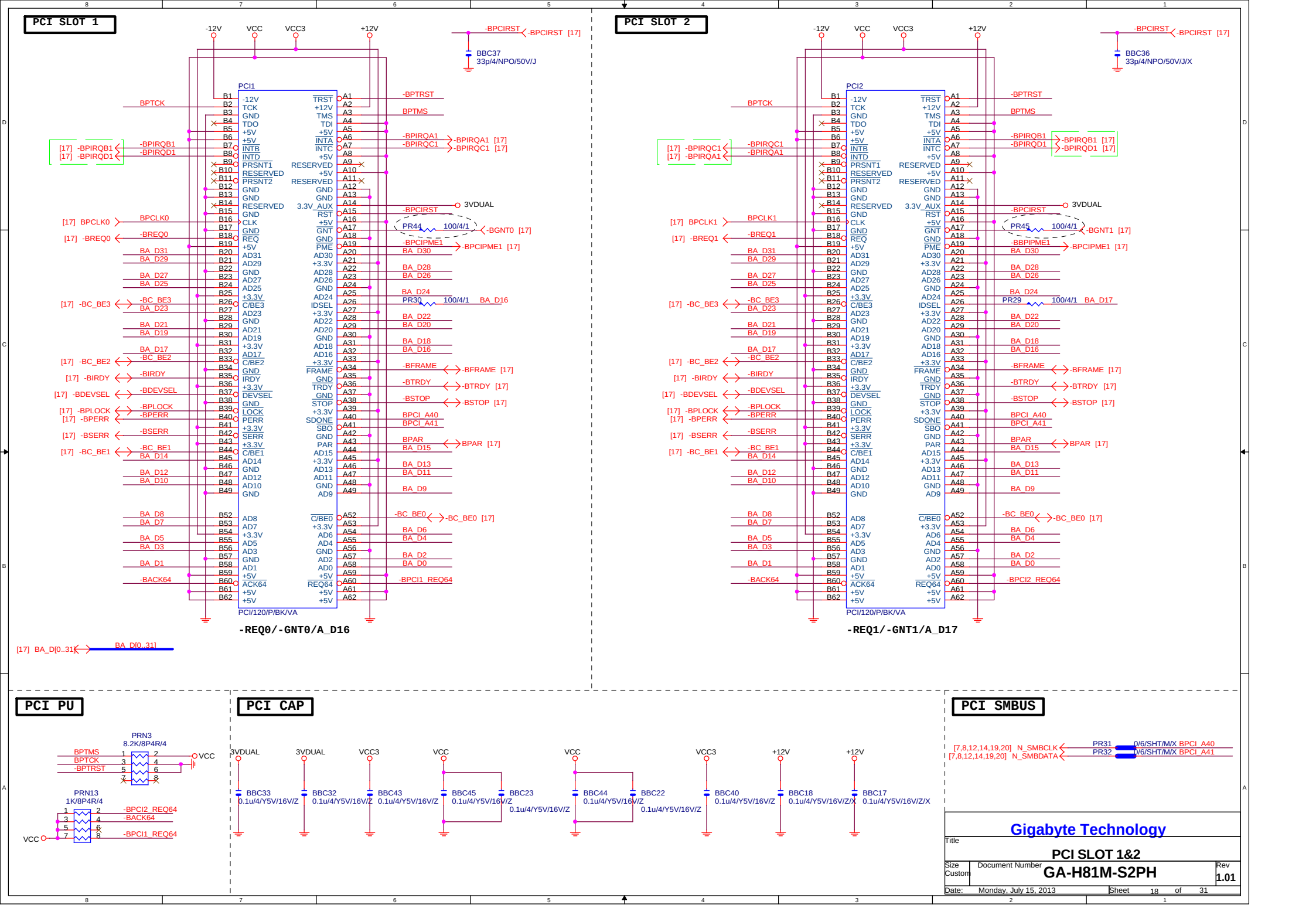
Close to chip

LDOAUX_18V PBC32 10u/6/X5R/6.3V/M PBC40 1u/4/X5R/6.3V/K PBC41 0.01u/4/X7R/25V/K

LDO_18V PBC58 10u/6/X5R/6.3V/M PBC42 1u/4/X5R/6.3V/K PBC45 0.01u/4/X7R/25V/K

PCB layout note:
Close to chip

Gigabyte Technology		
Title: ITE IT8892E		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 17 of 31	


PCI SLOT 1
PCI1
PCI/120/P/BK/VA
-REQ0/-GNT0/A_D16
PCI SLOT 2
PCI2
PCI/120/P/BK/VA
-REQ1/-GNT1/A_D17
PCI PU
PRN3
8.2K/8P4R/4
PRN13
1K/8P4R/4
PCI CAP
PCI SMBUS
Gigabyte Technology
Title
PCI SLOT 1&2
Size
Custom
Document Number
GA-H81M-S2PH
Rev
1.01
Date: Monday, July 15, 2013
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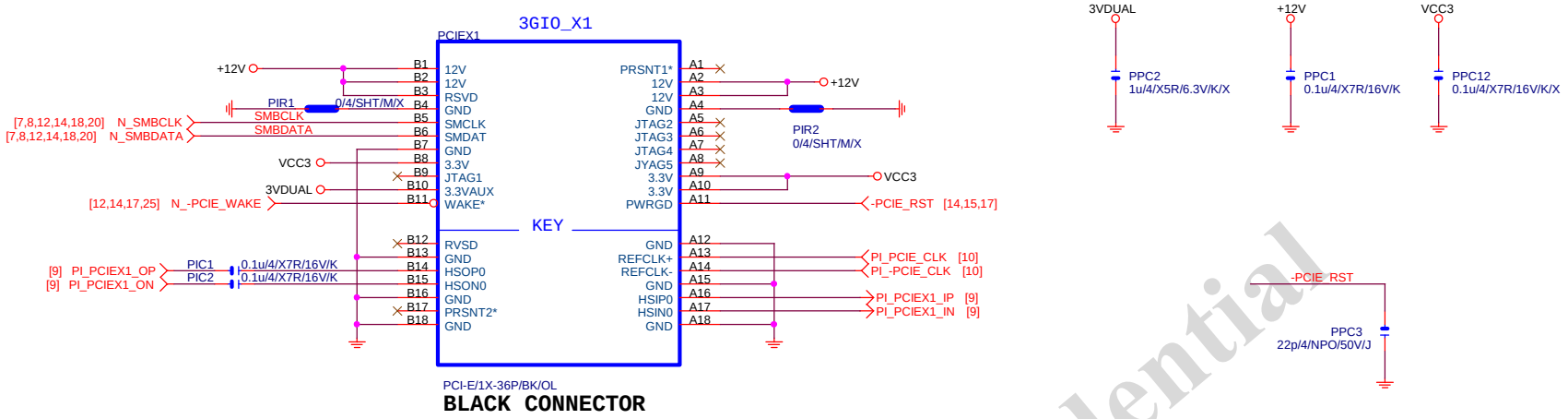
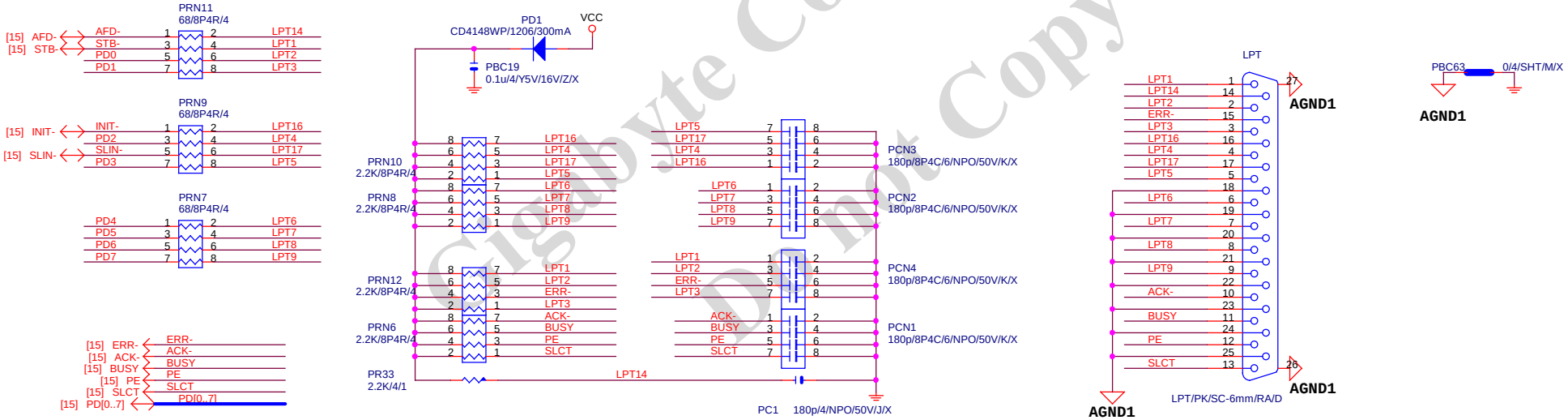
PCIEX1 SLOT



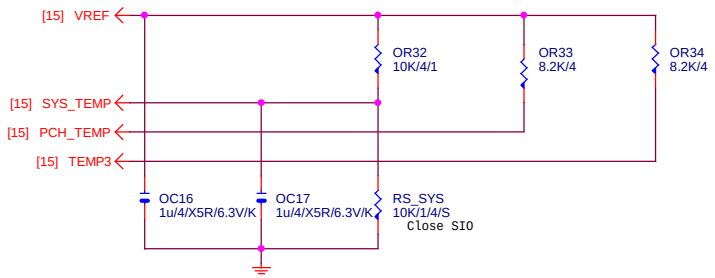
LPT PORT

【技術通報R&D技術通報151】
33ohm Change to 68ohm

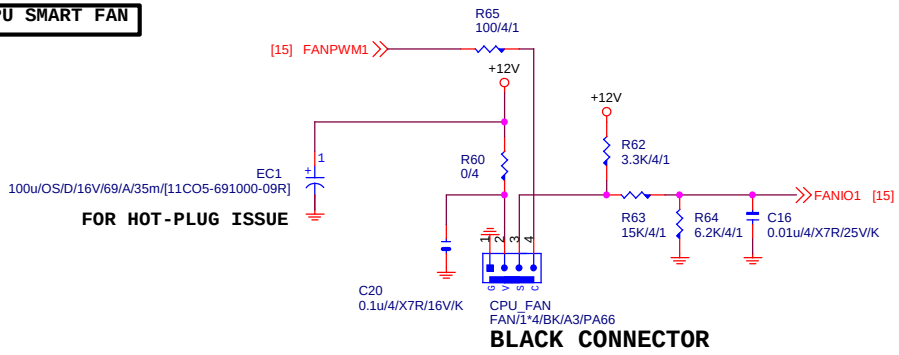


Gigabyte Technology		
Title: LPT		
Size: B	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 19 of 31	

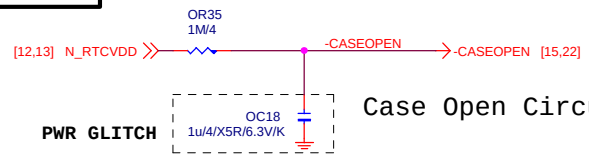
TEMP H/W MONITOR



CPU SMART FAN

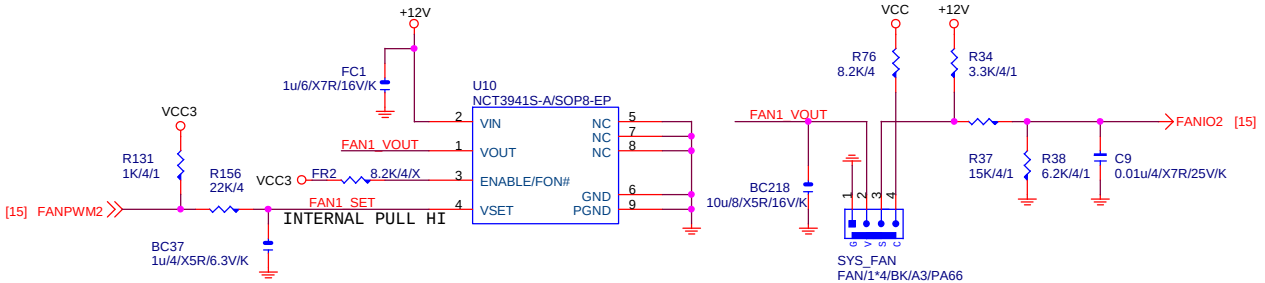


CASE OPEN



SYS SMART FAN

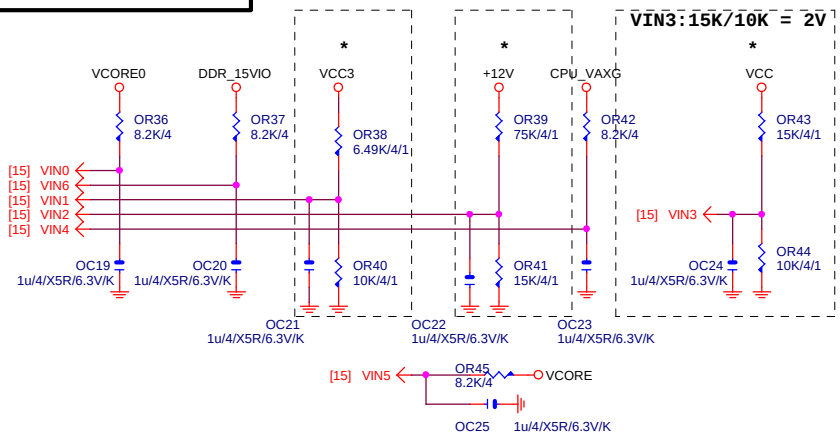
Linear SYS_FAN



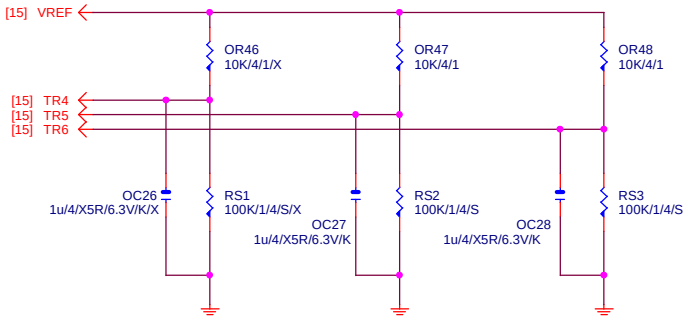
VOLTAGE-- H/W MONITOR

VIN2:75K/15K = 2V

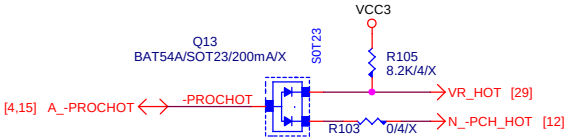
VIN3:15K/10K = 2V



THERMISTOR MONITOR

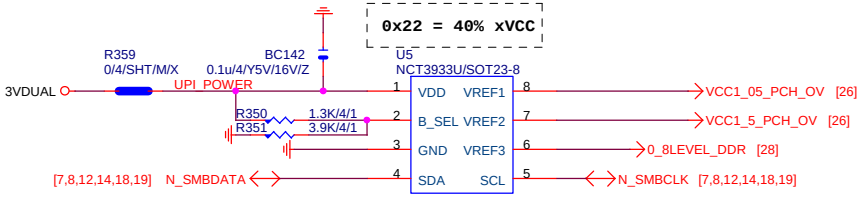


-PROHOT

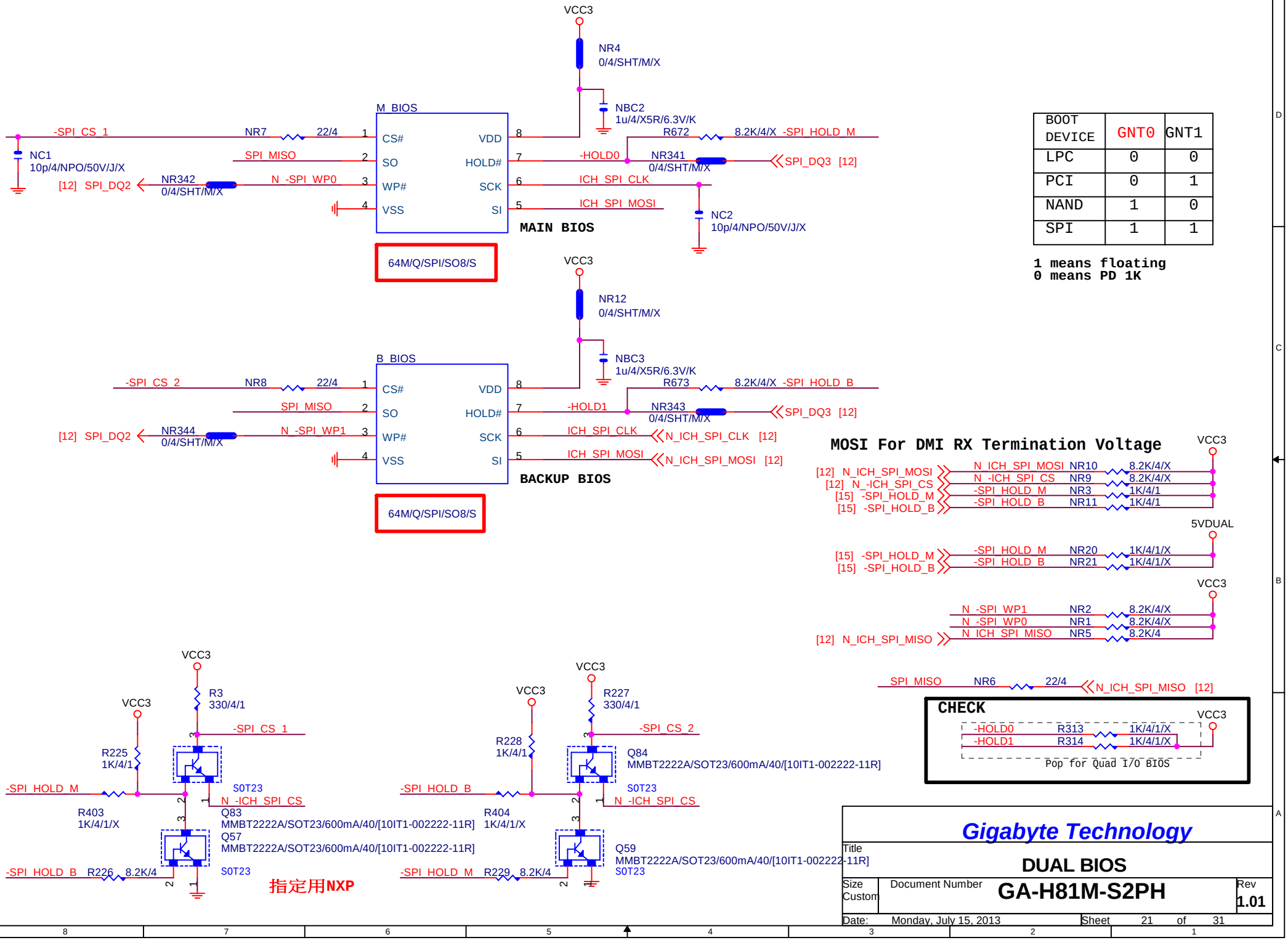


OV NCT3933

接pwm feedback pin



Gigabyte Technology		
Title: HWM,FAN CTRL,OV		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 20 of 31	



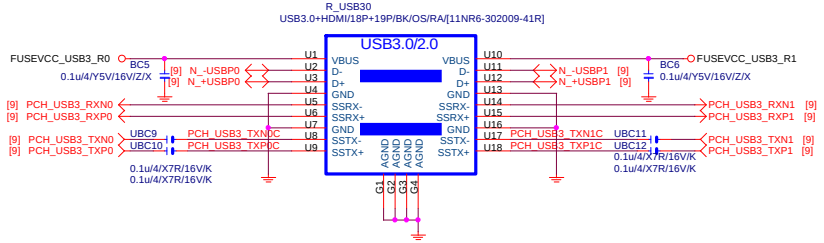
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

Gigabyte Technology		
Title: DUAL BIOS		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 21 of 31	

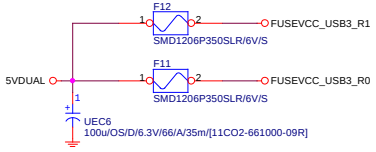
R_USB30

R_USB30/HDMI SEPARATE



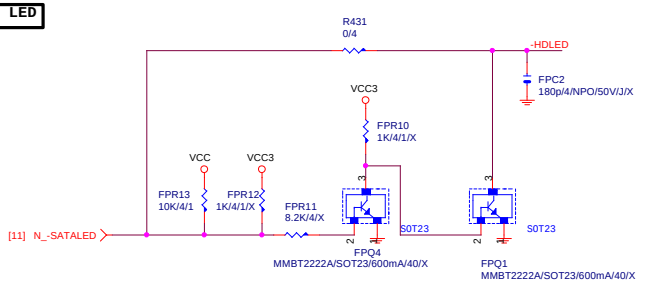
R_USB30 PWR

Polyswitch-1206

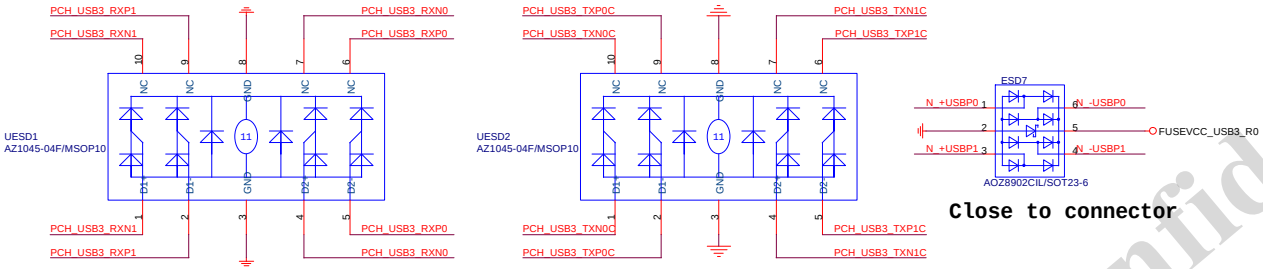


USB3.0 1Port - 1Fuse (3.5A)

SATA LED

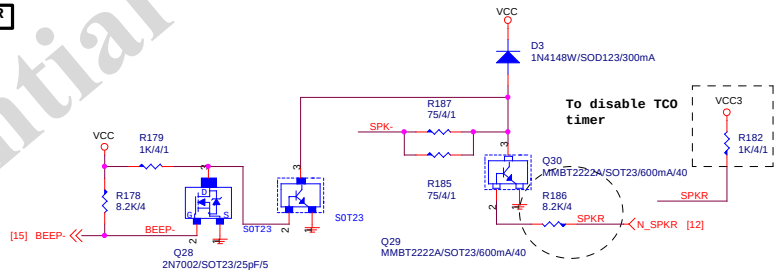


USB30_HDMI ESD PROTECT

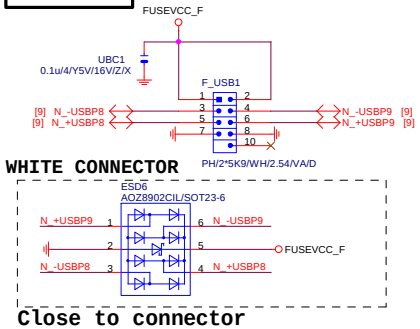


Close to connector

SPKR



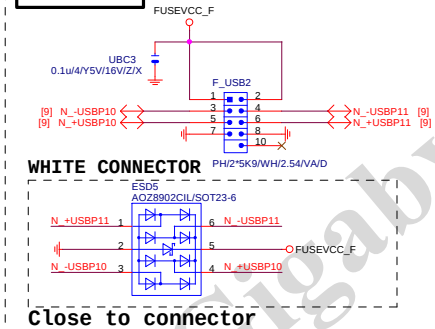
FRONT USB1



WHITE CONNECTOR

Close to connector

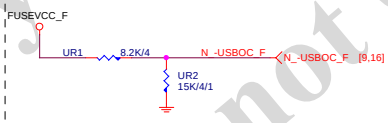
FRONT USB2



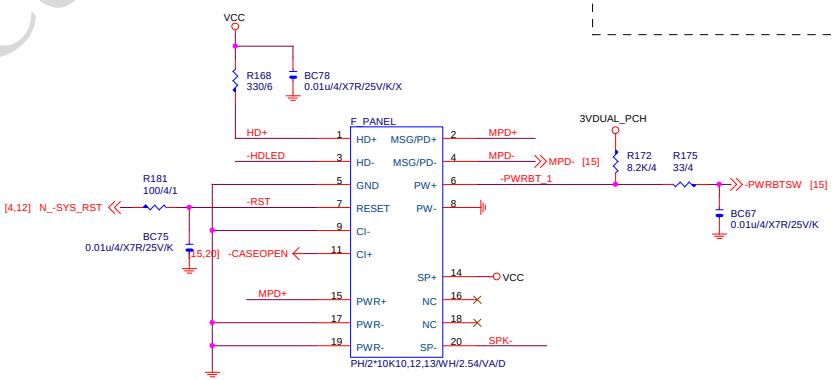
WHITE CONNECTOR

Close to connector

-USBOC_F



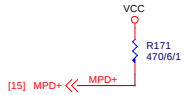
INTEL FRONT PANEL

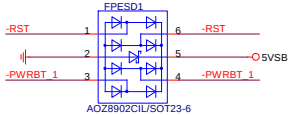


PIN2X10PANEL_NEW

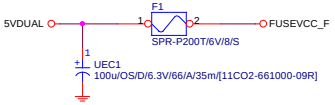
PWR LED

FOR IT8620 Ctrl





FUSEVCC_F

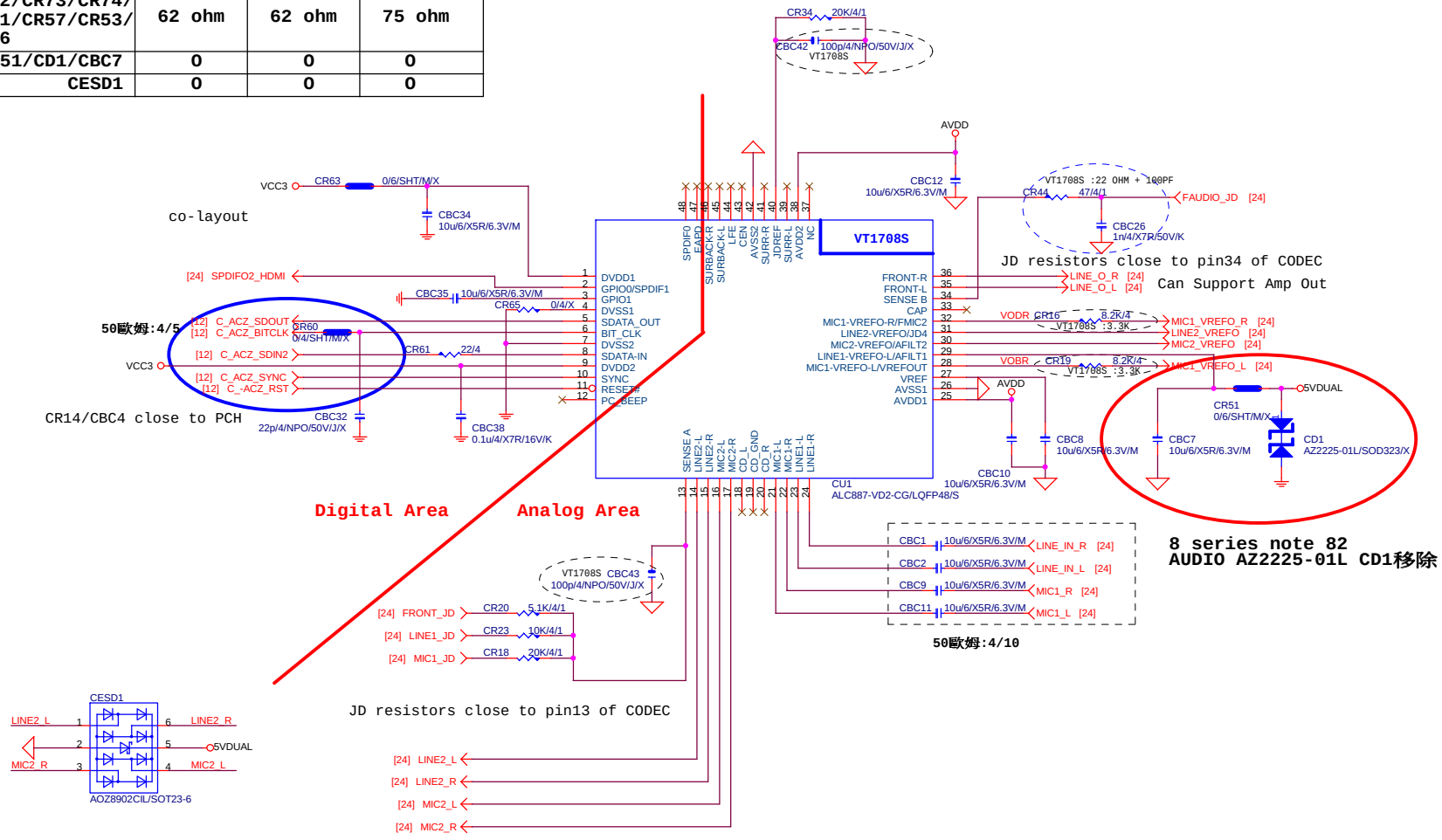


F_USB1, F_USB2 4-Port 2.6A

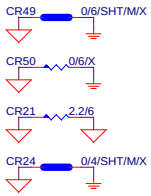
Gigabyte Technology		
Title: FP,F_USB,USB PWR,SPKR,SATA LED		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 22 of 31	

AZALIA CODEC *ALC892/ALC887-VD2/VT1708-CE Colay*

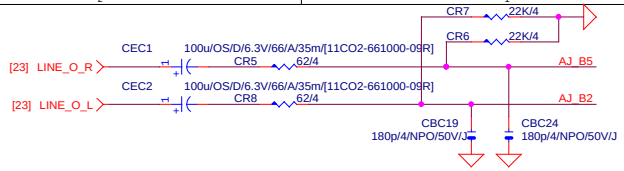
	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	0	0	0
CESD1	0	0	0



Gigabyte Technology	
Title	HD AUDIO ALC887B-VD2/VT1708S/VT2021
Size: Custom	Document Number: GA-H81M-S2PH
Rev: 1.01	
Date: Monday, July 15, 2013	Sheet 23 of 31

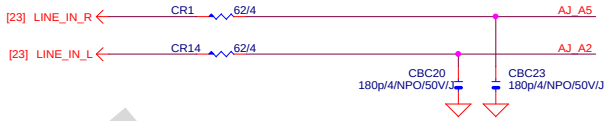


LINE-OUT



Only reserved for ALC888

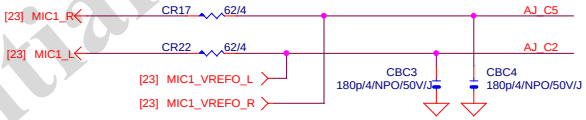
LINE-IN



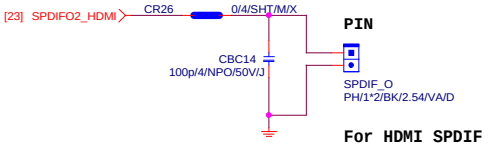
Verify MIC function in LINE-in

For 889A/888

MIC-IN

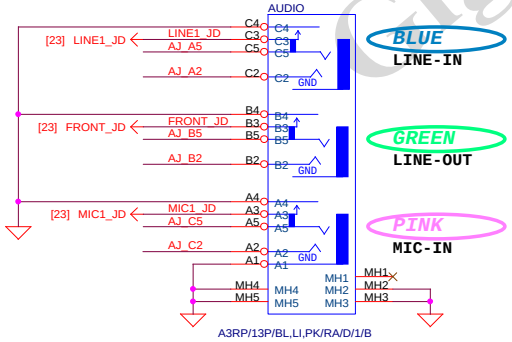


SPDIF_OUT

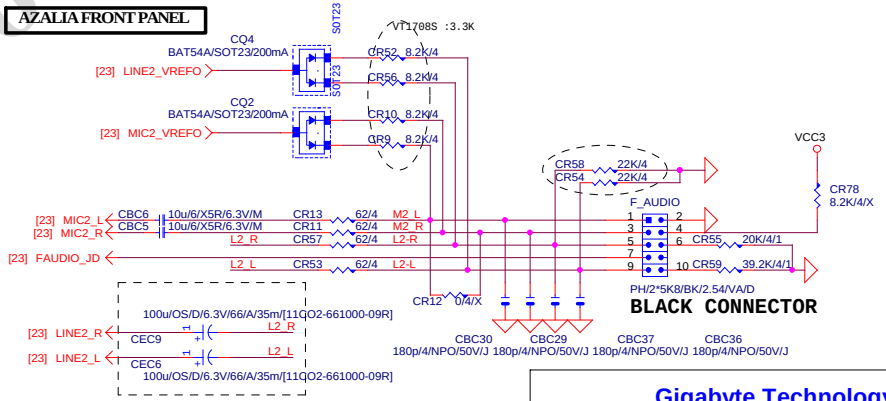


For HDMI SPDIF

SPDIF_OUT

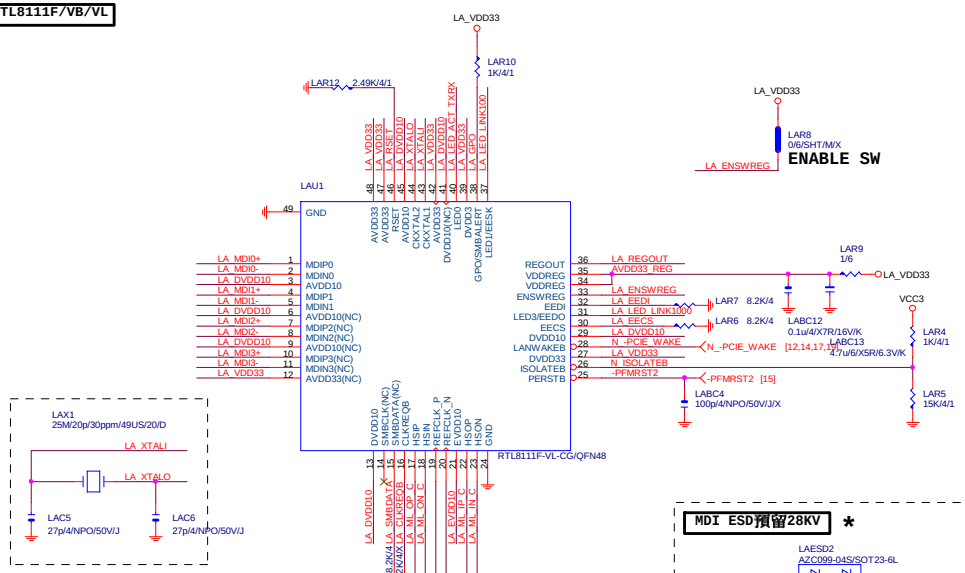


AZALIA FRONT PANEL



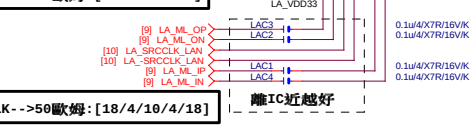
Gigabyte Technology		
Title	AUDIO JACK	
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
Date: Monday, July 15, 2013	Sheet 24 of 31	

LAN:RTL8111F/VB/VL

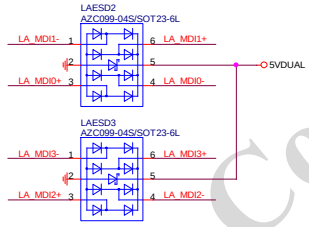


LA_ ML-->80歐姆:[15/5/5/5/15]

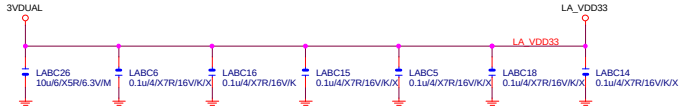
SRCCLK-->50歐姆:[18/4/10/4/18]



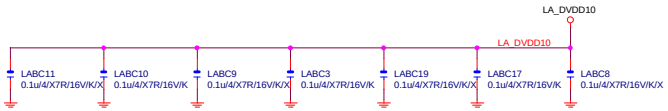
MDI ESD預留28KV *



LAN POWER

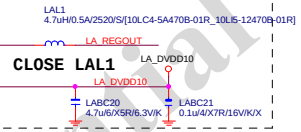


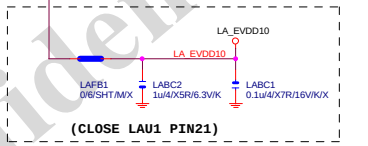
(CLOSE LAU1 PIN:12,27,39,42,47,48)



(CLOSE LAU1 PIN3,6,9,13,29,41,45)

(CLOSE LAU1 PIN36)



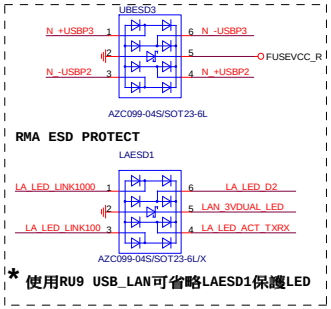


(CLOSE LAU1 PIN21)

Power domain chart

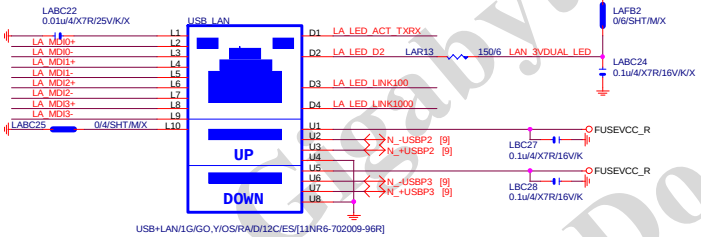
	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

USB_LAN CONNECTOR



* 使用RU9 USB_LAN可省略LAESD1保護LED

LA_MDI-->100歐姆:[20/4/8/4/20]

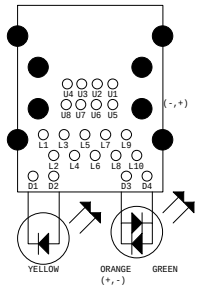


USB+LAN/1G/GO,Y/OS/RA/D/12C/ES/[11NR6-702009-96R]

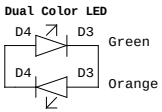
EMI SHORT PAD

PS:視EMI需求

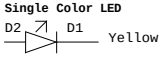
注意:USB PORT(目前:暫代6,7PORT)
USB-->90歐姆:[15/4.5/7.5/4.5/15]



Dual Color LED



Single Color LED



BOM NOTICE *

料號	規格	廠商
11NR6-702009-96R	1G LAN (12core)	UDE(RU9 ESD+)

[LED獨立走線,可省略外加AZC099料件LAESD1]

1. 9KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R
2. 28KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R
LAESD2,LAESD3:上件AZC398-04S

Gigabyte Technology

Title: Realtek RTL8111F

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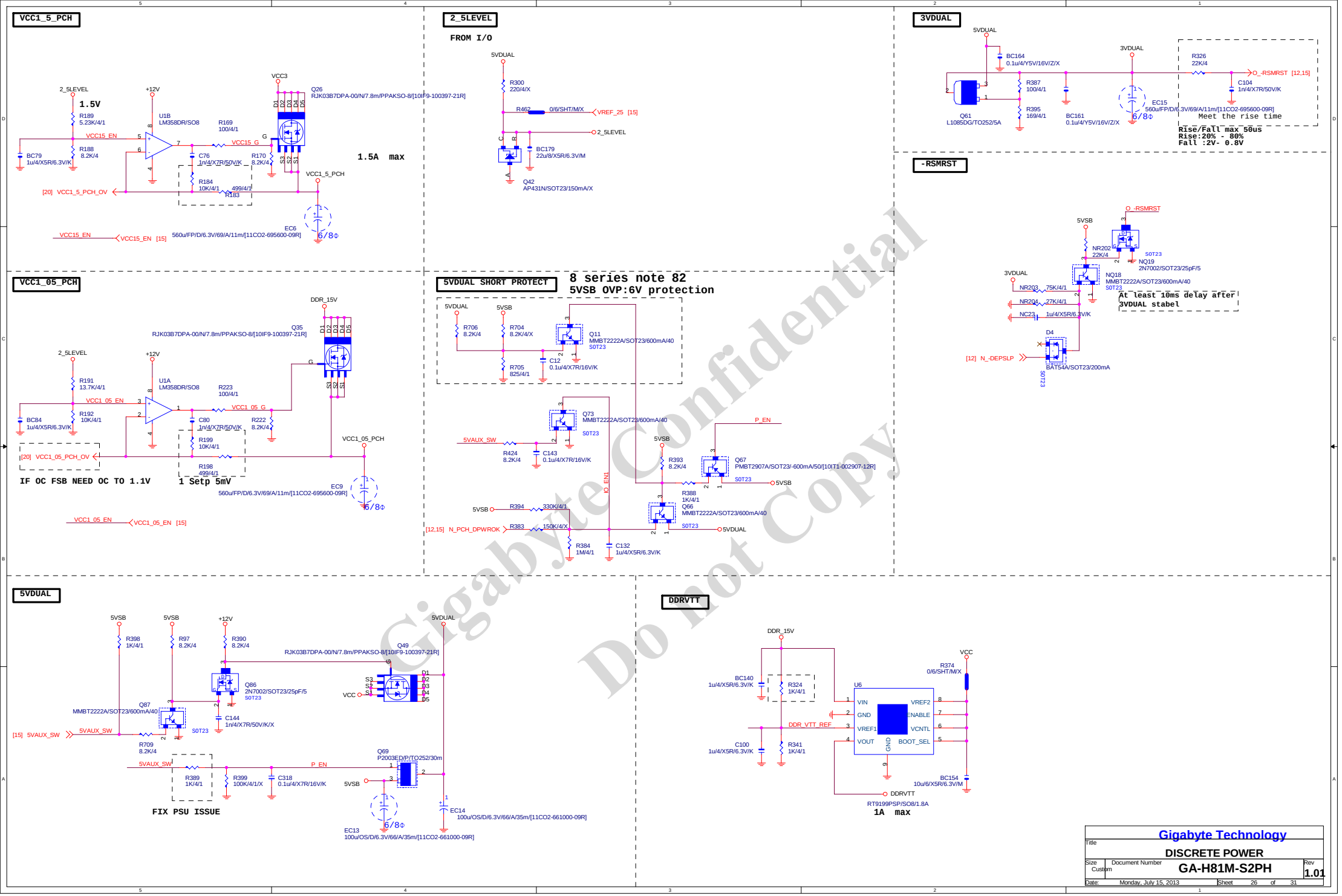
VCC1_5_PCH
2_5LEVEL
1.5V
R189 5.23K/4/1
R188 8.2K/4
BC79 1u/4/X5R/6.3V/K
+12V
U1B LM358DR/SO8
R169 100/4/1
VCC15_EN
VCC15_G
C76 1n/4/X7R/50V/K
R170 8.2K/4
R184 10K/4/1
R183 499/4/1
[20] VCC1_5_PCH_OV
VCC3
Q26 RJK03B7DPA-00/N/7.8m/PPAKSO-8/[10IF9-100397-21R]
VCC1_5_PCH
EC6 560u/FP/D/6.3V/69/A/11m/[11CO2-695600-09R]
6/8Φ
1.5A max
VCC15_EN [15]
2_5LEVEL
FROM I/O
5VDUAL
R300 220/4/X
R462 0/6/SHT/M/X
VREF_25 [15]
2_5LEVEL
BC179 22u/8/X5R/6.3V/M
Q42 AP431N/SOT23/150mA/X
3VDUAL
BC164 0.1u/4/Y5V/16V/Z/X
Q61 L1085DG/TO252/5A
R387 100/4/1
R395 169/4/1
BC161 0.1u/4/Y5V/16V/Z/X
EC15 560u/FP/D/6.3V/69/A/11m/[11CO2-695600-09R]
R326 22K/4
O_-RSMRST [12,15]
C104 1n/4/X7R/50V/K
Meet the rise time
Rise/Fall max 50us
Rise:20% - 80%
Fall :2V- 0.8V
-RSMRST
5VSB
NR202 22K/4
NQ19 2N7002/SOT23/25pF/5
SOT23
3VDUAL
NQ18 MMBT2222A/SOT23/600mA/40
NR203 75K/4/1
NR204 27K/4/1
NC23 1u/4/X5R/6.3V/K
At least 10ms delay after
3VDUAL stabel
D4
BAT54A/SOT23/200mA
[12] N_-DEPSLP
VCC1_05_PCH
DDR_15V
Q35 RJK03B7DPA-00/N/7.8m/PPAKSO-8/[10IF9-100397-21R]
2_5LEVEL
R191 13.7K/4/1
R192 10K/4/1
BC84 1u/4/X5R/6.3V/K
U1A LM358DR/SO8
R223 100/4/1
VCC1_05_EN
VCC1_05_G
C80 1n/4/X7R/50V/K
R222 8.2K/4
R199 10K/4/1
R198 499/4/1
[20] VCC1_05_PCH_OV
VCC1_05_PCH
EC9 560u/FP/D/6.3V/69/A/11m/[11CO2-695600-09R]
6/8Φ
IF OC FSB NEED OC TO 1.1V
1 Setp 5mV
VCC1_05_EN [15]
5VDUAL SHORT PROTECT
8 series note 82
5VSB OVP:6V protection
5VDUAL
5VSB
R706 8.2K/4
R704 8.2K/4/X
R705 825/4/1
Q11 MMBT2222A/SOT23/600mA/40
C12 0.1u/4/X7R/16V/K
Q73 MMBT2222A/SOT23/600mA/40
5VAUX_SW
R424 8.2K/4
C143 0.1u/4/X7R/16V/K
P_EN
R393 8.2K/4
Q67 PMBT2907A/SOT23/-600mA/50/[10IT1-002907-12R]
R388 1K/4/1
Q66 MMBT2222A/SOT23/600mA/40
R394 330K/4/1
R383 150K/4/X
R384 1M/4/1
C132 1u/4/X5R/6.3V/K
[12,15] N_PCH_DPWROK
5VDUAL
5VDUAL
5VSB
+12V
R398 1K/4/1
R97 8.2K/4
R390 8.2K/4
Q49 RJK03B7DPA-00/N/7.8m/PPAKSO-8/[10IF9-100397-21R]
Q86 2N7002/SOT23/25pF/5
SOT23
Q87 MMBT2222A/SOT23/600mA/40
C144 1n/4/X7R/50V/K/X
[15] 5VAUX_SW
R709 8.2K/4
P_EN
R389 1K/4/1
R399 100K/4/1/X
C318 0.1u/4/X7R/16V/K
Q69 P2003ED/P/TO252/30m
EC13 100u/OS/D/6.3V/66/A/35m/[11CO2-661000-09R]
EC14 100u/OS/D/6.3V/66/A/35m/[11CO2-661000-09R]
FIX PSU ISSUE
DDRVTT
DDR_15V
BC140 1u/4/X5R/6.3V/K
R324 1K/4/1
DDR_VTT_REF
C100 1u/4/X5R/6.3V/K
R341 1K/4/1
U6
VIN
GND
VREF1
VOUT
VREF2
ENABLE
VCNTL
BOOT_SEL
R374 0/6/SHT/M/X
BC154 10u/6/X5R/6.3V/M
DDRVTT
RT9199PSP/SO8/1.8A
1A max
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Do not Copy
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Title
DISCRETE POWER
Size Custom
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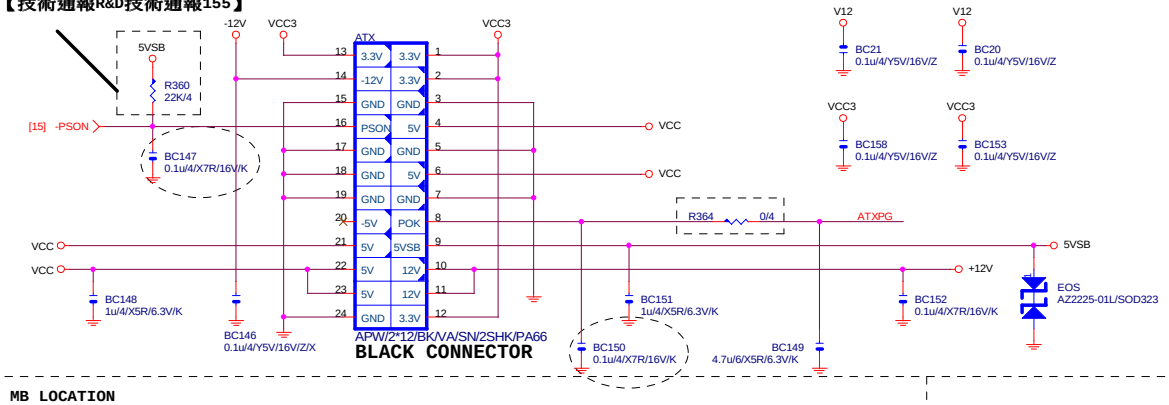
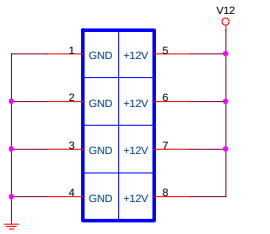
ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】

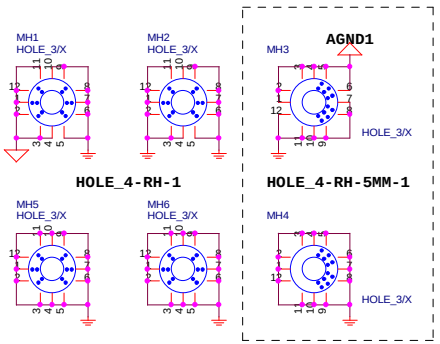


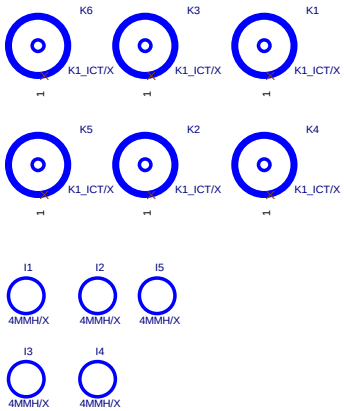
ATXX4 POWER CONNECTOR



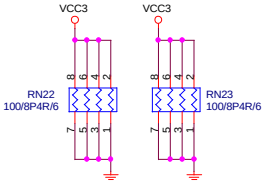
BLACK CONNECTOR

MB LOCATION



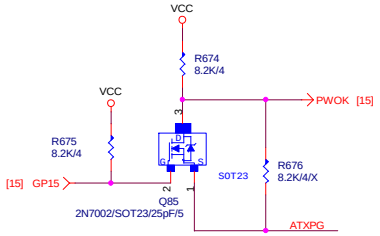


FIX PWR MINMUN LOAD



PWOK PATCH

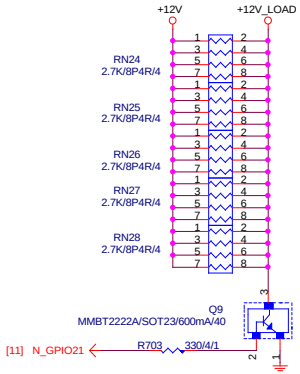
【技術通報R&D技術通報154】



CLK GEN

N/A

【技術通報R&D技術通報153】



To fix 12V light load abnromal issue

Gigabyte Technology			
Title: ATX CONNECTOR			
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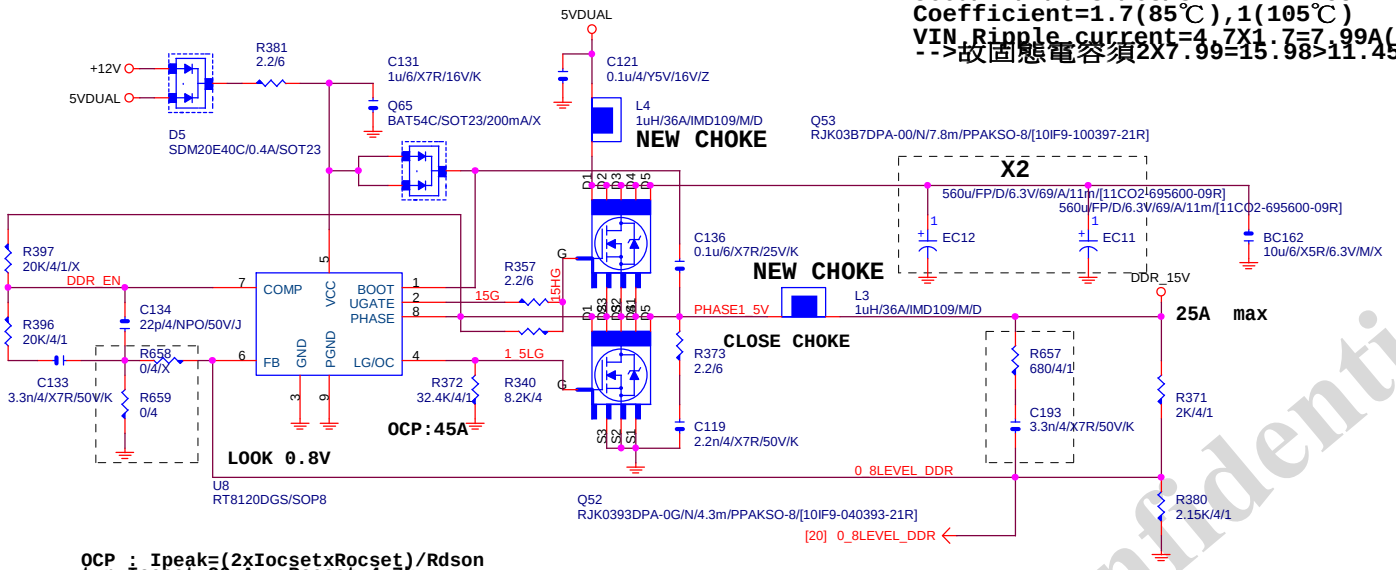
DDR15V

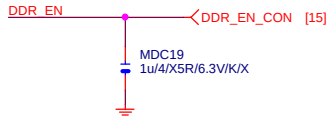
VIN=5V,VOUT=1.5V,IOUT=25A,PHASE=1
IRMS=11.45A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85℃),1(105℃)
VIN Ripple current=4.7X1.7=7.99A(85℃)
-->故固態電容須2X7.99=15.98>11.45A



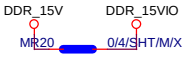
OCP : Ipeak=(2xIocsetxRocset)/Rdson
typ Iocset=20uA , Rocset=4.7k

OCP :53.71A=(2x20uax4.7k)/(7m//7m)

PWR SEQ



From DDR_15V source
10 mils trace to SIO



VCC1_05_ME

Z81 N/A

VCC3_ME

H81 N/A

Gigabyte Technology		
Title: DDR & M3 POWER		
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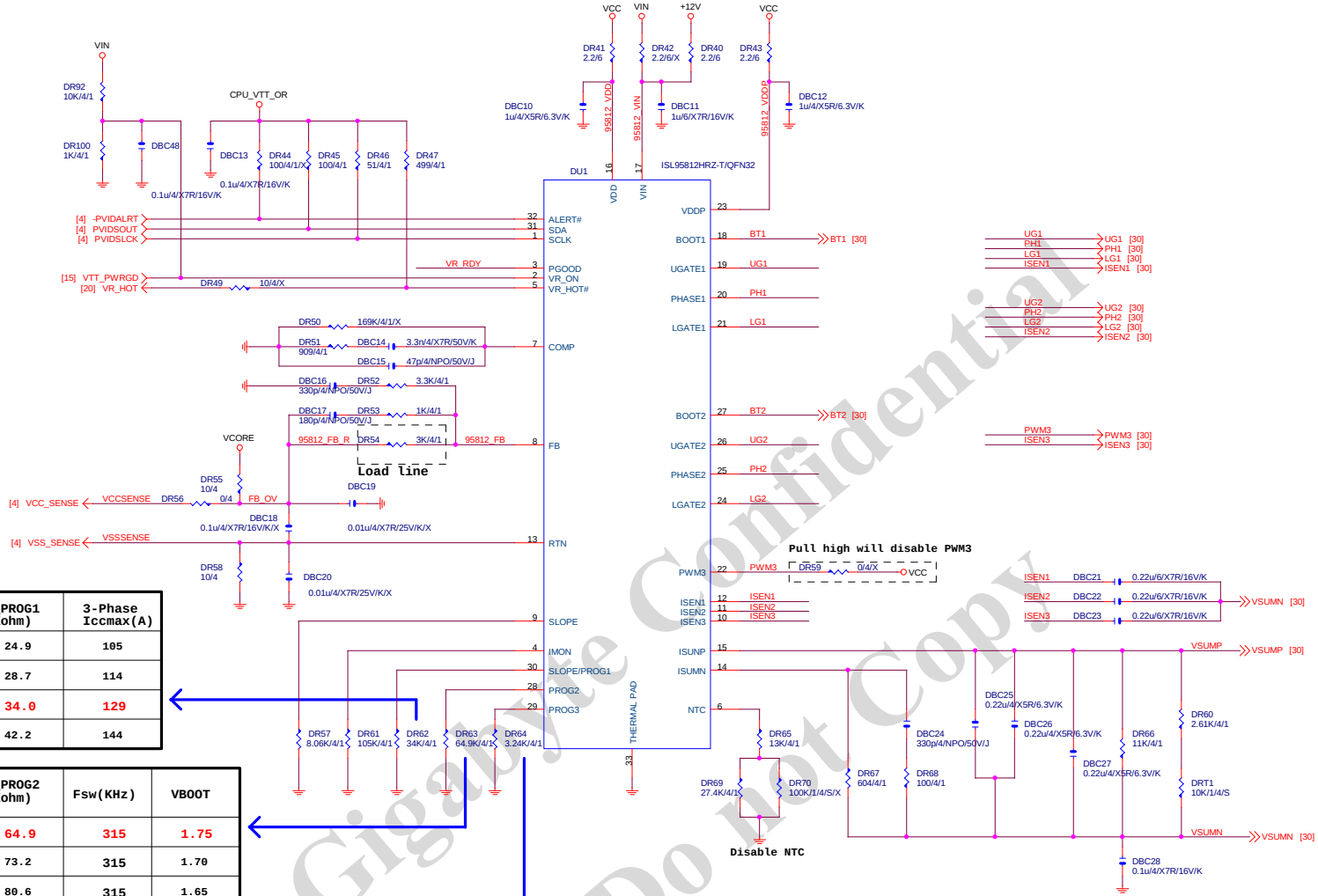

R_PROG1 (Kohm)	3-Phase Iccmax(A)
24.9	105
28.7	114
34.0	129
42.2	144

R_PROG2 (Kohm)	Fsw(KHz)	VBOOT
64.9	315	1.75
73.2	315	1.70
80.6	315	1.65
90.9	315	0

R_PROG3 (Kohm)	Fast Slew Rate (mV/us)
3.24	12
5.76	24
9.31	40
13.3	45

Gigabyte Technology		
Title: CPU CORE VR-1		
Size: Custom	Document Number: GA-H81M-S2PH	Rev: 1.01
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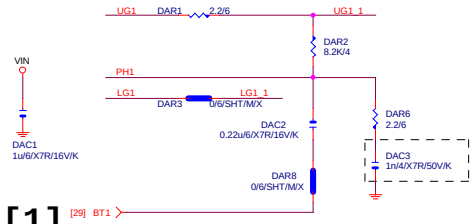
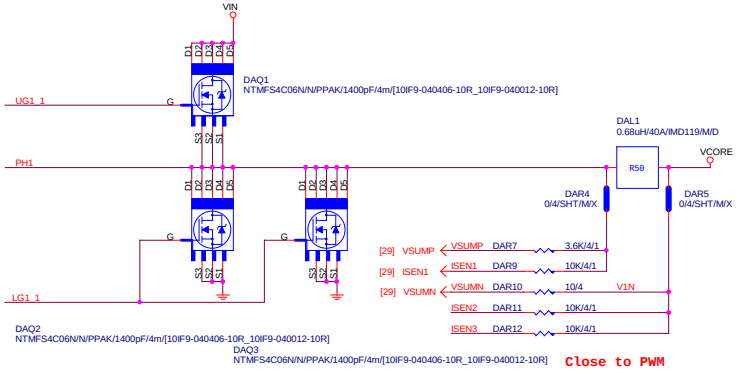
PHASE 1

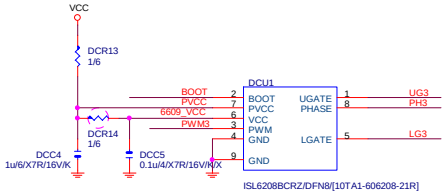


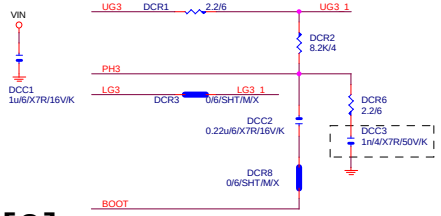
[1]



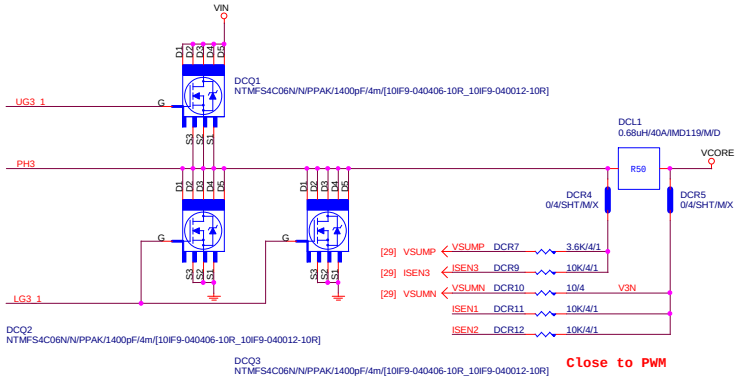
Close to PWM

PHASE 3



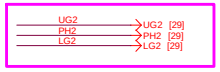


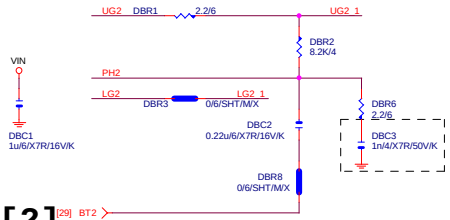
[3]



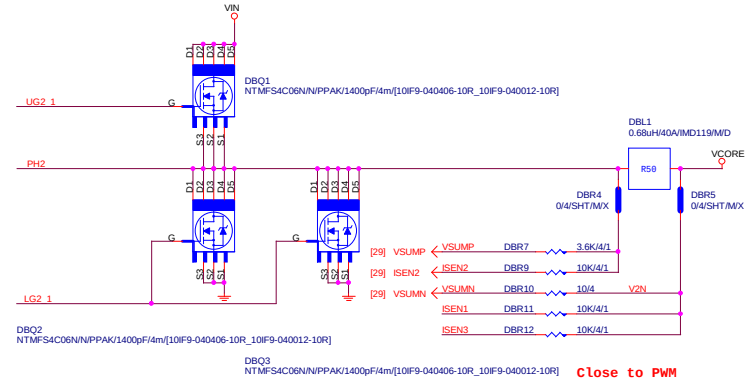
Close to PWM

PHASE 2

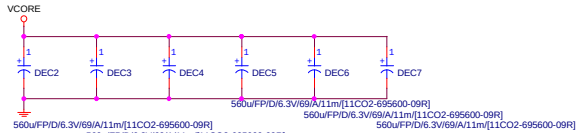


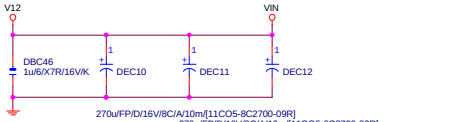


[2]



Close to PWM





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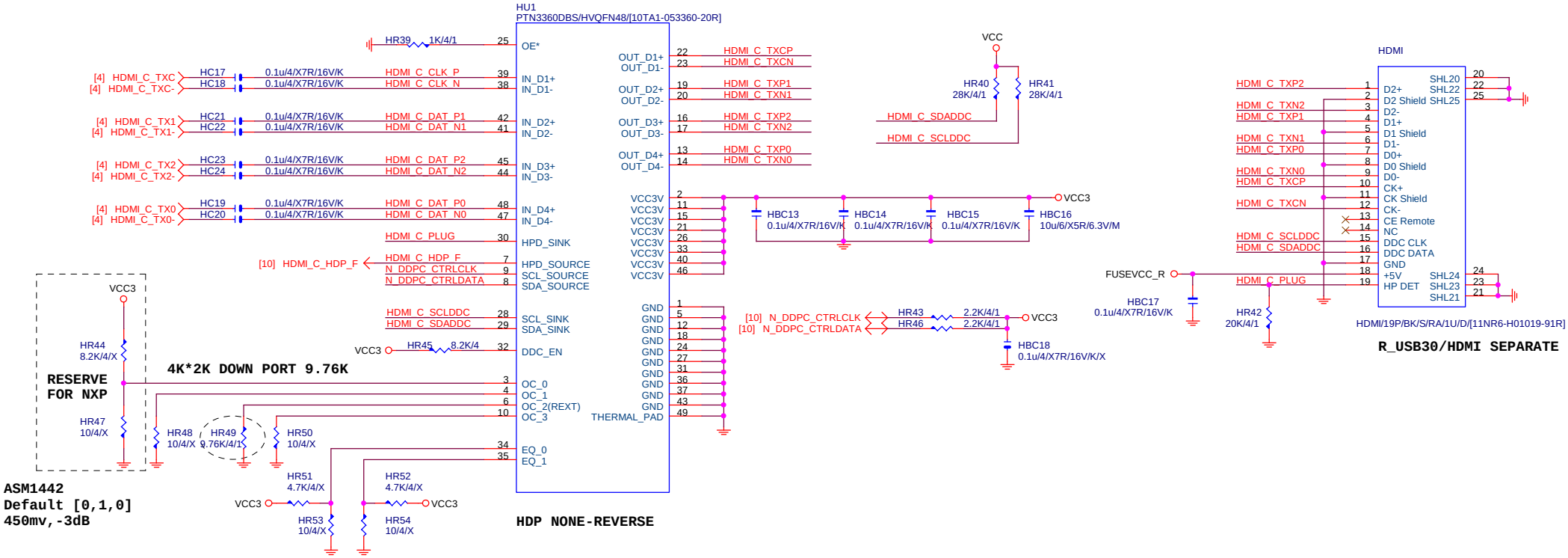
HDMI LEVEL SHIFT



Gigabyte Technology		
Title: HDMI		
Size: B	Document Number: GA-H81M-S2PH	Rev: 1.01
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