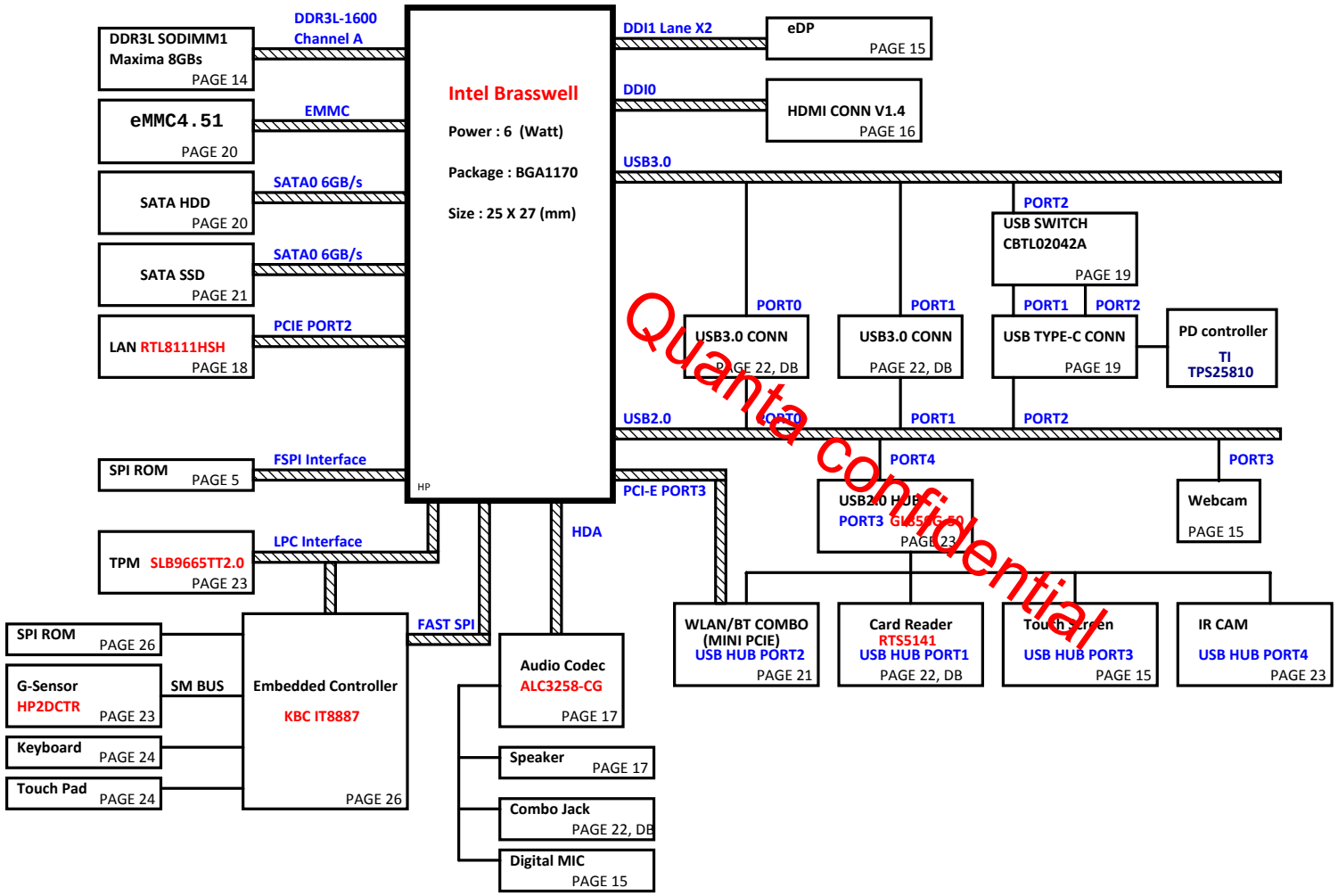
NFL-P G72D BSW UMA (14"/15.6")
Intel Braswell-M Platform Block Diagram

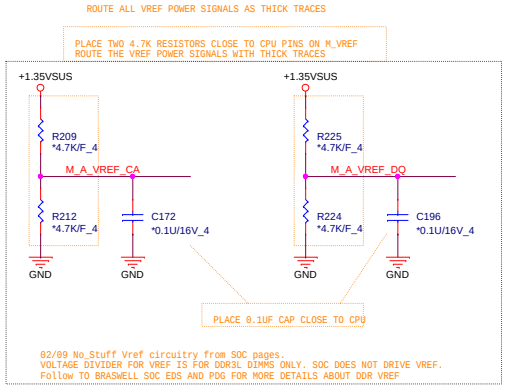


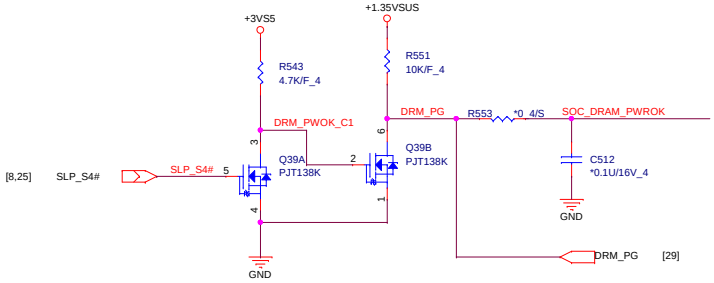
PCB 6L STACK UP

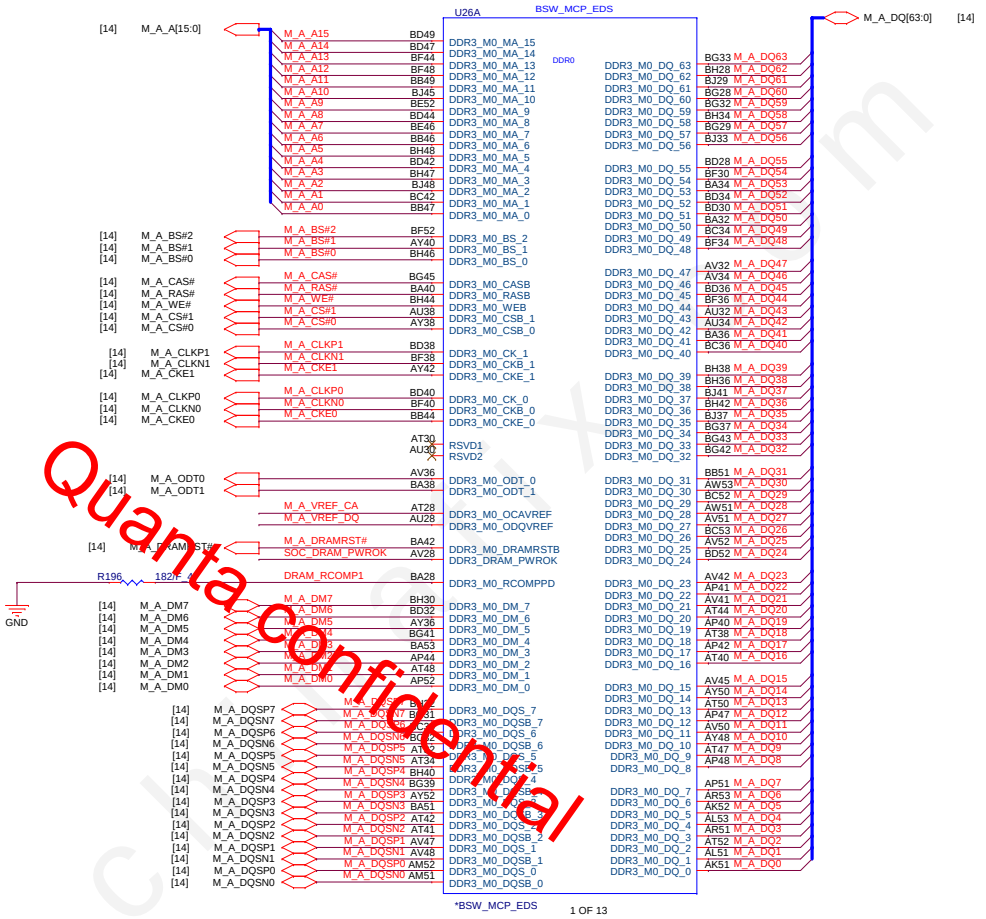
LAYER 1 : TOP
LAYER 2 : SVCC
LAYER 3 : IN1(LOW)
LAYER 4 : IN2(HIGH)
LAYER 5 : SGND
LAYER 6 : BOT

PROJECT : G72D
Quanta Computer Inc.

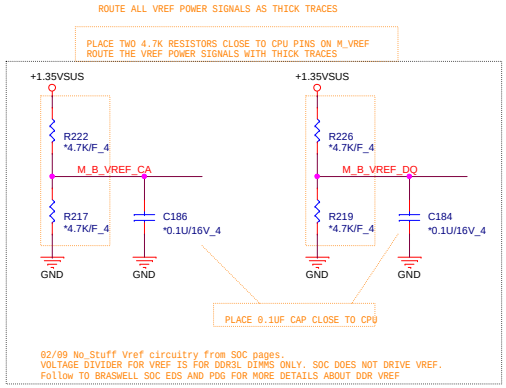
Size	Document Number	Rev
Custom	Braswell Block Diagram	1A
Date: Tuesday, January 24, 2017	Sheet 1 of 35	

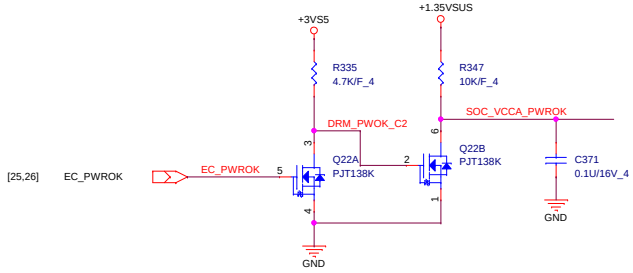


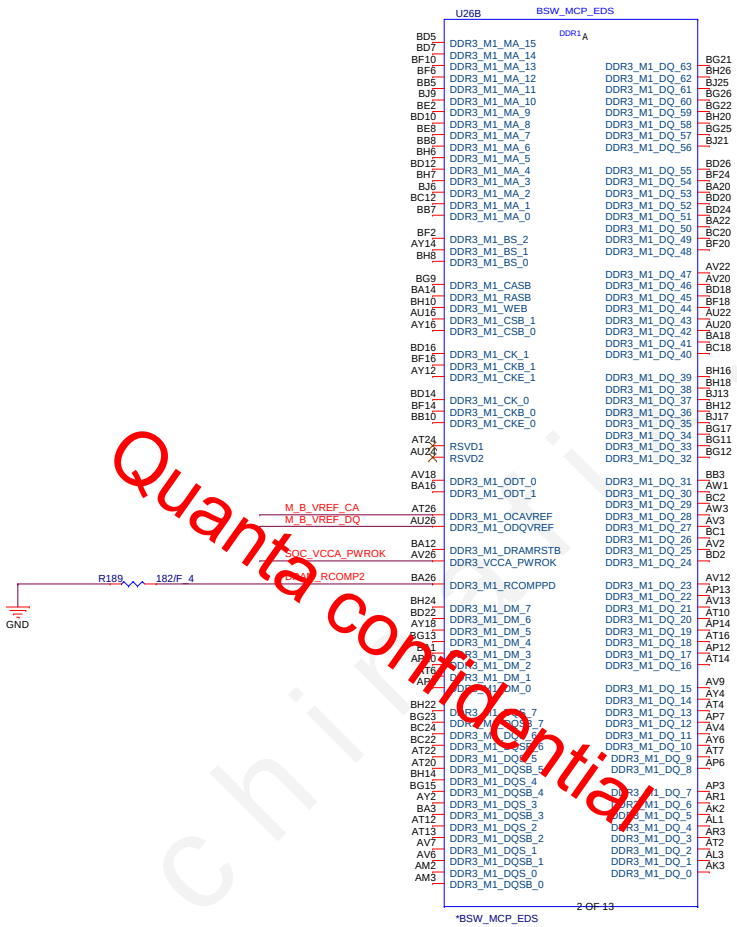




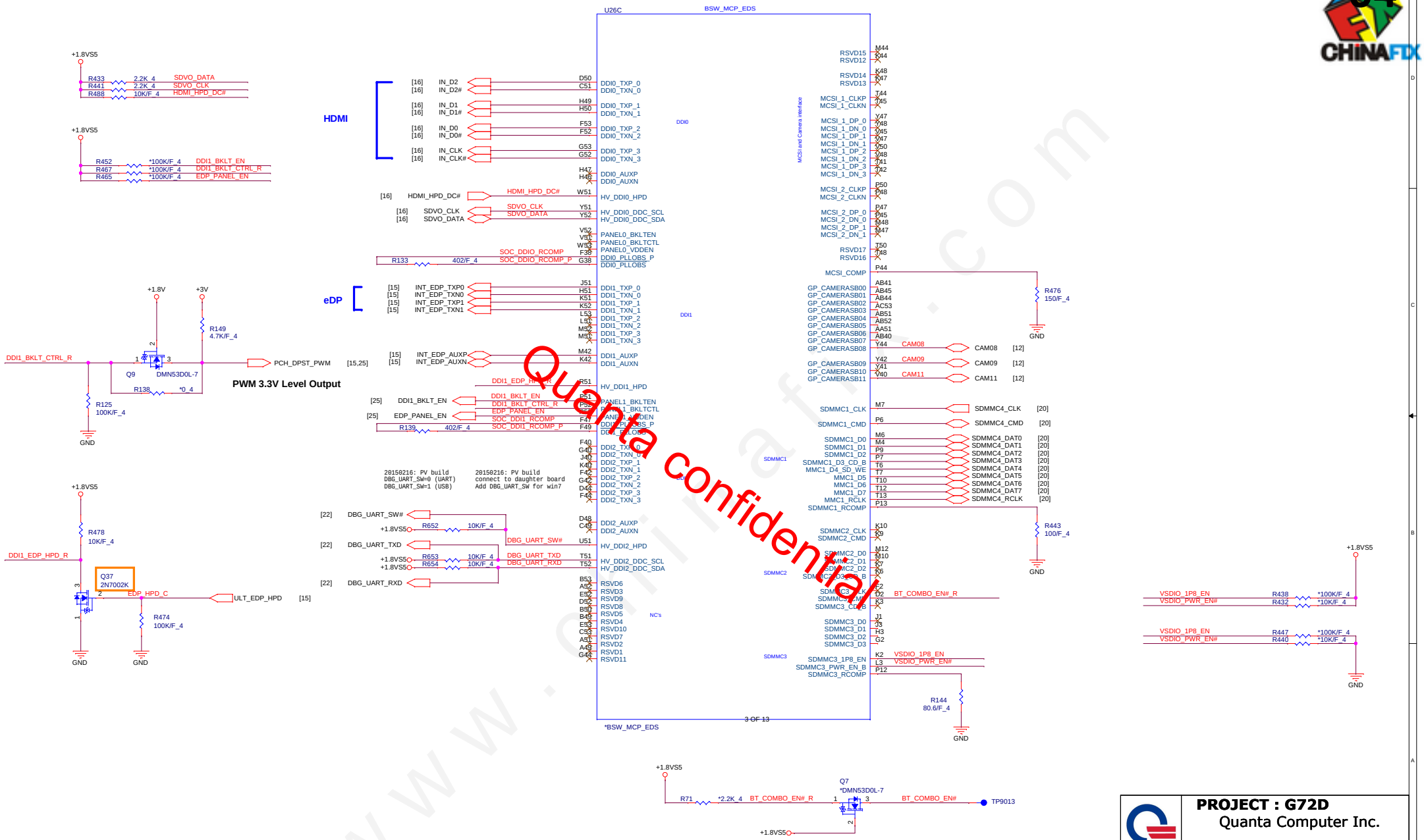
PROJECT : G72D		
Quanta Computer Inc.		
Size: Custom	Document Number: Braswell 1/11 (DDRA)	Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 2 of 35	

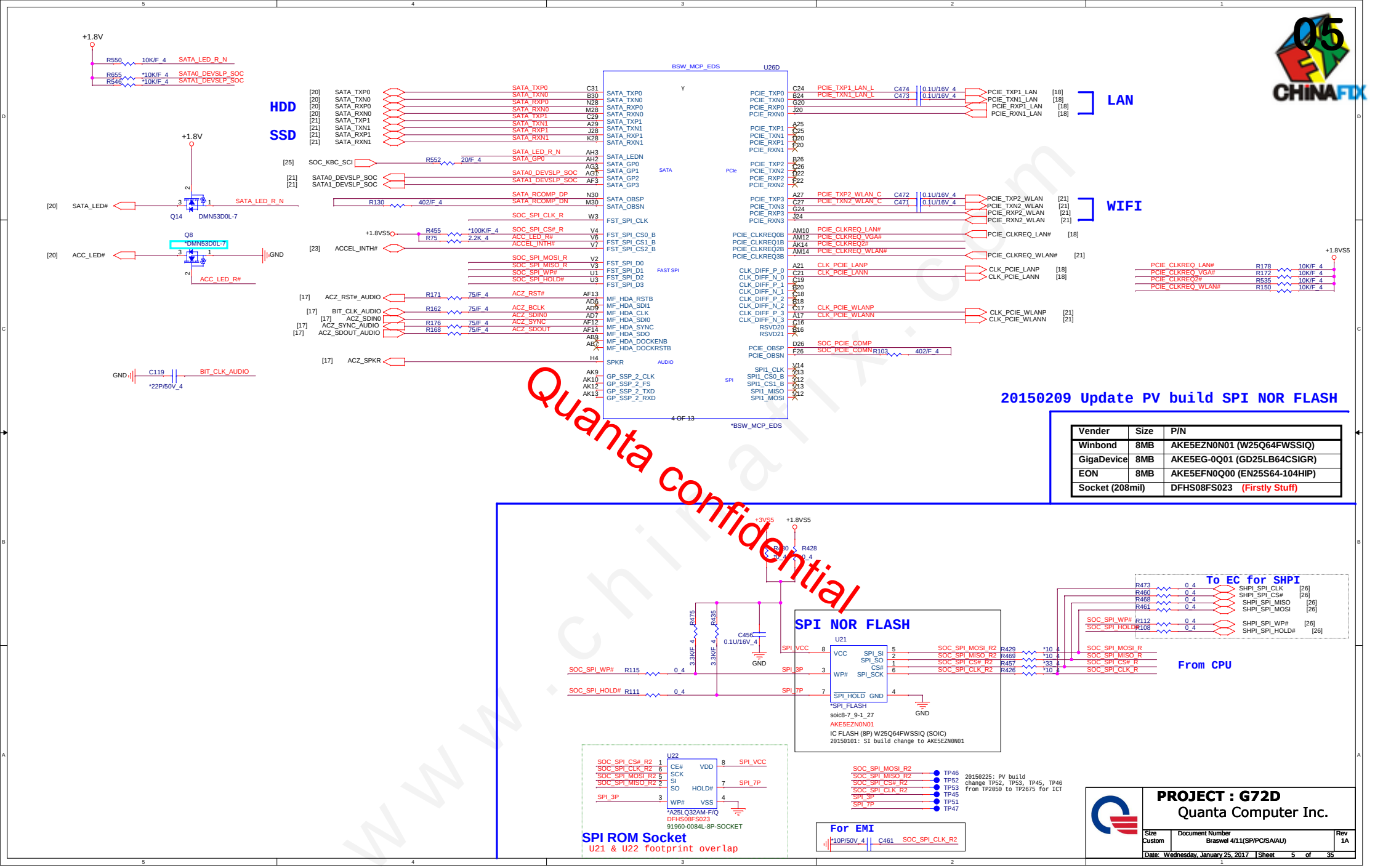






PROJECT : G72D			
Quanta Computer Inc.			
Size: Custom	Document Number: Braswell 2/11 (DDRB)		Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 3 of 35		


U26C
BSW_MCP_EDS
HDMI
eDP
PWM 3.3V Level Output
20150216: PV build
DBG_UART_SW=0 (UART)
DBG_UART_SW=1 (USB)
20150216: PV build
connect to daughter board
Add DBG_UART_SW for win7
3 OF 13
*BSW_MCP_EDS
Quanta confidential
PROJECT : G72D
Quanta Computer Inc.
Braswell 3/11 (Display)
Wednesday, January 25, 2017


HDD
SSD
LAN
WIFI
BSW_MCP_EDS
U26D
*BSW_MCP_EDS
4 OF 13
Q14 DMN53D0L-7
Q8 *DMN53D0L-7
20150209 Update PV build SPI NOR FLASH
Vender | Size | P/N
Winbond | 8MB | AKE5EZN0N01 (W25Q64FWSSIQ)
GigaDevice | 8MB | AKE5EG-0Q01 (GD25LB64CSIGR)
EON | 8MB | AKE5EFN0Q00 (EN25S64-104HIP)
Socket (208mil) | DFHS08FS023 (Firstly Stuff)
SPI NOR FLASH
U21
*SPI_FLASH
soic8-7_9-1_27
AKE5EZN0N01
IC FLASH (8P) W25Q64FWSSIQ (SOIC)
20150101: SI build change to AKE5EZN0N01
To EC for SHPI
From CPU
SPI ROM Socket
U21 & U22 footprint overlap
U22
*A25LQ32AM-F/Q
DFHS08FS023
91960-0084L-8P-SOCKET
For EMI
20150225: PV build change TP52, TP53, TP45, TP46 from TP2050 to TP2675 for ICT
PROJECT : G72D
Quanta Computer Inc.
Size Custom
Document Number Braswel 4/11(SP/PC/SA/AU)
Rev 1A
Date: Wednesday, January 25, 2017
Sheet 5 of 35
Quanta confidential
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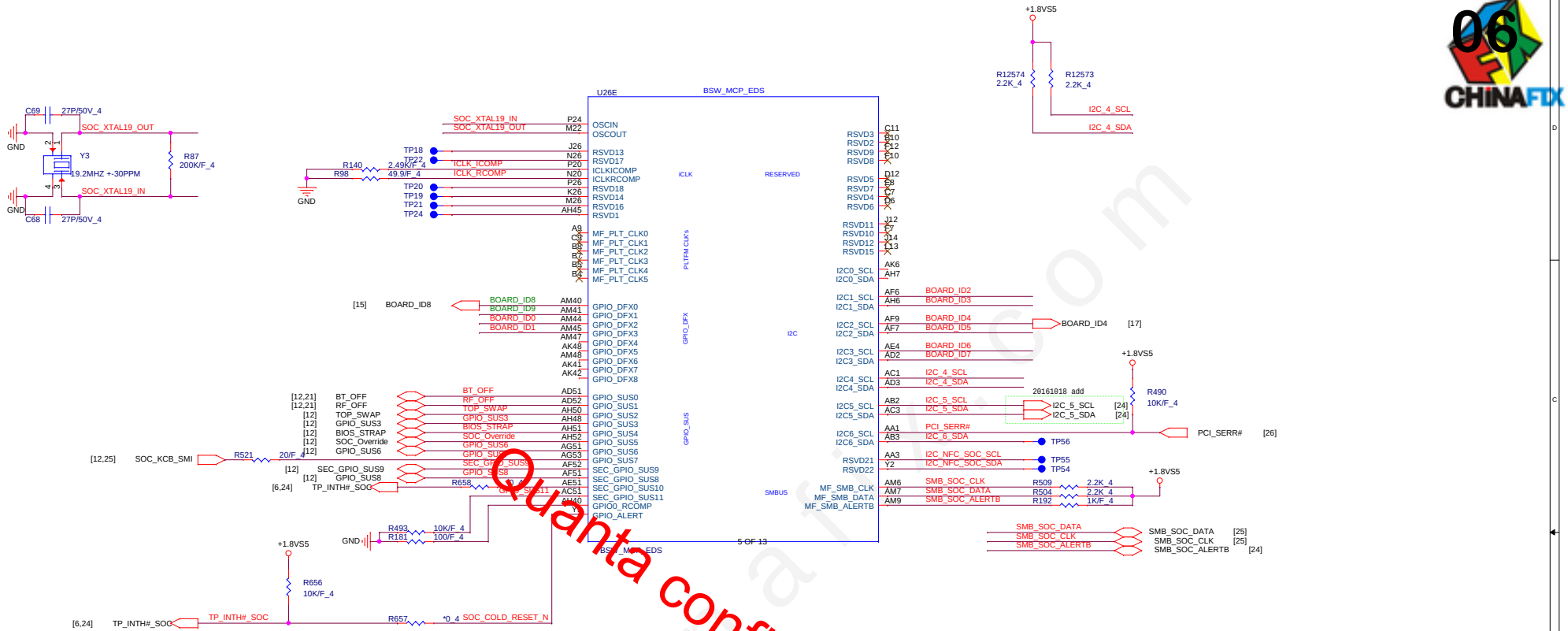
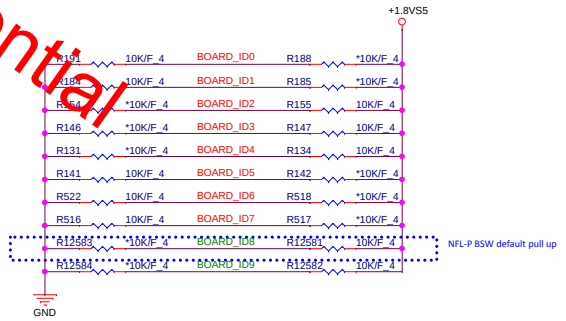
05
CHINAFIX



Board ID	BIOS Strap Description
BOARD_ID0	(Reserve)
BOARD_ID1	SATA/EMMC SELECT
BOARD_ID2	
BOARD_ID3	1 = TPM 0 = Non-TPM
BOARD_ID4	SPK_ID (Reserve)
BOARD_ID5	Memory Vendor for MD USE
BOARD_ID6	
BOARD_ID7	
BOARD_ID8	0 = ELAN Touchpad 1 = Synaptics Touchpad
BOARD_ID9	1 = SO-DIMM 0 = Memory Down

BOARD_ID7	BOARD_ID6	BOARD_ID5	Vender	Quanta PN	Description
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			

BOARD_ID1	BOARD_ID2	
0	0	EMMC only
0	1	EMMC +SATA
1	0	SATA only
1	1	(Reserve)



PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Braswel 5/11(I2C/GPIO/CLK	1A
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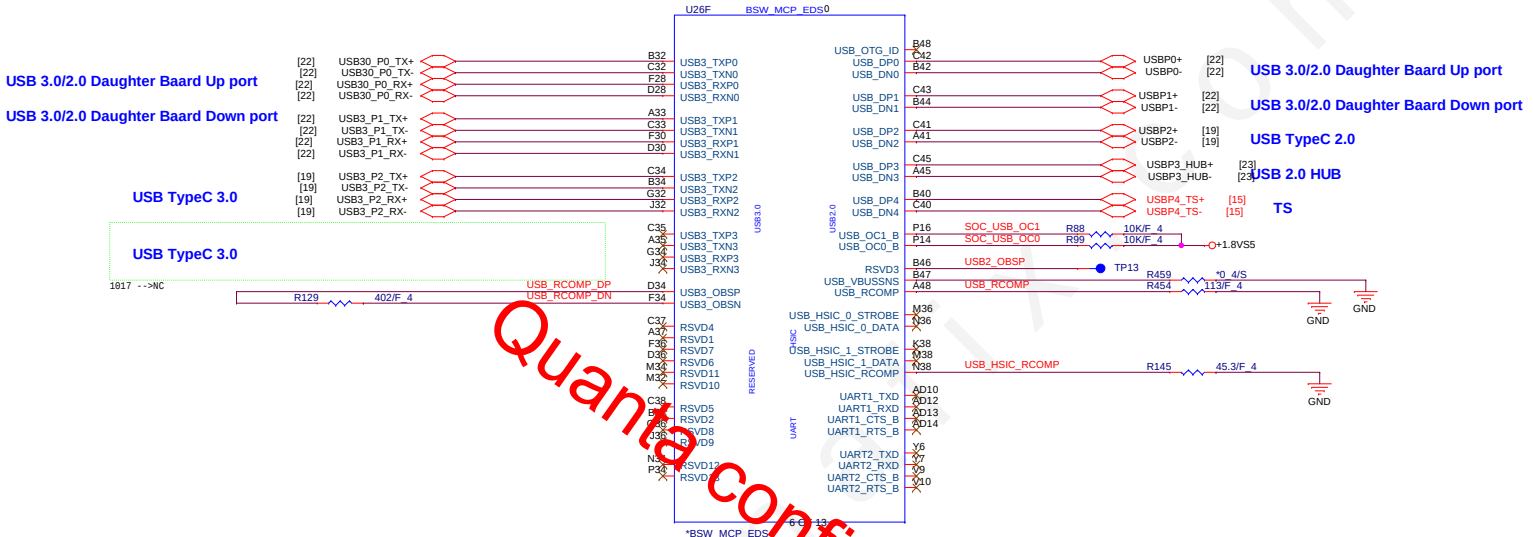

PROJECT : G72D		
Quanta Computer Inc.		
Size: Custom	Document Number: Braswel 6/11 (USB/URAT)	Rev: 1A
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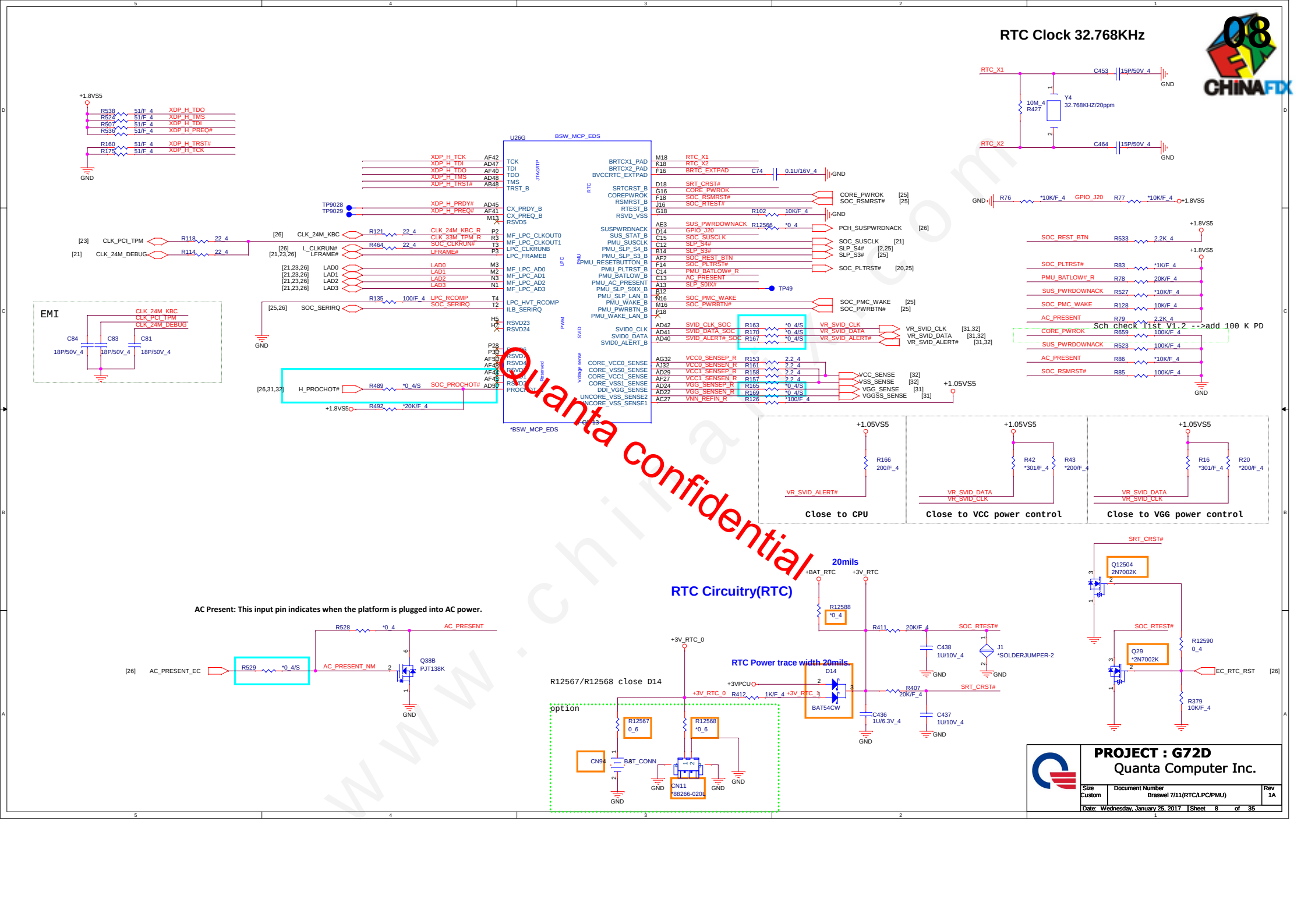
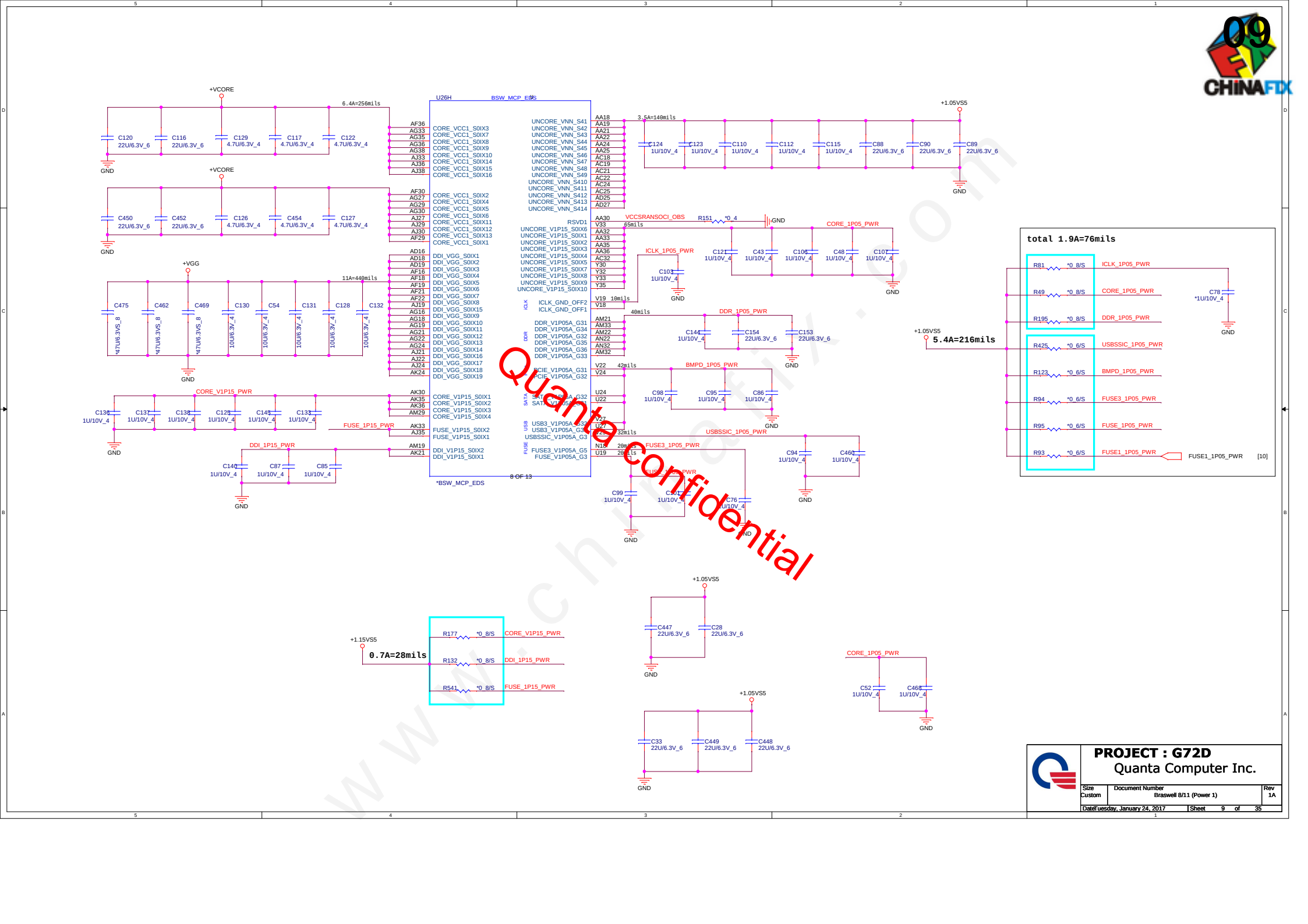
RTC Clock 32.768KHz




U26H
BSW_MCP_EDS
*BSW_MCP_EDS
8 OF 13
+VCORE
+VGG
GND
6.4A=256mils
11A=440mils
CORE_VCC1_S0IX3
CORE_VCC1_S0IX7
CORE_VCC1_S0IX8
CORE_VCC1_S0IX9
CORE_VCC1_S0IX10
CORE_VCC1_S0IX14
CORE_VCC1_S0IX15
CORE_VCC1_S0IX16
CORE_VCC1_S0IX2
CORE_VCC1_S0IX4
CORE_VCC1_S0IX5
CORE_VCC1_S0IX6
CORE_VCC1_S0IX11
CORE_VCC1_S0IX12
CORE_VCC1_S0IX13
CORE_VCC1_S0IX1
DDI_VGG_S0IX1
DDI_VGG_S0IX2
DDI_VGG_S0IX3
DDI_VGG_S0IX4
DDI_VGG_S0IX5
DDI_VGG_S0IX6
DDI_VGG_S0IX7
DDI_VGG_S0IX8
DDI_VGG_S0IX15
DDI_VGG_S0IX9
DDI_VGG_S0IX10
DDI_VGG_S0IX11
DDI_VGG_S0IX12
DDI_VGG_S0IX13
DDI_VGG_S0IX14
DDI_VGG_S0IX16
DDI_VGG_S0IX17
DDI_VGG_S0IX18
DDI_VGG_S0IX19
CORE_V1P15_PWR
CORE_V1P15_S0IX1
CORE_V1P15_S0IX2
CORE_V1P15_S0IX3
CORE_V1P15_S0IX4
FUSE_1P15_PWR
FUSE_V1P15_S0IX2
FUSE_V1P15_S0IX1
DDI_1P15_PWR
DDI_V1P15_S0IX2
DDI_V1P15_S0IX1
UNCORE_VNN_S41
UNCORE_VNN_S42
UNCORE_VNN_S43
UNCORE_VNN_S44
UNCORE_VNN_S45
UNCORE_VNN_S46
UNCORE_VNN_S47
UNCORE_VNN_S48
UNCORE_VNN_S49
UNCORE_VNN_S410
UNCORE_VNN_S411
UNCORE_VNN_S412
UNCORE_VNN_S413
UNCORE_VNN_S414
3.5A=140mils
+1.05VS5
VCCSRANSOCI_OBS
R151
*0_4
RSVD1
UNCORE_V1P15_S0IX6
UNCORE_V1P15_S0IX1
UNCORE_V1P15_S0IX2
UNCORE_V1P15_S0IX3
UNCORE_V1P15_S0IX4
UNCORE_V1P15_S0IX5
UNCORE_V1P15_S0IX7
UNCORE_V1P15_S0IX8
UNCORE_V1P15_S0IX9
UNCORE_V1P15_S0IX10
CORE_1P05_PWR
ICLK_1P05_PWR
ICLK_GND_OFF2
ICLK_GND_OFF1
DDR_1P05_PWR
DDR_V1P05A_G31
DDR_V1P05A_G34
DDR_V1P05A_G32
DDR_V1P05A_G35
DDR_V1P05A_G36
DDR_V1P05A_G33
BMPD_1P05_PWR
USBSSIC_1P05_PWR
USB3_V1P05A_G31
USB3_V1P05A_G33
USBSSIC_V1P05A_G3
FUSE3_1P05_PWR
FUSE3_V1P05A_G5
FUSE_V1P05A_G3
FUSE_1P05_PWR
total 1.9A=76mils
5.4A=216mils
R81
R49
R195
R425
R123
R94
R95
R93
*0_8/S
*0_6/S
ICLK_1P05_PWR
CORE_1P05_PWR
DDR_1P05_PWR
USBSSIC_1P05_PWR
BMPD_1P05_PWR
FUSE3_1P05_PWR
FUSE_1P05_PWR
FUSE1_1P05_PWR
[10]
+1.15VS5
0.7A=28mils
R177
R132
R541
CORE_V1P15_PWR
DDI_1P15_PWR
FUSE_1P15_PWR
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PROJECT : G72D
Quanta Computer Inc.
Size Custom
Document Number Braswell 8/11 (Power 1)
Rev 1A
Date Tuesday, January 24, 2017
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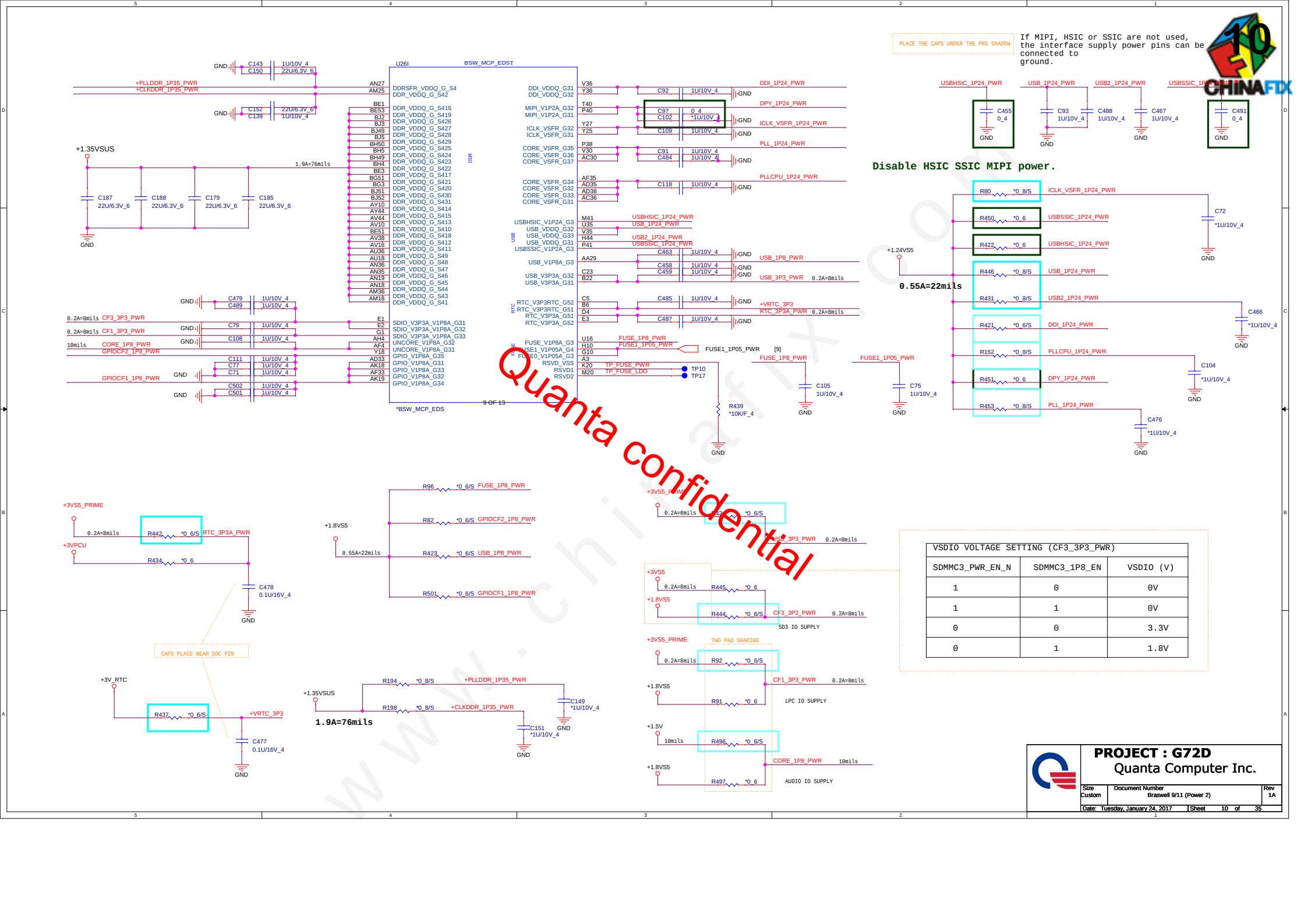

VSDIO VOLTAGE SETTING (CF3_3P3_PWR)		
SDMMC3_PWR_EN_N	SDMMC3_1P8_EN	VSDIO (V)
1	0	0V
1	1	0V
0	0	3.3V
0	1	1.8V

PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Braswell 9/11 (Power 2)	1A
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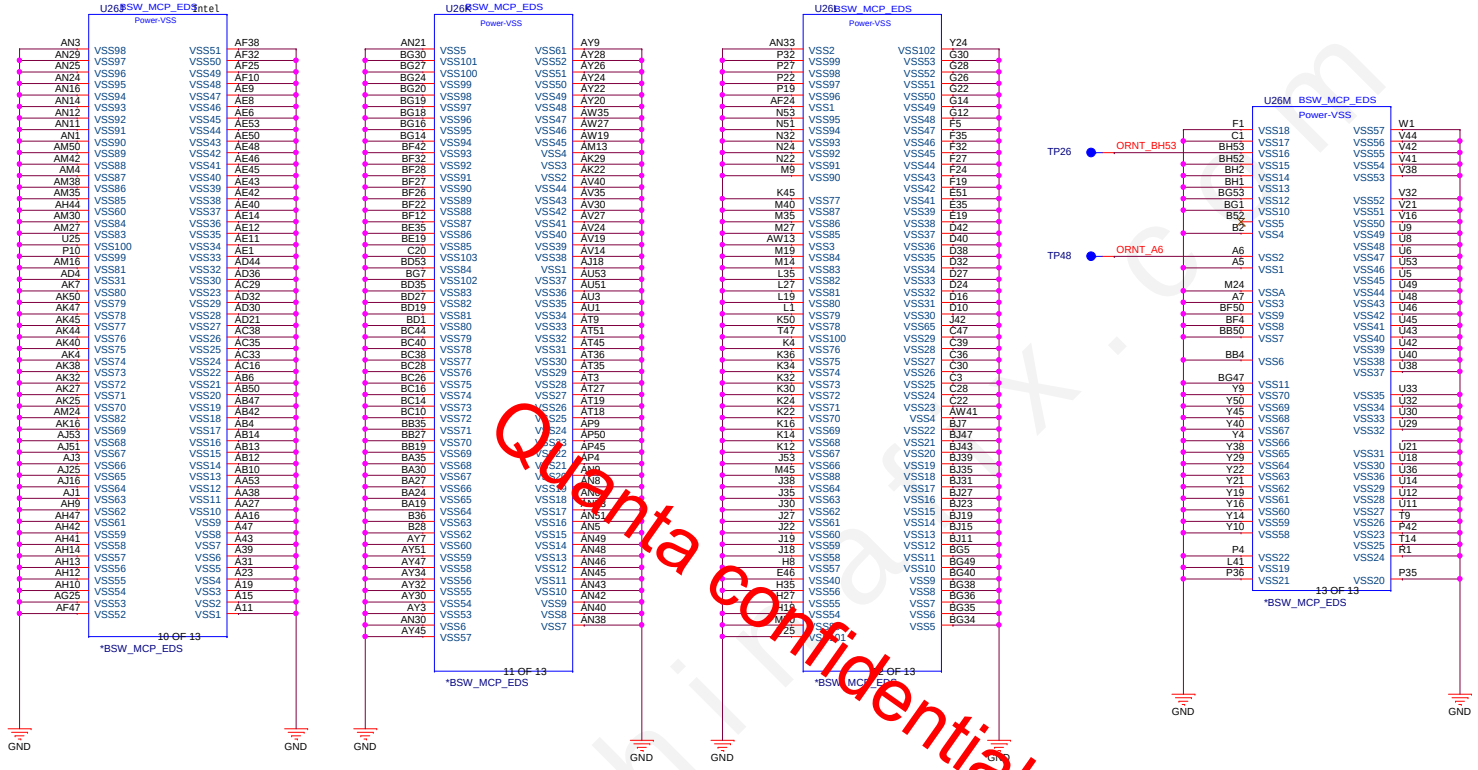
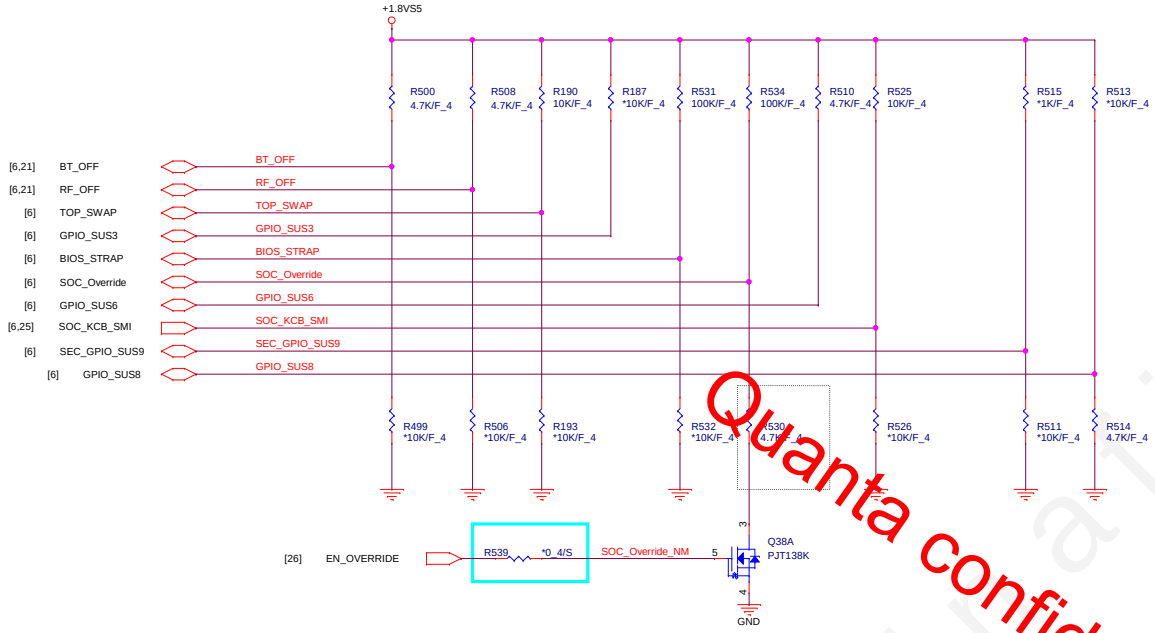
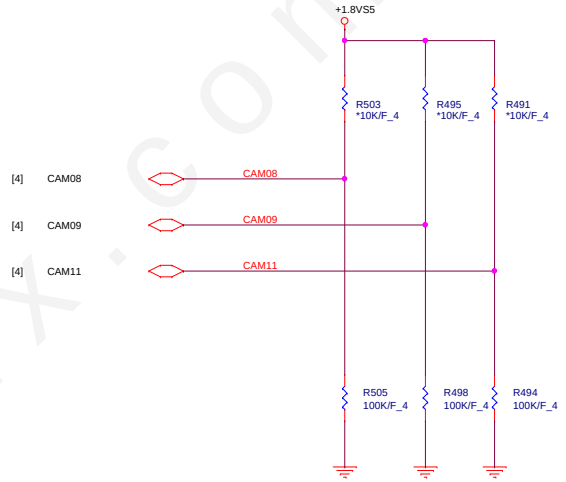


PROJECT : G72D		
Quanta Computer Inc.		
Size: Custom	Document Number: Braswell 10/11 (GND)	Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 11 of 35	





REQUIRED STRAPS

	GPIO_SUS0	GPIO_SUS1	TOP_SWAP	GPIO_SUS3	BIOS_STRAP	SOC_Override	GPIO_SUS6	SOC_KCB_SMI	GPIO_SUS8
PULL HIGH	DDI0 detected DEFAULT	DDI1 detected DEFAULT	Normal Operation DEFAULT	Reserve 10 KΩ PU DEFAULT	SPI DEFAULT	Normal Operation DEFAULT 20150209 PV change	10 KΩ PU to 1.8V DEFAULT	Reserve 10 KΩ PU DEFAULT	Supply is 1.35V
PULL LOW	DDI0 not detected	DDI1 not detected	Change Boot Loader address		LPC	Override			Supply is 1.25V DEFAULT

	CAM08	CAM09	CAM11
PULL HIGH	ICLK Xtal OSC Bypass	CCU SUS RO Bypass	RTC OSC Bypass
PULL LOW	ICLK Xtal OSC No Bypass DEFAULT	CCU SUS RO No Bypass DEFAULT	RTC OSC No Bypass DEFAULT

PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Braswell 11/11 (Strap)	1A

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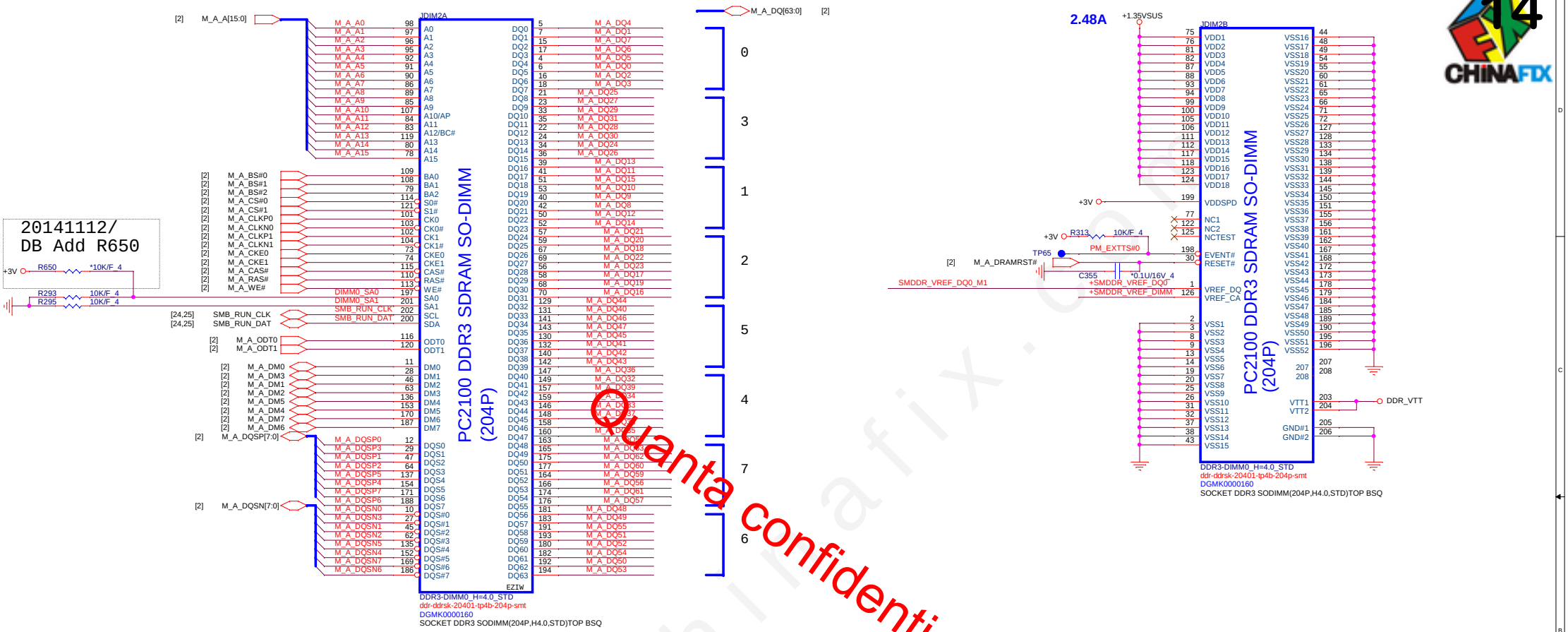
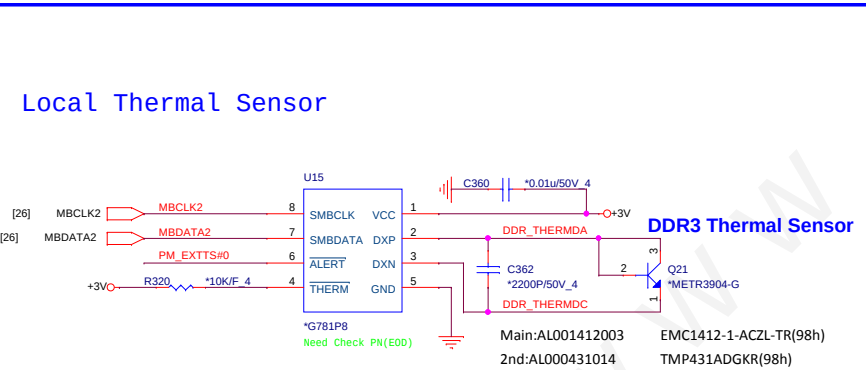
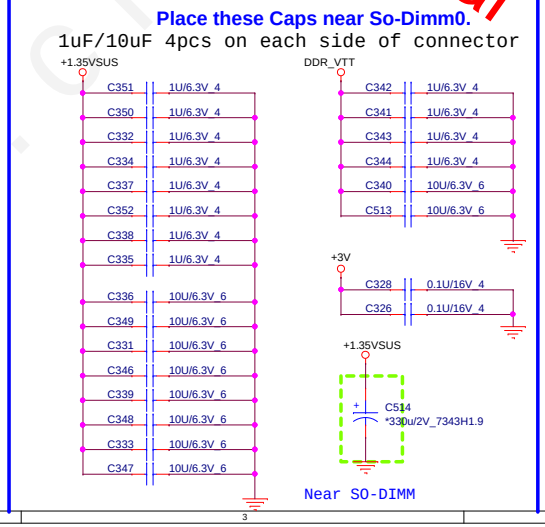
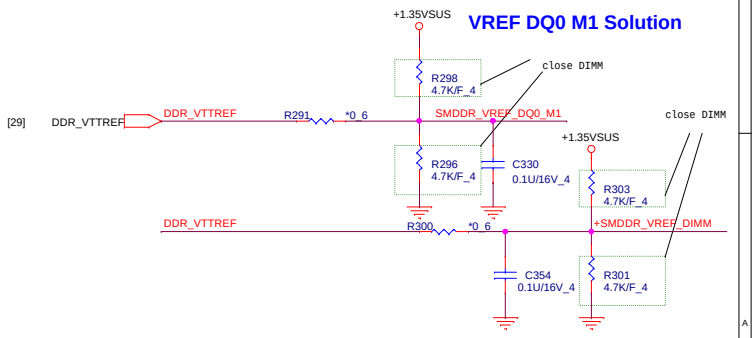
C	PROJECT : G72D Quanta Computer Inc.	
	Size:	Document Number: BSW XDP/APS — Rev: 1A
	Date: Tuesday, January 24, 2017	Sheet 13 of 35









PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	DDR3 DIMM0-STD(4.0H)	1A

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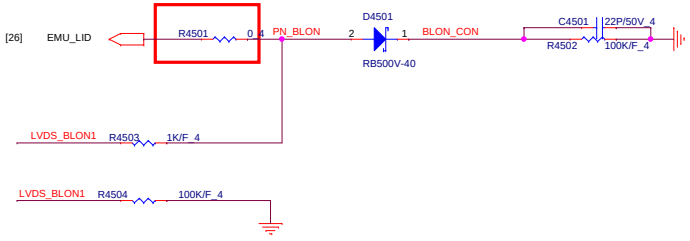
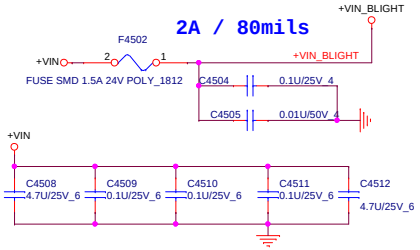
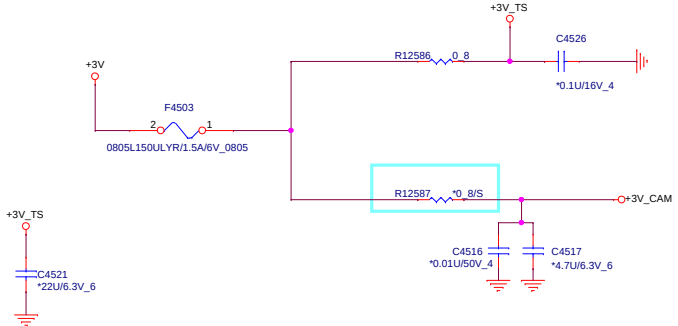
LID Switch

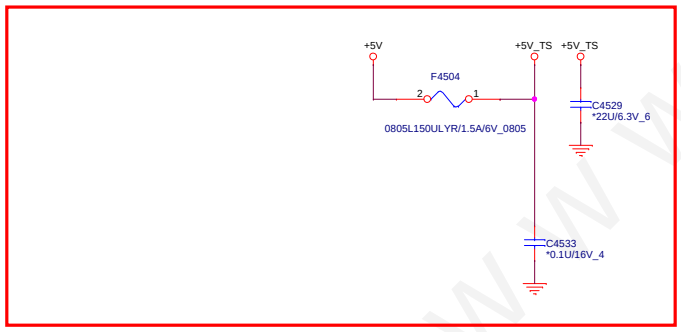
R4501 can't change to shortpad



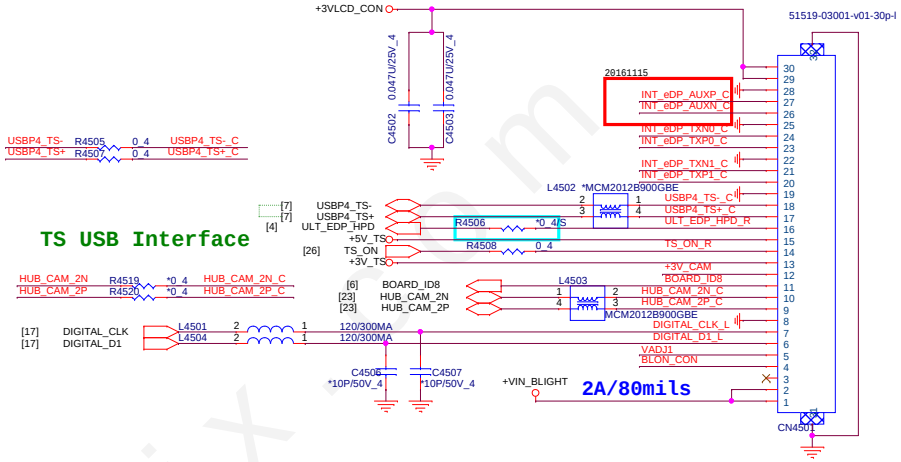


Touch screen



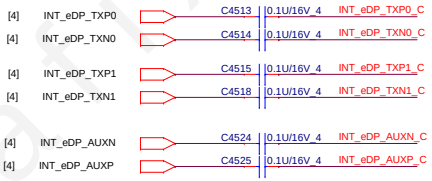


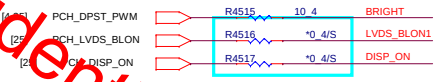
eDP Conn.

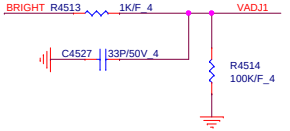


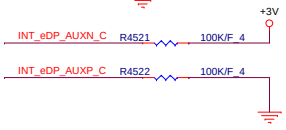
TS USB Interface

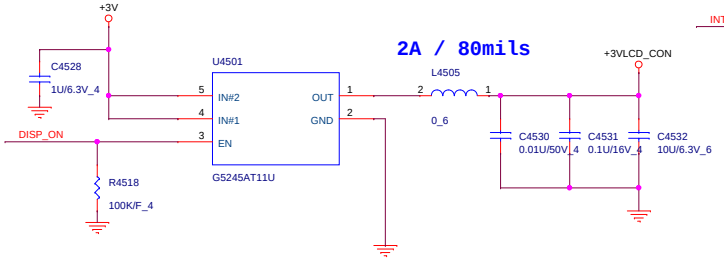
20161018 FP/PN check ok









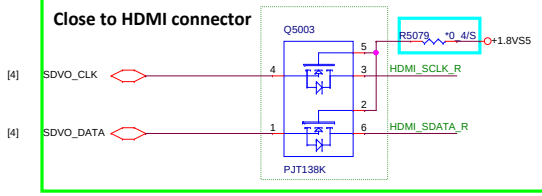


PROJECT : G72D Quanta Computer Inc.		
Size: Custom	Document Number: eDP CONN/LID/CAM/D-MIC/TS	Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 15 of 35	

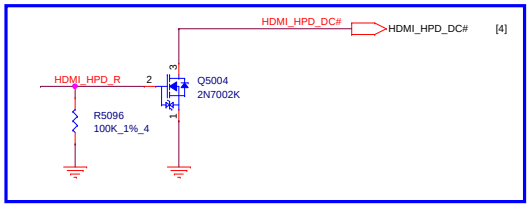
20161018 FP/PN check ok

HDMI SMBus isolation

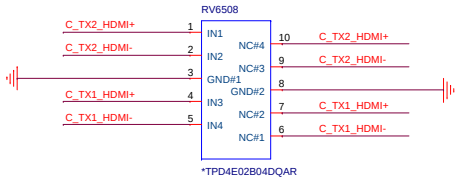
Close to HDMI connector



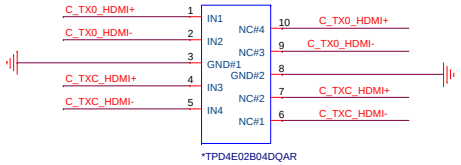
For From CPU use

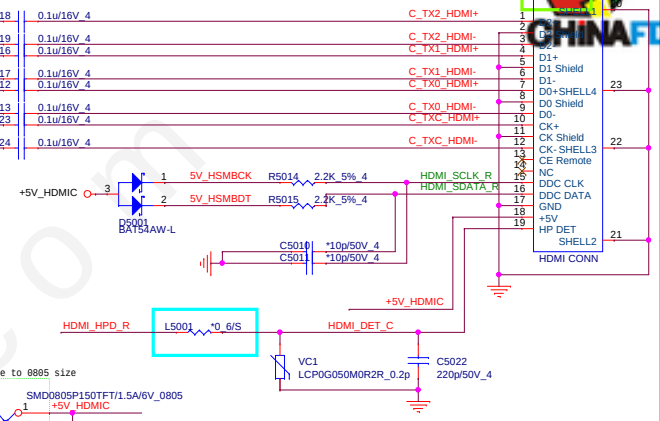


HDMI ESD

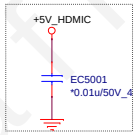


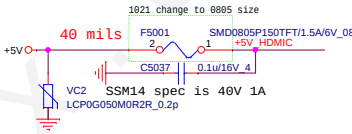
HDMI ESD



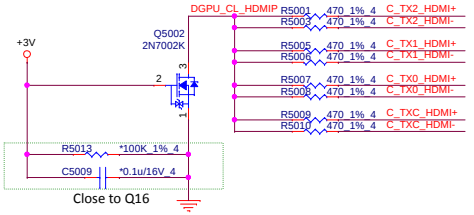


Intel EMI Solution



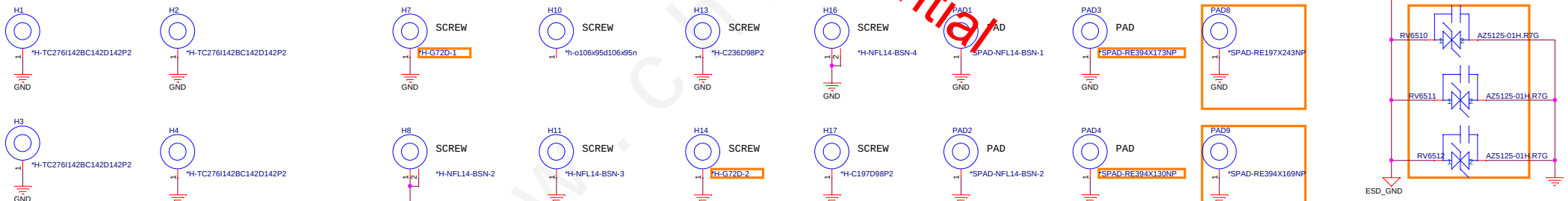


Close to HDMI connector

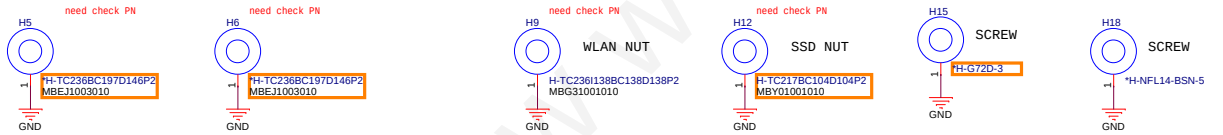


HOLE

CPU BKT



VGA NUT



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Quanta Computer Inc.

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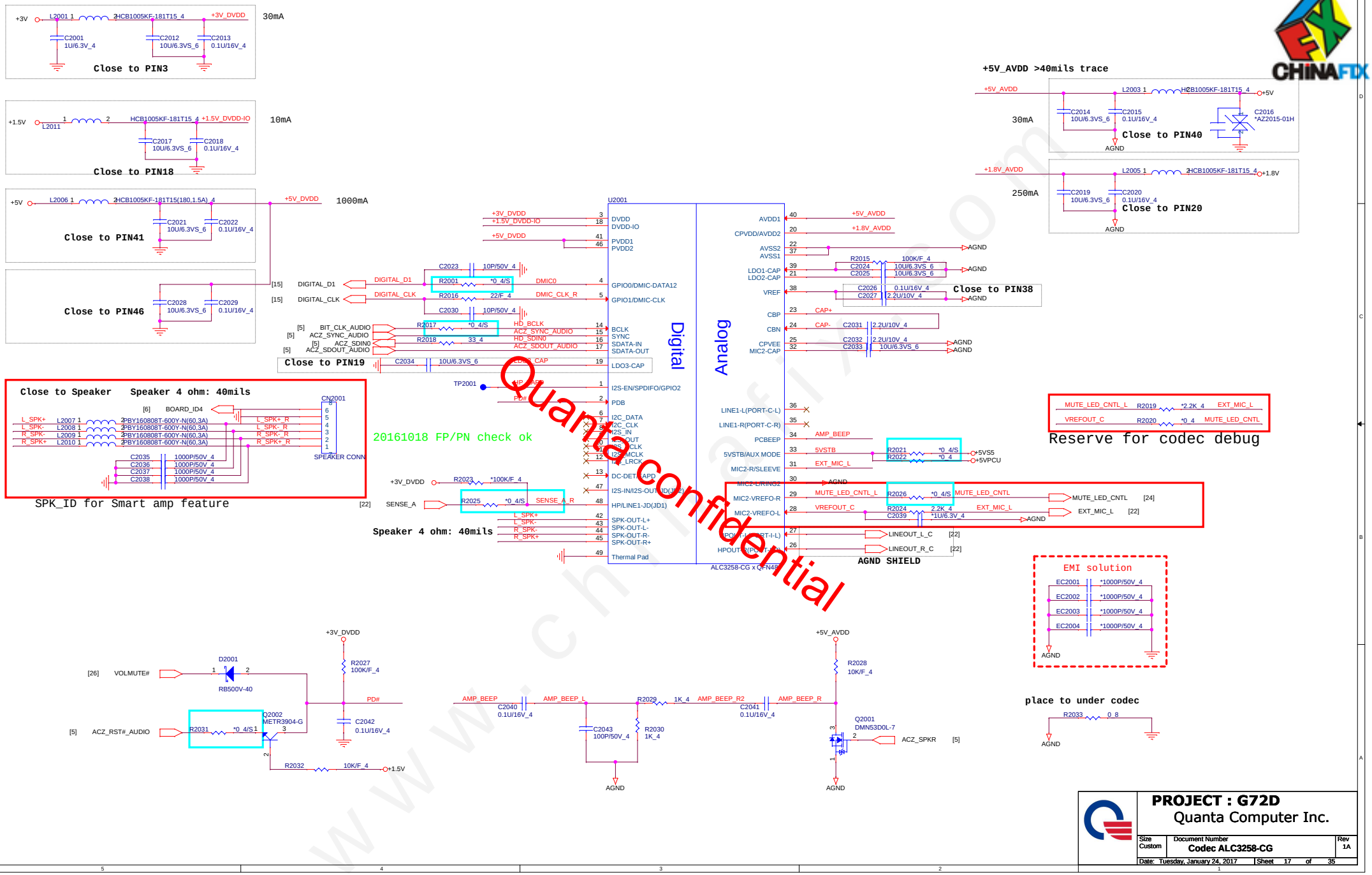
Close to PIN3
30mA
Close to PIN18
10mA
Close to PIN41
Close to PIN46
1000mA
+5V_AVDD >40mils trace
Close to PIN40
Close to PIN20
250mA
Close to PIN38
Close to PIN19
Digital
Analog
ALC3258-CG x QFN48
Close to Speaker
Speaker 4 ohm: 40mils
SPK_ID for Smart amp feature
20161018 FP/PN check ok
Reserve for codec debug
AGND SHIELD
EMI solution
place to under codec
Quanta confidential
PROJECT : G72D
Quanta Computer Inc.
Codec ALC3258-CG
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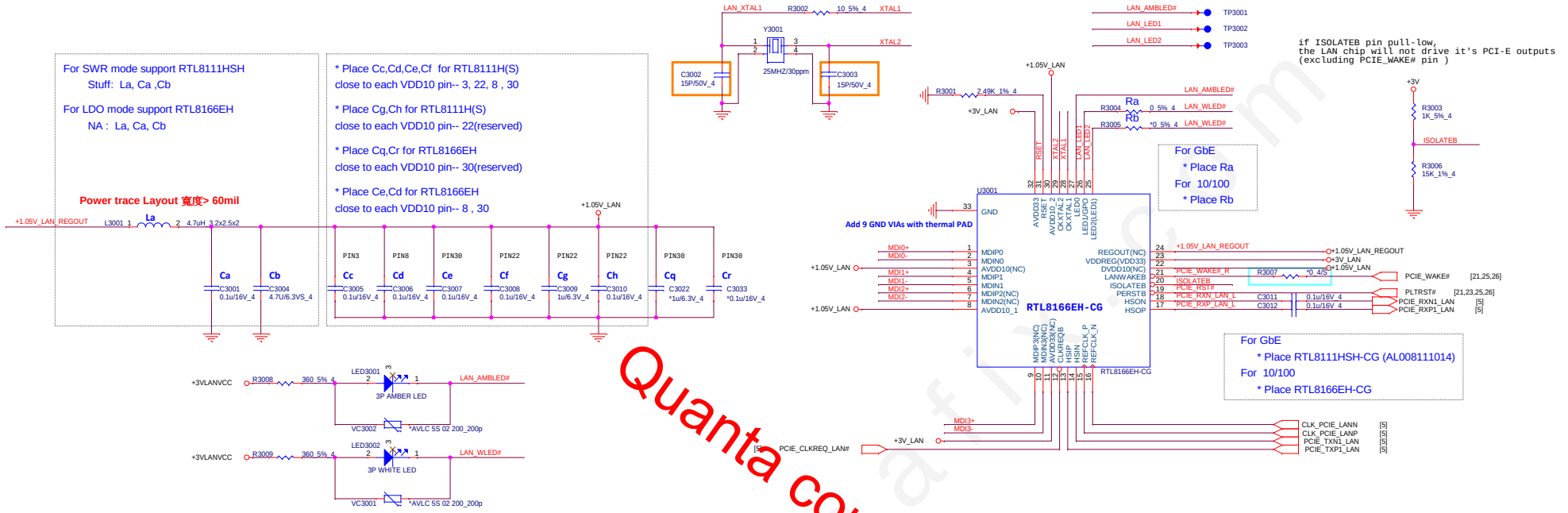
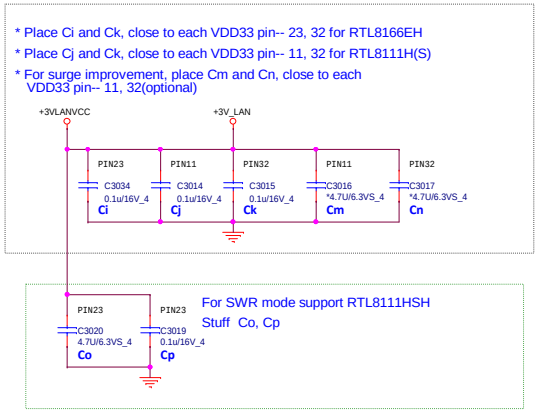
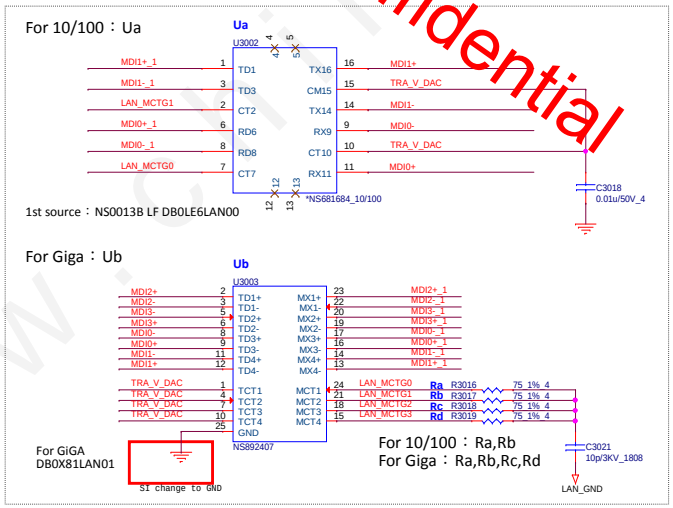
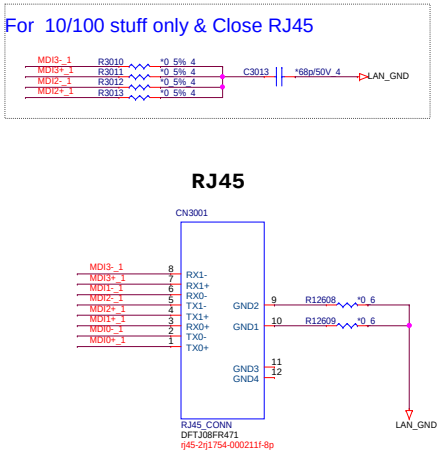
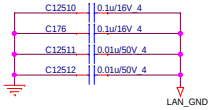
LAN RTL8166EH/RTL8111HSH











PROJECT : G72D Quanta Computer Inc.			
Size C	Document Number LAN RTL8166EH/RTL8111HSH		Rev 1A
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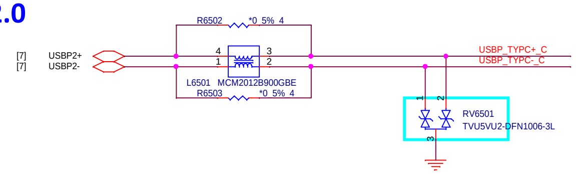
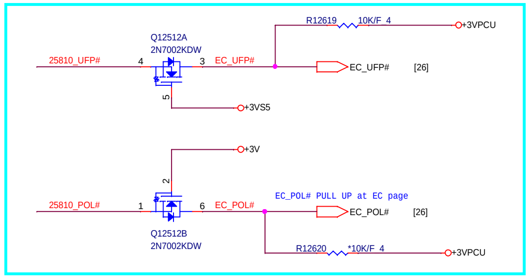
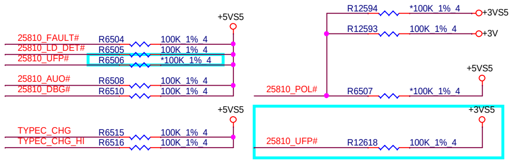
USB TYPE-C - TPS25810

USB2.0



PV add To judge the usb typeC port for factory





CHG	CHG_HI	CC Capability Broadcast	Current Limit	Load Detect Threshold
0	0	STD	1.67 A	NA
0	1	STD	1.67 A	NA
1	0	1.5 A	1.67 A	NA
1	1	3.0 A	3.34 A	1.77 A

USB Type-C re driver

to Type C SW

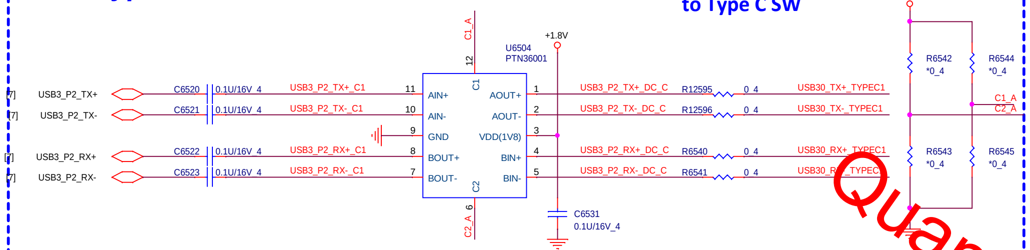


Table 4. C1 pin controls long/medium/short traces

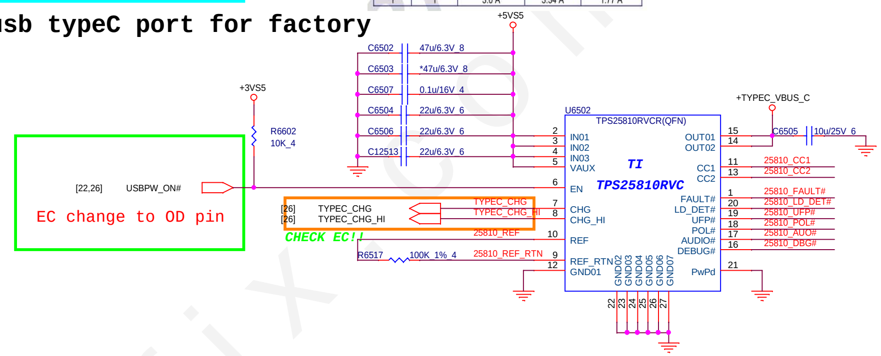
State	Channel type	Pin C1 state	Channel B	Channel A	
			EQ[1]	DE[2]	OS[3]
H	Long	H	9 dB	–5.3 dB	1.1 V
high-Z	Medium	high-Z	6 dB	–3.1 dB	1.0 V
L	Short	L	3 dB	0 dB	0.9 V

Table 5. C2 pin controls long/medium/short traces

State	Channel type	Pin C2 state	Channel A	Channel B	
			EQ[1]	DE[2]	OS[3]
H	Long	H	9 dB	–5.3 dB	1.1 V
high-Z	Medium	high-Z	6 dB	–3.1 dB	1.0 V
L	Short	L	3 dB	0 dB	0.9 V

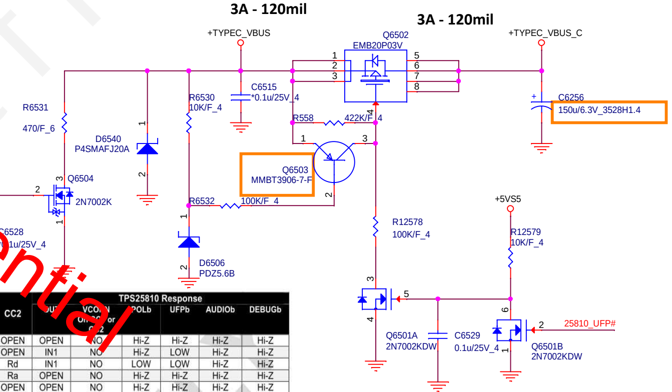
EC change to OD pin

CHECK EC!!



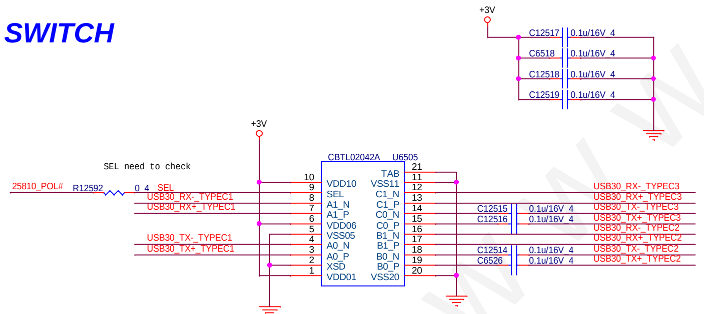
3A - 120mil

3A - 120mil



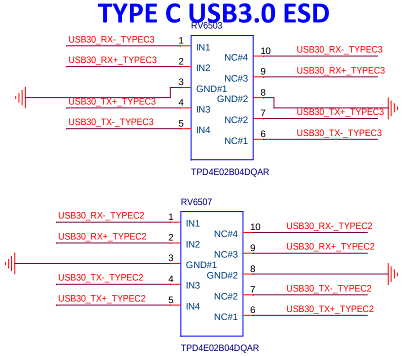
TPS25810 Port	CC1	CC2	TPS25810 Response					
			OUT	VCONN On CC1 or CC2	POLb	UFPb	AUDIOb	DEBUGb
Nothing Attached	OPEN	OPEN	OPEN	NO	Hi-Z	Hi-Z	Hi-Z	Hi-Z
UFP Connected	Rd	OPEN	IN1	NO	Hi-Z	LOW	Hi-Z	Hi-Z
UFP Connected	OPEN	Rd	IN1	NO	LOW	LOW	Hi-Z	Hi-Z
Powered Cable/No UFP Connected	OPEN	Ra	OPEN	NO	Hi-Z	Hi-Z	Hi-Z	Hi-Z
Powered Cable/No UFP Connected	Ra	OPEN	OPEN	NO	Hi-Z	Hi-Z	Hi-Z	Hi-Z
Powered Cable/UFP Connected	Rd	Ra	IN1	CC2	Hi-Z	LOW	Hi-Z	Hi-Z
Powered Cable/UFP Connected	Ra	Rd	IN1	CC1	LOW	LOW	Hi-Z	Hi-Z
Debug Accessory Connected	Rd	Rd	OPEN	NO	Hi-Z	Hi-Z	Hi-Z	LOW
Audio Adapter Accessory Connected	Ra	Ra	OPEN	NO	Hi-Z	Hi-Z	LOW	Hi-Z

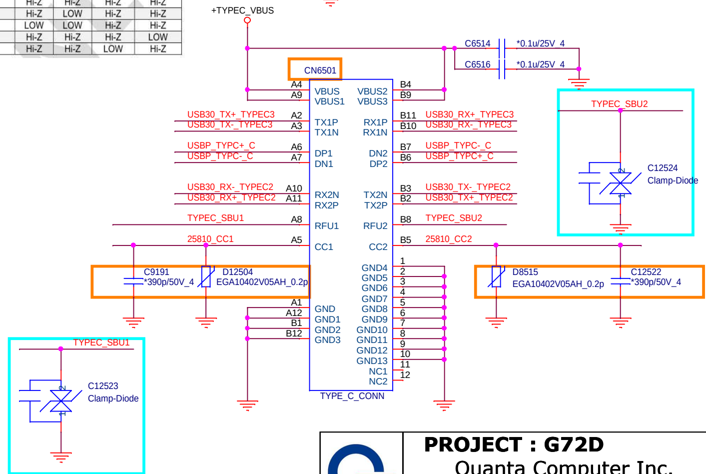
USB SWITCH



SEL = CMOS single-ended input
operation mode select
SEL = LOW: A <---> B
SEL = HIGH: A <---> C

TYPE C USB3.0 ESD





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Quanta Computer Inc.

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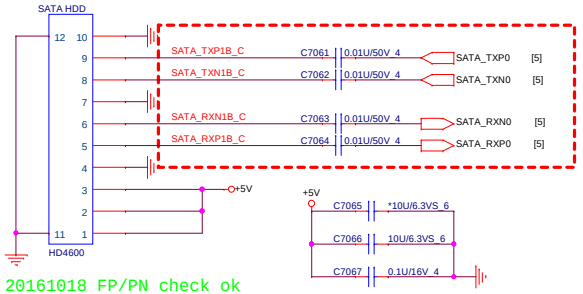
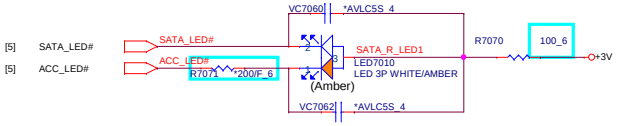
HDD

SATA HDD

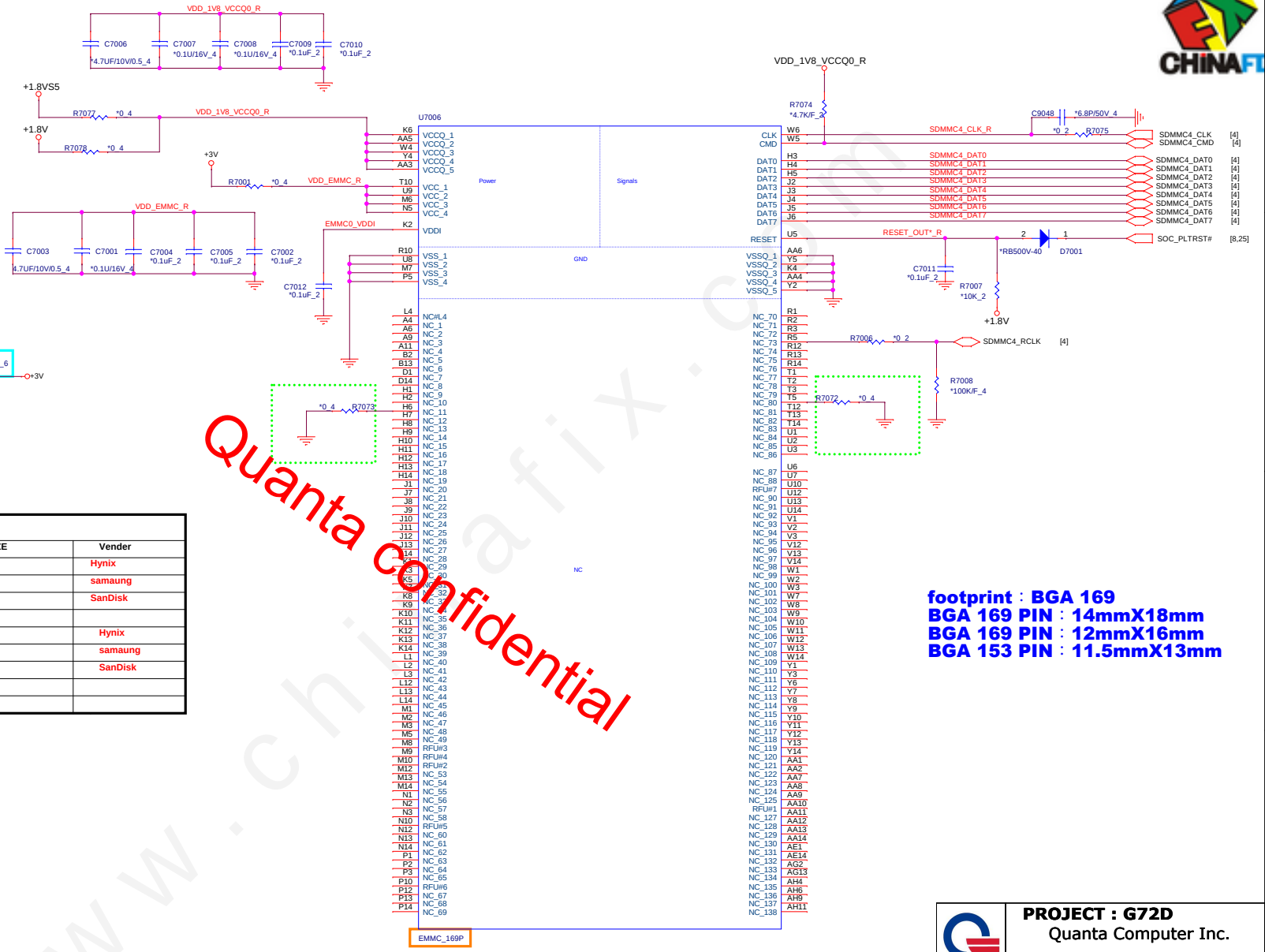


20161018 FP/PN check ok

SATA LED



eMMC



iNAND (eMMC) V4.51				
TOPBSQ	QBCON	Description	SIZE	Vender
AKE3SZ-TW01	AKE3SZ-TW02	IC FLASH(153P)H26M64103EMR(FBGA)	32G	Hynix
AKE5SZ0T511	AKE5SZ0T512	IC FLASH(153)KLMBG4GEND-B031(FBGA)	32G	samaung
AKE3SFUT000	AKE3SFUT001	IC FLASH(153P)SDIN9DW4-32G(FBGA)	32G	SanDisk
AKE3TG-TW01	AKE3TG-TW02	IC FLASH(153P)H26M78103CCR(FBGA)	64G	Hynix
AKE3TZPT521	AKE3TZPT520	IC FLASH(153)KLMCG8GEND-B031(FBGA)	64G	samaung
AKE3TFUT101	AKE3TFUT102	IC FLASH(153P)SDIN9DW4-64G(FBGA)	64G	SanDisk

footprint : BGA 169
BGA 169 PIN : 14mmX18mm
BGA 169 PIN : 12mmX16mm
BGA 153 PIN : 11.5mmX13mm

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Quanta Computer Inc.

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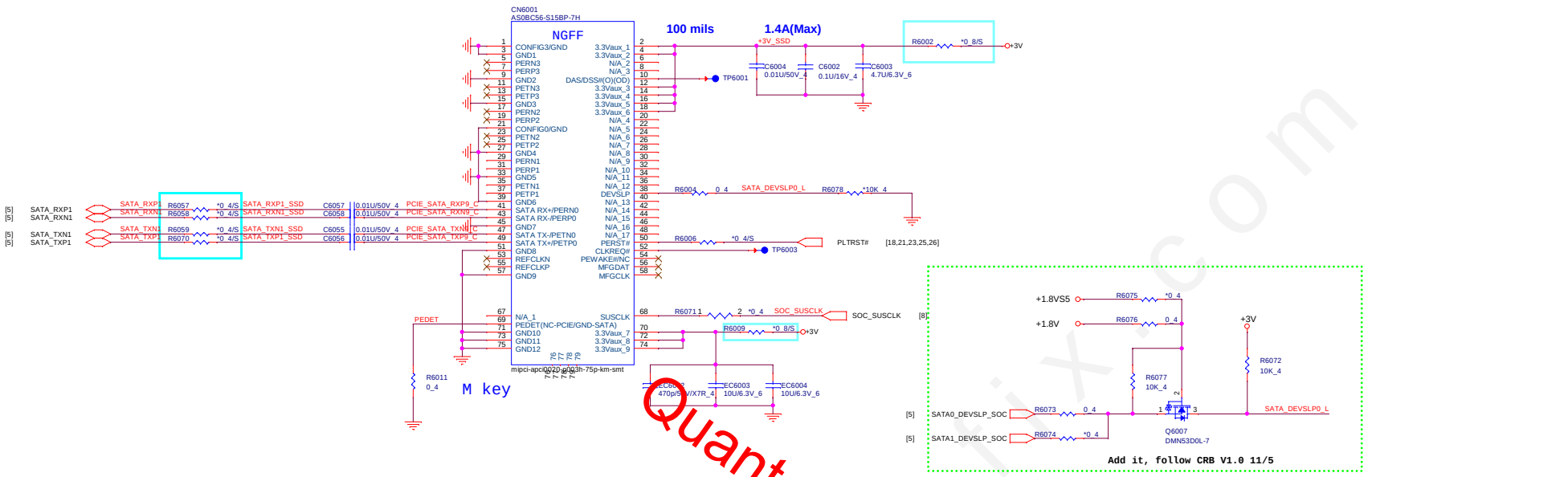
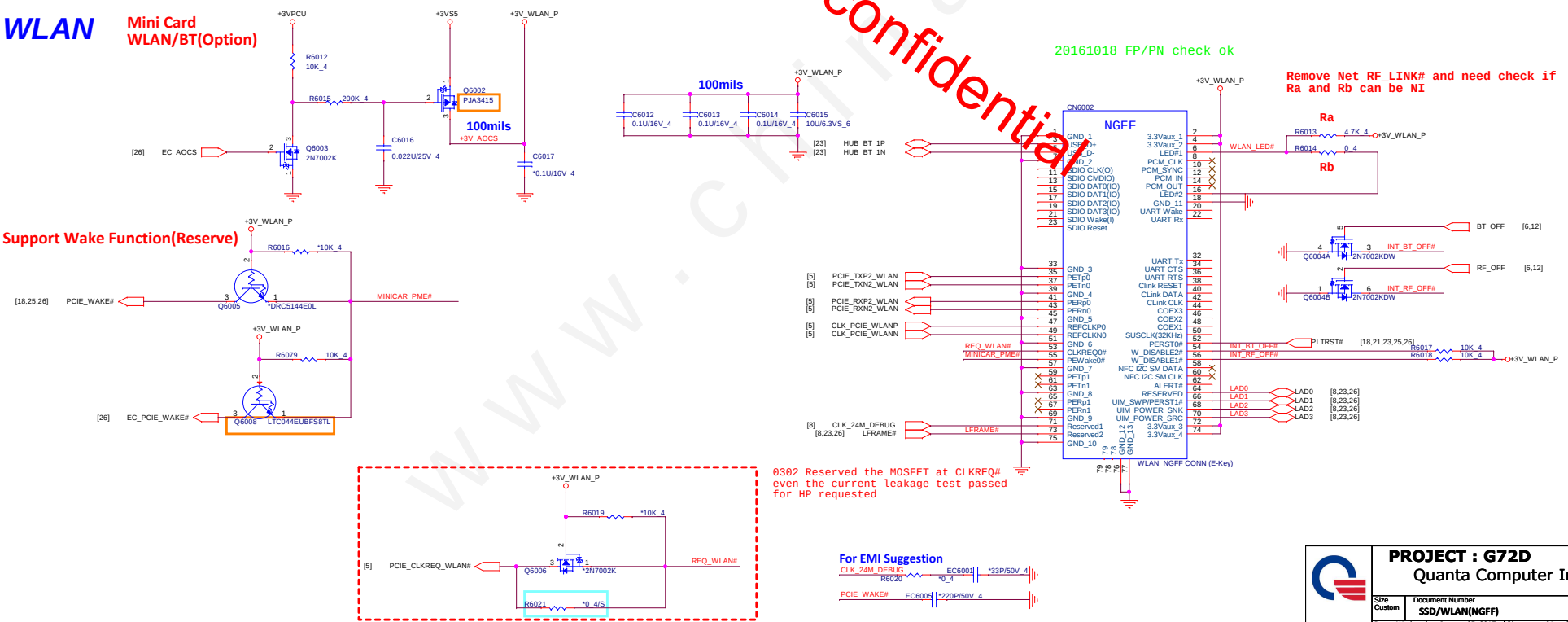
SATA SSD

20161018 FP/PN check ok



WLAN

Mini Card
WLAN/BT(Option)



PROJECT : G72D
Quanta Computer Inc.

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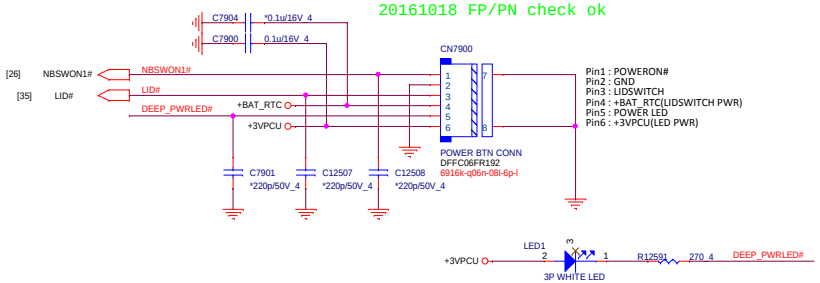
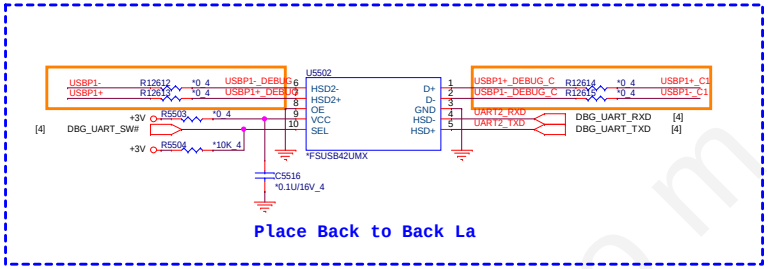
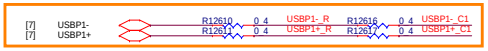
Power Botton Connector

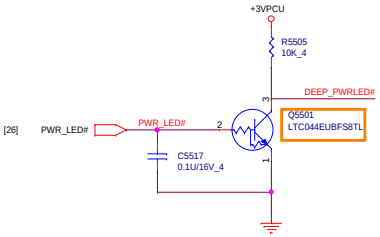
20161018 FP/PN check ok



UART for Win7 WHQL DEBUG





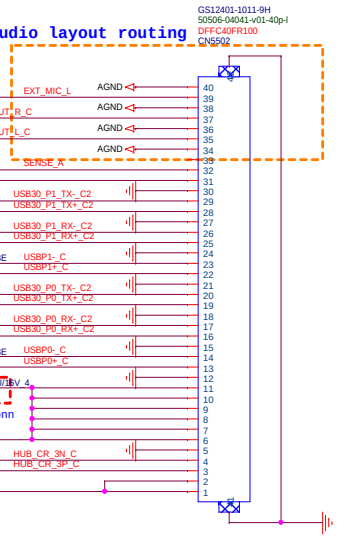


Quanta confidential

20161026 FP/PN check ok

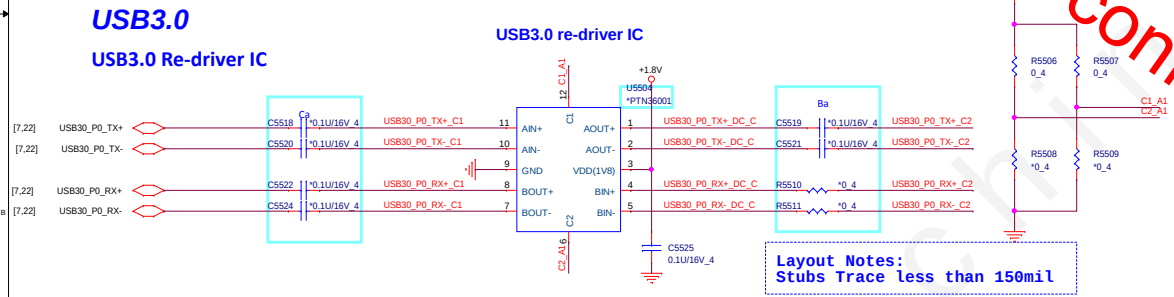
Daughter Board

For Audio layout routing



USB3.0

USB3.0 Re-driver IC

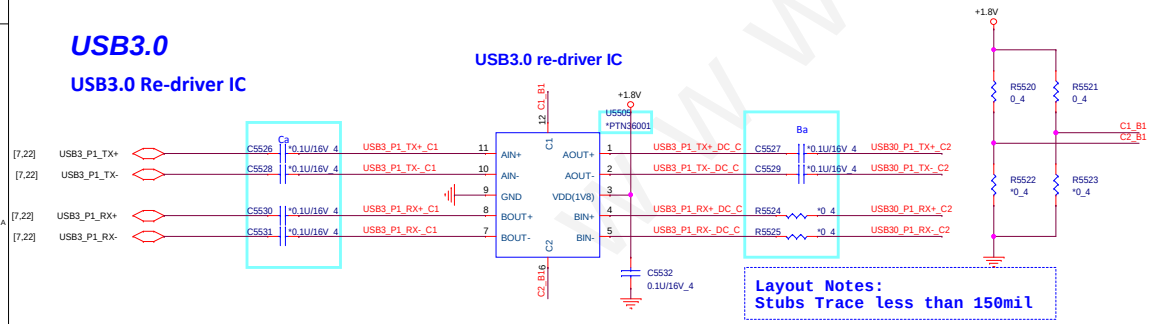


SOC to CON--> Stuff Ra, remove Ca,Ba

SOC to re-driver to conn--> Stuff Ca,Ba, remove Ra

USB3.0

USB3.0 Re-driver IC



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Quanta Computer Inc.		
Size: C	Document Number: USB3.0/DB	Rev: 1A
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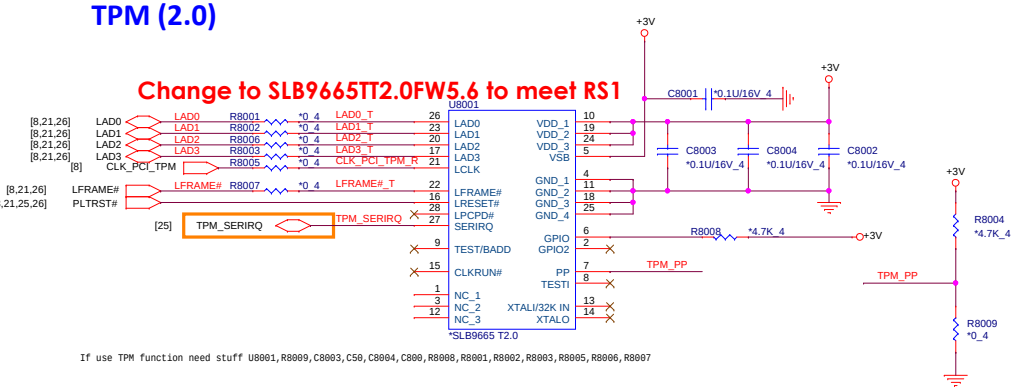
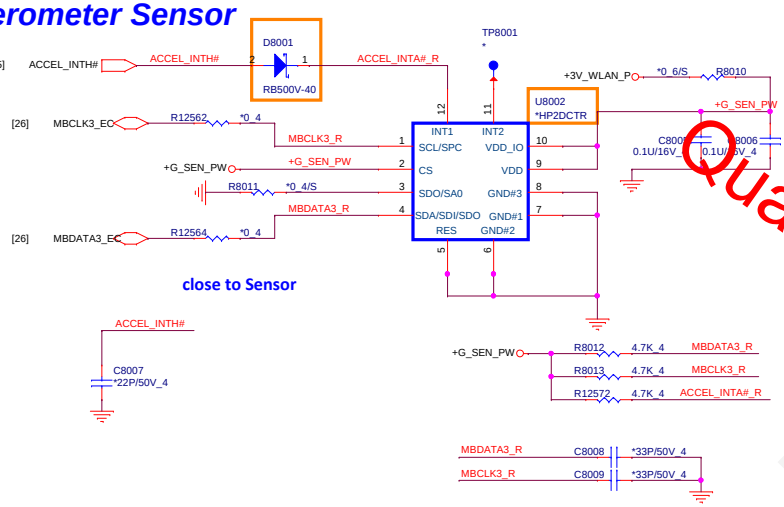
TPM (2.0)

Change to SLB9665TT2.0FW5.6 to meet RS1

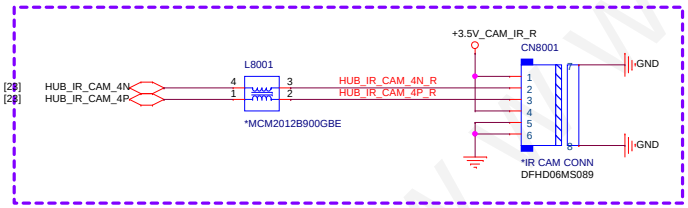


If use TPM function need stuff U8001,R8009,C8003,C50,C8004,C800,R8008,R8001,R8002,R8003,R8005,R8006,R8007

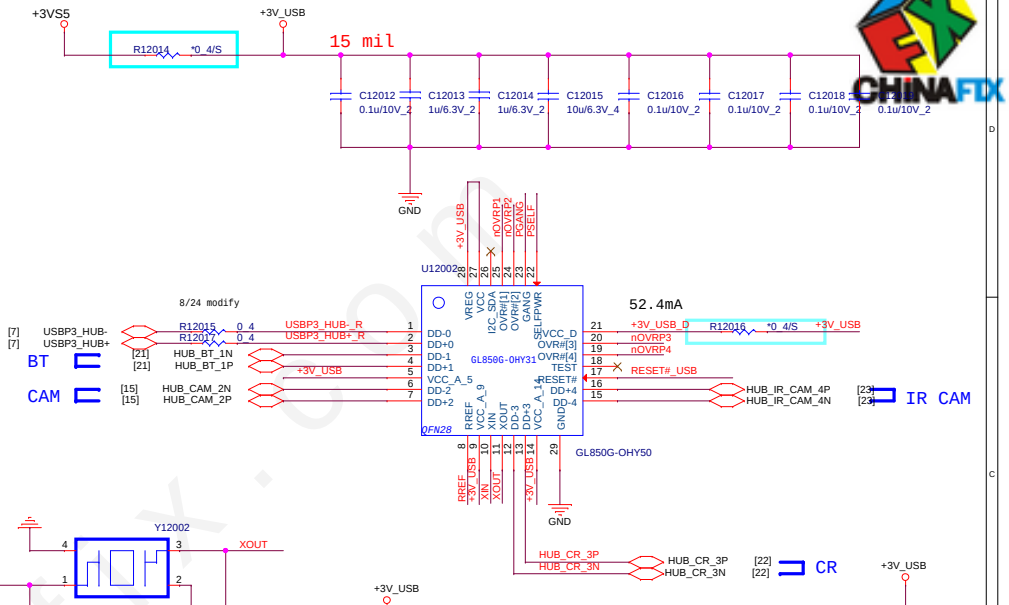
Accelerometer Sensor

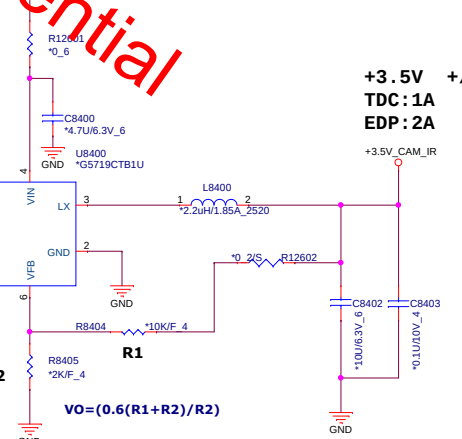


IR CAM



USB HUB





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Quanta Computer Inc.		
Size: Custom	Document Number: TPM/G-Sensor/IR CAM/HUB	Rev: 1A
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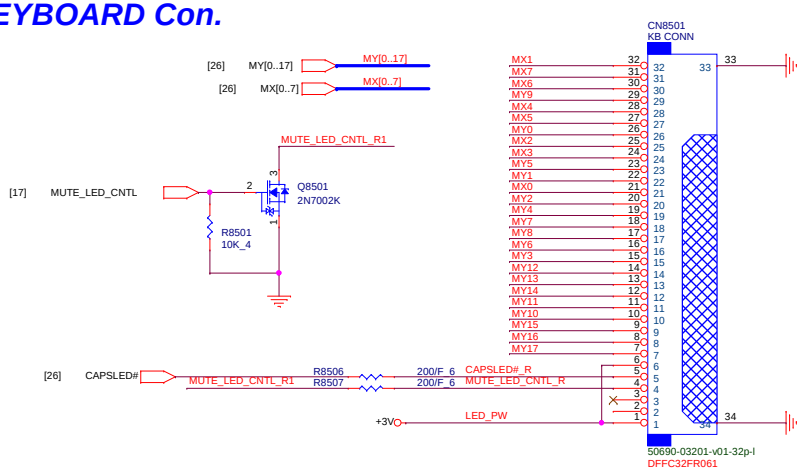
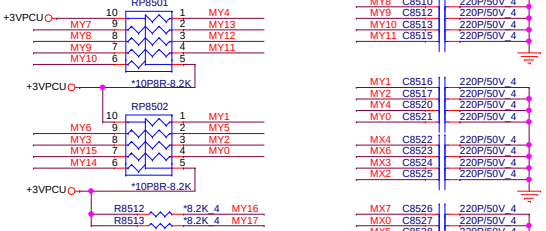
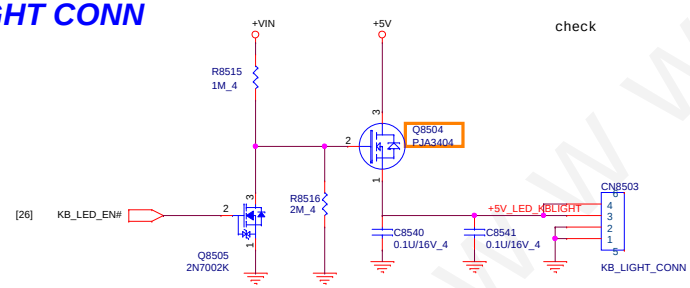
KEYBOARD Con.



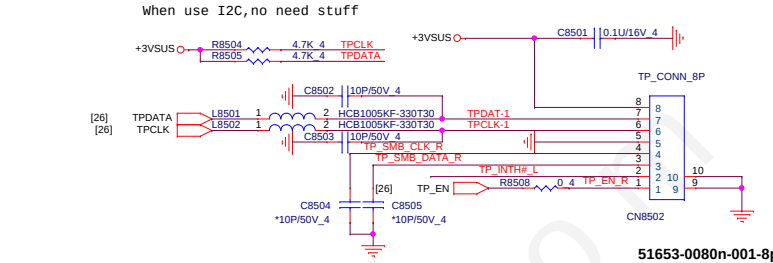
KEYBOARD PULL-UP



KB LIGHT CONN



Touch Pad Connector



51653-0080n-001-8p-l
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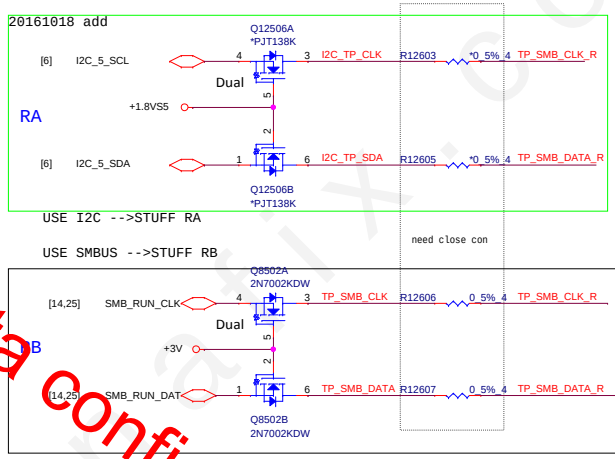
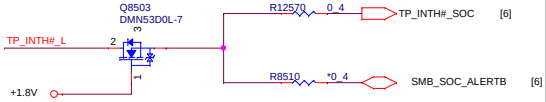
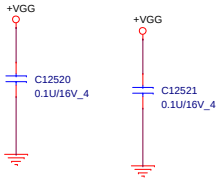




Cap



PROJECT : G72D
Quanta Computer Inc.

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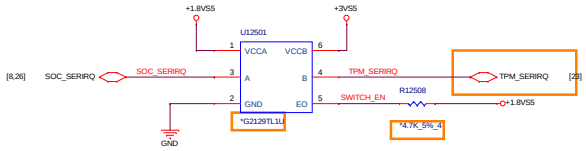
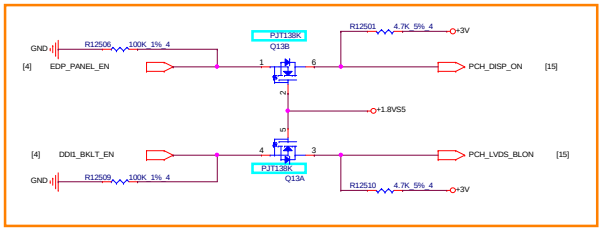
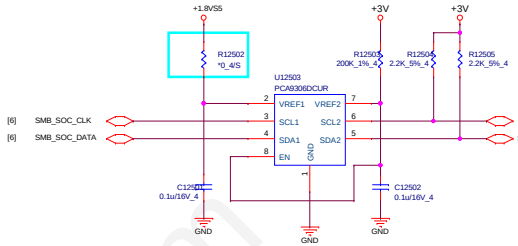
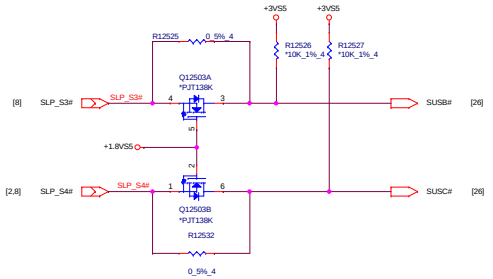
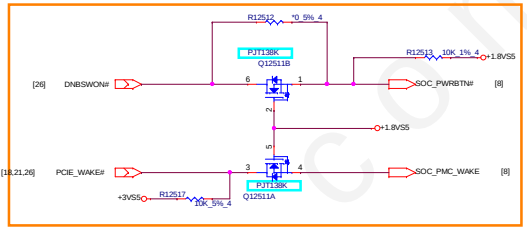
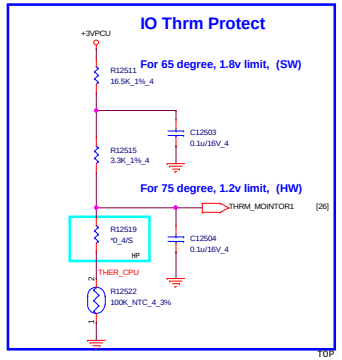


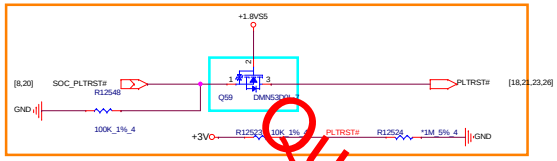


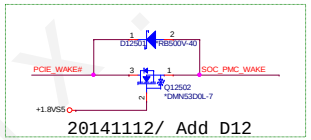




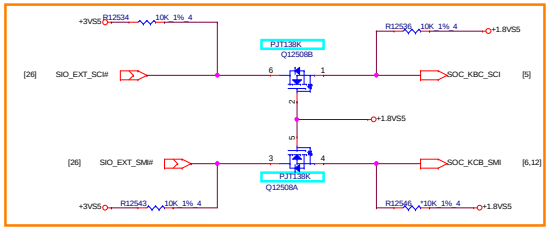


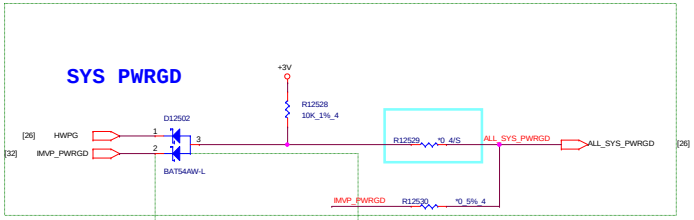




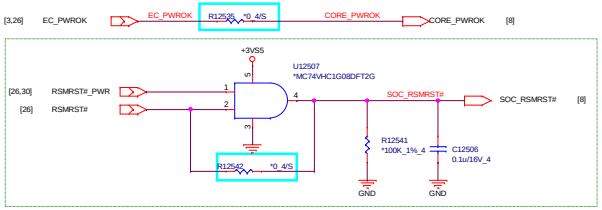


20141112/ Add D12



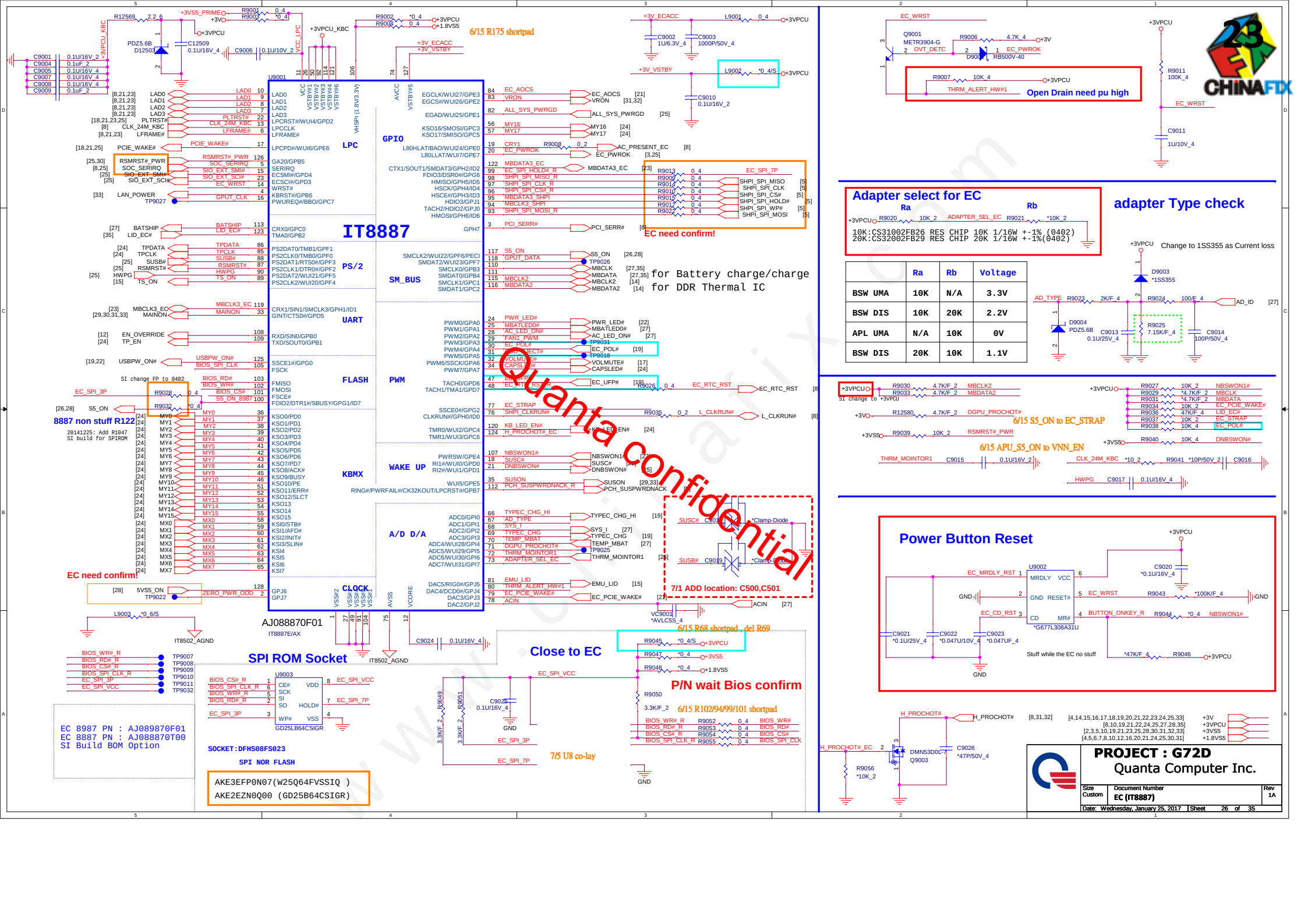


20150408
Del C358 for layout modify



11/5 Add AND gate

PROJECT : G72D		
Quanta Computer Inc.		
Size	Document Number Level shift/Thermistor	Rev 1A
Date: Wednesday, January 25, 2017	Sheet 25 of 35	



	Ra	Rb	Voltage
BSW UMA	10K	N/A	3.3V
BSW DIS	10K	20K	2.2V
APL UMA	N/A	10K	0V
BSW DIS	20K	10K	1.1V

+3VPCU [8,10,19,21,22,24,25,26,27,28,35]
+5VPCU [17,28,30,33]
+BAT_RTC [8,22,35]
+VIN [15,24,28,29,30,31,32,34]
+3VPCU [8,10,19,21,22,24,25,26,27,28,35]

ADP=65W

3S1P 41Whr

CN1
51483-00801-V01_Header

PQ2
AP0203GMT-HF

PQ3
EMB20N03V

N-channel

PQ1
AON6414AL

PL1
*Short_0805

PL2
*Short_0805

PL3
*Short_0805

PL4
*Short_0805

PL5
4.7uH_7x7x3

CN2
BBP28BC-B401Q-9H

PD1
P4SMAFJ20A

PD2
*P4SMAFJ20A

PD3
*PDZ5.6B

PD4
*PDZ5.6B

PD6
RB500V-40

PD7
*RB500V-40

PD40
BAS316

PD41
BAS316

PQ4
2N7002K

PQ5
METR3904-G

PQ6
EMB20N03V

PQ7
EMB20N03V

PQ8
METR3904-G

PQ9
2N7002K

PQ10
2N7002K

PQ11
METR3904-G

PU1
BQ24728HRSRR

Place this ZVS close to DCIN CONN

Do Not add test pad on BATDIS_G signal

Place this ZVS close to Far-Far away +VIN

Place this cap close to EC

Place this R&C close to EC

MIN. BATV=7.2V

Vacdet=2.4V

Ra

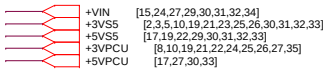
VIN>22.5V (AC OVP)
VIN>17.2V (Enable Charging)
VIN>15.2V (AC present)

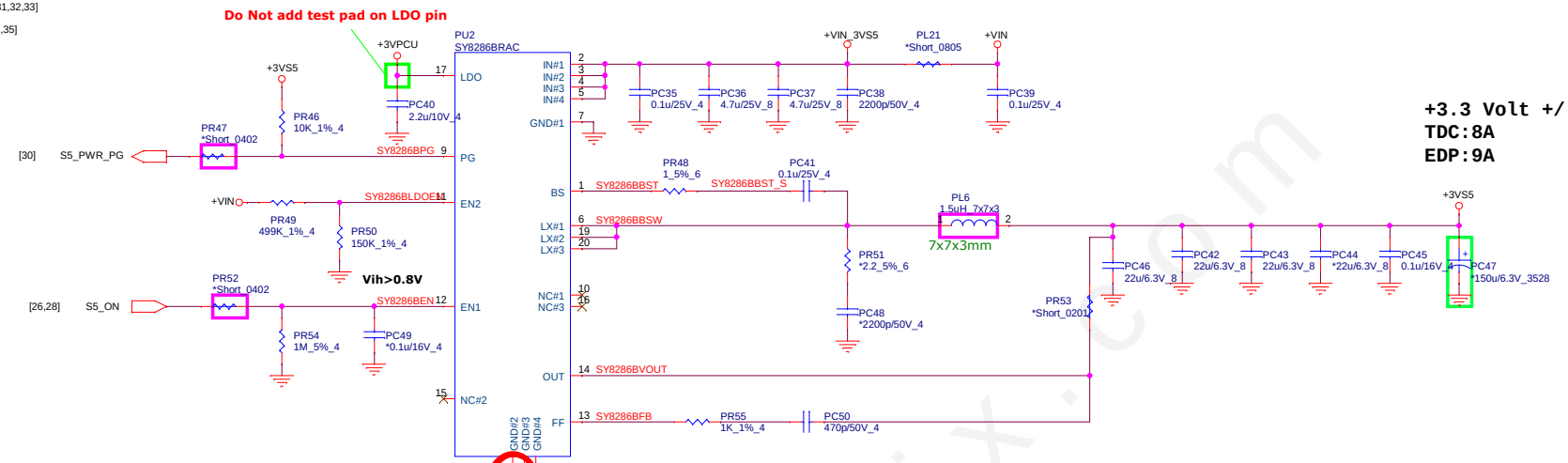
PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Charger (BQ24738H)	1A

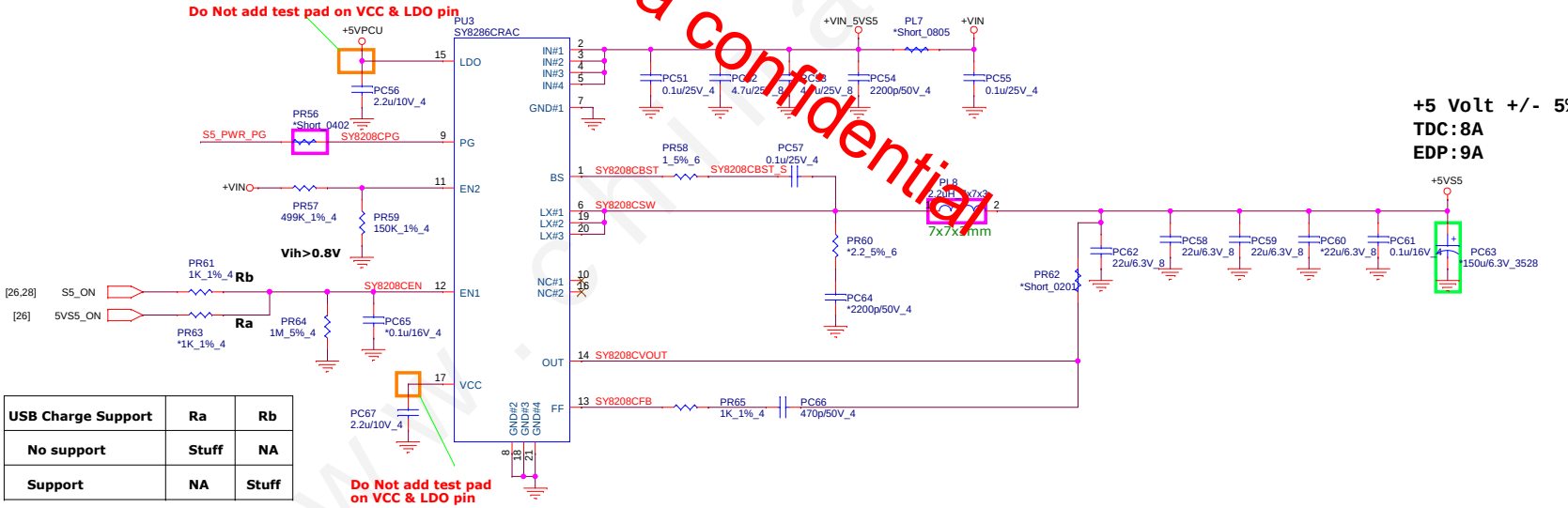
Date: Wednesday, January 25, 2017 Sheet 27 of 35

DC/DC +3VS5/+5VS5





+3.3 Volt +/- 5%
TDC:8A
EDP:9A

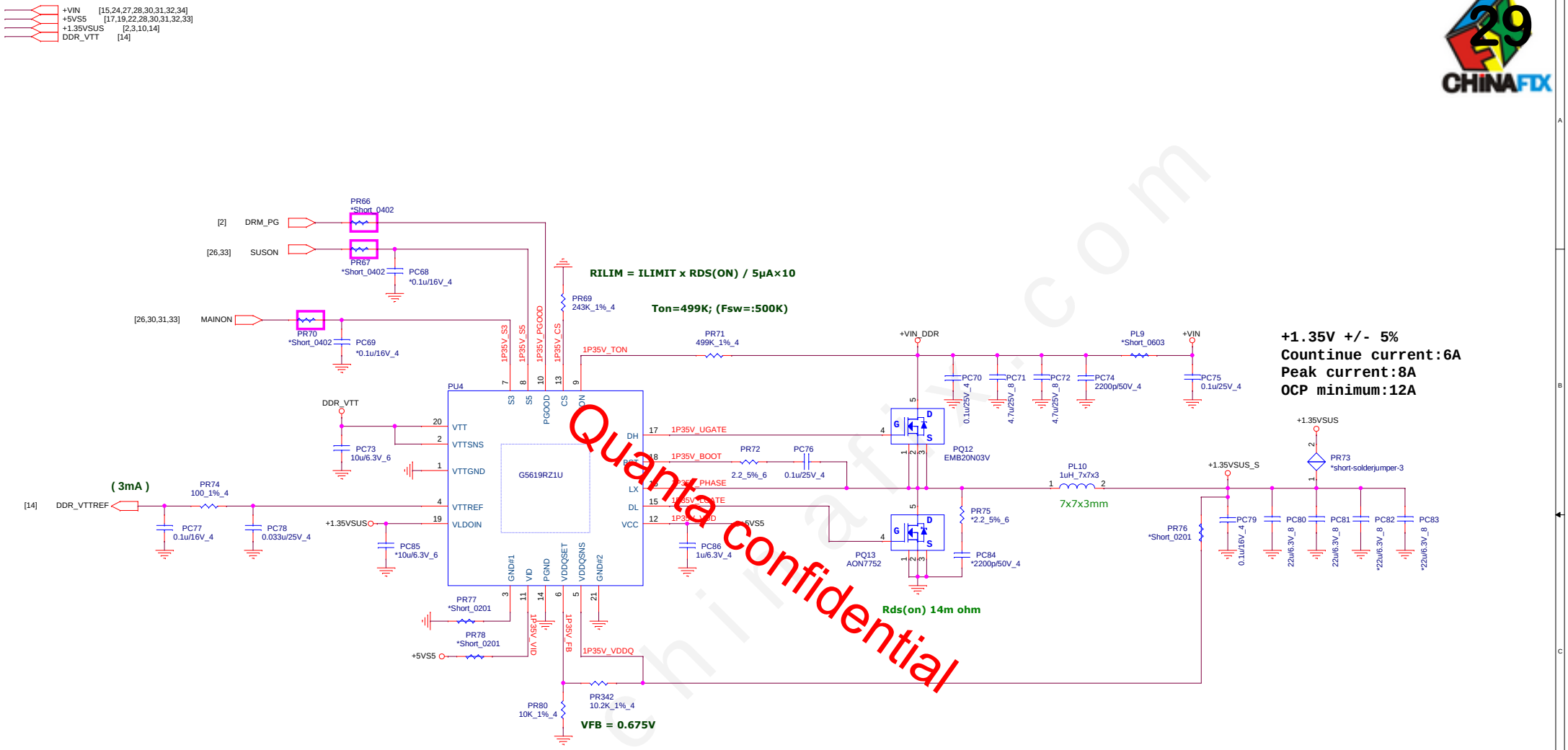


+5 Volt +/- 5%
TDC:8A
EDP:9A

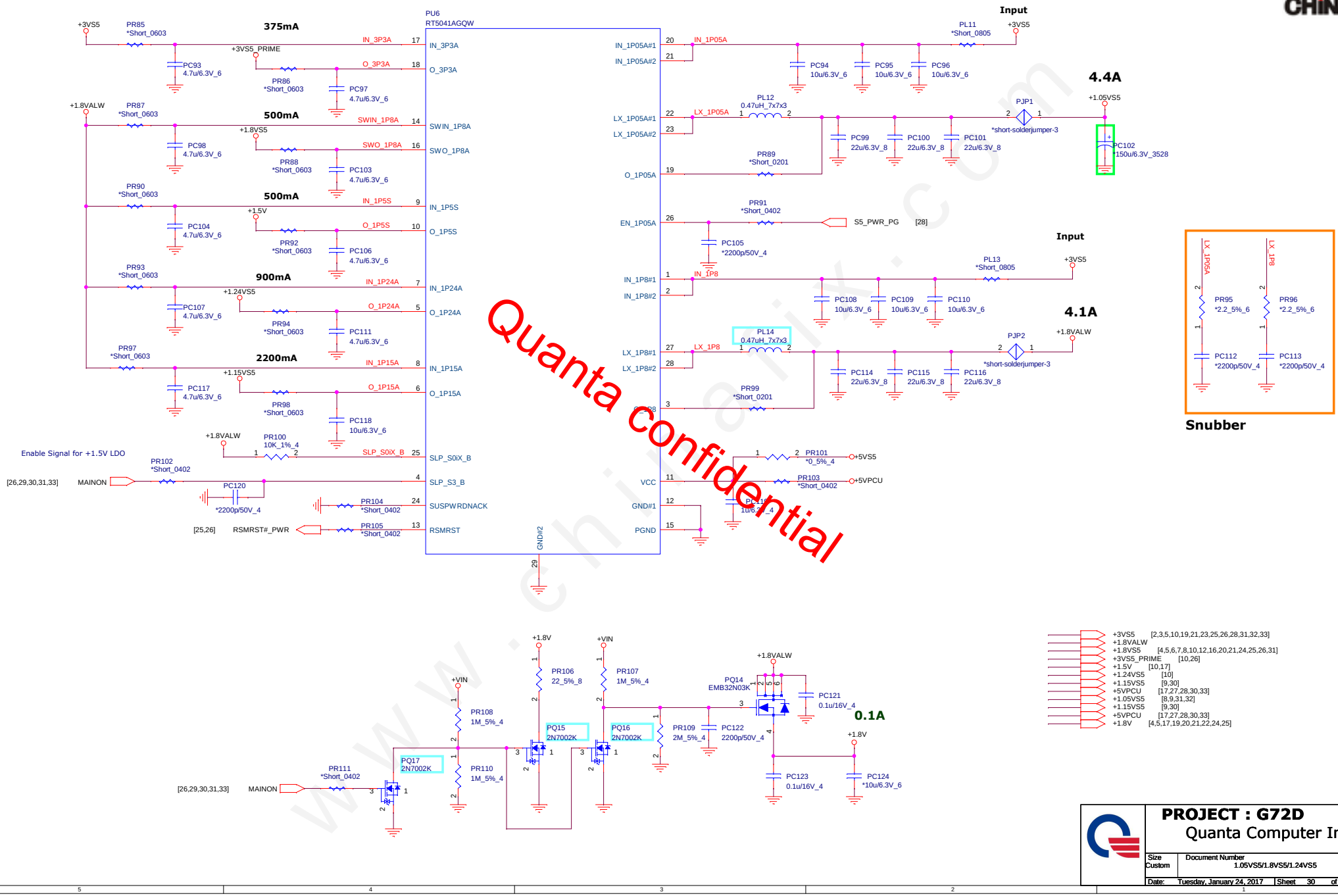
USB Charge Support	Ra	Rb
No support	Stuff	NA
Support	NA	Stuff

PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	3/5VS5 (SY8208B/SY8208C)	1A
Date: Tuesday, January 24, 2017	Sheet 28 of 35	



PROJECT : G72D Quanta Computer Inc.		
Size: Custom	Document Number: DDR4 (G5619RZ1U)	Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 29 of 35	


PU6
RT5041AGQW
375mA
500mA
500mA
900mA
2200mA
Input
4.4A
Input
4.1A
0.1A
Snubber
Enable Signal for +1.5V LDO
Quanta confidential
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PROJECT : G72D
Quanta Computer Inc.
Size Custom
Document Number 1.05VS5/1.8VS5/1.24VS5
Rev 1A
Date: Tuesday, January 24, 2017
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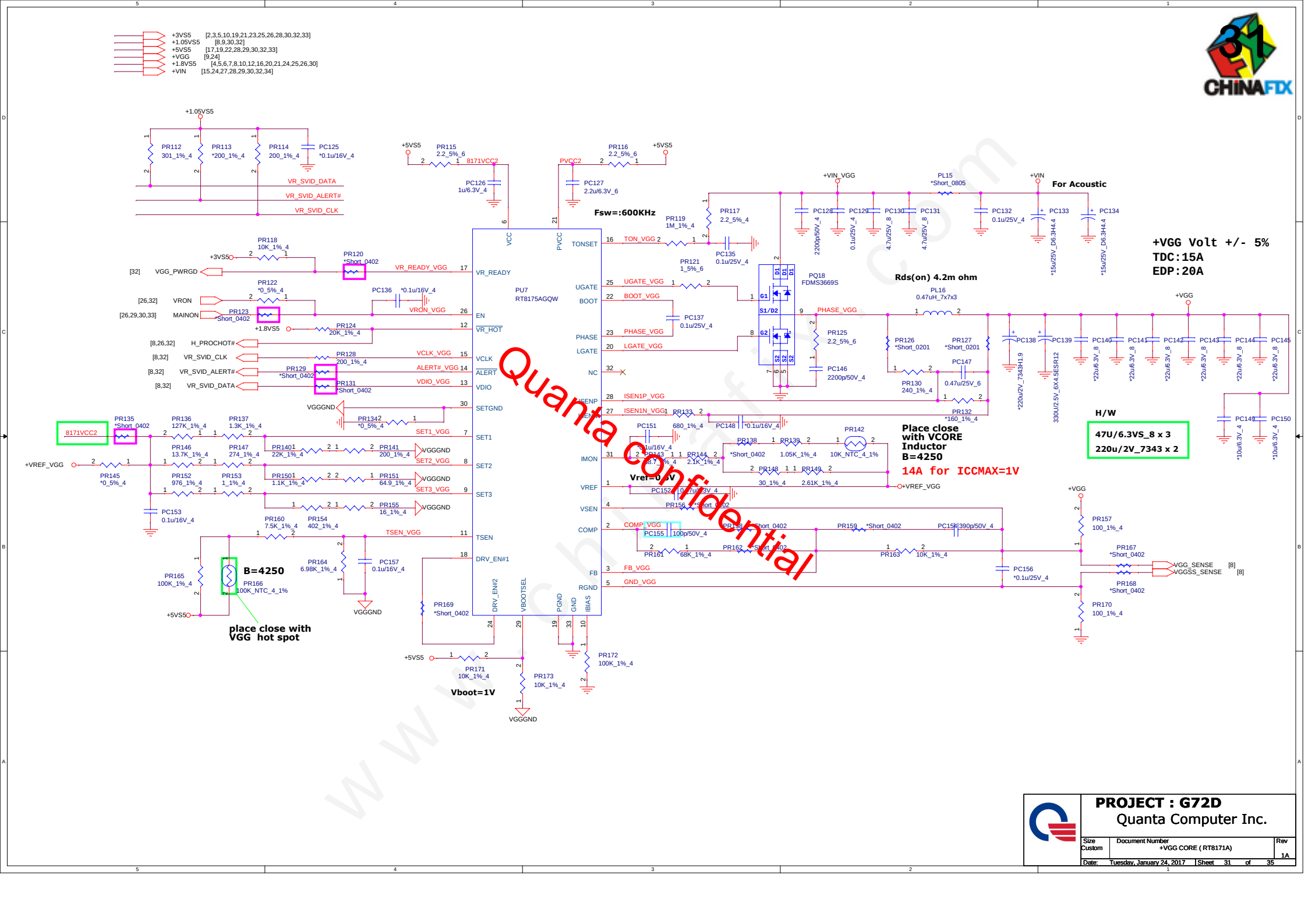
+3VS5 [2,3,5,10,19,21,23,25,26,28,30,32,33]
+1.05VS5 [8,9,30,32]
+5VS5 [17,19,22,28,29,30,32,33]
+VGG [9,24]
+1.8VS5 [4,5,6,7,8,10,12,16,20,21,24,25,26,30]
+VIN [15,24,27,28,29,30,32,34]
PU7
RT8175AGQW
Fsw=:600KHz
Rds(on) 4.2m ohm
PQ18
FDMS3669S
For Acoustic
+VGG Volt +/- 5%
TDC:15A
EDP:20A
H/W
47U/6.3VS_8 x 3
220u/2V_7343 x 2
Place close with VCORE Inductor B=4250
14A for ICCMAX=1V
Vref=0.6V
B=4250
place close with VGG hot spot
Vboot=1V
VR_SVID_DATA
VR_SVID_ALERT#
VR_SVID_CLK
VR_READY_VGG
VRON_VGG
MAINON
H_PROCHOT#
VCLK_VGG
ALERT#_VGG
VDIO_VGG
SET1_VGG
SET2_VGG
SET3_VGG
TSEN_VGG
TON_VGG
UGATE_VGG
BOOT_VGG
PHASE_VGG
LGATE_VGG
ISEN1P_VGG
ISEN1N_VGG
COMP_VGG
FB_VGG
GND_VGG
VGG_SENSE
VGGSS_SENSE
Quanta Confidential
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PROJECT : G72D
Quanta Computer Inc.
+VGG CORE (RT8171A)
Tuesday, January 24, 2017
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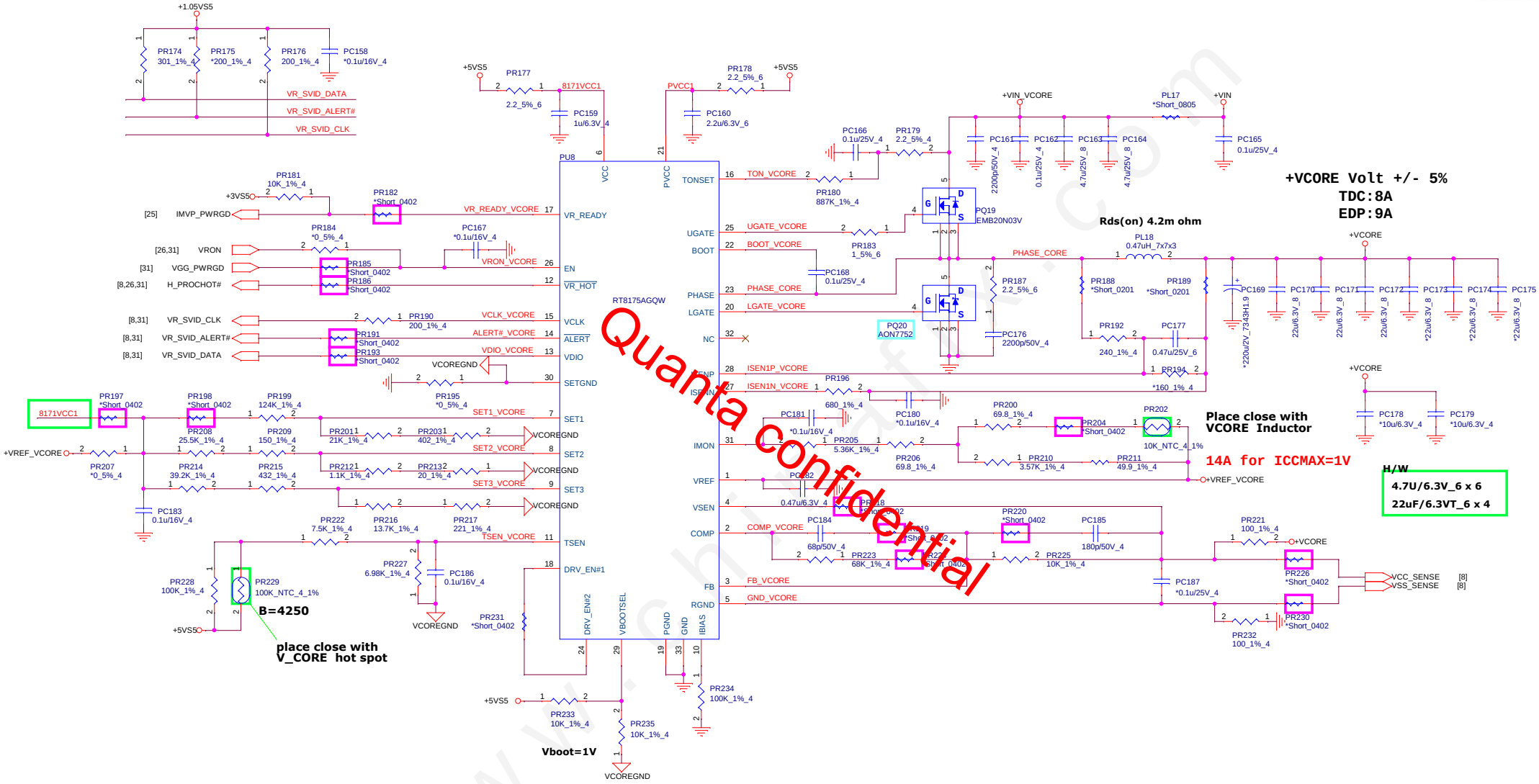
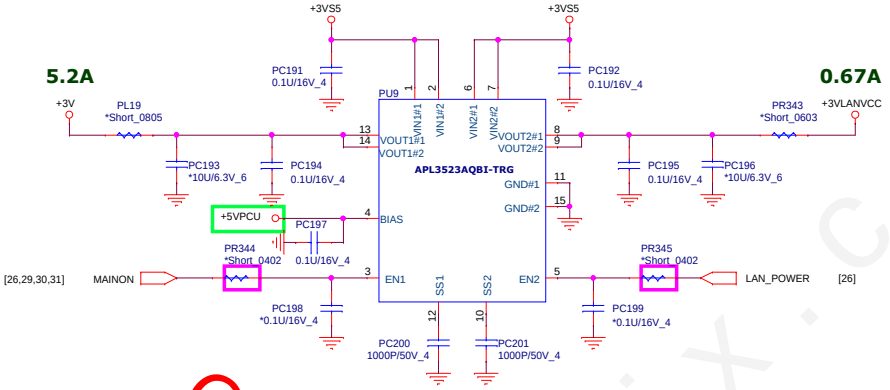
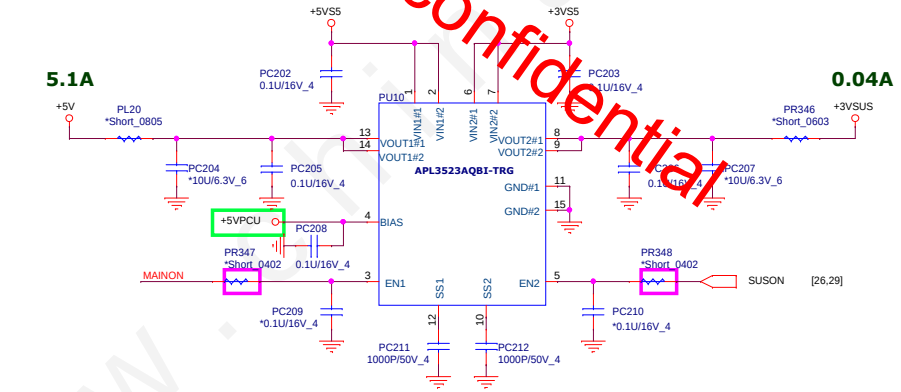


PROJECT : G72D		
Quanta Computer Inc.		
Size: Custom	Document Number: +VCC CORE (RT8171A)	Rev: 1A
Date: Tuesday, January 24, 2017	Sheet 32 of 35	

+3V [4,14,15,16,17,18,19,20,21,22,23,24,25,26]
+5V [15,16,17,20,23,24]
+VIN [15,24,27,28,29,30,31,32,34]
+3VS5 [2,3,5,10,19,21,23,25,26,28,30,31,32]
+5VS5 [17,19,22,28,29,30,31,32]
+3VSUS [24]
+5VPCU [17,27,28,30]
+3VLANVCC [18]

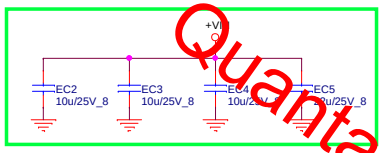




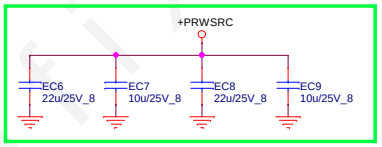
PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Load switch IC (APL3523A)	1A
Date: Tuesday, January 24, 2017	Sheet 33 of 35	

EMI request for ISN



EMI request for ISN

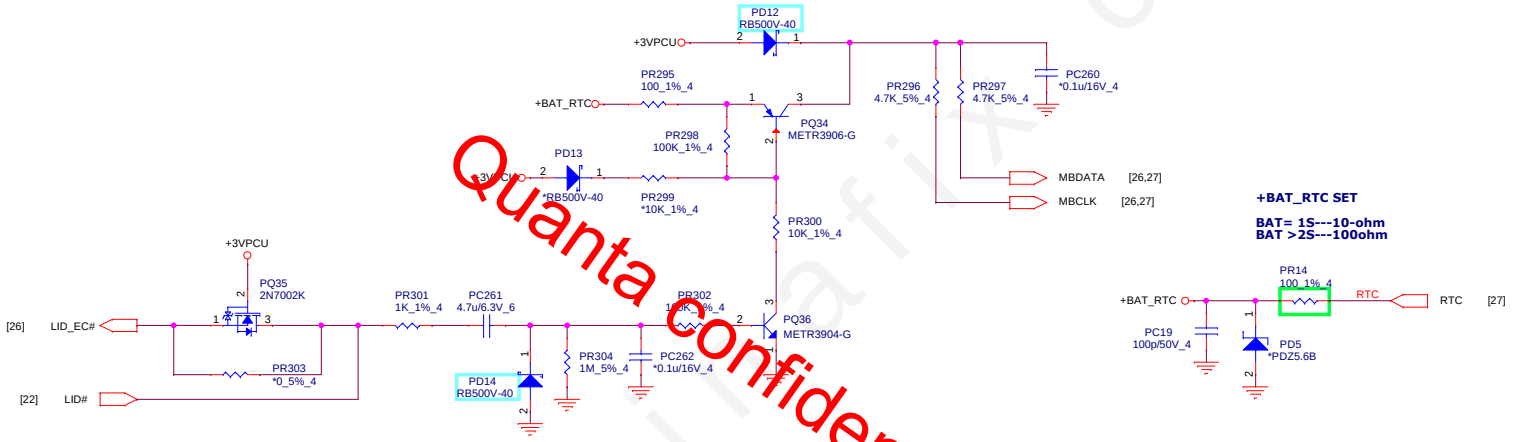


PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	EMI solution	1A

Date: Tuesday, January 24, 2017 Sheet 34 of 35

+3VPCU [8,10,19,21,22,24,25,26,27,28]
+BAT_RTC [8,22]



PROJECT : G72D
Quanta Computer Inc.

Size	Document Number	Rev
Custom	LID SW for storage mode	1A
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