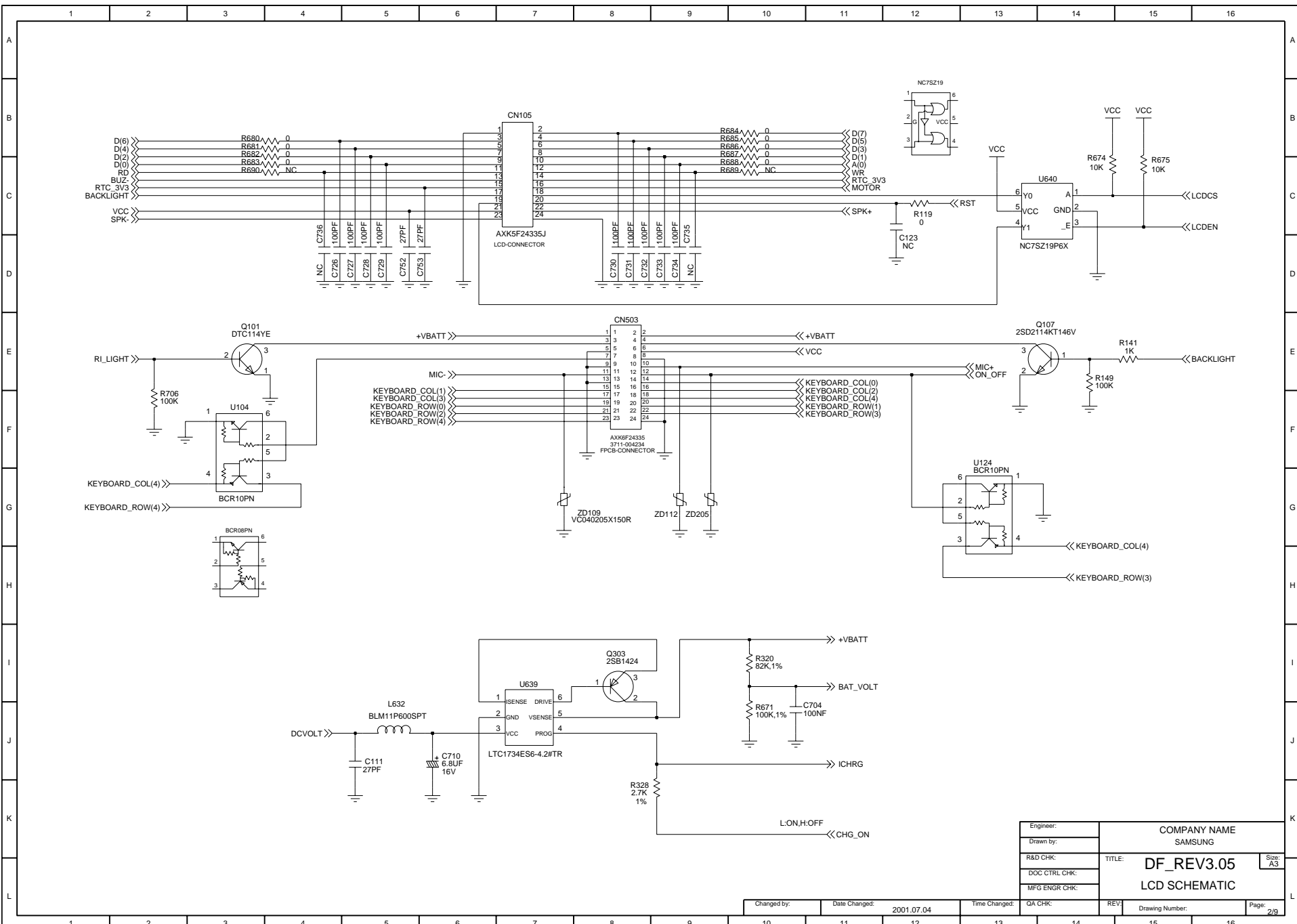
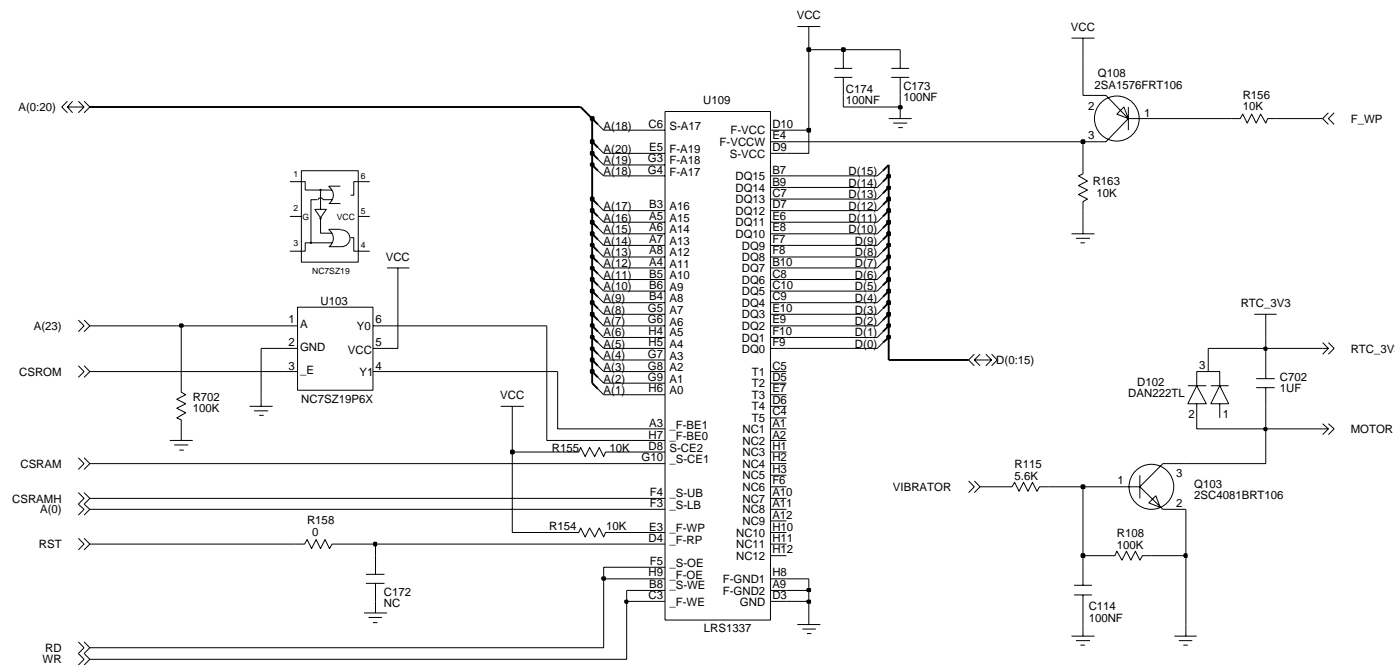


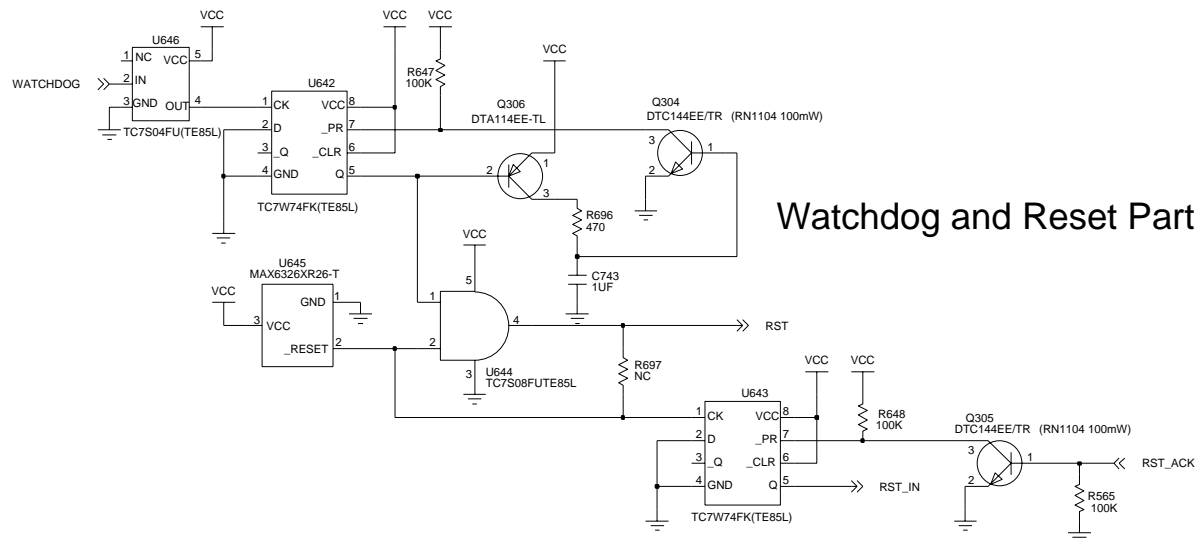
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Drawn by:	SAMSUNG		
R&D CHK:	TITLE:	DF_REV3.05	Size: A3
DOC CTRL CHK:	POWER MANAGEMENT & SIM INTERFACE		
MFG ENGR CHK:	REV:	Drawing Number:	Page: 1/9

Changed by: Date Changed: 2001.07.04 Time Changed:

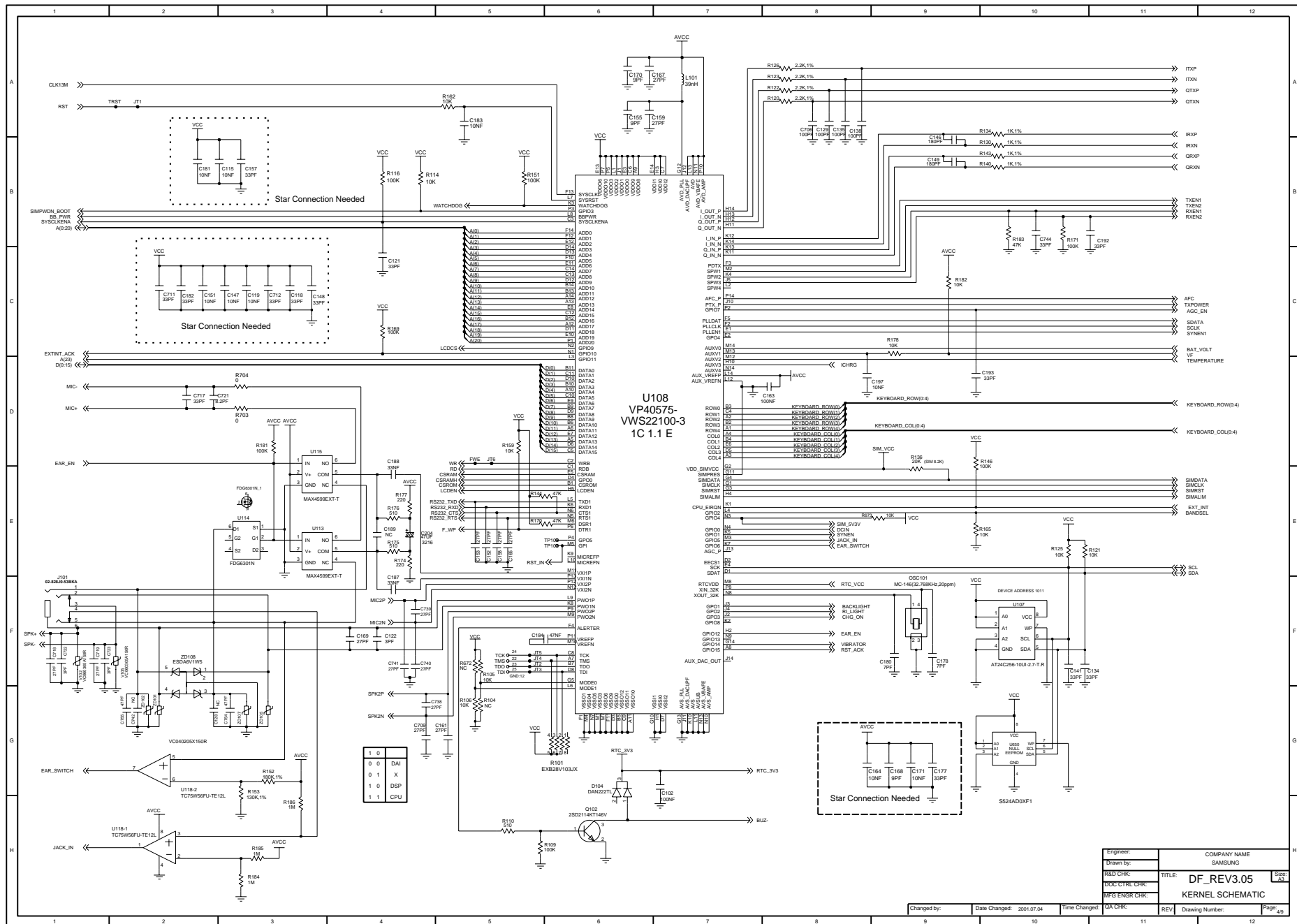


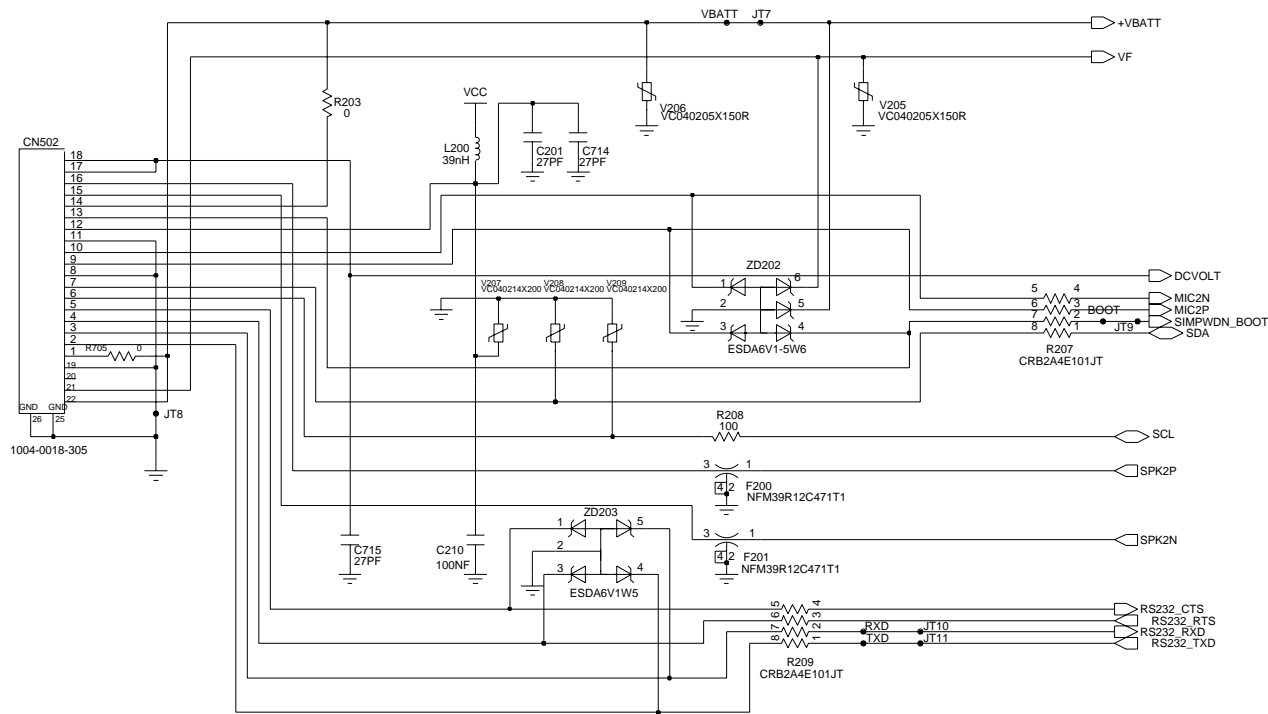


Watchdog and Reset Part



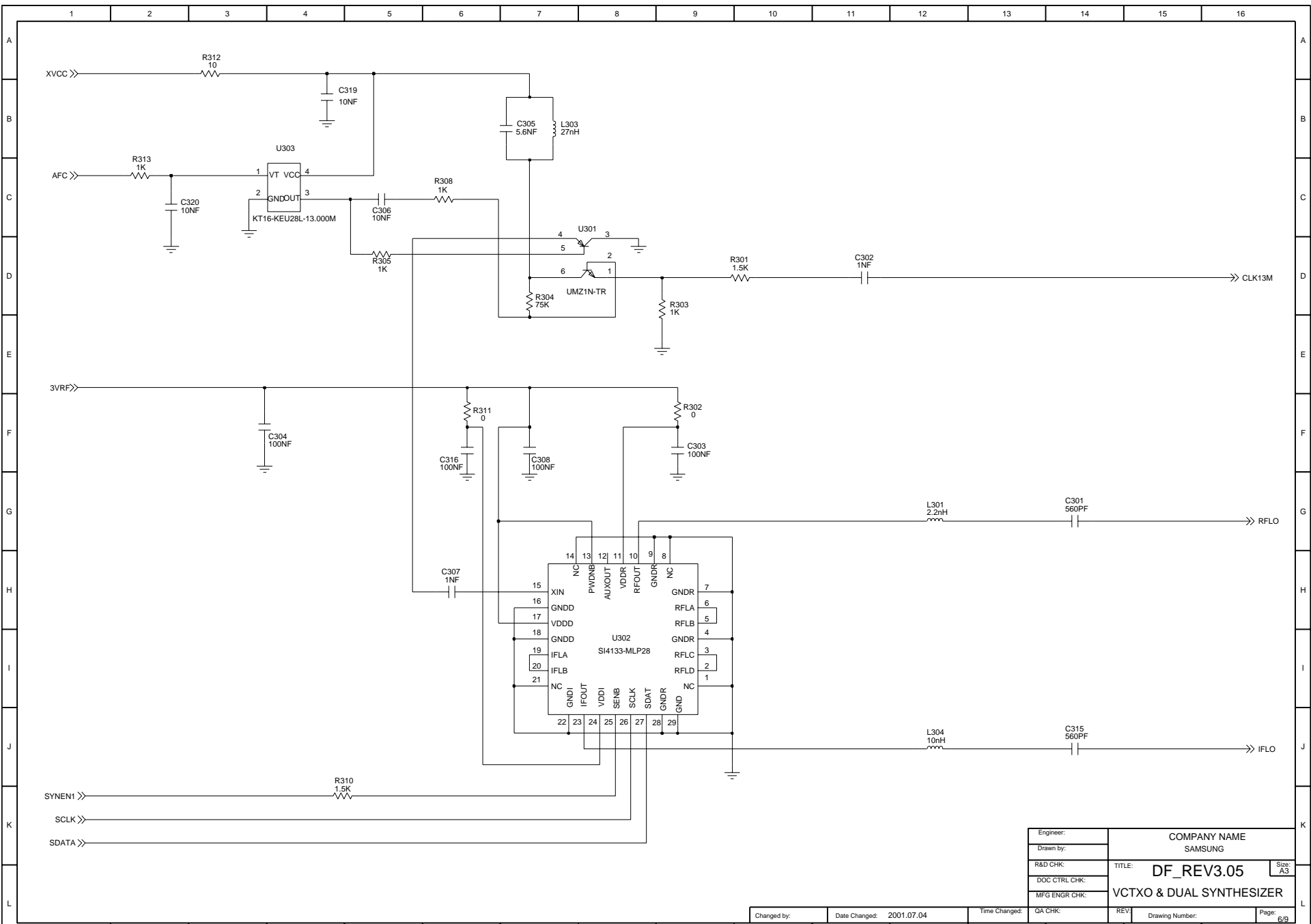
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R&D CHK:	TITLE: DF_REV3.05		
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QA CHK:	REVISION: 3.05		
Changed by:	Date Changed:	Time Changed:	Page:
2001.07.04	2001.07.04	2001.07.04	3/3





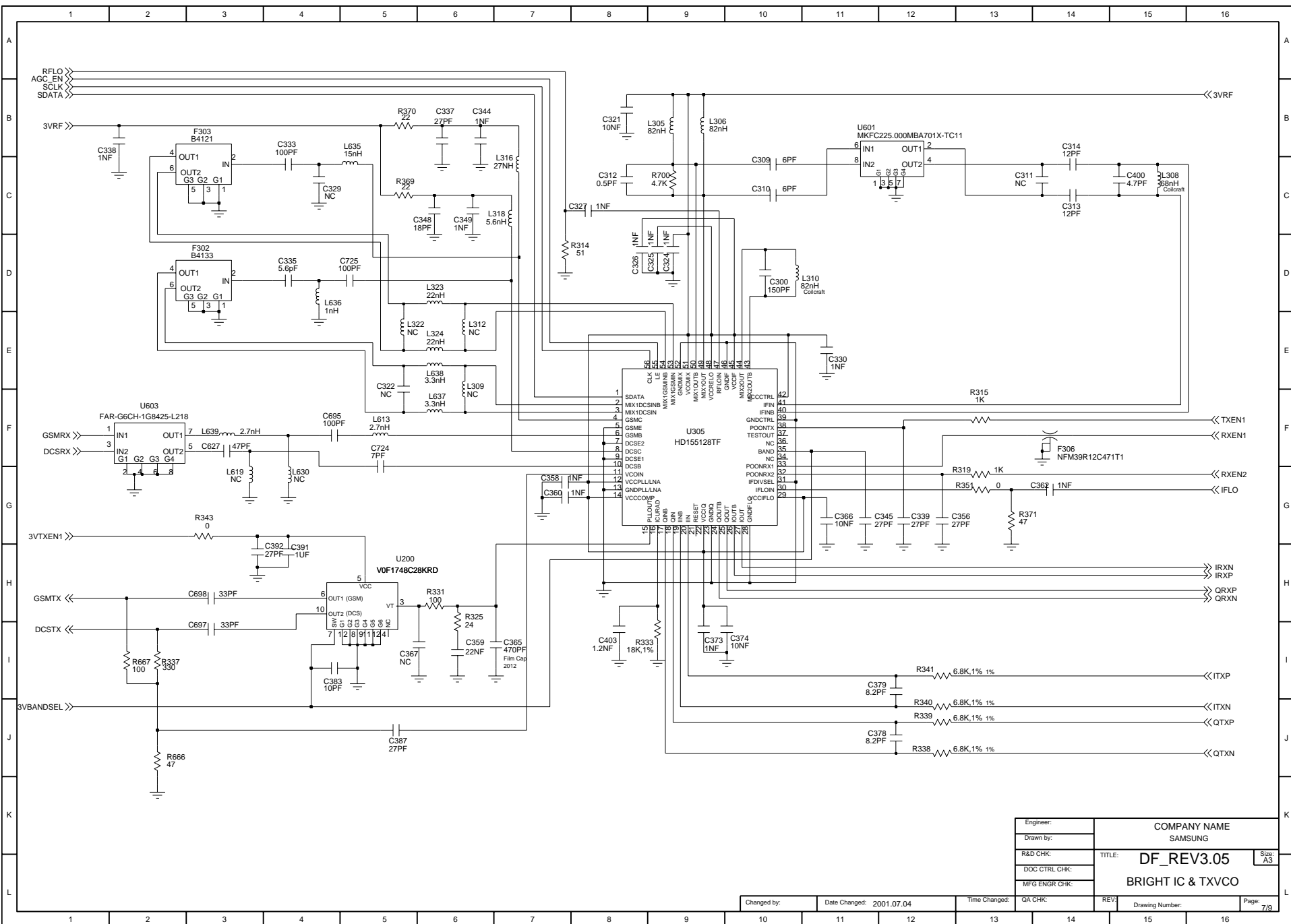
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R&D CHK:	TITLE:	DF_REV3.05	Size: A3
DOC CTRL CHK:	INTERFACE CONNECTOR SCHEMATIC		
MFG ENGR CHK:	REV:	Drawing Number:	Page: 5/9

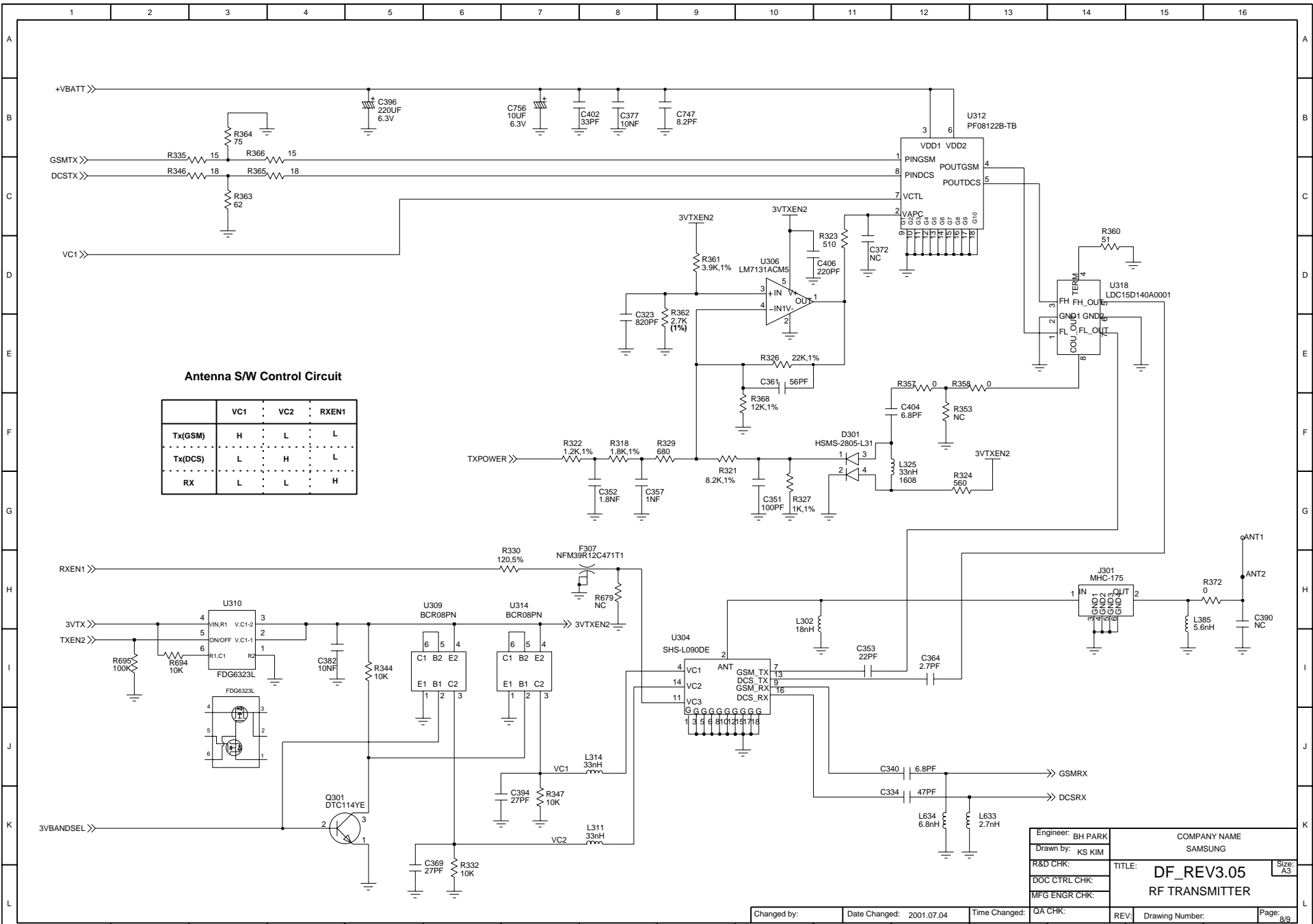
Changed by: Date Changed: 2001.07.04 Time Changed: QA CHK:

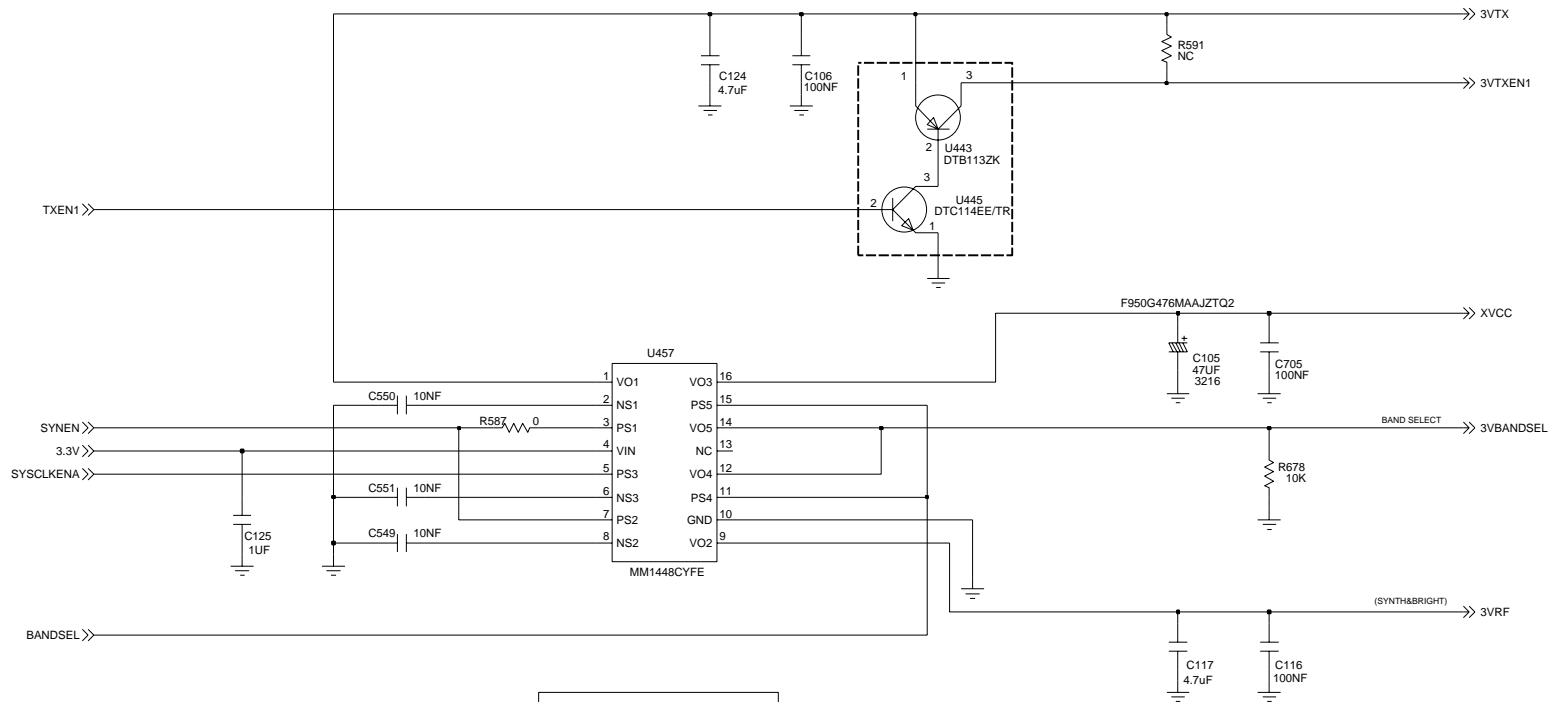


Engineer:	COMPANY NAME		
Drawn by:	SAMSUNG		
R&D CHK:	TITLE: DF_REV3.05		Size: A3
DOC CTRL CHK:	VCTXO & DUAL SYNTHESIZER		
MFG ENGR CHK:			
QA CHK:	REV:	Drawing Number:	Page: 6/9

Changed by: Date Changed: 2001.07.04 Time Changed:







Engineer:	COMPANY NAME		
Drawn by:	SAMSUNG		
R&D CHK:	TITLE:	DF_REV3.05	Size: A3
DOC CTRL CHK:			
MFG ENGR CHK:	POWER MANAGEMENT SCHEMATIC		
REV:	QA CHK:	Drawing Number:	Page: g/g

Changed by: Date Changed: 2001.07.04 Time Changed: