PCB BOARD SIZE

4 Layers

244mmX 200mm

On Board Header

CONN	Default	DESCRIPTION
JP1701	1-2	CMOS CLEAR
CN2902		HDMI SPDIF 1x4 pin
CN2903		Internal Speaker 2x4 pin
CN2901		Audio Front Panel 2x5 pin
CN601		HDT+ CONN (APU Debug)
CN5501		CPU FAN CONN 4 pin
CN5502		System FAN CONN 4 pin
CN6101		Internal USB 2.0
CN6102		Front USB 2.0
CN6202		Front USB 3.0
CN6801		ATX Power 24 pin
CN6802		Front Panel 2x8 pin
CN4201		ATX Power 4 pin
CN7101		Debug Port 2x7
CN7801		IR Reciever 1x3 pin
CN7802		GPIO 1x2 pin
CN7803		GPIO 1x2 pin

XTAL Description

XTAL	Function	Frequency	Spec	Capacitance
X1702	FCH	25M	+-20ppm CL:12P	C1=C2=12pF
X1701	FCH	32.768K	+-20ppm CL:12.5P	C1=C2=18pF
X1901	LAN	25M	+-20ppm CL:12P	C1=C2=22pF
X6501	LAN	25M	+-20ppm CL:12P	C1=C2=22pF

FCH : HUDSON-D3 71.HUDD3.M01
SIO : IT8772E 71.08772.A0G
LAN : RTL8111E-VL 71.08111.J03
Audio Codec : ALC662-GR 71.00662.00G

aParker

PROJECT CODE :48.3FU01.011
PCB Version: -1
PCB Number : 11005

PAGE	TITLE	Quantity
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02	Block Diagram	
03	Table of Content	
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05	APU_DDR_(2/6)	
06	APU_Control&Debug_(3/6)	
07	APU_POWER_(4/6)	
08	APU_VSS_(5/6)	
09	APU DISPLAY & MISC (6/6)	
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11	(Reserve)	
12	(Reserve)	
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14	DDR-DIMM1 Socket2	
15	DDR-DIMM0 Socket3	
16	DDR-DIMM1 Socket4	
17	HUDSON-M2_ACPI/PCI/CLK_(1/6)	
18	HUDSON-M2 GPIO/USB_(2/6)	
19	HUDSON-M2 SATA/HWM/SPI_(3/6)	
20	HUDSON-M2 POWER_(4/6)	
21	HUDSON-M2 STRAPPINGS_(5/6)	
22	HUDSON-M2 VSS_(6/6)	
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24	(Reserve)	
25	(Reserve)	
26	(Reserve)	
27	(Reserve)	
28	(Reserve)	
29	Audio Codec (ALC662)	
30	(Reserve)	
31	(Reserve)	
32	(Reserve)	
33	(Reserve)	
34	(Reserve)	
35	(Reserve)	
36	(Reserve)	
37	(Reserve)	
38	(Reserve)	
39	(Reserve)	
40	DUAL POWER	
41	VREG : +3.3V_ALW&+5V_ALW	
42	VREG : +VCC_CORE	
43	VREG : +VDDNB	
44	VREG: +1.5V_SUS & +V_DDR_REF	
45	VREG: +VDDR&_VDDP	
46	VREG: +1.1V_ALW/+1.2V_ALW	
47	(Reserve)	
48	LDO: 2.5V_RUN&_+1.5V_RUN	
49	(Reserve)	
50	CRT Connector (VGA CONN)	
51	HDMI	

PAGE	TITLE	Quantity
52	(Reserve)	
53	(Reserve)	
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55	FAN CONN	
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58	Microphone/Speaker/Audio	
59	RJ45+Transformer	
60	(Reserve)	
61	USB Port + Power Switch	
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63	(Reserve)	
64	(Reserve)	
65	(Reserve)	
66	(Reserve)	
67	(Reserve)	
68	LED/Button	
69	Keyboard/Touch pad	
70	(Reserve)	
71	Serial port/JTAG/Debug port	
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75	Express Card(x16 and x1slot)	
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78	SIO (ITE8772E)	
79	(Reserve)	
80	(Reserve)	
81	Screw Holes, Springs, BOSS	
82	(Reserve)	
83	(Reserve)	
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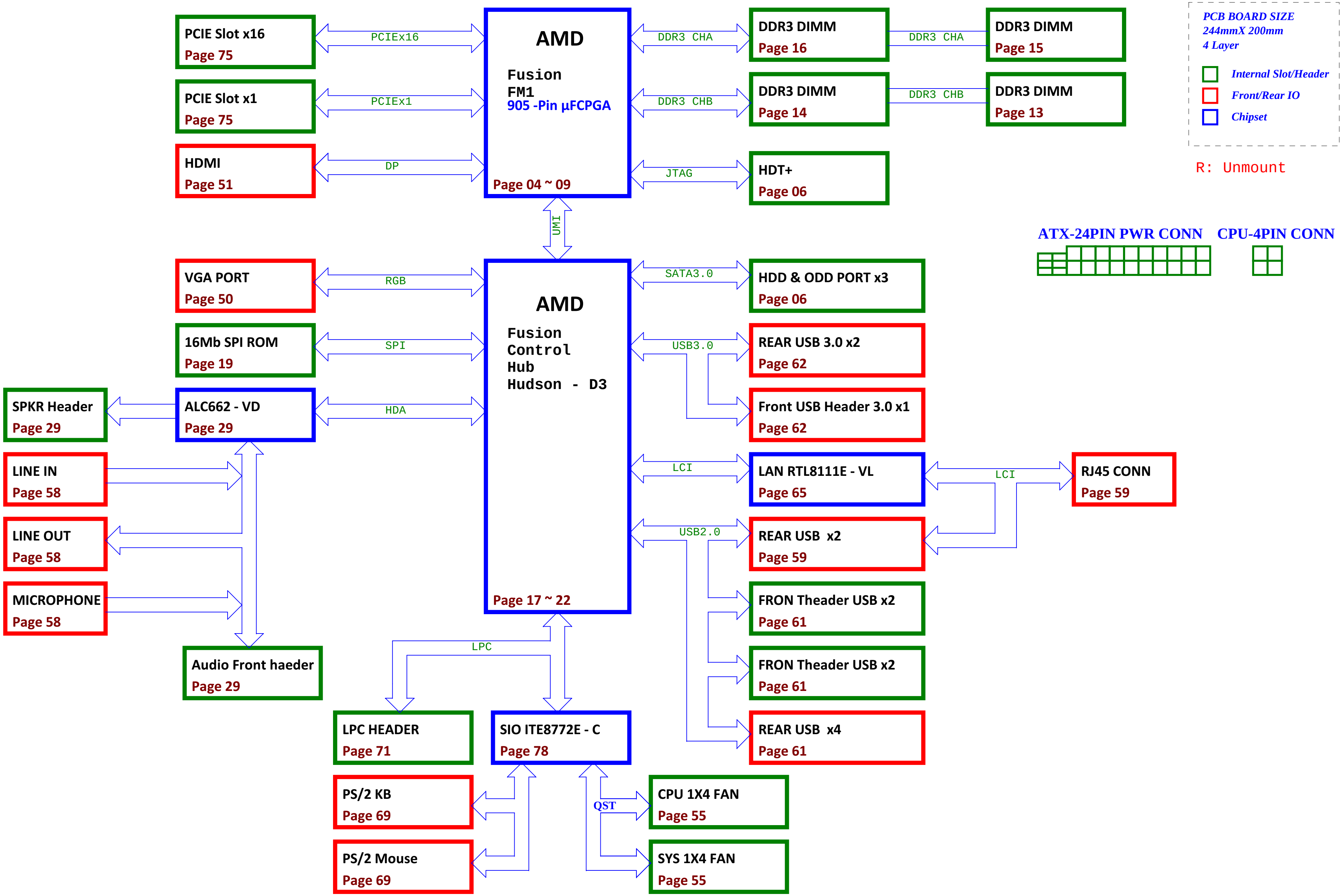
85	(Reserve)	
86	(Reserve)	
87	(Reserve)	
88	(Reserve)	
89	(Reserve)	
90	(Reserve)	
91	(Reserve)	
92	(Reserve)	
93	(Reserve)	
94	(Reserve)	
95	(Reserve)	
96	Power Block Diagram	
97	(Reserve)	
98	(Reserve)	
99	(Reserve)	



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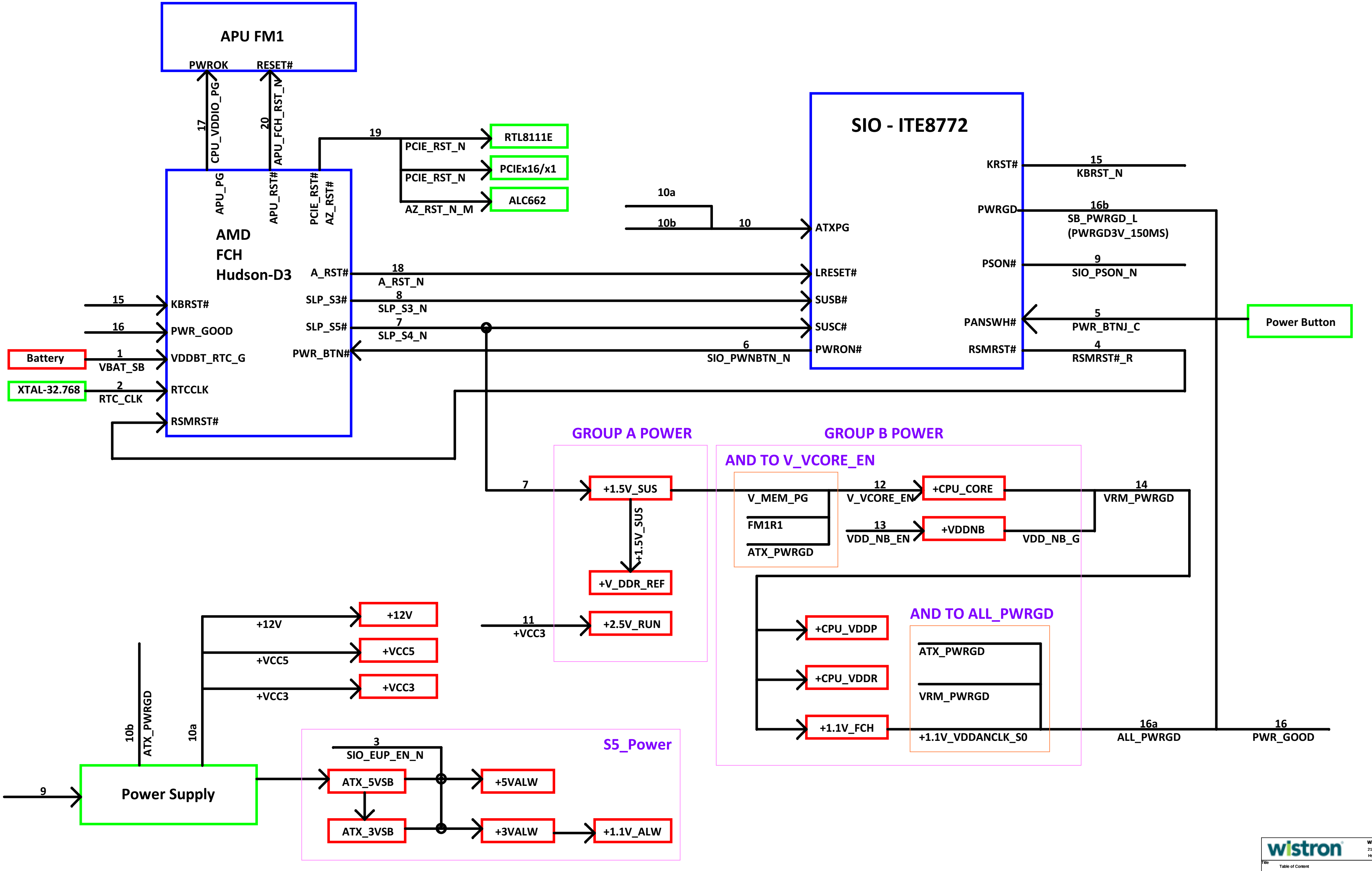
Hsichih, Taipei Hsien

Title: BLOCK DIAGRAM		
Size: C	Document Number: Parker	Rev: -1
Date: Tuesday, June 28, 2011	Sheet 2 of 99	



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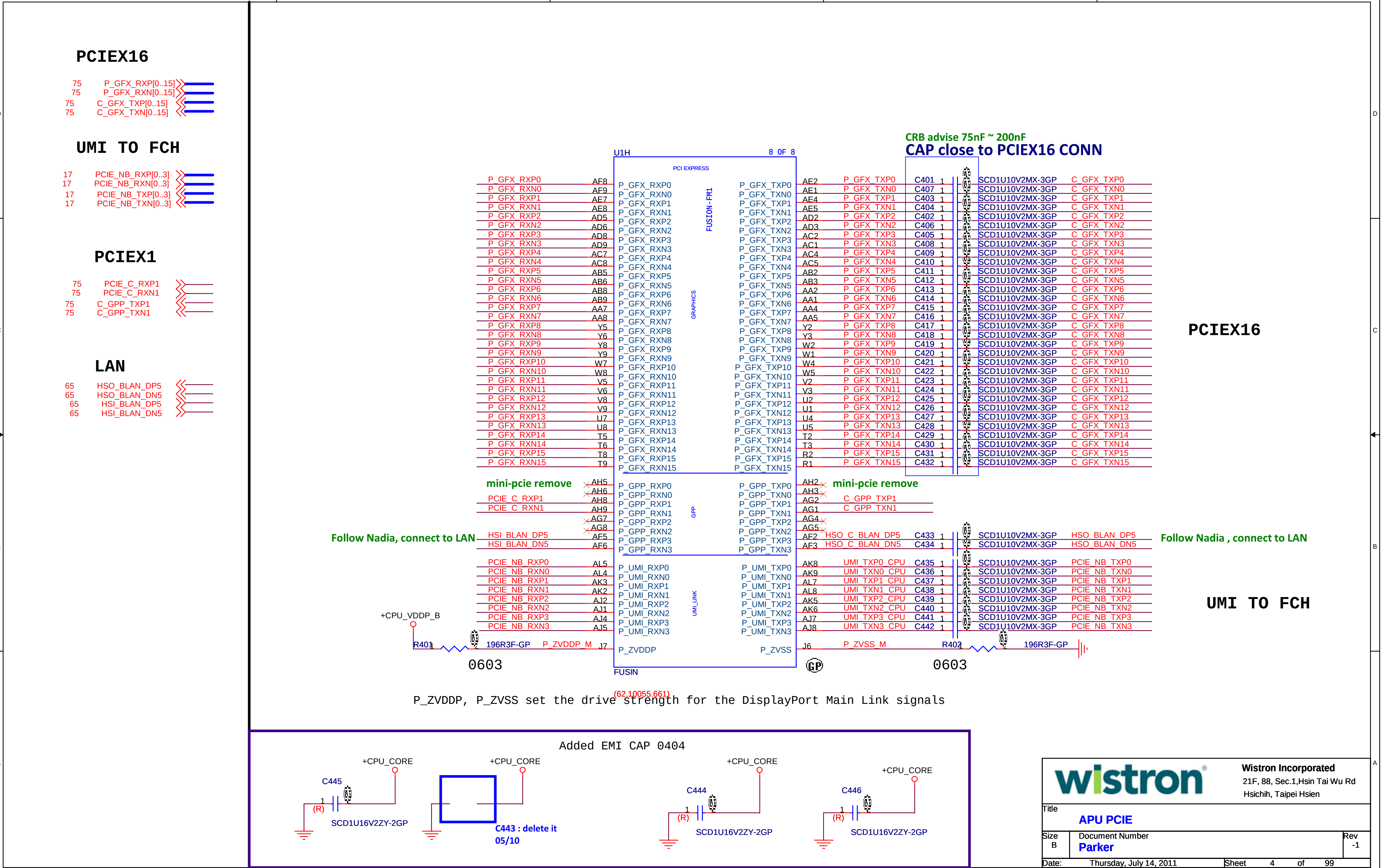
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title: Table of Content

Size: D | Document Number: Parker | Rev: -1

Date: Thursday, July 14, 2011 | Sheet 3 of 99



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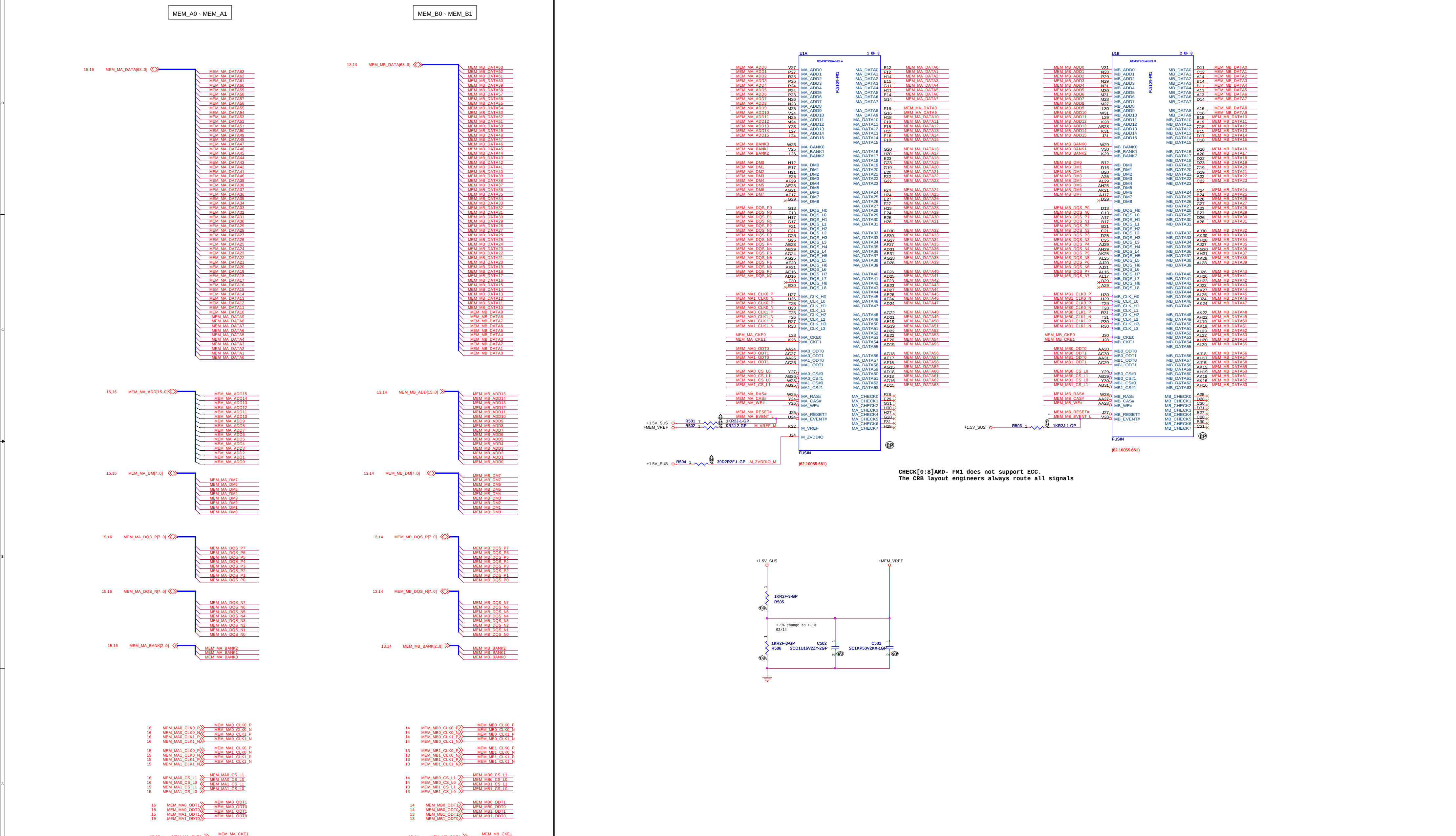
Hsichih, Taipei Hsien

Title: **APU PCIE**

Size: B | Document Number: **Parker** | Rev: -1

Date: Thursday, July 14, 2011 | Sheet 4 of 99



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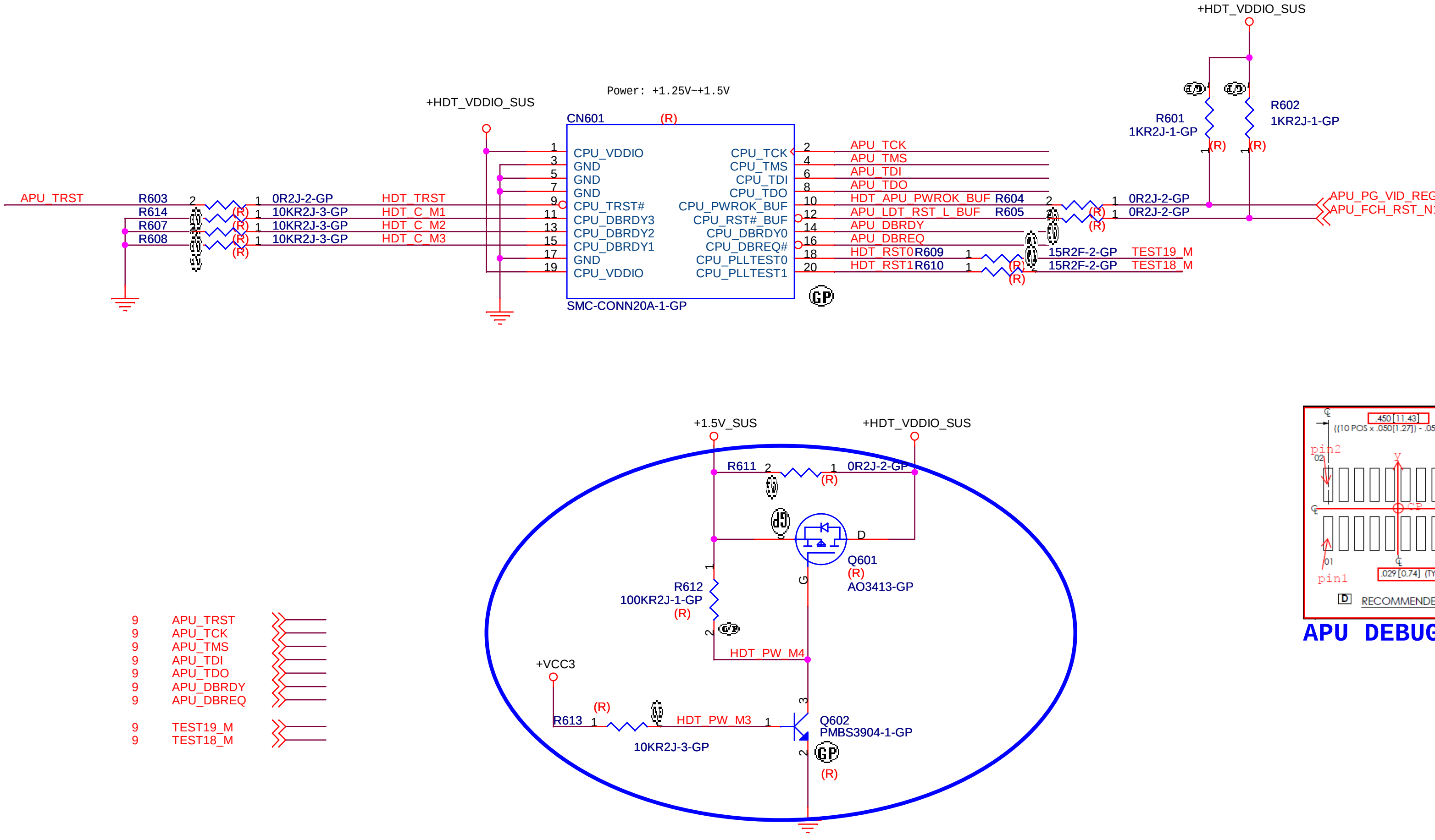
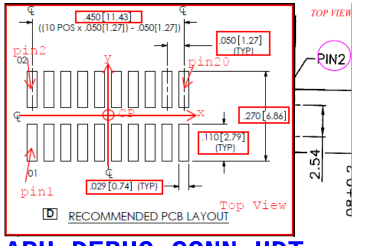
Hsichih, Taipei Hsien

Title	APU FM1 DDR3	
Size D	Document Number Parker	Rev -1
Date: Thursday, July 14, 2011	Sheet 5 of 99	

Debug Header





APU DEBUG CONN HDT

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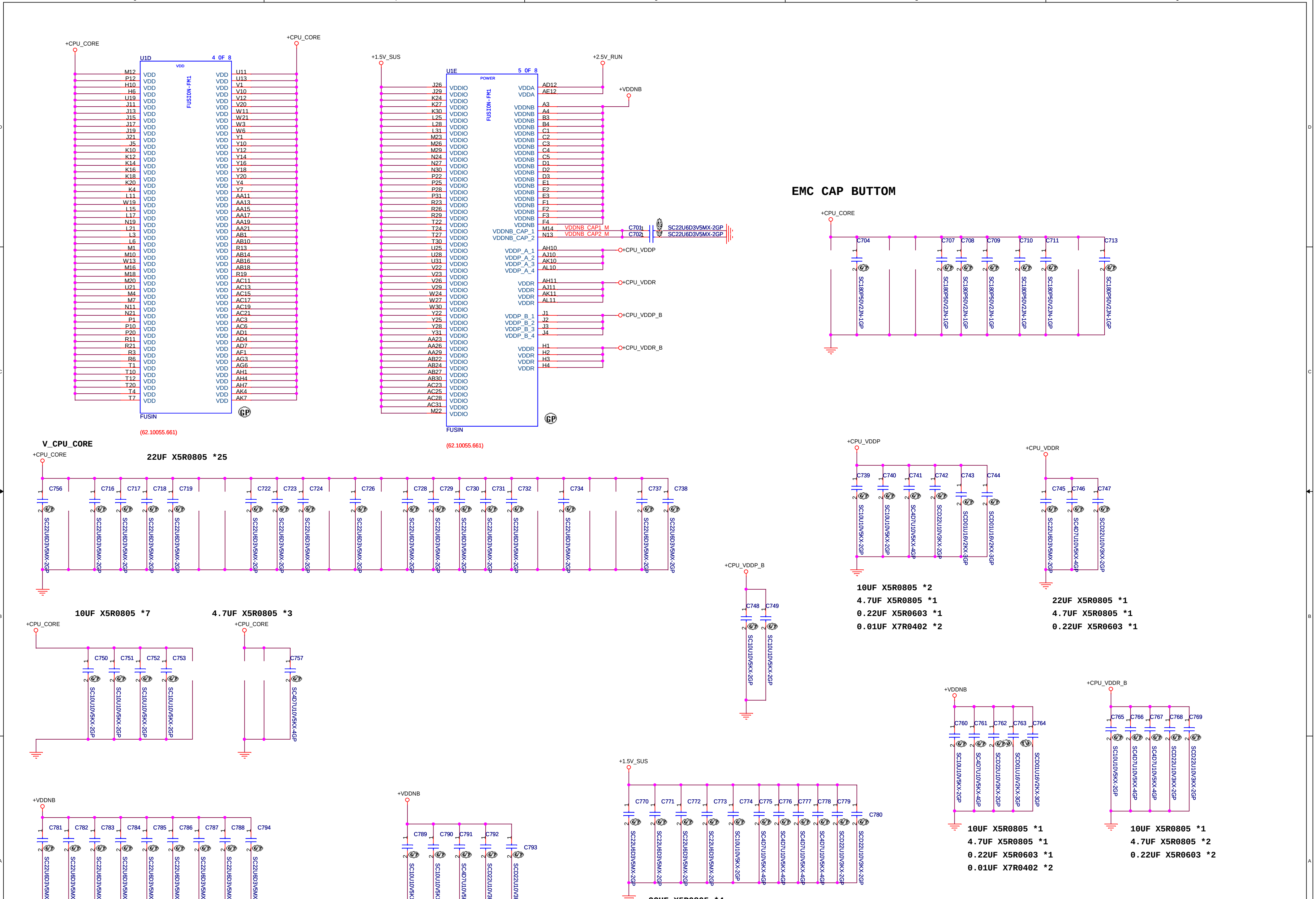
Title	APU Control & Debug	
Size B	Document Number Parker	Rev -1
Date: Thursday, July 14, 2011	Sheet 6 of 99	



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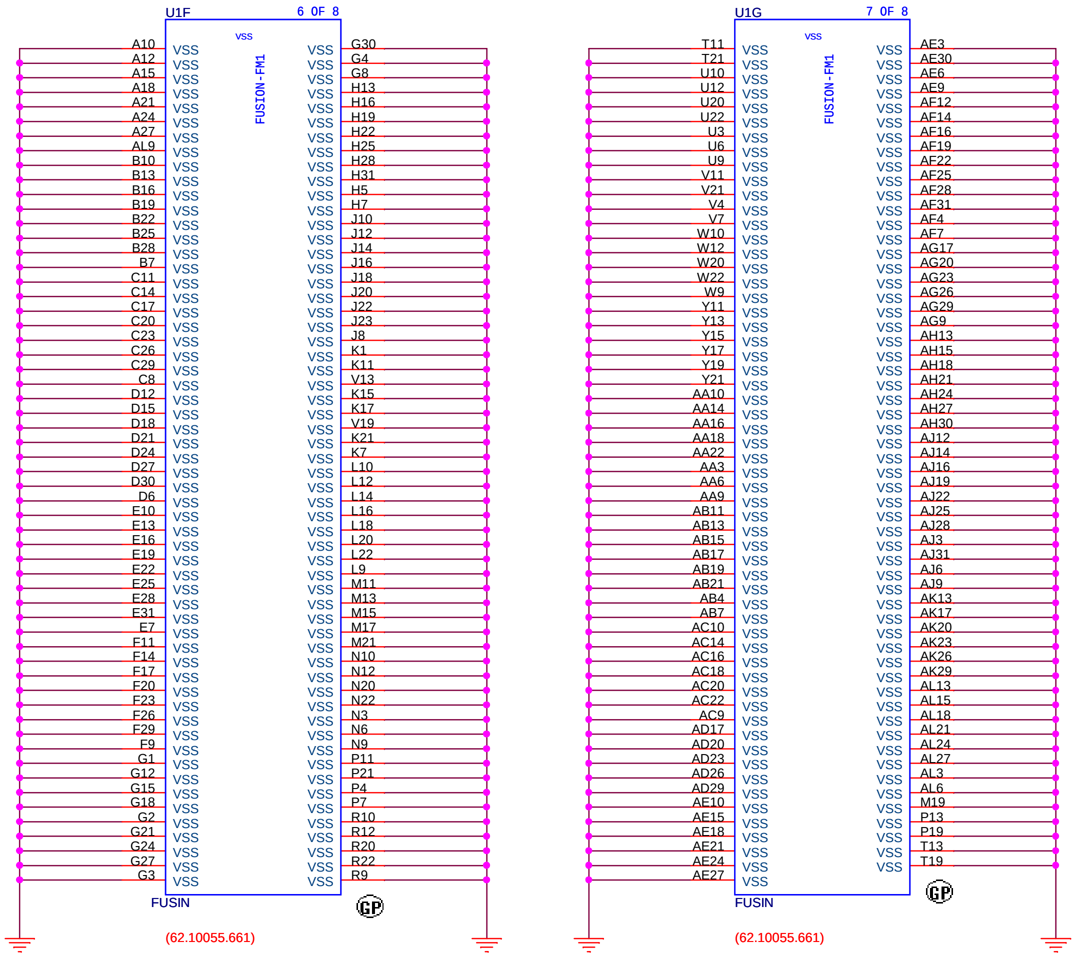
Title	APU POWER	
Size C	Document Number Parker	Rev -1
Date: Thursday, July 14, 2011	Sheet 7 of 99	



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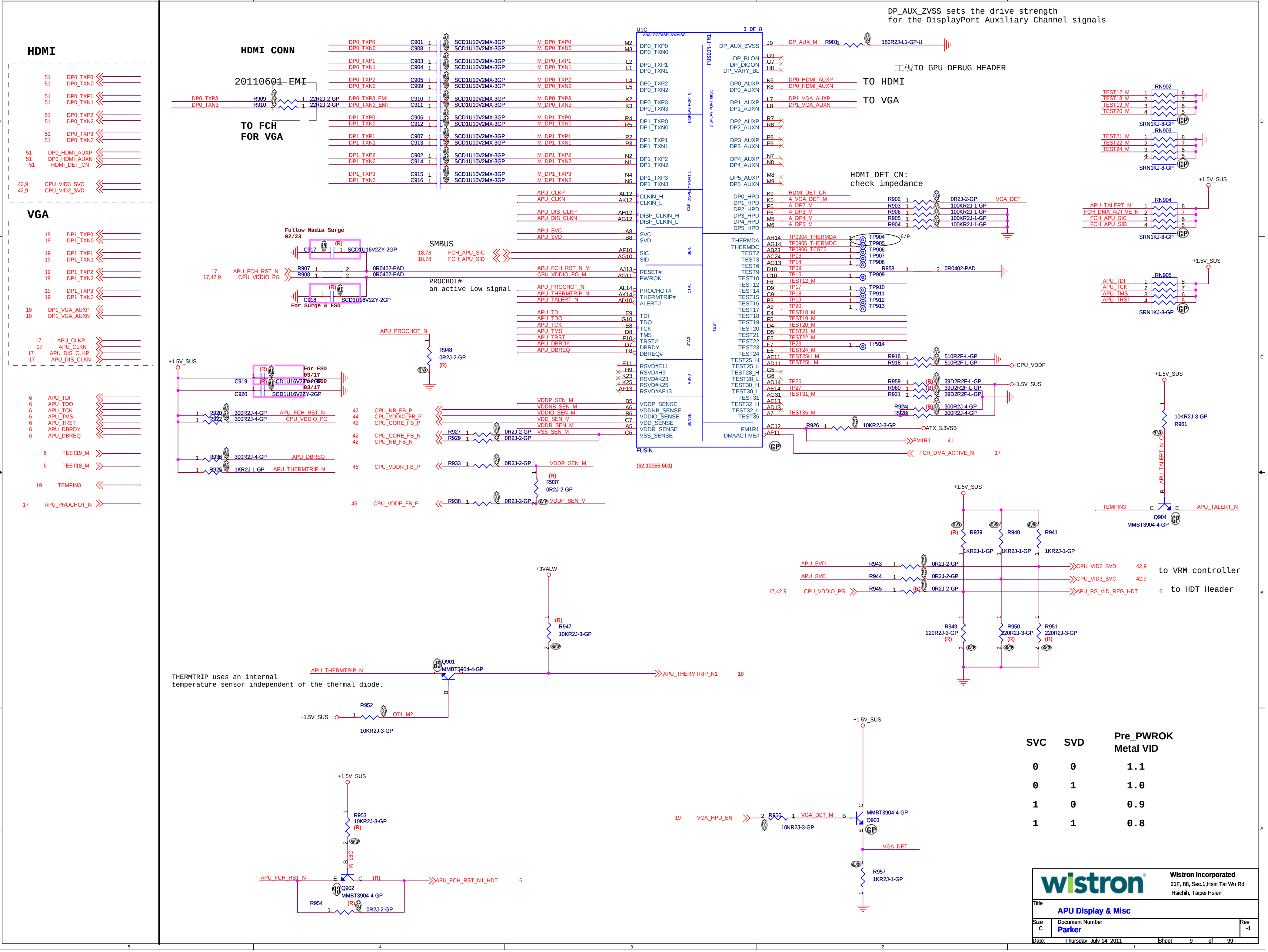
Title	APU VSS	
Size: B	Document Number: Parker	Rev: -1
Date: Thursday, July 14, 2011	Sheet 8 of 99	



SVC	SVD	Pre_PWROK Metal VID
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

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Hsichih, Taipei Hsien

Title: APU Display & Misc

Size: C | Document Number: Parker | Rev: -1

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TBD

5 4 3 2 1

D

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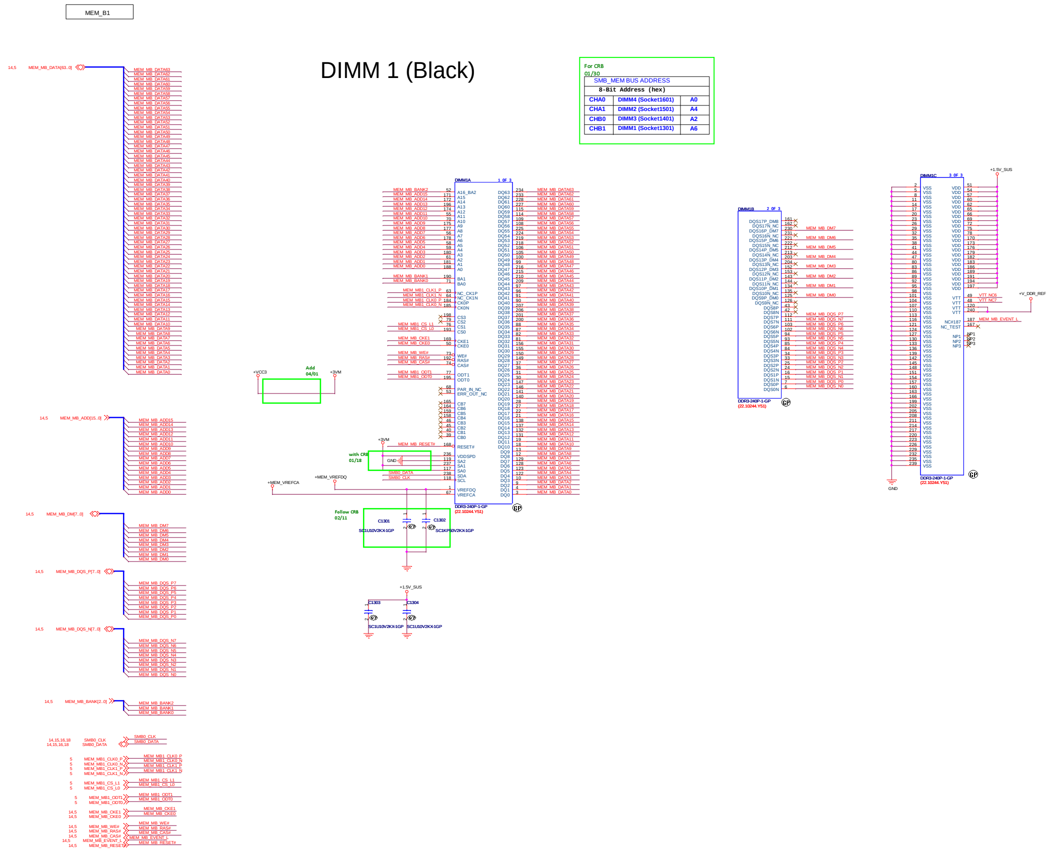
TBD

DIMM 1 (Black)

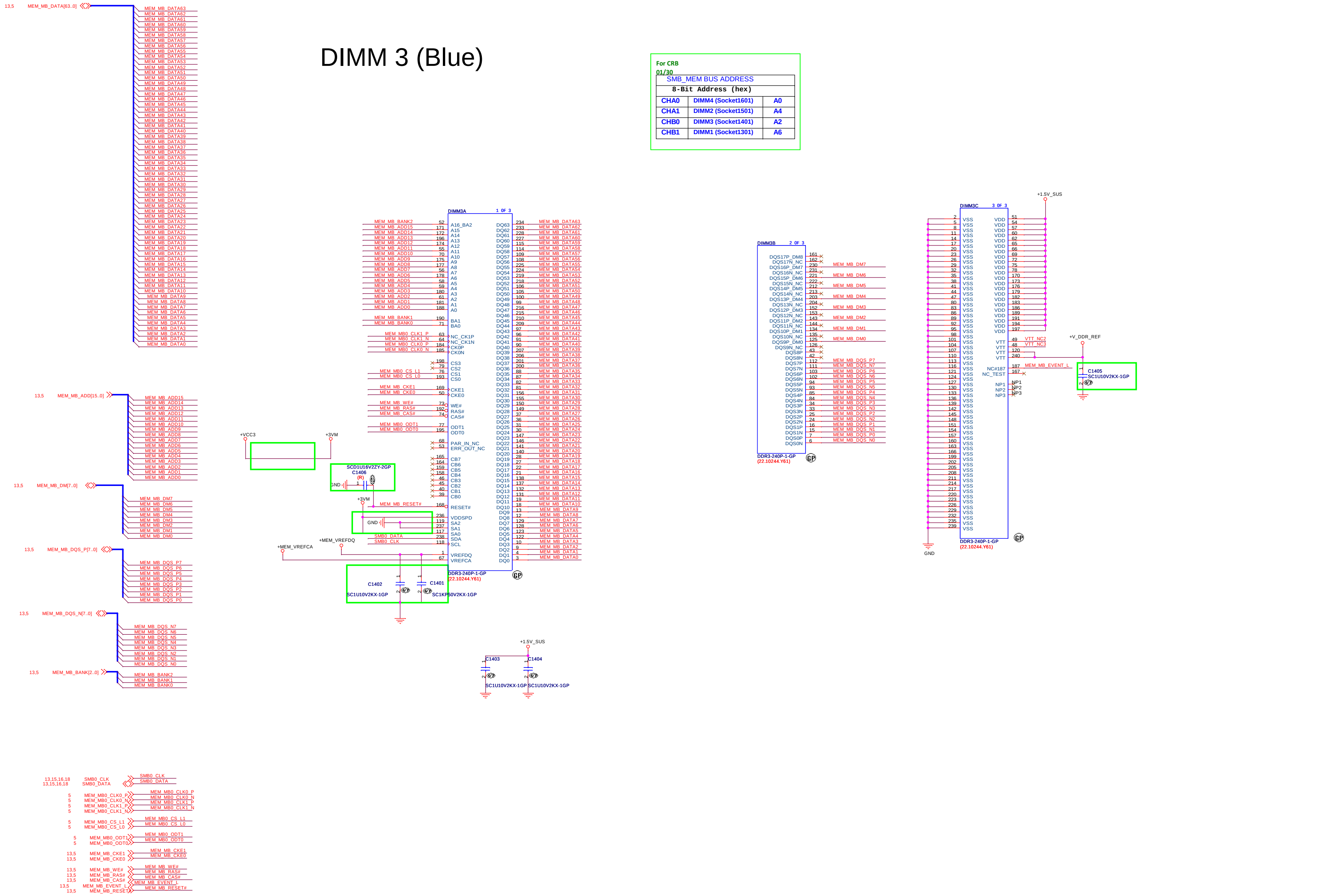


For CRB
01/30

SMB_MEM BUS ADDRESS		
8-Bit Address (hex)		
CHA0	DIMM4 (Socket1601)	A0
CHA1	DIMM2 (Socket1501)	A4
CHB0	DIMM3 (Socket1401)	A2
CHB1	DIMM1 (Socket1301)	A6

MEM_B0

DIMM 3 (Blue)



For CRB
01/30

SMB_MEM BUS ADDRESS		
8-Bit Address (hex)		
CHA0	DIMM4 (Socket1601)	A0
CHA1	DIMM2 (Socket1501)	A4
CHB0	DIMM3 (Socket1401)	A2
CHB1	DIMM1 (Socket1301)	A6

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Title	DDR3 DIMM0 Socket2	
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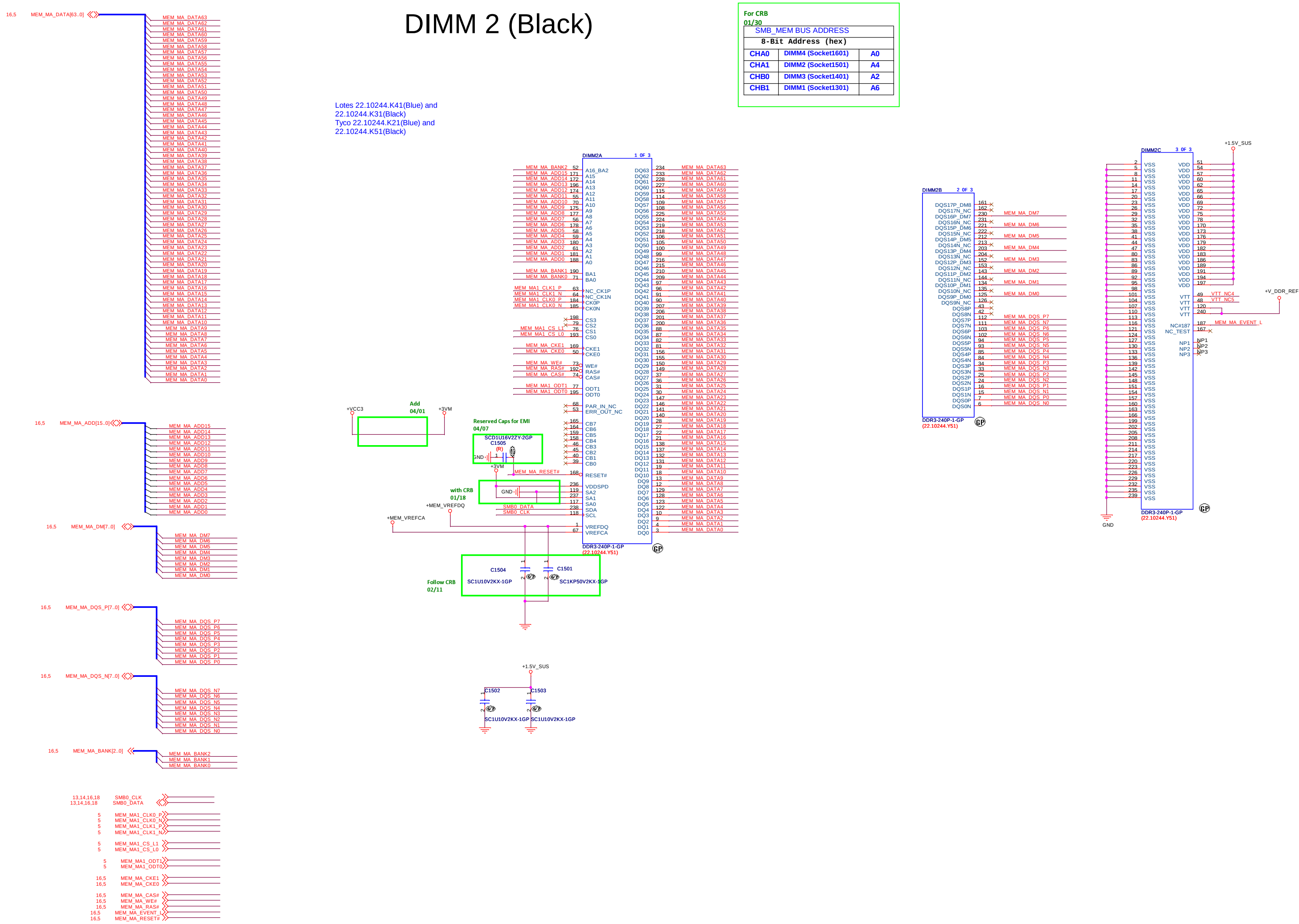
MEM_A1

DIMM 2 (Black)

Lotes 22.10244.K41(Blue) and
22.10244.K31(Black)
Tyco 22.10244.K21(Blue) and
22.10244.K51(Black)

For CRB
01/30

SMB_MEM BUS ADDRESS		
8-Bit Address (hex)		
CHA0	DIMM4 (Socket1601)	A0
CHA1	DIMM2 (Socket1501)	A4
CHB0	DIMM3 (Socket1401)	A2
CHB1	DIMM1 (Socket1301)	A6



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Title
DDR3 DIMM1 Socket3
Size D
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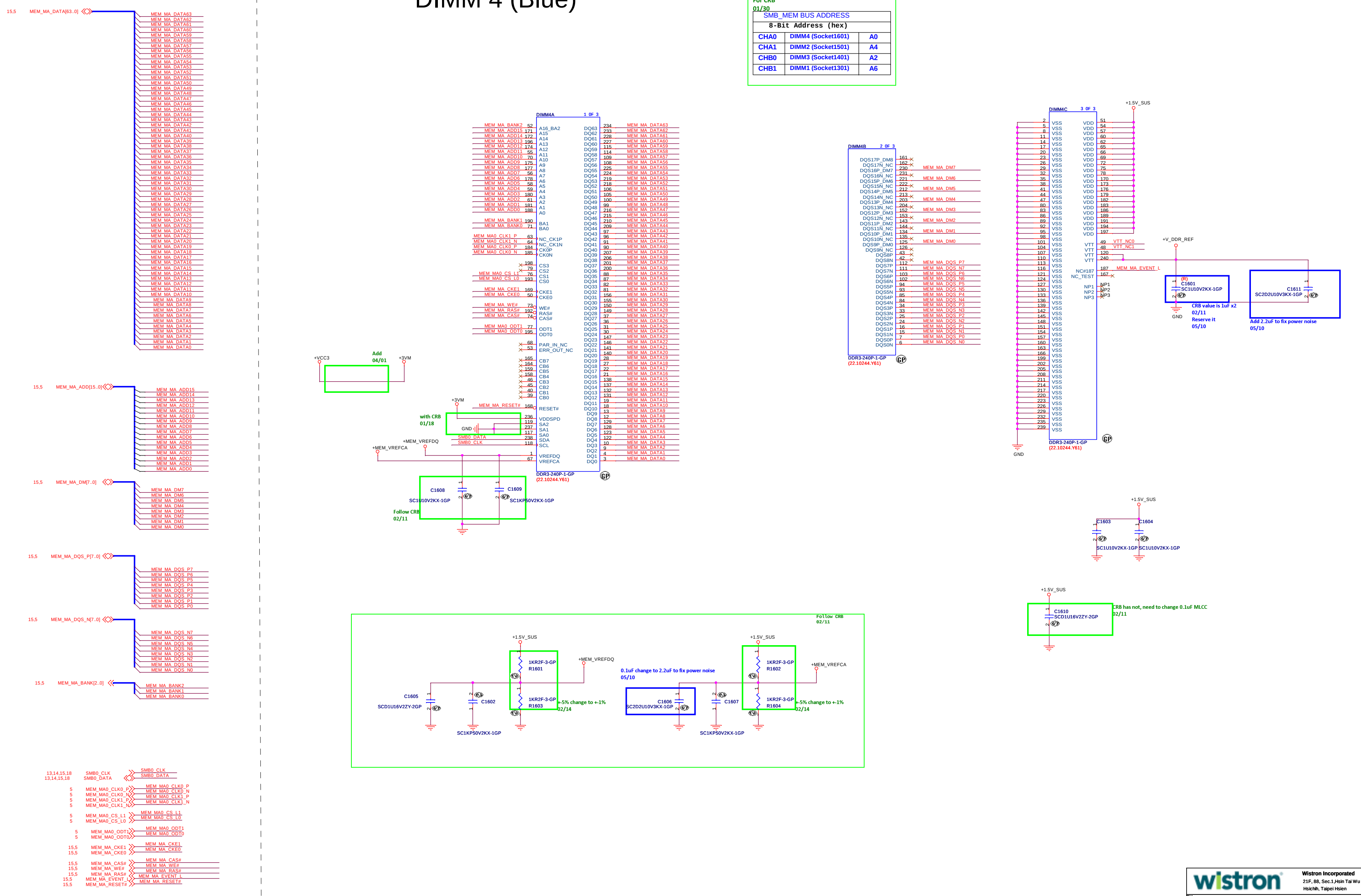
Sheet 15 of 99

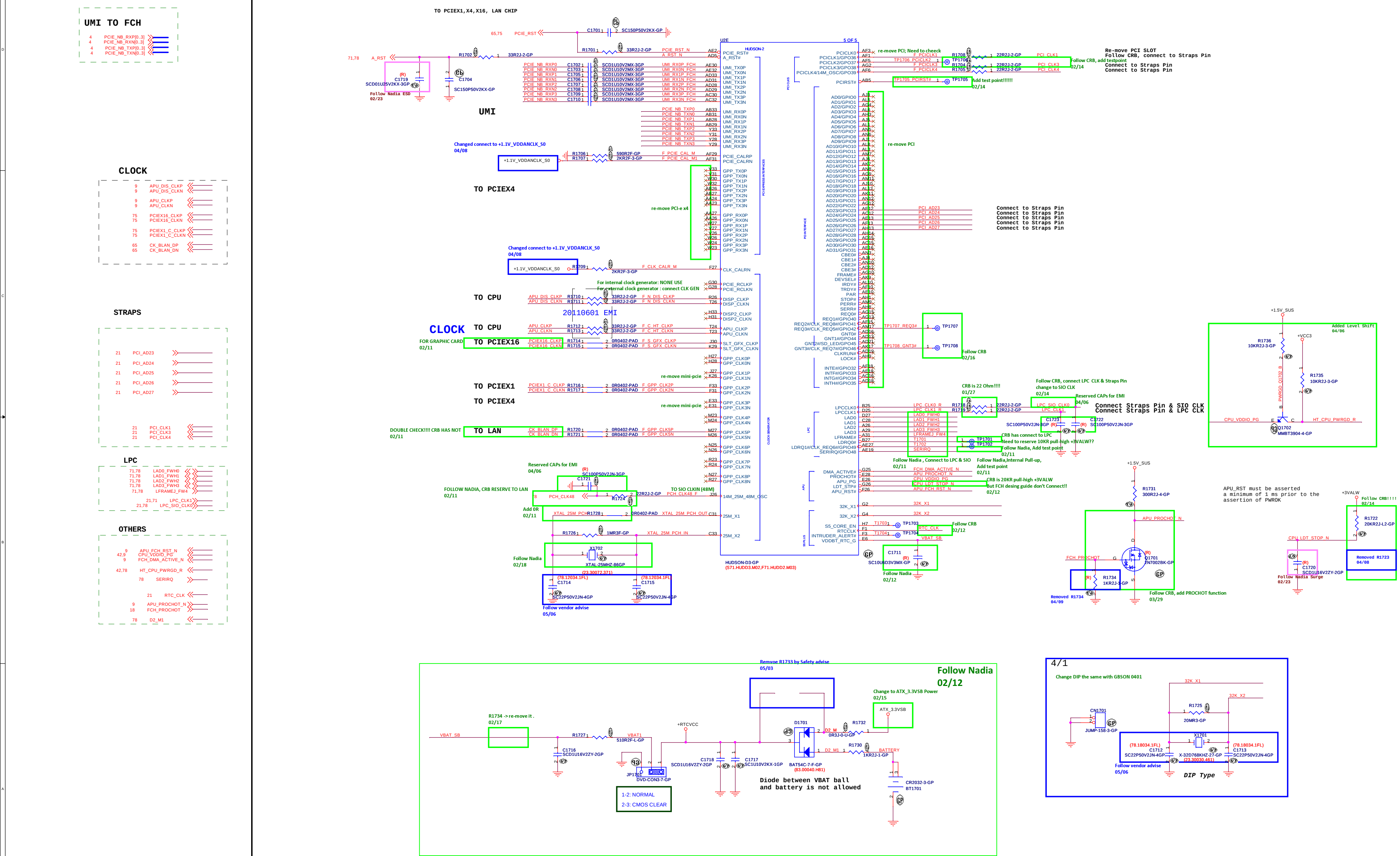
MEM_A0

DIMM 4 (Blue)

For CRB
01/30

SMB_MEM BUS ADDRESS		
8-Bit Address (hex)		
CHA0	DIMM4 (Socket1601)	A0
CHA1	DIMM2 (Socket1501)	A4
CHB0	DIMM3 (Socket1401)	A2
CHB1	DIMM1 (Socket1301)	A6





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Title	Hudson-D3 ACPI/PCI/CLK
Size: D	Document Number: Parker — Rev: -1
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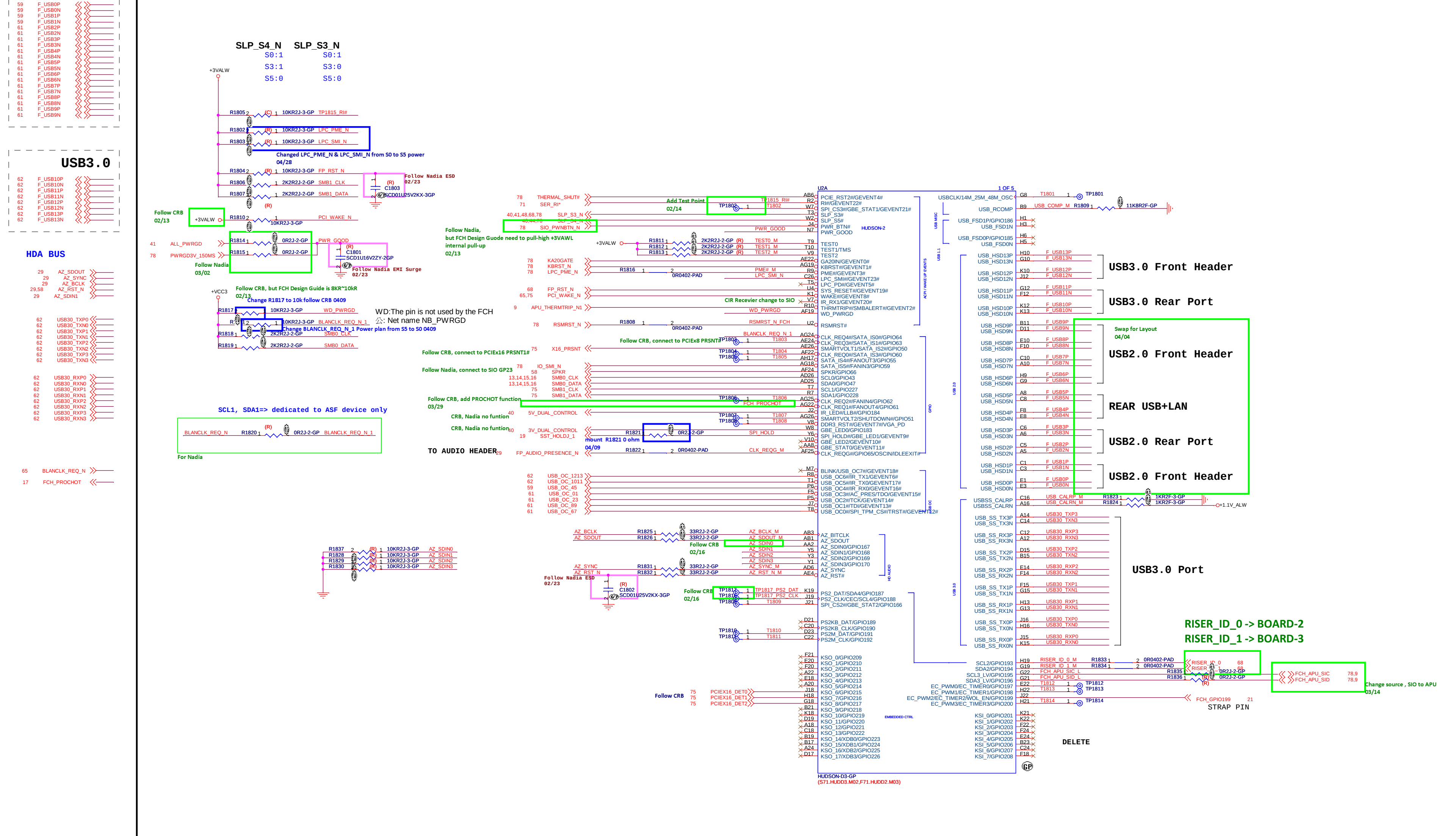
Table 59. Test Mode Signals

TEST2	TEST1	TEST0	Test Mode	Description
0	0	0	None	Normal operation
0	0	1	Reserved	Reserved for ASIC debug
0	1	X	Test Mode	Enable Test Mode
1	X	X	Reserved	Reserved for ASIC debug



PCIE adapter card ID

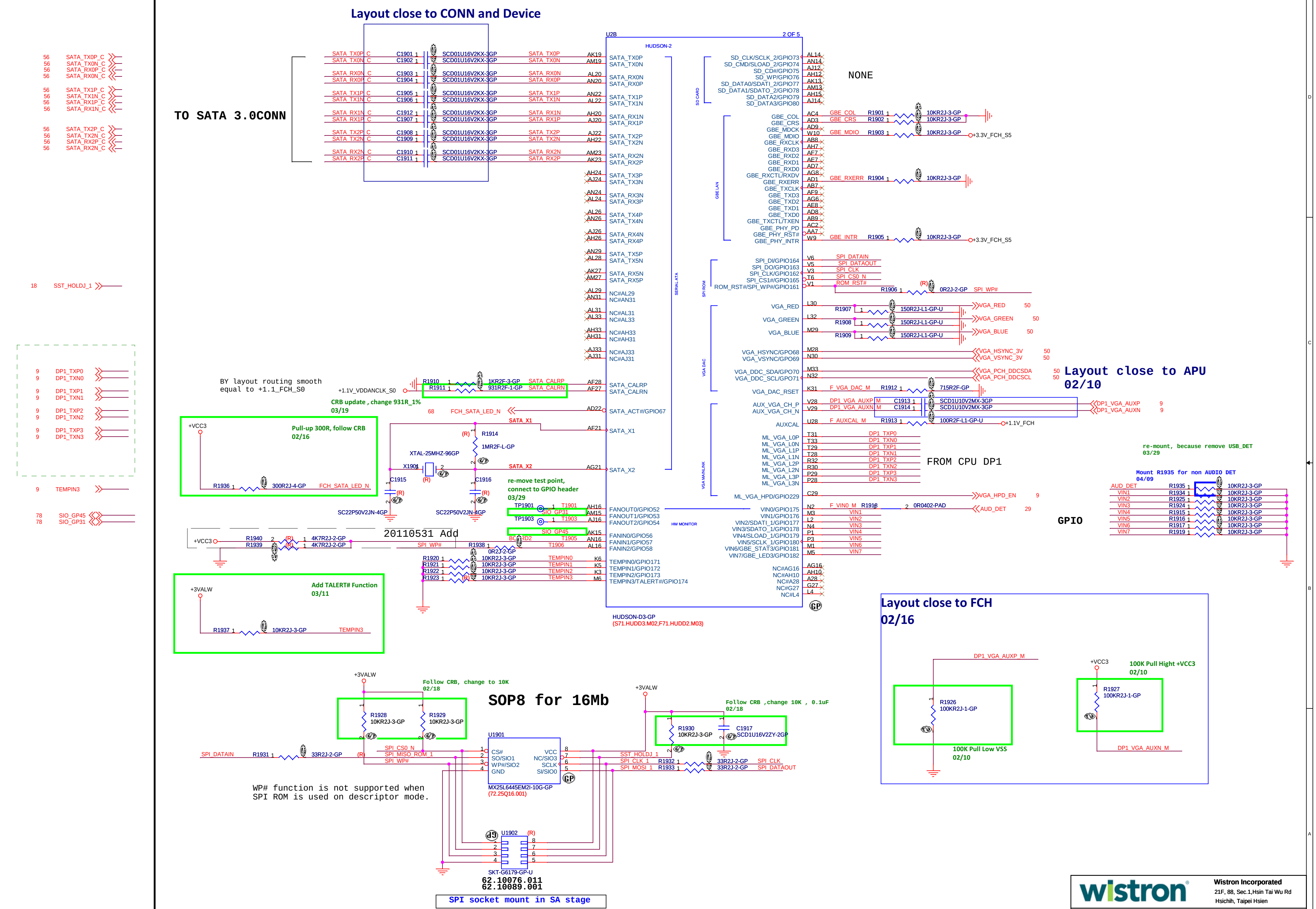
	DET2	DET1	DET0	TBD
A	0	0	0	B962---PCIEx16 to 4x4PCIE
B	0	0	1	B964---PCIEx16 to 2x8PCIE
C	0	1	0	B966---PCIEx16 to Upperx8 PCIE+2x4 PCIE
D	0	1	1	B967---PCIEx16 to Lowerx8 PCIE+2x4 PCIE
E	1	0	0	TBD
F	1	0	1	TBD
G	1	1	0	TBD
H	1	1	1	GFX VGA Card

BOARD CONFIG ID

	C_ID1	C_ID0	TBD
A	0	0	TBD
B	0	1	TBD
C	1	0	TBD
D	1	1	TBD

BOARD REV ID

	B_ID2	B_ID1	B_ID0
A	0	0	0
B	0	0	1
C	0	1	0
D	0	1	1
E	1	0	0
F	1	0	1
G	1	1	0
H	1	1	1



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Title	Hudson-D3 SATA/HWM/SPI		
Size C	Document Number Parker		Rev -1
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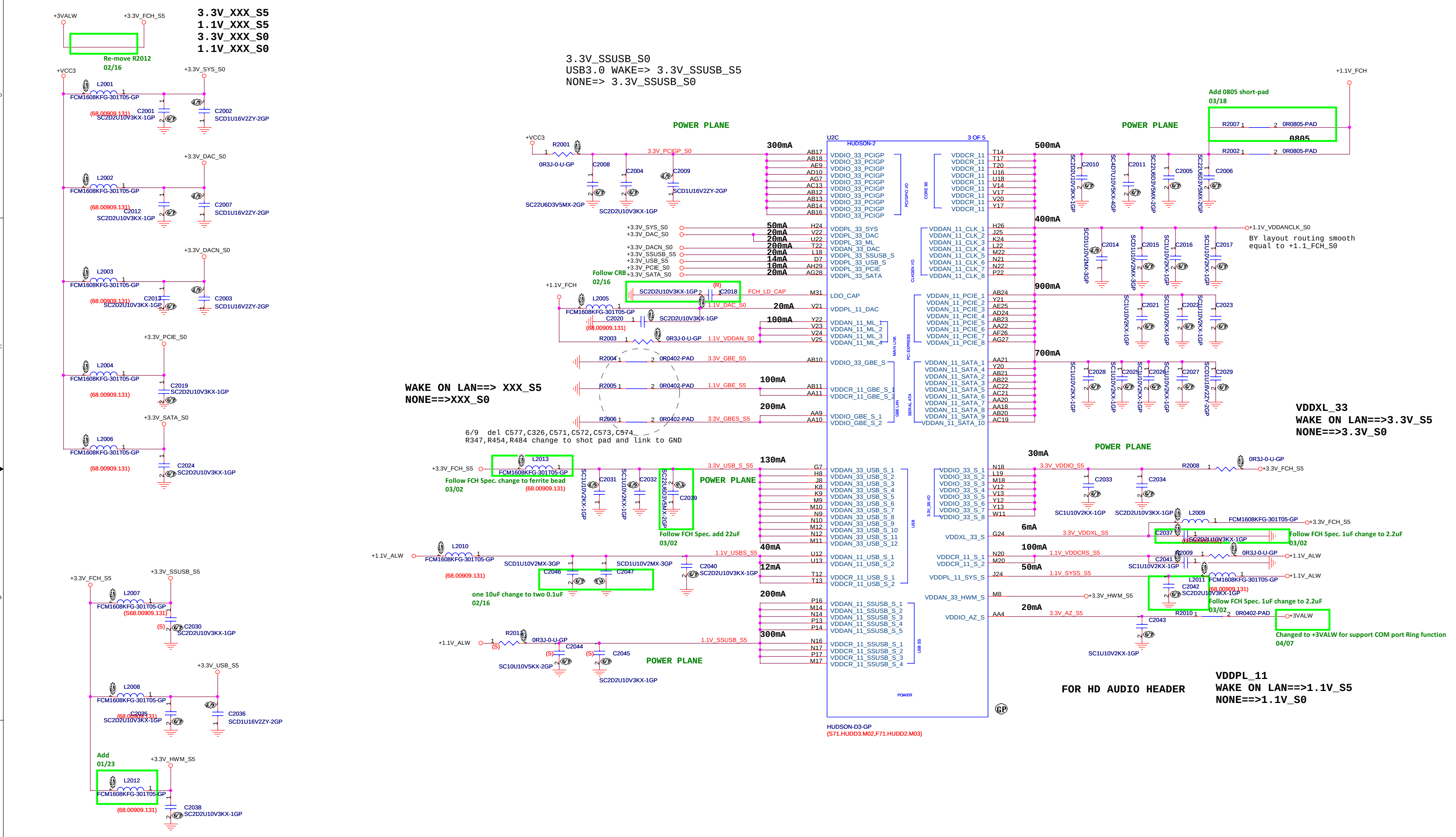

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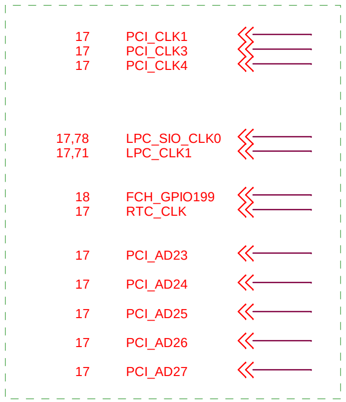
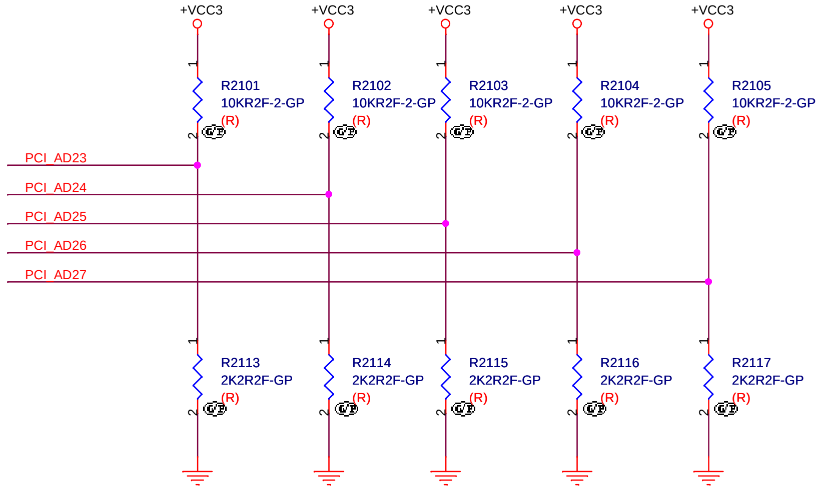
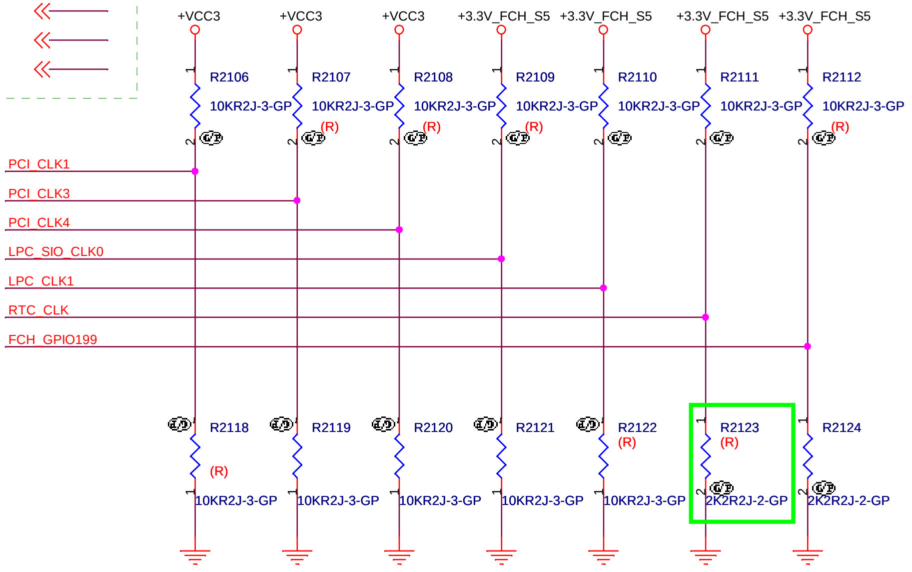
Title	Hudson-D3 POWER	
Size: Custom	Document Number: Parker	Rev: -1
Date: Tuesday, June 07, 2011	Sheet 20 of 99	



FCH DEBUG STRAPS



FCH REQUIRES STRAPS



Follow CRB, change to 2.2K
02/21

	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	RESERVED	Normal Refclk termination DEFAULT	USE RESERVED PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCIPLL	RESERVED	Inverted Refclk termination	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	RTC_CLK	GPIO199
PULL HIGH	ALLOW PCIE GEN2 DEFAULT	USE DEBUG STRAPS	NON-FUSION CLOCK MODE	EC ENABLED	CLKGEN ENABLED DEFAULT	S5+DISABLE DEFAULT	ROM TYPE: H =LPC ROM
PULL LOW	FORCE PCIE GEN1	IGNORE DEBUG STRAPS DEFAULT	FUSION APU CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLED	S5+ENABLE	L = SPI ROM DEFAULT

Just EC Enable is different from AMD CRB
AMD: enable

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Title: Hudson-D3 STRAPPINGS

Size: B | Document Number: Parker | Rev: -1

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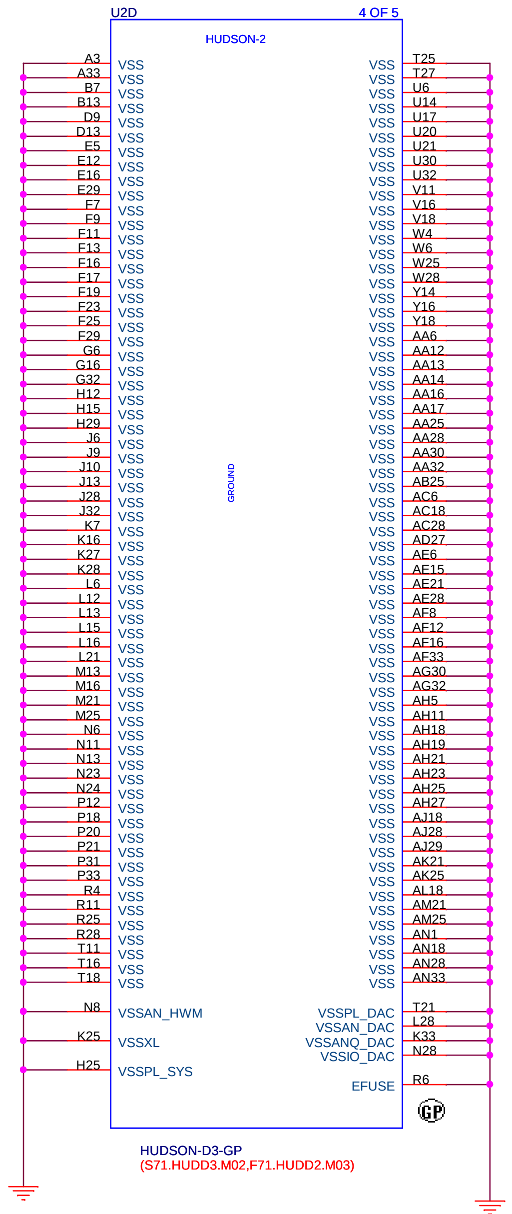

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Title	Hudson-D3 VSS	
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Title: Reserve			
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TBD

wistron	Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title	Reserve		
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TBD

wistron		**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title Reserve		
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Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: Reserve			
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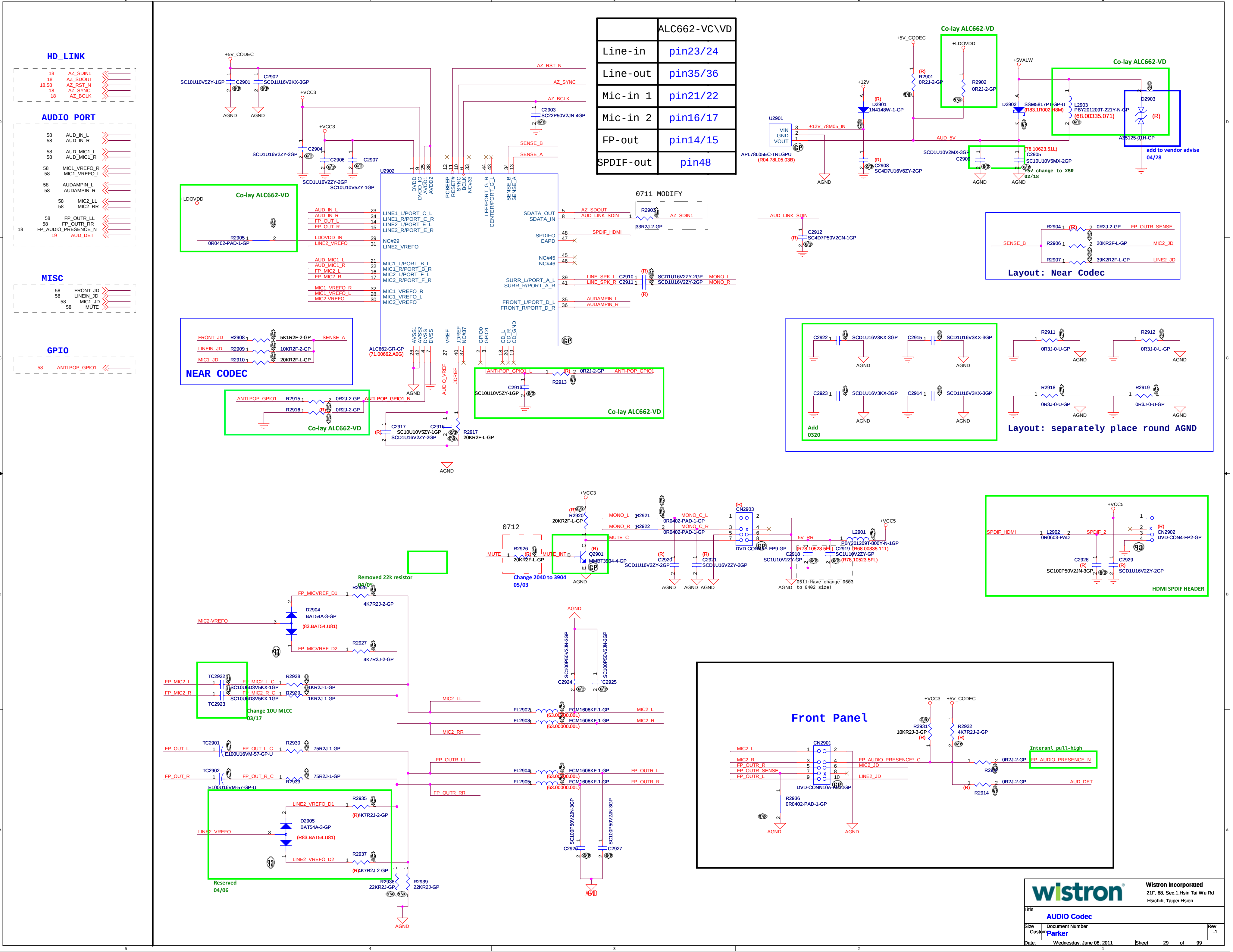
TBD

TBD



	ALC662-VC\VD
Line-in	pin23/24
Line-out	pin35/36
Mic-in 1	pin21/22
Mic-in 2	pin16/17
FP-out	pin14/15
SPDIF-out	pin48

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Hsichih, Taipei Hsien

Title: AUDIO Codec

Size: Custom | Document Number: Parker | Rev: -1

Date: Wednesday, June 08, 2011 | Sheet 29 of 99

5 4 3 2 1

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TBD

wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title Audio AMP			
Size B	Document Number Parker		Rev -1
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5 4 3 2 1

TBD

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title: Card Reader		
Size: B	Document Number: Parker	Rev: -1
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TBD

wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title 1394			
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5 4 3 2 1

D

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A

TBD

5 4 3 2 1

TBD

wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title	USB3.0 Solution	
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TBD

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Hsichih, Taipei

Title	Power plan Enable	
Size B	Document Number Parker	Rev -1
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TBD

TBD

wistron		Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title	DCIN Jack	
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TBD

wistron		**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title	BATT CONN/DOCK DCIN	
Size B	Document Number Parker	Rev -1
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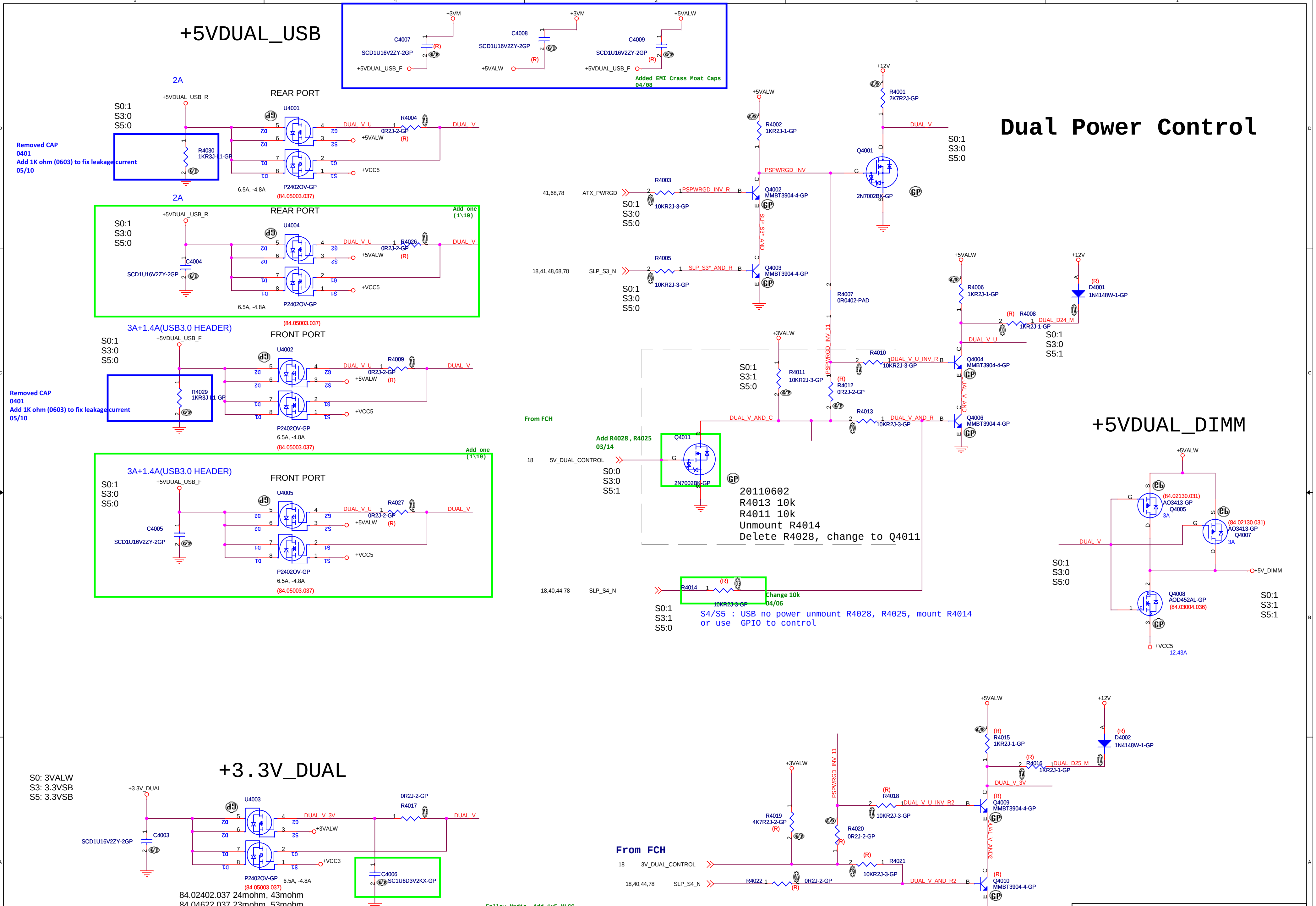

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Dual Power	
Size C	Document Number Parker	Rev -1
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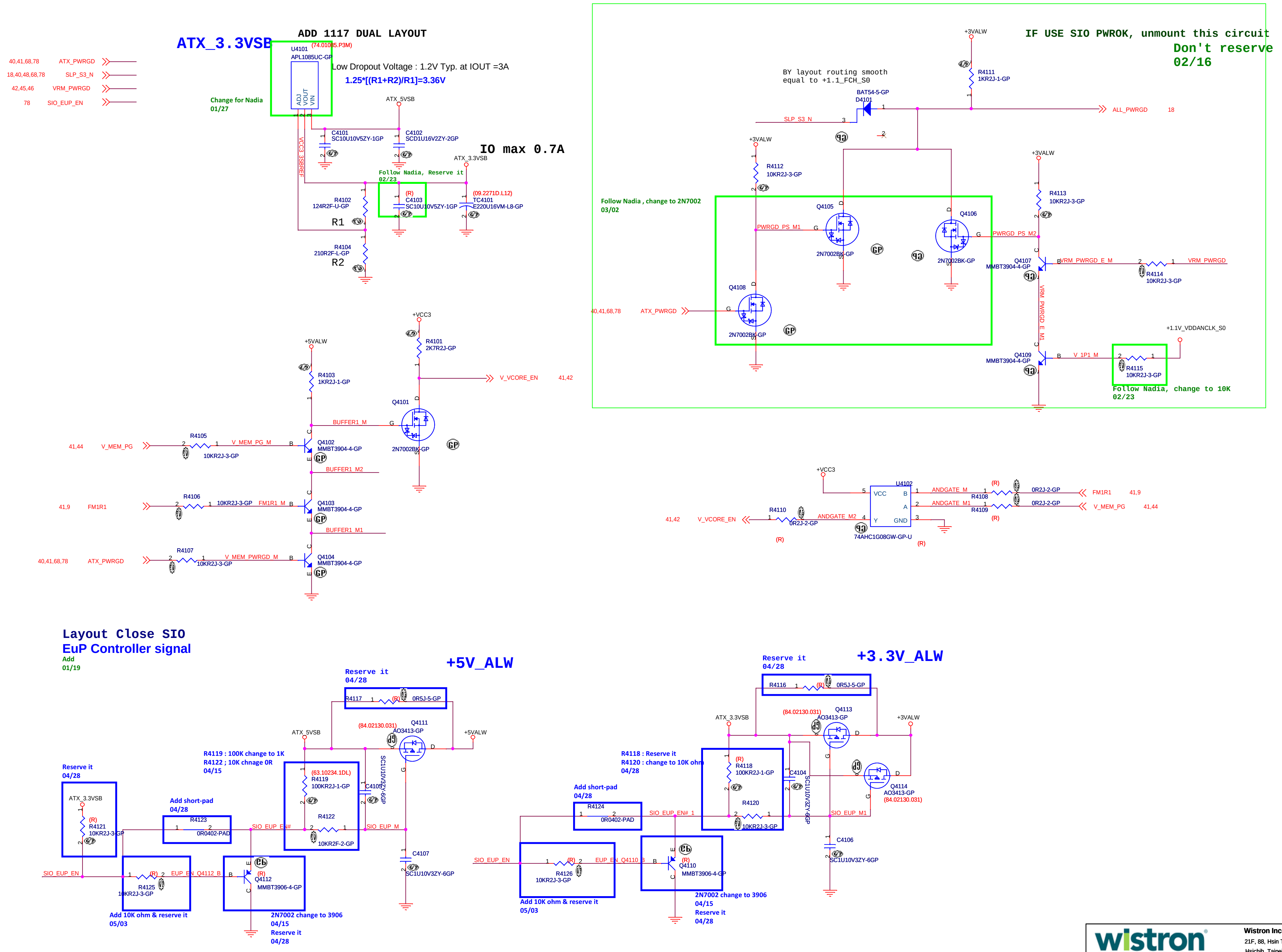

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21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	+3.3V_ALW & +5V_ALW	
Size C	Document Number Parker	Rev -1
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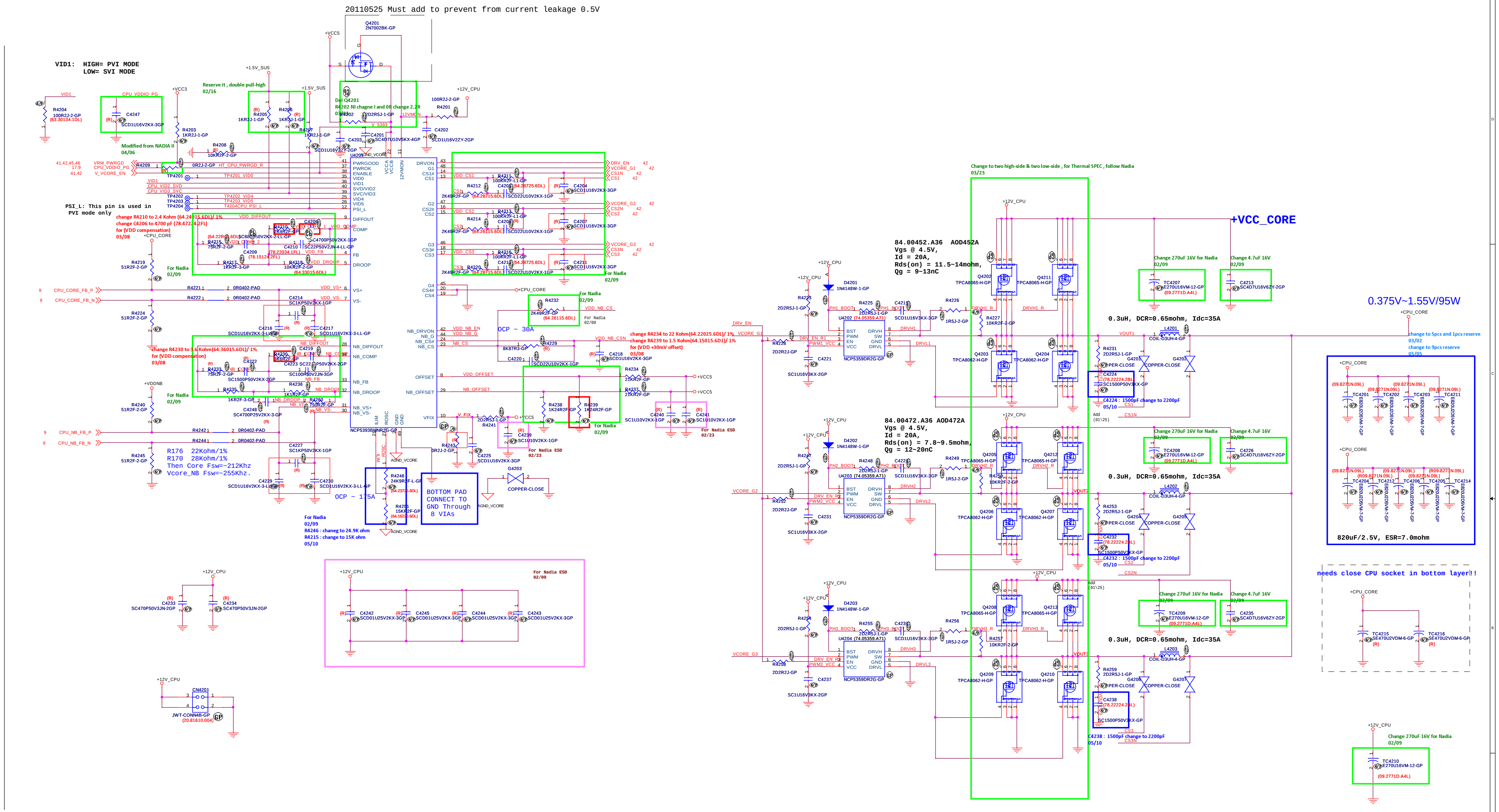

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	+VCC_CORE
Size	D
Document Number	Parker
Rev	-1
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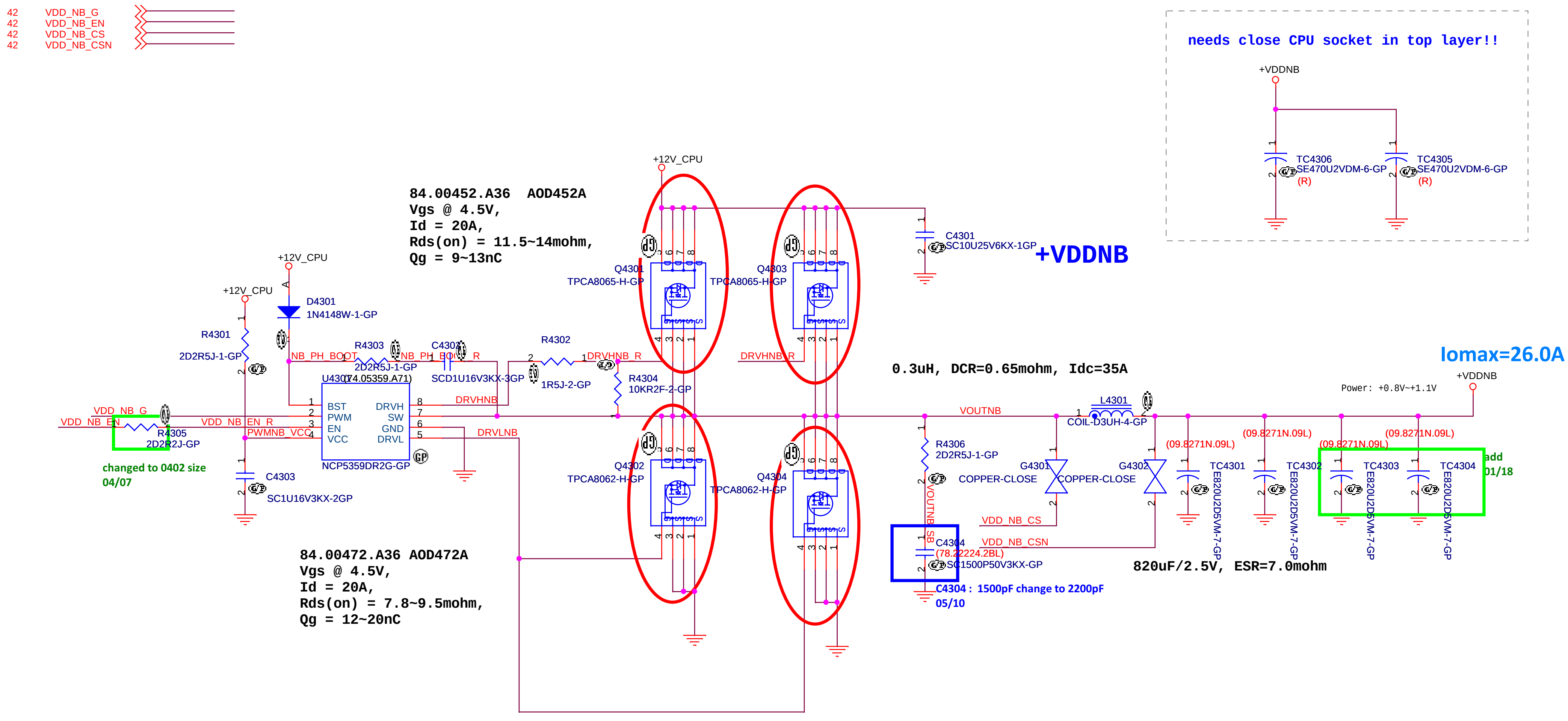

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Hsichih, Taipei

Title	+VDDNB	
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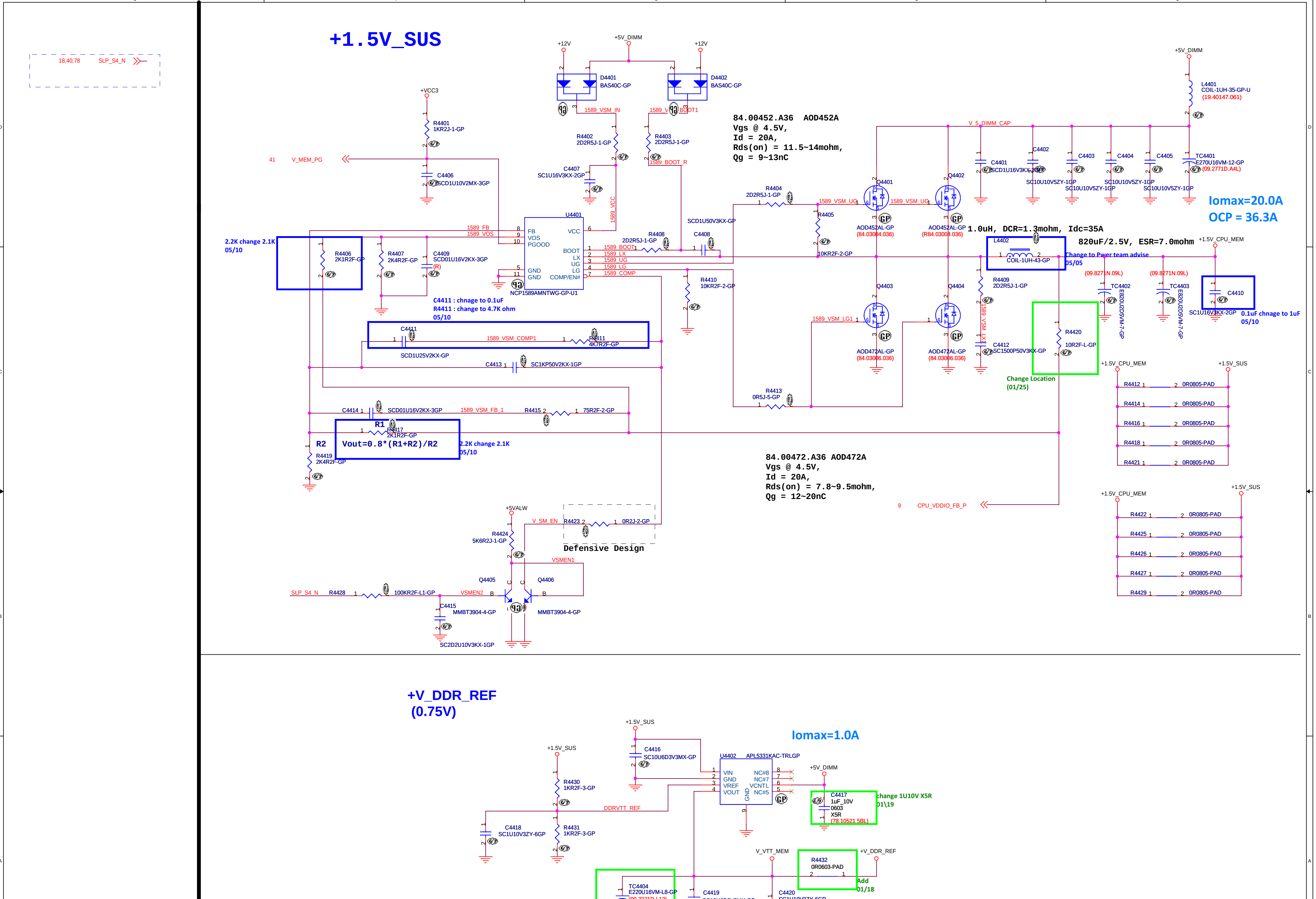

wistron

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	+1.5V_SUS & +V_DDR_REF	
Size C	Document Number Parker	Rev -1
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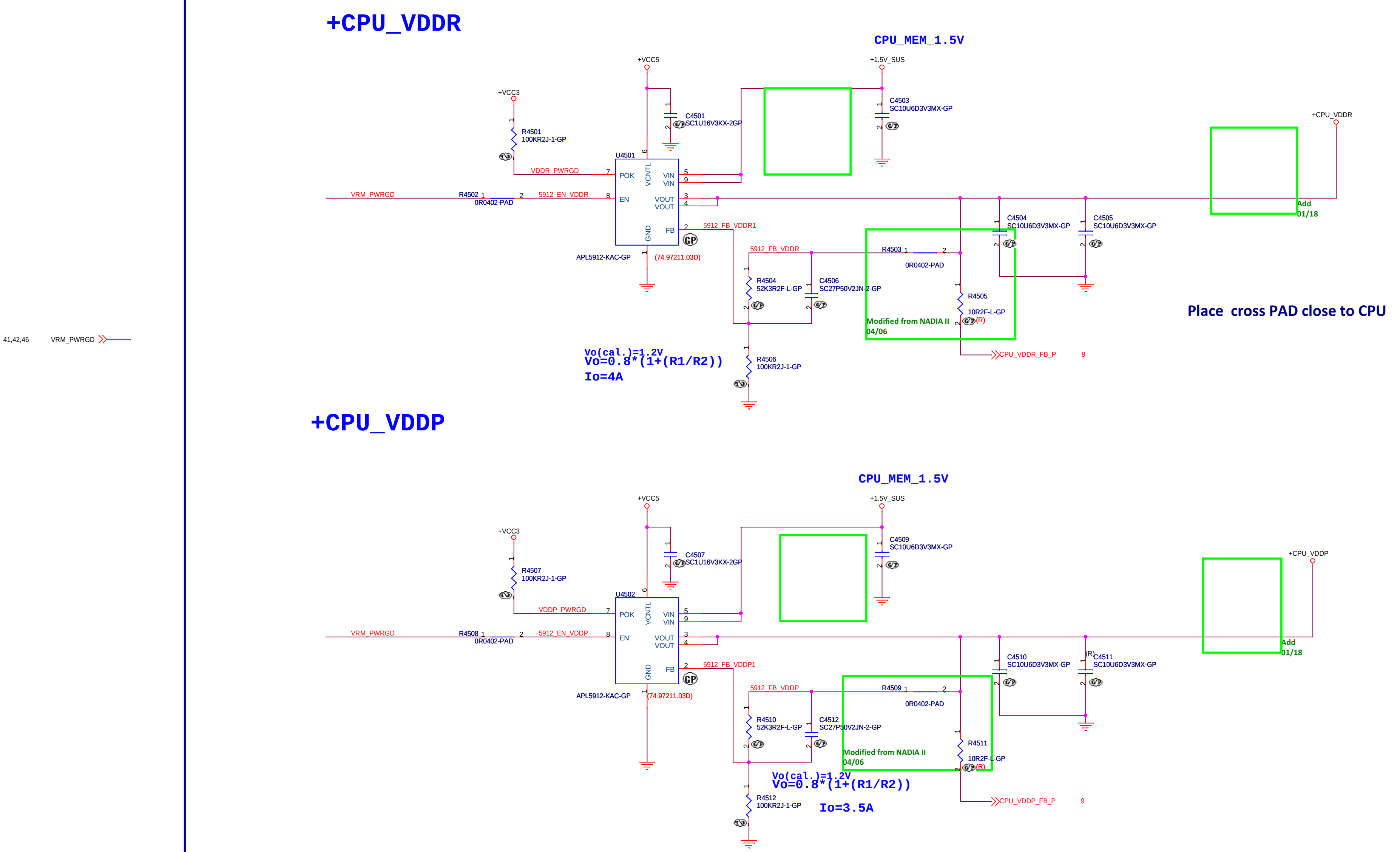
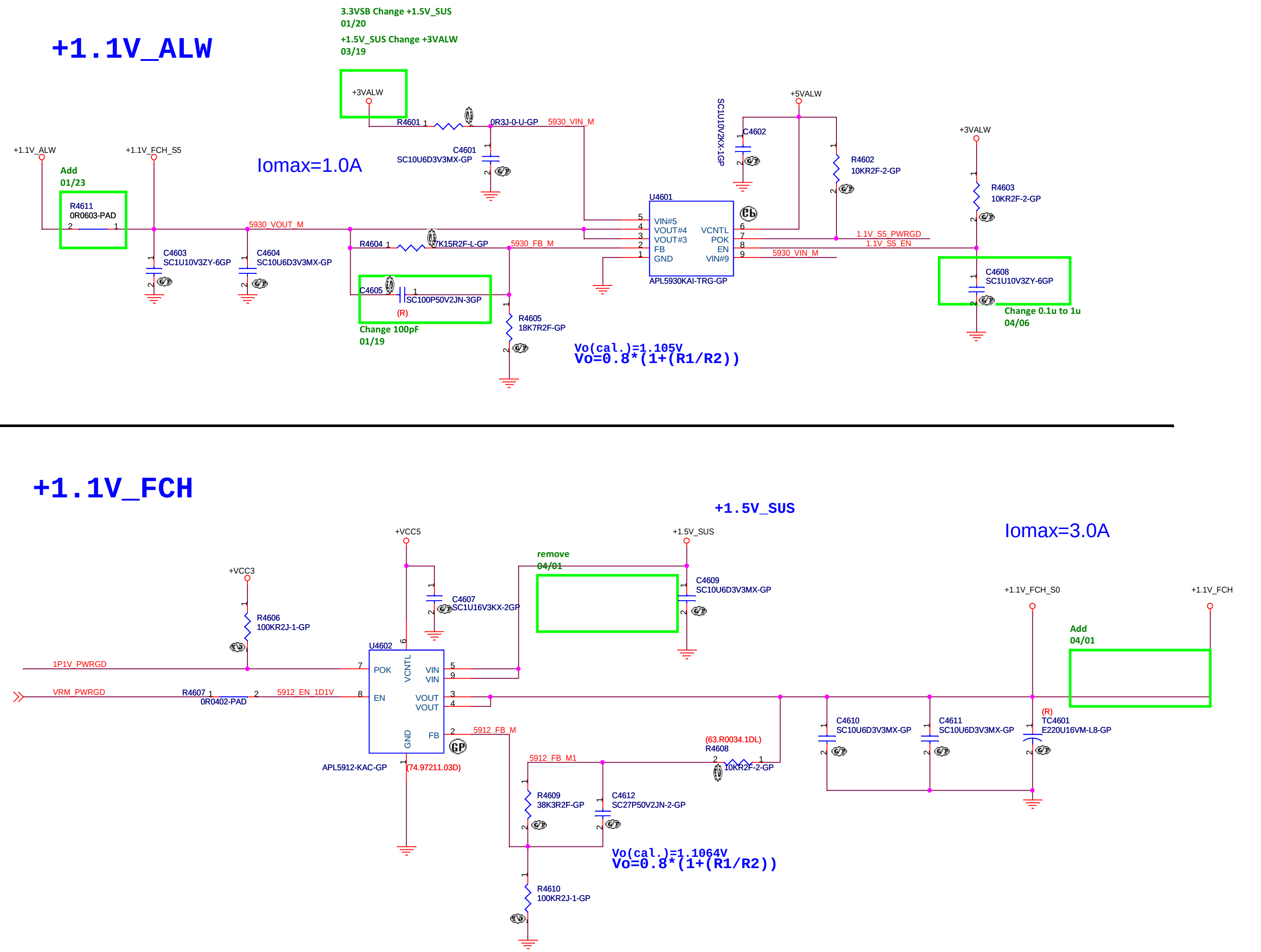


wistron	Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title	+VDDR & +VDDP	
Size C	Document Number Parker	Rev -1
Date: Tuesday, June 07, 2011	Sheet 45 of 99	



wistron

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	+1.1V_ALW/+1.2V_ALW	
Size C	Document Number Parker	Rev -1
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Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	LDO : +1.8V_RUN	
Size B	Document Number Parker	Rev -1
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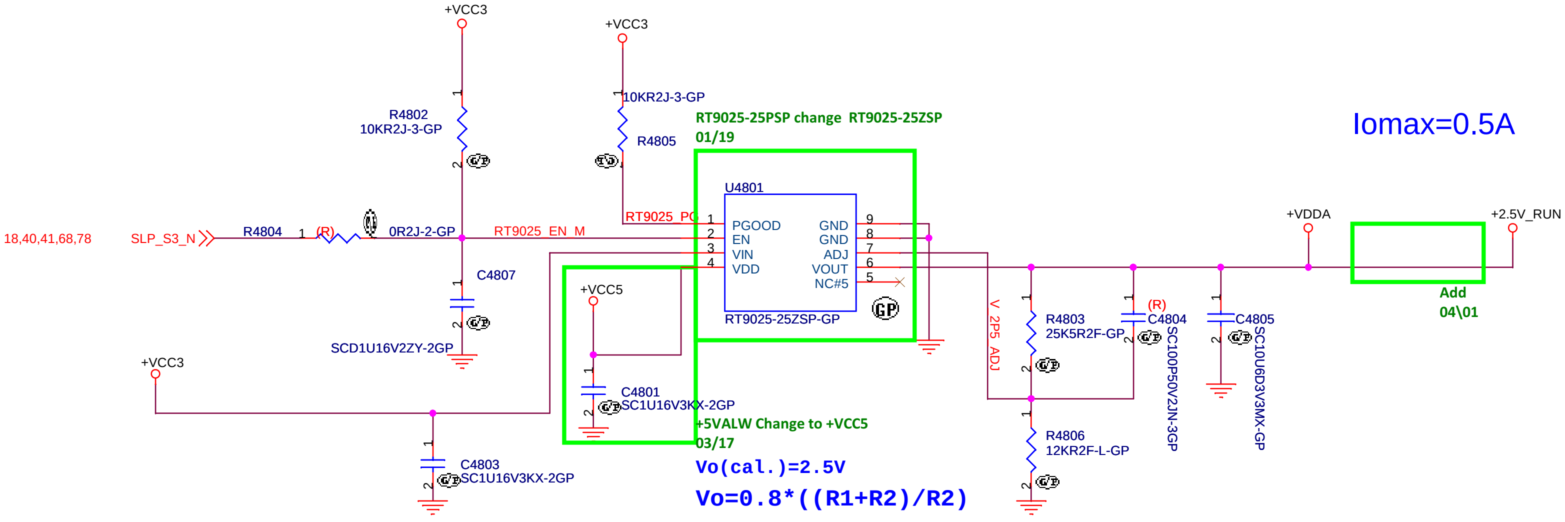
+2.5V_RUN



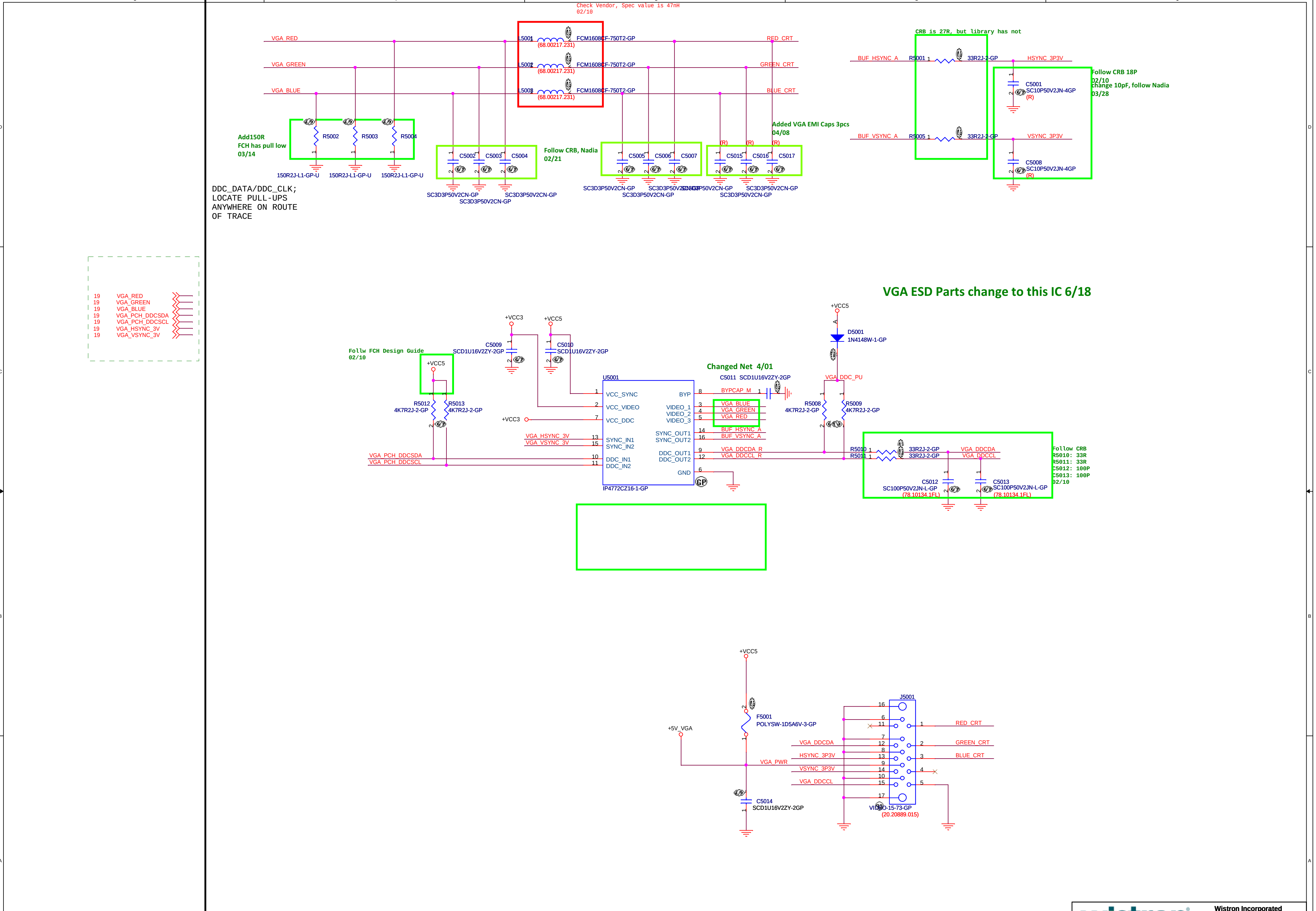
wistron

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	LDO : +2.5V_RUN & +1.5V_RUN	
Size B	Document Number Parker	Rev -1
Date: Friday, May 13, 2011	Sheet 48 of 99	

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title	LCD/Inverter Connector + Cam		
Size B	Document Number Parker		Rev -1
Date:	Friday, May 13, 2011	Sheet 49 of 99	



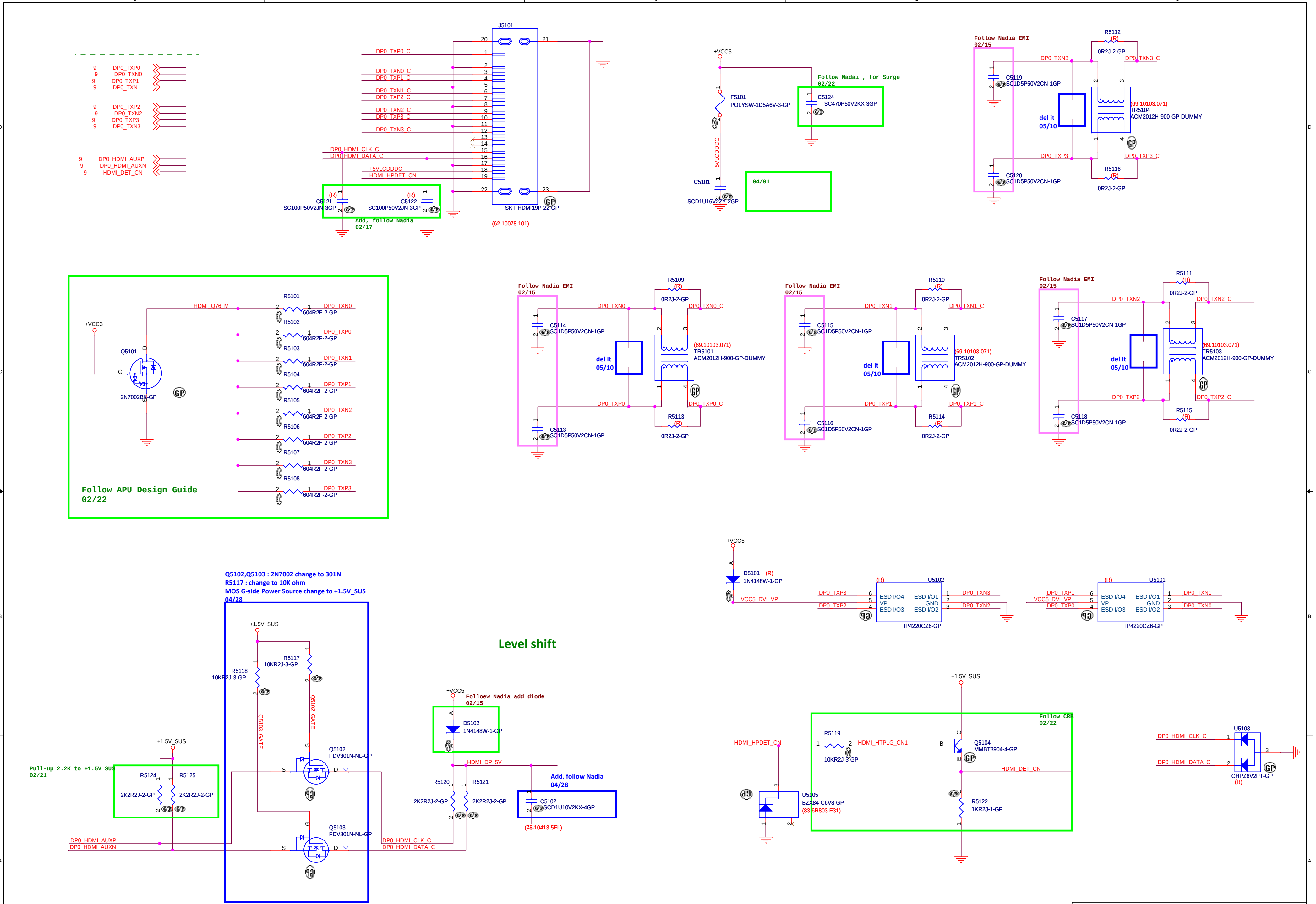
wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title: CRT Connector

Size: C | Document Number: Parker | Rev: -1

Date: Friday, May 13, 2011 | Sheet 50 of 99



wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	HDMI		
Size C	Document Number Parker		Rev -1
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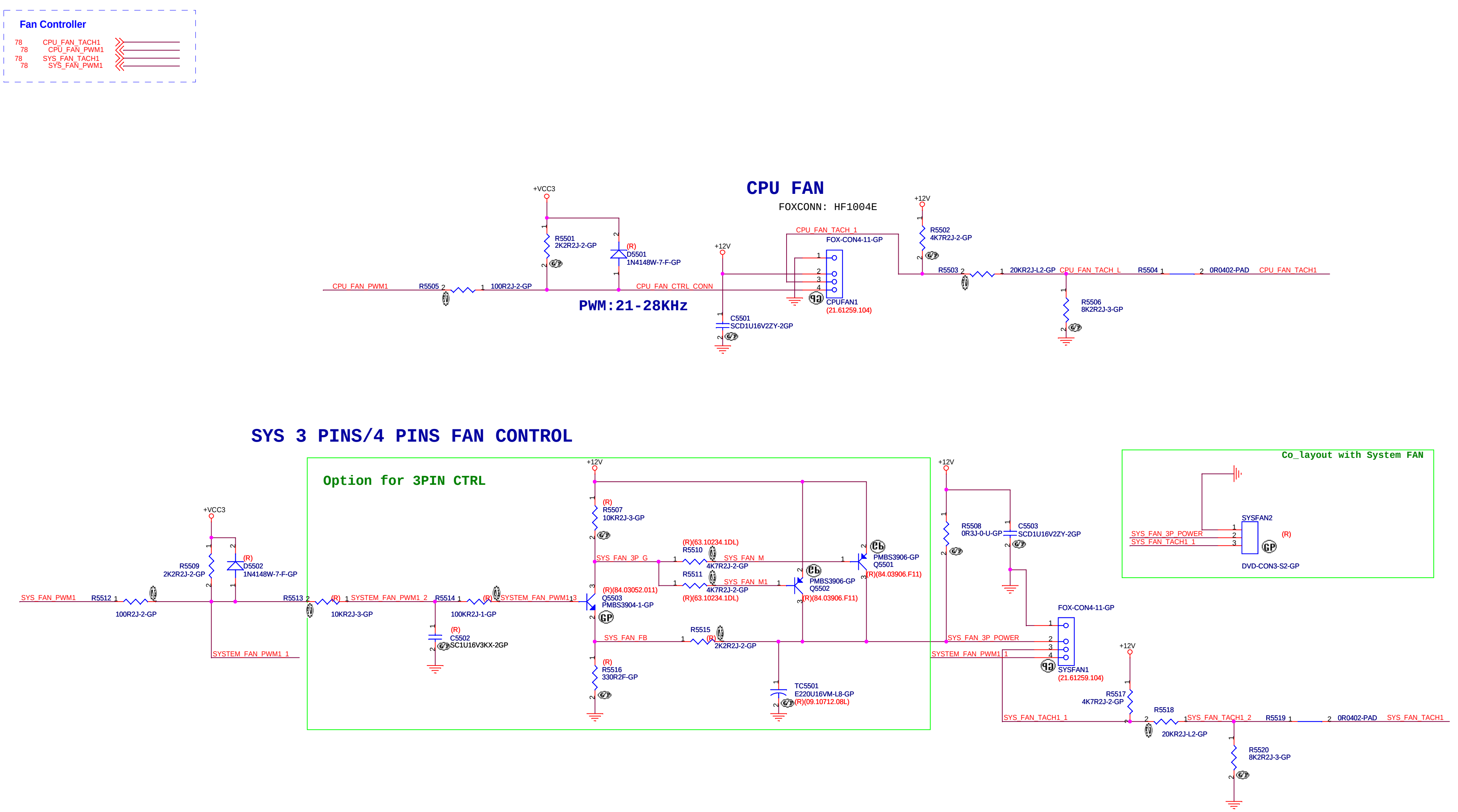
TBD

TBD

wistron				Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title	S-Video				
Size B	Document Number Parker				Rev -1
Date:	Friday, May 13, 2011	Sheet	53	of	99

TBD

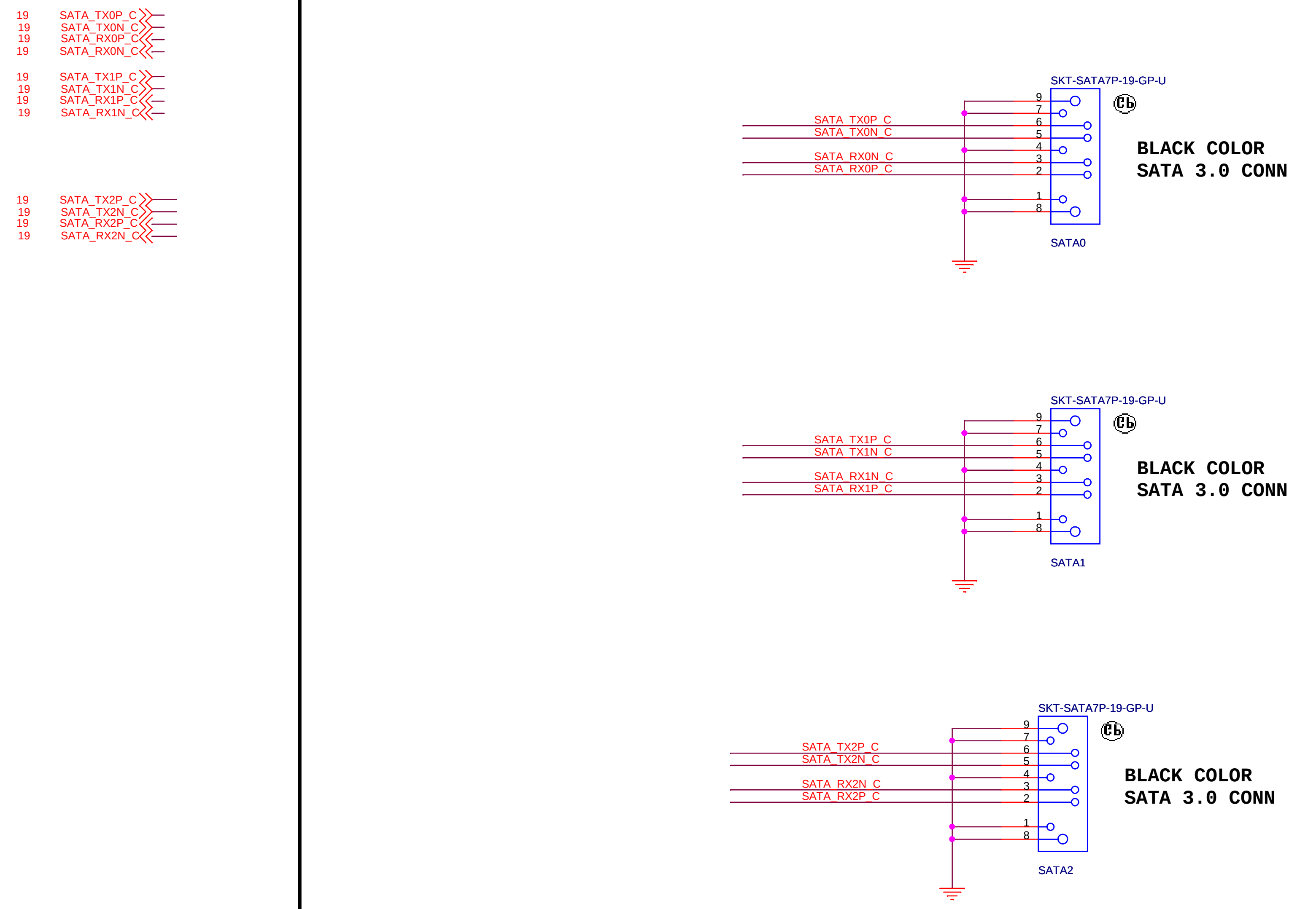
Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: DVI			
Size: B	Document Number: Parker		Rev: -1
Date: Friday, May 13, 2011	Sheet 54 of 99		



wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	FAN CONN		
Size C	Document Number Parker		Rev -1
Date: Friday, May 13, 2011	Sheet 55 of 99		



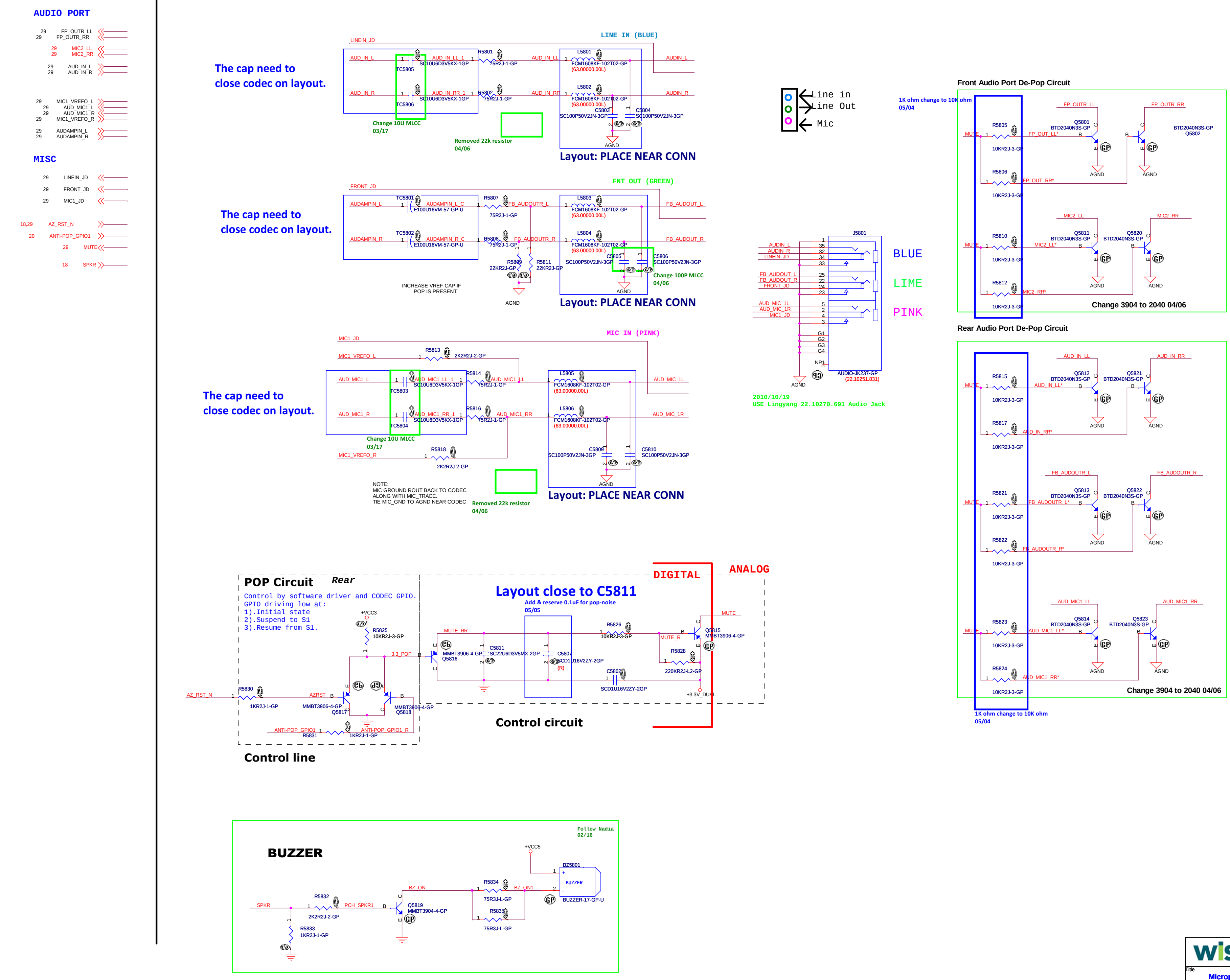
wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title: HDD/ODD		
Size: B	Document Number: Parker	Rev: -1
Date: Friday, May 13, 2011	Sheet 56 of 99	

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: E-SATA + Re-driver IC			
Size: B	Document Number: Parker		Rev: -1
Date: Friday, May 13, 2011	Sheet 57 of 99		



Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	Microphone/Speaker/Audio	
Size: Custom	Document Number: Parker	Rev: -1
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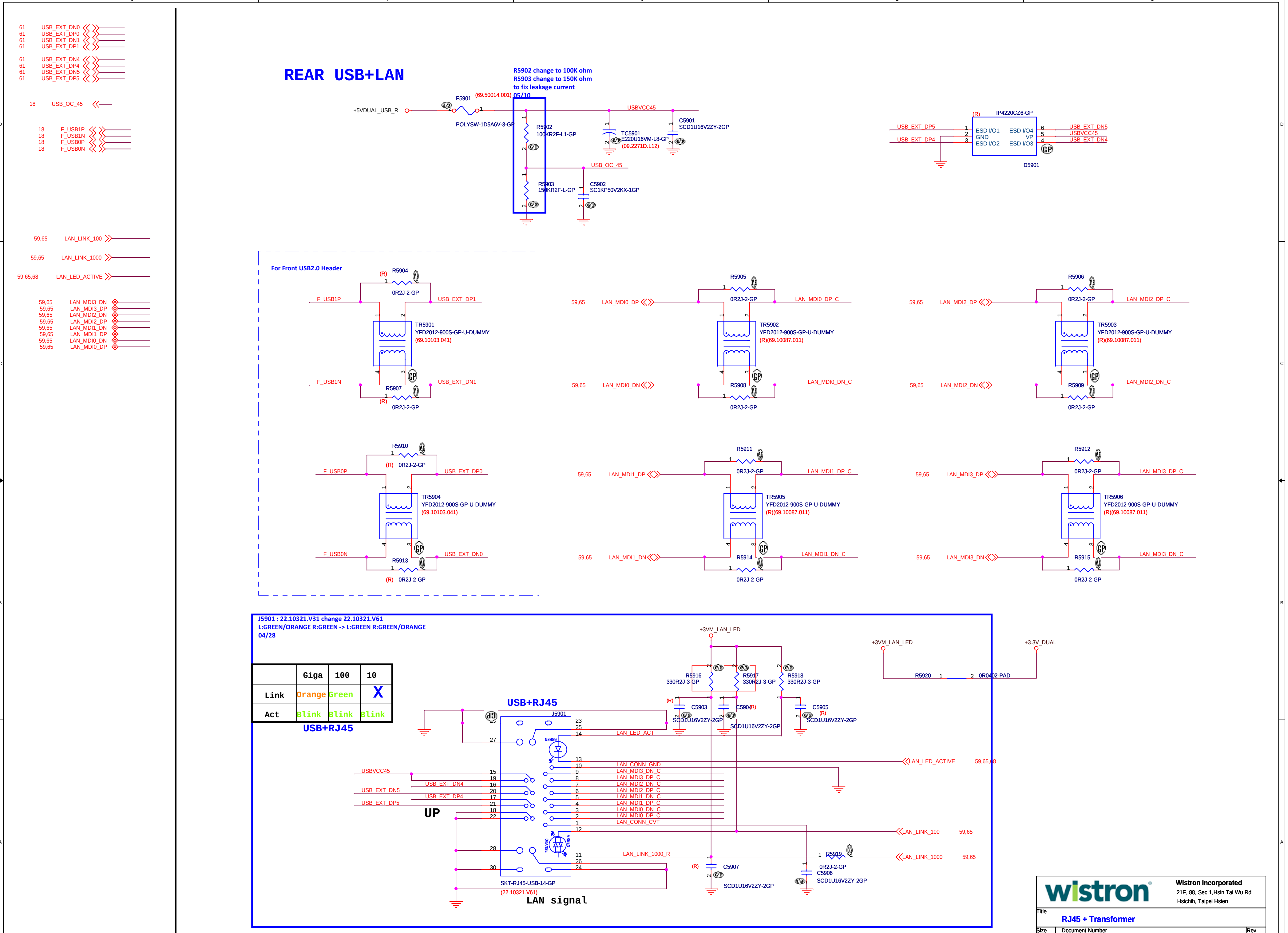
REAR USB+LAN

R5902 change to 100K ohm
R5903 change to 150K ohm
to fix leakage current
05/10



	Giga	100	10
Link	Orange	Green	X
Act	Blink	Blink	Blink

USB+RJ45

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	RJ45 + Transformer	
Size C	Document Number Parker	Rev -1
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TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei					
Title	Flash (KBC + PCH)/RTC				
Size B	Document Number Parker				Rev -1
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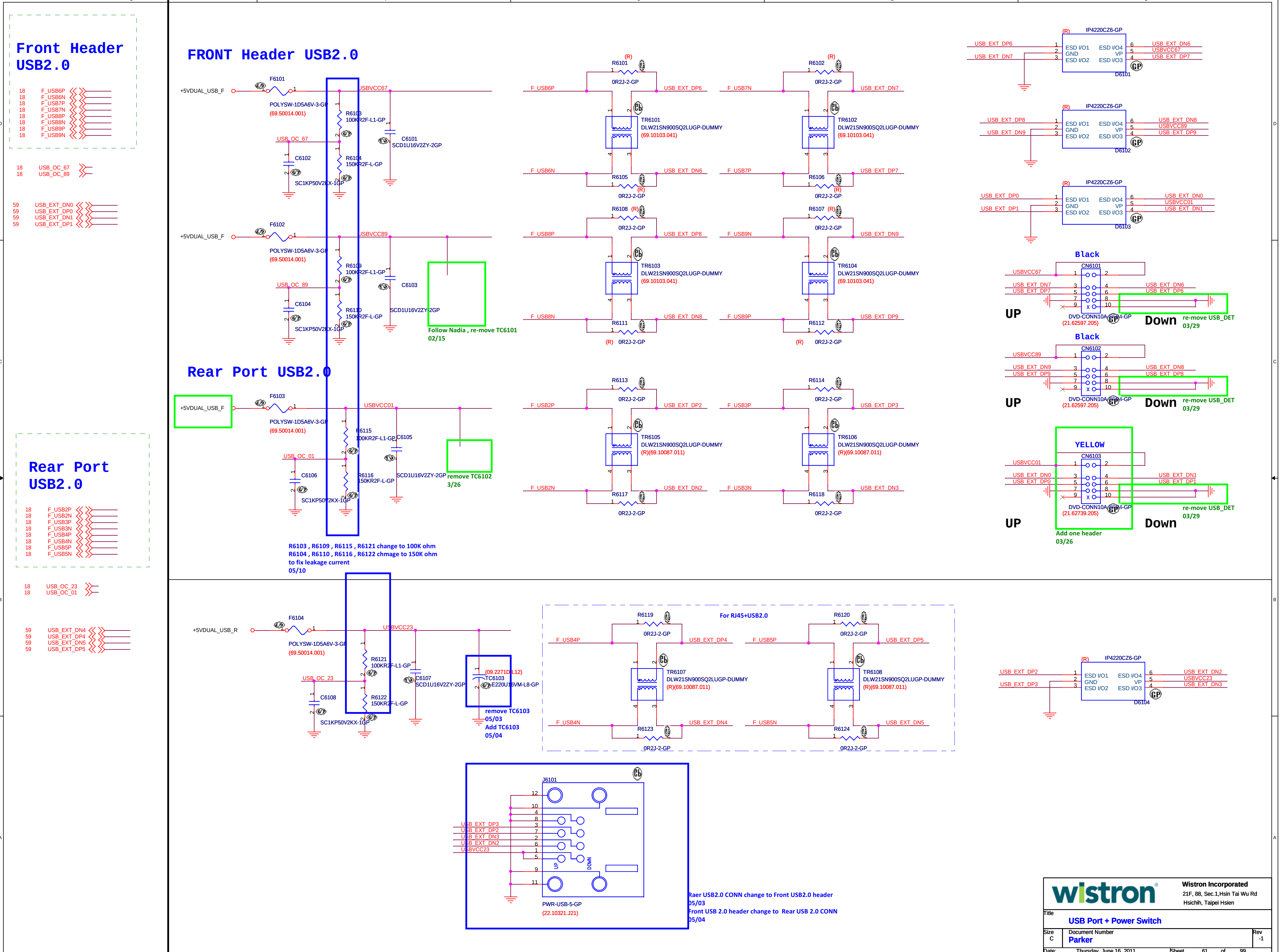

wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	USB Port + Power Switch	
Size: C	Document Number: Parker	Rev: -1
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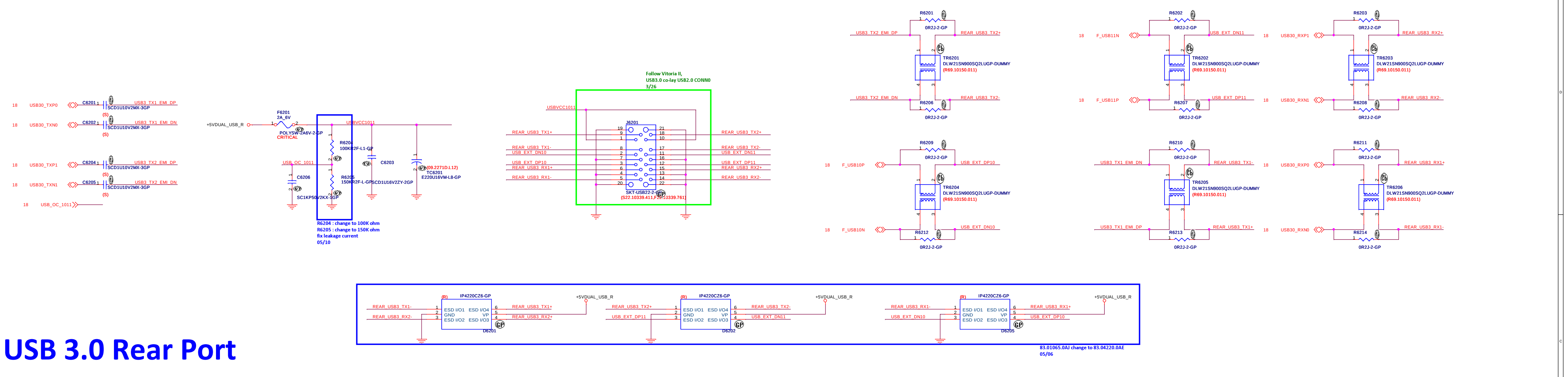
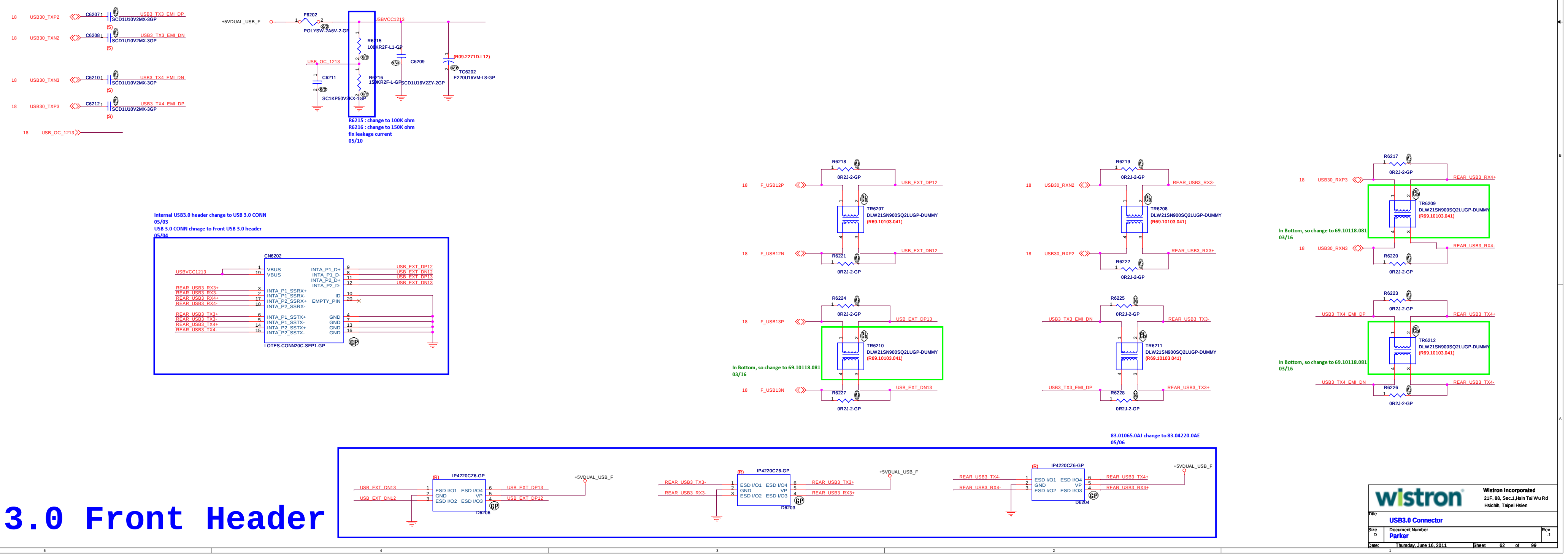
USB 3.0 Rear Port



USB 3.0 Front Header



wistron	Wistron Incorporated 21F, 88, Sec.1,Hsin Tai Wu Rd Hsichih, Taipei Hsien
Title	USB3.0 Connector
Size D	Document Number: Parker — Rev -1
Date: Thursday, June 16, 2011	Sheet 62 of 99

TBD

wistron		**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title	Bluetooth	
Size B	Document Number Parker	Rev -1
Date:	Friday, May 13, 2011	Sheet 63 of 99

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TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title	Finger Printer Connector		
Size B	Document Number Parker		Rev -1
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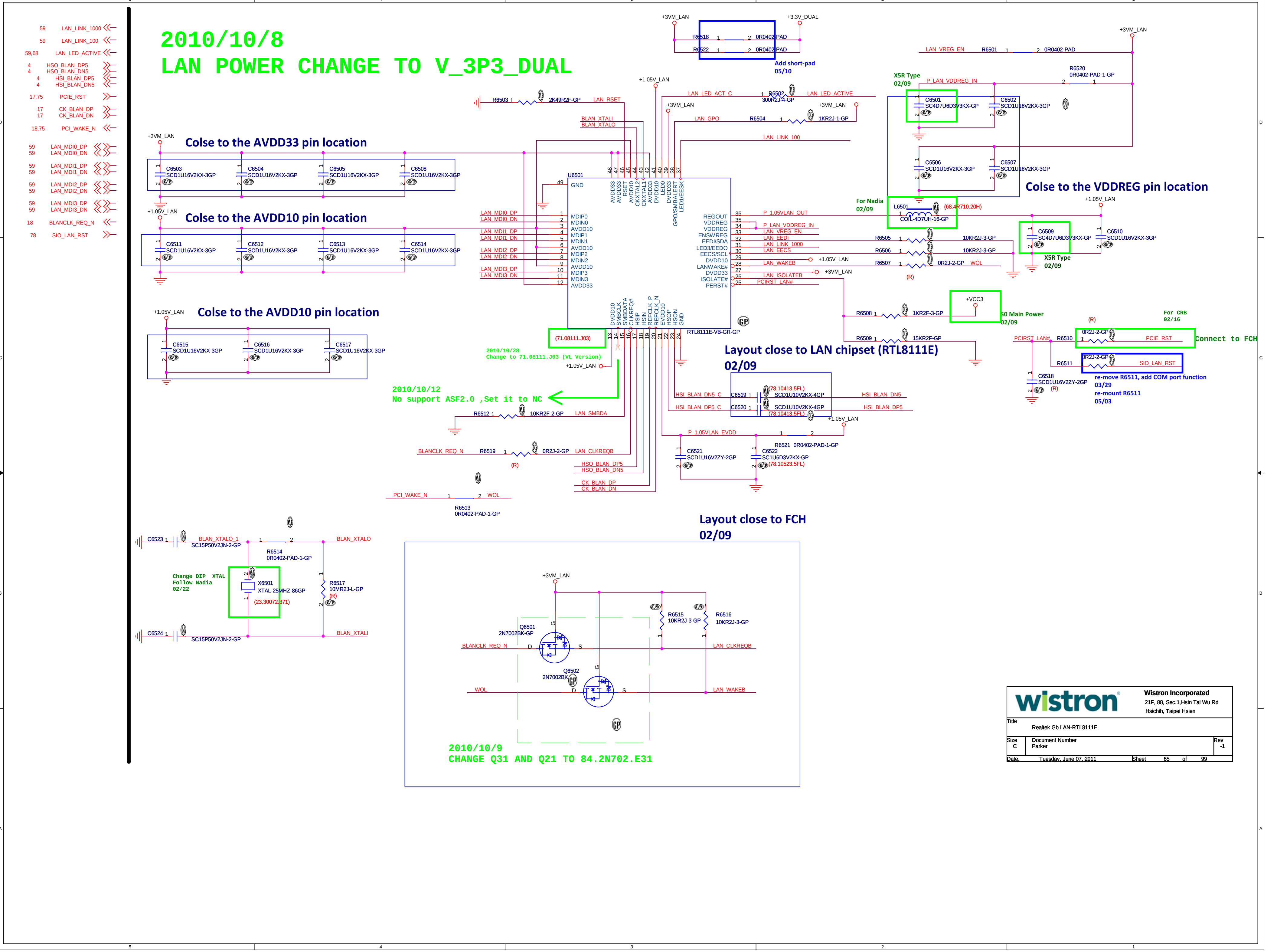

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title	Realtek Gb LAN-RTL8111E	
Size: C	Document Number: Parker	Rev: -1
Date: Tuesday, June 07, 2011	Sheet 65 of 99	

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title: WWAN CONN/SIM Socket		
Size: B	Document Number: Parker	Rev: -1
Date: Friday, May 13, 2011	Sheet 66 of 99	

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei				
Title: 3rd MINICARD				
Size: B	Document Number: Parker			Rev: -1
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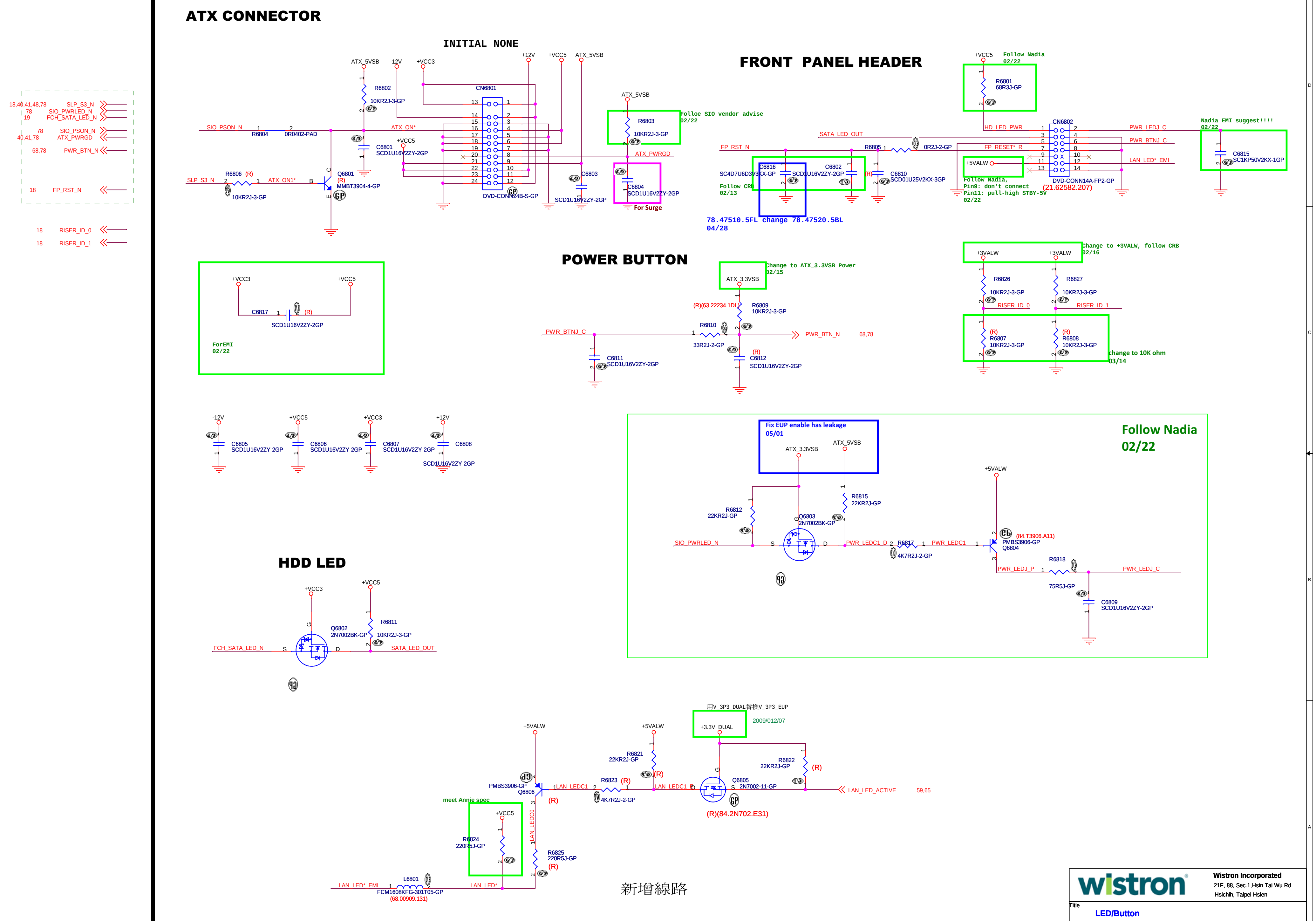
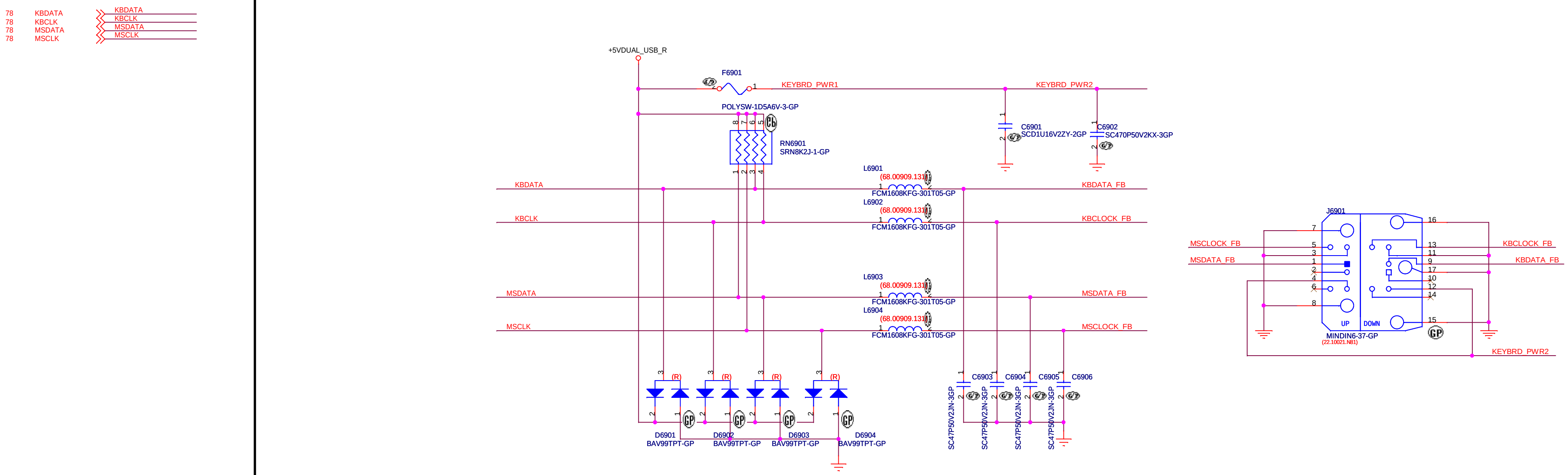


Wistron Incorporated	21F, 88, Sec.1,Hsin Tai Wu Rd Hsichih, Taipei Hsien
Title	LED/Button
Size C	Document Number Parker	Rev -1
Date: Tuesday, June 07, 2011	Sheet 68 of 99



Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title: **Keyboard/Touch Pad**		
Size: C	Document Number: **Parker**	Rev: -1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title: Hall Sensor		
Size: B	Document Number: Parker	Rev: -1
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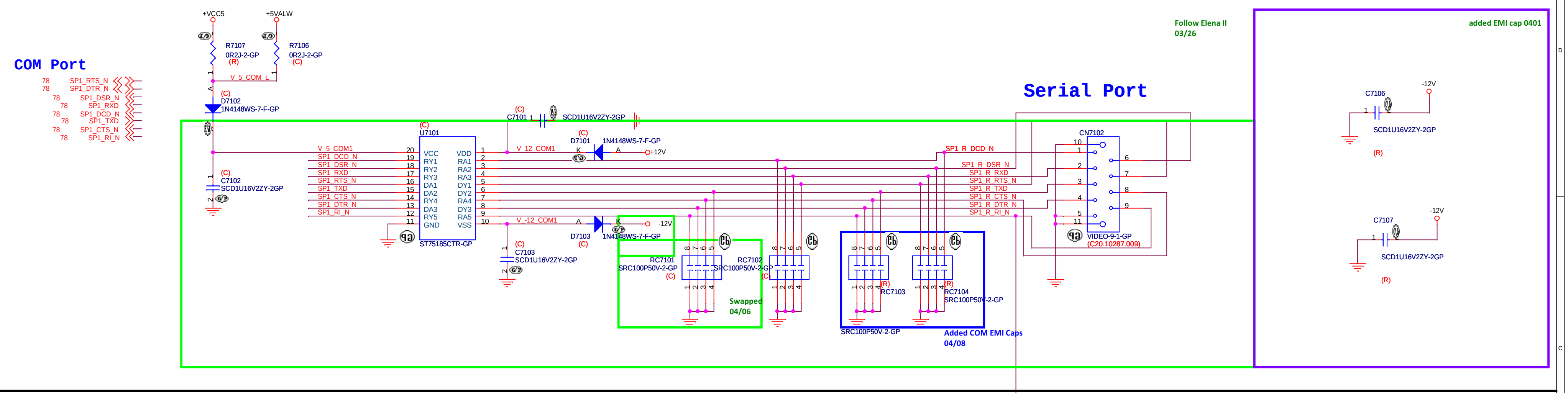
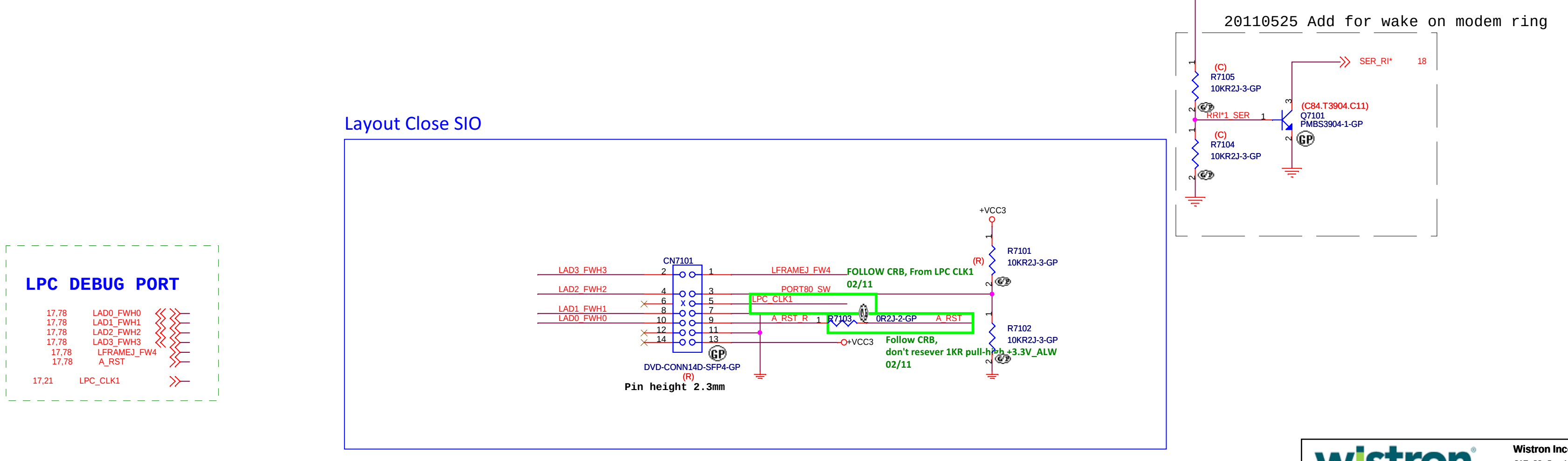
SERIAL PORT





wistron	Wistron Incorporated 21F, 88, Sec.1,Hsin Tai Wu Rd Hsichih, Taipei Hsien	
Title	Serial Port/JTAG/Debug Port	
Size C	Document Number Parker	Rev -1
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TBD

wistron	**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei
Title	RJ11+MDC
Size: B	Document Number: Parker
Rev	-1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	1394 CONN	
Size B	Document Number Parker	Rev -1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Card Reader Connector	
Size B	Document Number Parker	Rev -1
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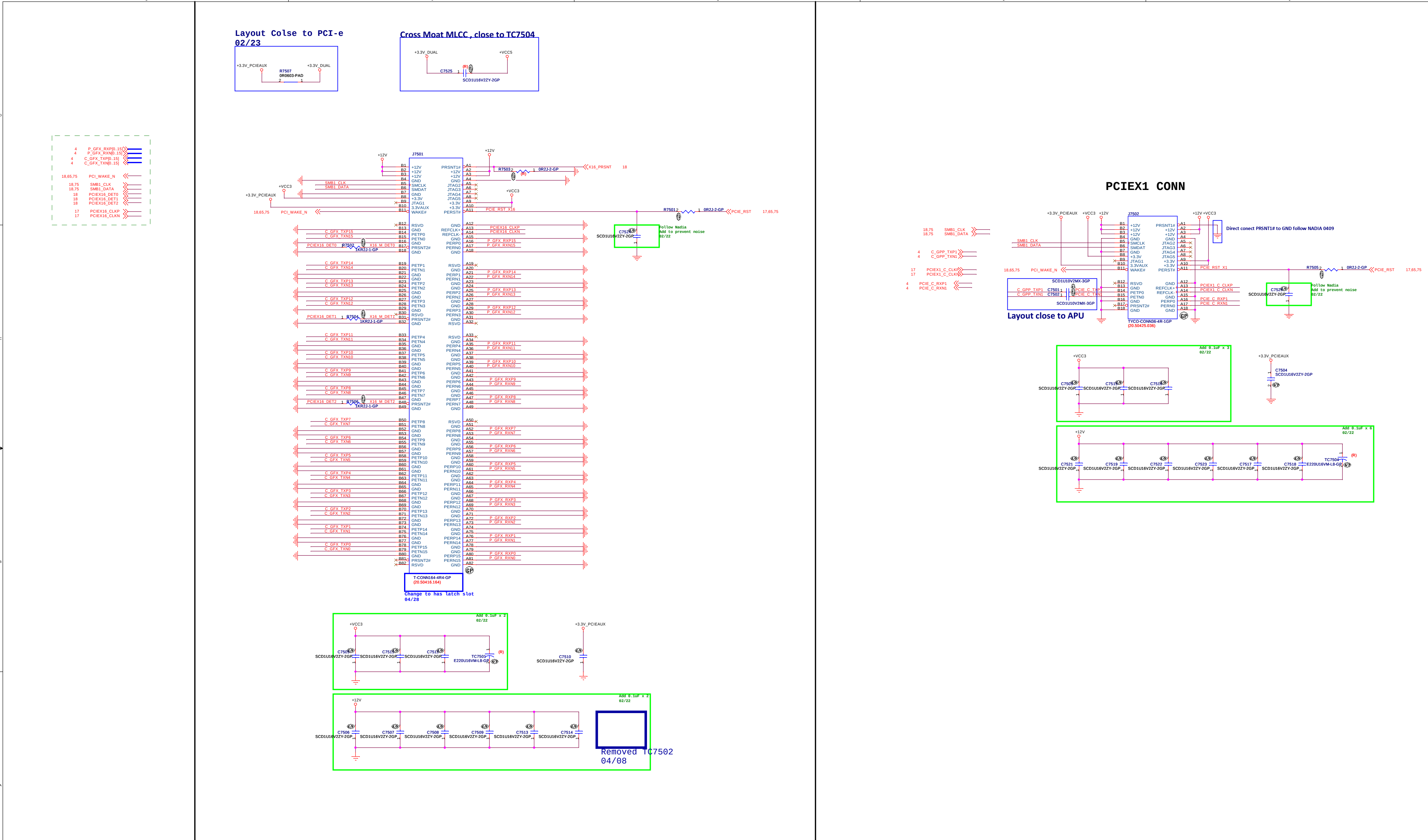
5 4 3 2 1



wistron

Wistron Incorporated
21F, 88, Sec.1,Hsin Tai Wu Rd
Hsichih, Taipei Hsien

Title: PCIEX16 & PCIEX1

Size: D | Document Number: Parker | Rev: -1

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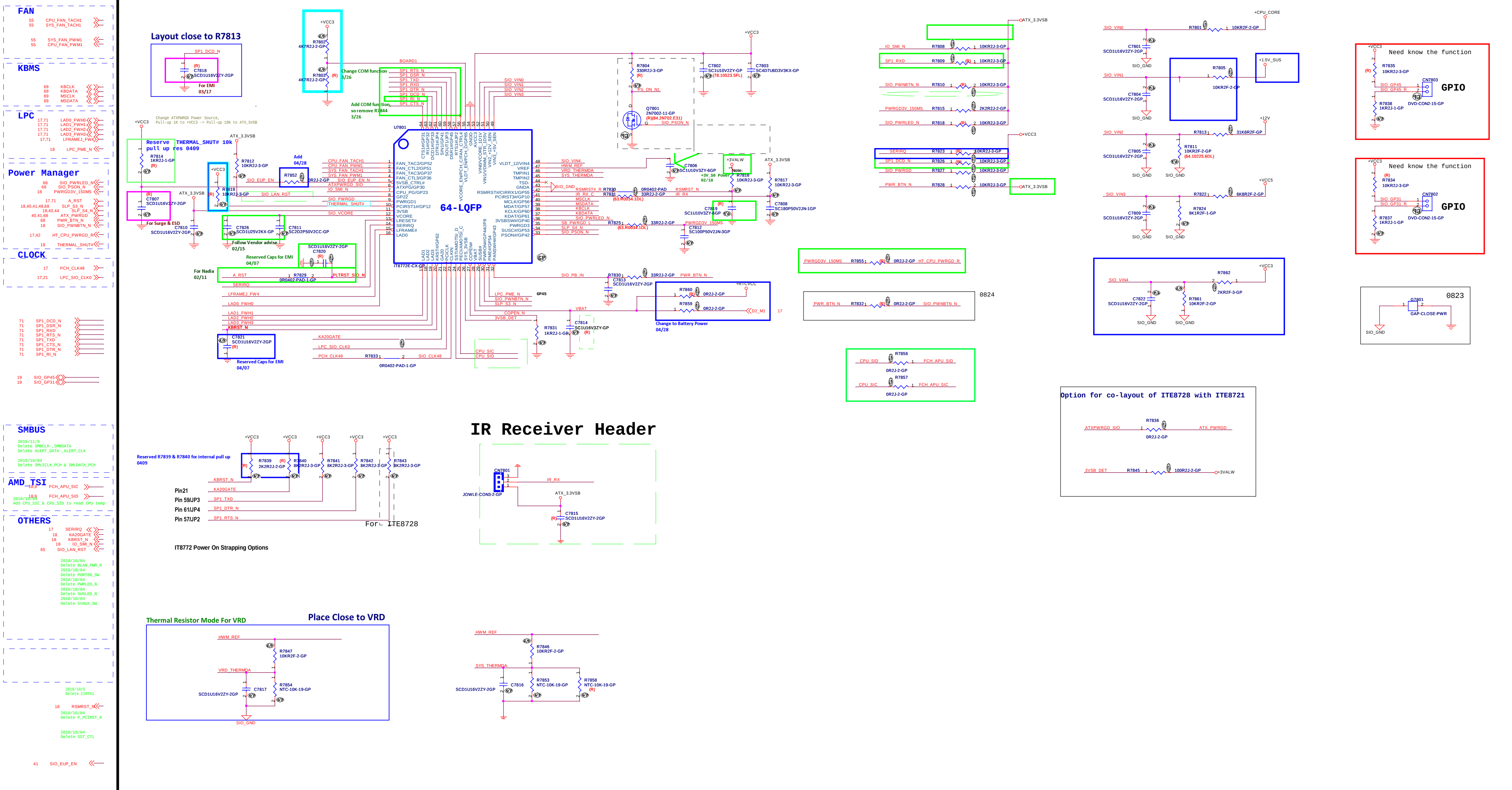
TBD

wistron	**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title	Smart Card Socket	
Size B	Document Number Parker	Rev -1
Date:	Friday, May 13, 2011	Sheet 76 of 99

TBD

Wistron Incorporated 21F, 88, Sec.1,Hsin Tai Wu Rd Hsichih, Taipei Hsien					
Title	TPM				
Size B	Document Number Parker				Rev -1
Date:	Friday, May 13, 2011	Sheet	77	of	99



PC Board Layout Checklist

1. Keep traces away from high voltages (+12V bus).
2. Keep traces away from fast data buses and CRTs.
3. Use recommended trace widths and spacing.
4. Place a ground plane under the traces

Recommand Thermal Diode PCB Traces

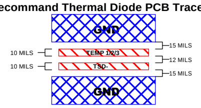


Table 7-1. Power On Strapping Options

	Symbol	Strapping Event	Value	Description
JP2 Pin 57	WDT_EN	Internal VCC-OK/ LRESET#	1	Disable WDT to reset PWRGD
			0	Enable WDT to reset PWRGD
JP4 Pin 61	K8PWR_EN	Internal VCC-OK	1	Disable K8 power sequence function
			0	Enable K8 power sequence function
[JP3] Pin 59	FAN_CTL_SEL	Internal VCC-OK	1	The default value of EC Index 63h/6Bh/73h is 80h.
			0	The default value of EC Index 63h/6Bh/73h is 00h.
JP8 Pin 30	RSMRST_SEL	Internal 3VSB_OK	1	RSMRST# output detected by 3VSB
			0	RSMRST# output detected by SYS_3VSB

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title	G-Sensor (Reserve)		
Size B	Document Number Parker		Rev -1
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TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei				
Title	Reserve (RF/Other)			
Size B	Document Number Parker			Rev -1
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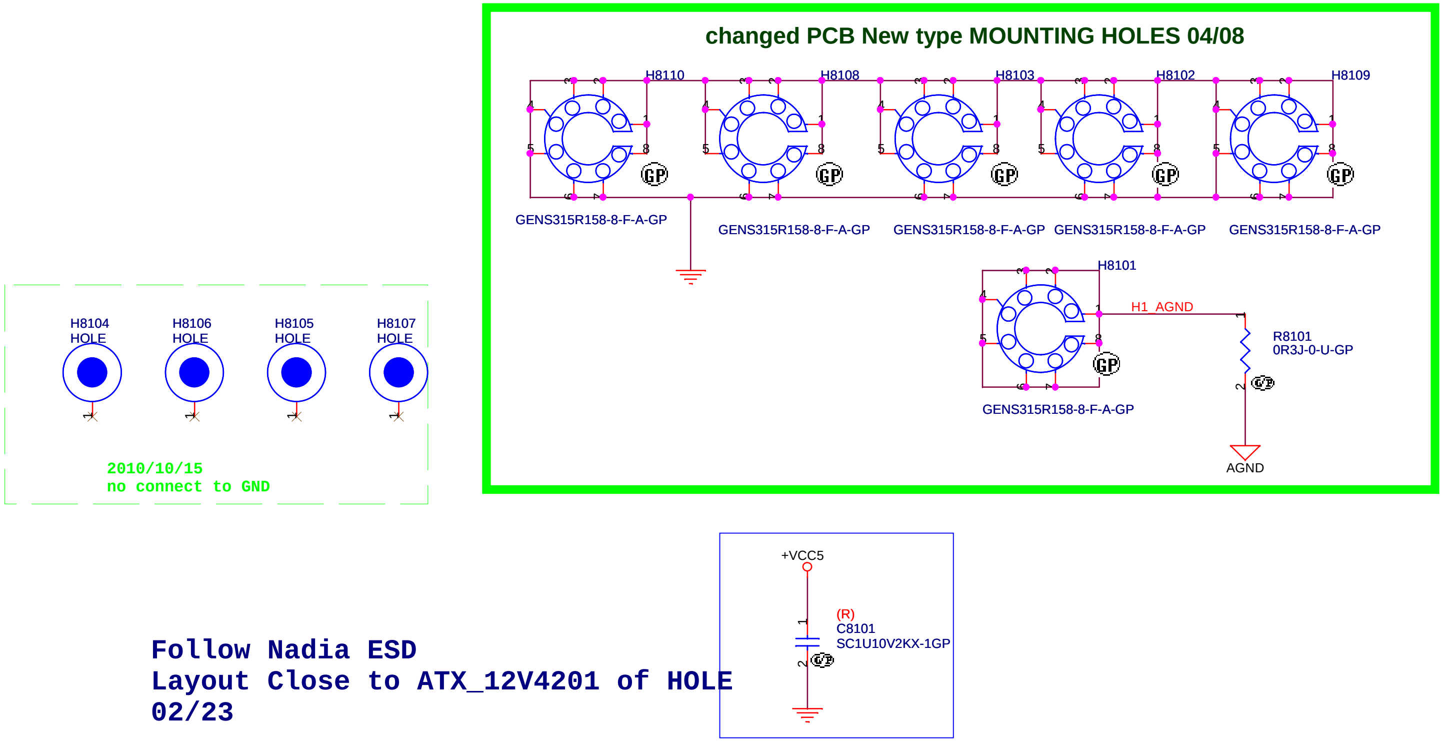

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Screw Holes, Springs, BOSS		
Size B	Document Number Parker		Rev -1
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Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title: Discrete GPU (PEG/Strapping)		
Size: B	Document Number: Parker	Rev: -1
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TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: Discrete GPU (Memory)			
Size: B	Document Number: Parker		Rev: -1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Discrete GPU (DP/LVDS/CRT/GP)	
Size B	Document Number Parker	Rev -1
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TBD

	Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title	Discrete GPU (Core Power)		
Size B	Document Number Parker		Rev -1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Discrete GPU (DPPWR/GND)	
Size: B	Document Number: Parker	Rev: -1
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TBD

TBD

wistron	**Wistron Incorporated** 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei	
Title	3rd and 4th VRAM chips	
Size B	Document Number Parker	Rev -1
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Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: 5th and 6th VRAM chips			
Size: B	Document Number: Parker		Rev: -1
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wistron	Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title	7rd and 8th VRAM chips		
Size B	Document Number Parker		Rev -1
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TBD

TBD

Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei			
Title: GPU Other Power			
Size: B	Document Number: Parker		Rev: -1
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TBD

Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Switchable GFX (1/2)	
Size B	Document Number Parker	Rev -1
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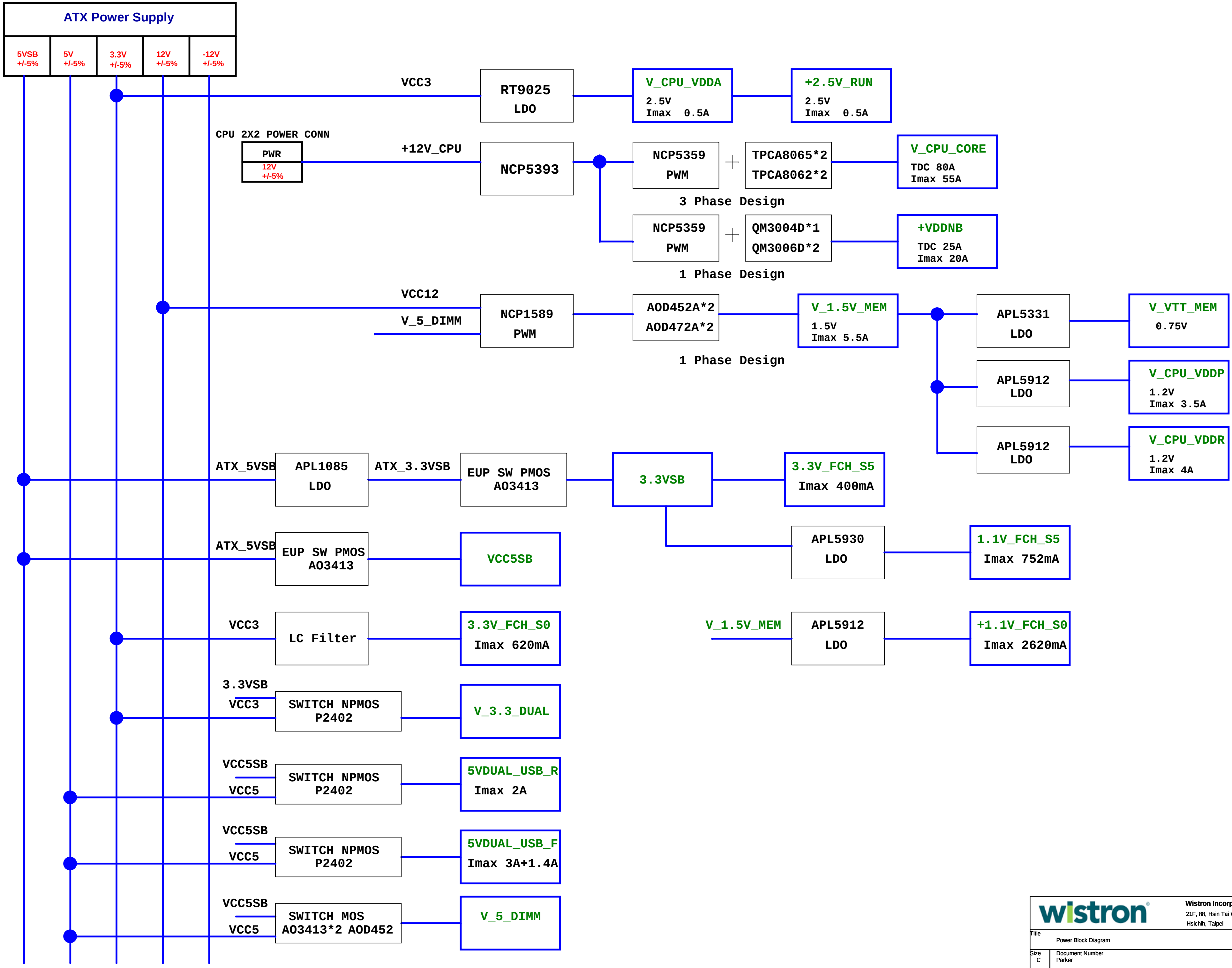
TBD



Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title: Power Block Diagram		
Size: C	Document Number: Parker	Rev: -1
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wistron			Wistron Incorporated 21F, 88, Hsin Tai Wu Rd Hsichih, Taipei		
Title	SMbus Block Digram				
Size B	Document Number Parker				Rev -1
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TBD

wistron

Wistron Incorporated

21F, 88, Hsin Tai Wu Rd

Hsichih, Taipei

Title	Thermal Block Diagram/Audio	
Size: B	Document Number: Parker	Rev: -1
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5 4 3 2 1

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Wistron Incorporated
21F, 88, Hsin Tai Wu Rd
Hsichih, Taipei

Title	Chang History	
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