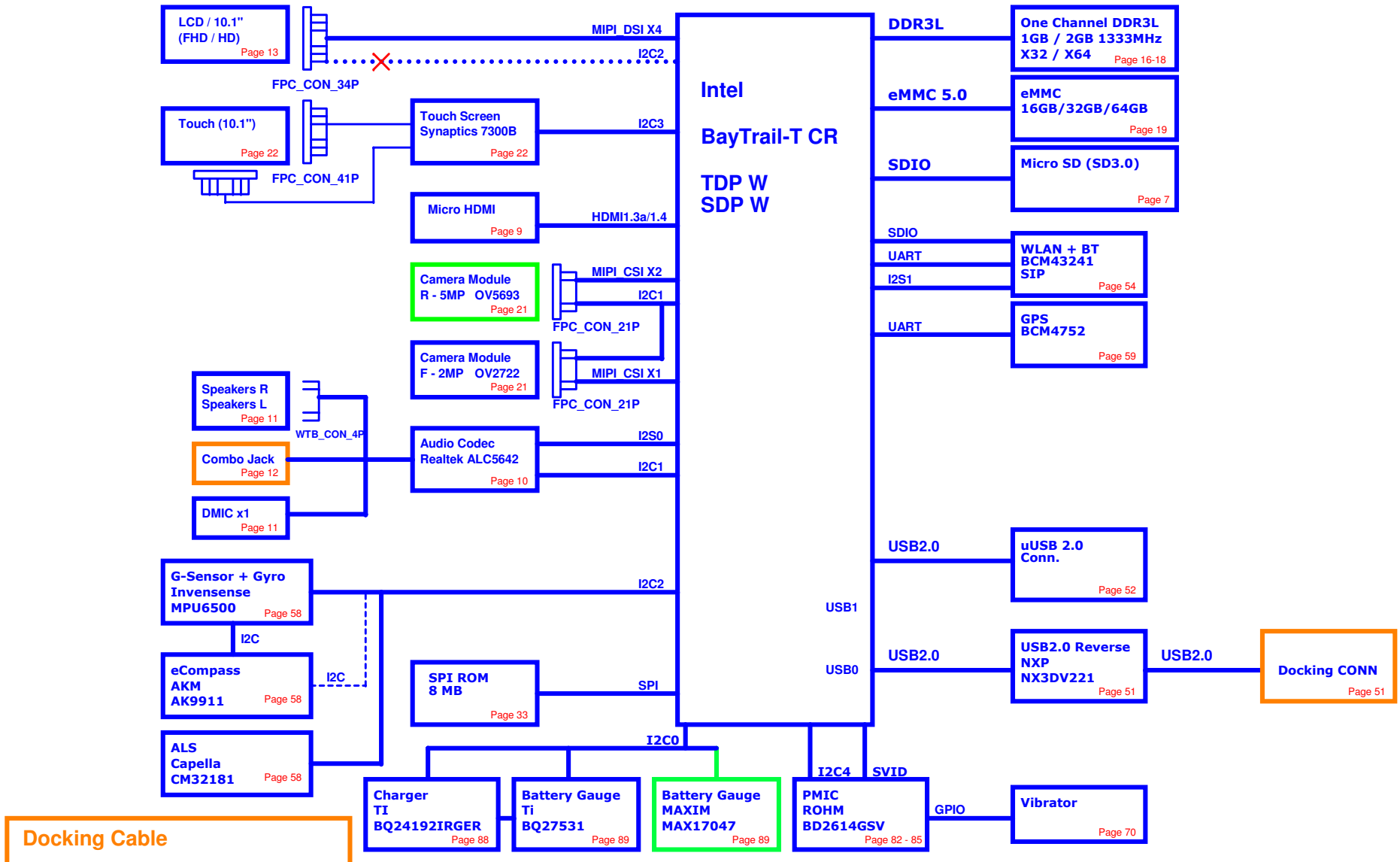
01

P0JAC2 Block Diagram



PEGATRON Title : Block Diagram

PEGATRON CONFIDENTIAL Engineer: Eli Lin

Size	Project Name	Rev
C	P0JAC2	1.4

Date: Thursday, April 24, 2014 Sheet 1 of 99

02

PAGE	PDF PAGE	TITLE
01	01	BLOCK DIAGRAM
02	02	REF PAGE
03	03	Port Assignment
04	04	Power Tree
05		****
06	05	Power Sequence
07	06	Micro SD
08		***
09	07	HDMI
10	08	Audio Codec
11	09	Audio_SPK/DMIC
12	10	Audio_BDCONN
13	11	MIPI Panel
14		***
15		***
16	12	DDR3L(1)
17	13	DDR3L(2)
18	14	DDR3L(3)
19	15	eMMC
20		***
21	16	Camera 5M/2M
22	17	Touch Screen
23	18	FHD DB CONN
24	19	CPU 1/6 MEMORY
25	20	CPU 2/6 DISPLAY,STORAGE,AUD
26	21	CPU 3/6 USB,I2C,CLK,DB,MISC
27	22	CPU 4/6 Power
28	23	CPU 5/6 Core Power
29	24	CPU 6/6 GND
30		***
31		***
32		***
33	25	BIOS ROM
34		***
35		***
36		***
37		***
38	26	Debug Circuit
39		***
40		***
41		***
42		***
43		***
44		***
45		***

PAGE	PDF PAGE	TITLE
46		***
47		***
48		***
49		***
50		***
51	27	USB Docking
52	28	Micro USB
53		***
54	29	WIFI/BT Foxconn T77H462.03
55		***
56		***
57		***
58	30	Sensor
59	31	GPS
60		***
61		***
62		***
63		***
64		***
65		***
66		***
67		***
68		***
69		***
70	32	VIBRATOR
71	33	Reset
72		***
73		***
74		***
75		***
76		***
77		***
78		***
79	34	ME parts & TP
80	35	DC/BATT CONN & MB ID
81	36	5V DCDC POWER
82	37	PMIC-1
83	38	PMIC-2
84	39	PMIC-3
85	40	PMIC-4
86		***
87		***
88	41	Charger IC
89	42	Gas Gauge
90		***

PAGE	PDF PAGE	TITLE
91		***
92		***
93	43	Audio FFC CONN
94	44	Audio Combo Jack
95	45	Button
96	46	VIBRATOR
97	47	LID Switch
98	48	LED
99	49	AUD Screw / TP

FENDI2

Optional	Stage SR	ER	PR	MP	Remark
N/A	POP	POP	POP		
@	NC	NC	NC		
/LED	NC	NC	NC		Have LED function
/HOMEKEY	POP	POP	POP		Have HOMEKEY function
/NON_HOMEKEY	NC	NC	NC		None HOMEKEY function
/DDR3L_X64	POP	POP	POP		Have 2G 64 bits function
/DEBUG	POP	POP	POP		Have Debug port function
/NONDEBUG	NC	NC	NC		No Debug port function
/USBCHG	NC	NC	NC		Have USB charger function
/NONUSBCHG	POP	POP	POP		None USB charger function
/ALS	POP	POP	POP		Have Light Sensor function
/GPS	NC	NC	NC		Have GPS function
/THERMAL	NC	NC	NC		Have THERMAL RESET function
/MAX	NC	NC	NC		Have Maxim gas function
/BQ	POP	POP	POP		Have TI gas function
/GASG	POP	POP	POP		Have GAS function
/VIB	NC	NC	NC		Have VIBRATOR function
/WINDOWS	POP	POP	POP		Have Volume function for WINDOWS
/ANDROID	NC	NC	NC		Have Volume function for ANDROID
/5M	NC	NC	NC		Have 5M CAMERA function
/HD	POP	POP	POP		Have HD panel
/FHD	NC	NC	POP		Have FHD panel

PEGATRON Title : REF PAGE
PEGATRON CONFIDENTIAL Engineer: SAM
Size C | Project Name POJAC2 | Rev 1.4
Date: Thursday, April 24, 2014 | Sheet 2 of 99

03 Port Assignment

I2C Table

I2C Port	Device	7-bit Addr	Power Rail	Pull High
NFC_I2C	Debug CONN	Check Intel	+V1P8S	PU +V1P8S
I2C_0	Charger	6BH	+V1P8S	
	Fuel Gauge Maxim MAX17047	0x6C for write 0x6D for read	+V1P8S	
	Fuel Gauge TI BQ27531	0xAA for write 0xAB for read	+VSYS	
	USB MUX	68H	+VUSB_CONN	PU +V1P8S
I2C_1	Rear CAM (5M)	0x20 for write 0x21 for read	+V1P8SX	
	Front CAM (2M)	0x6C for write 0x6D for read	+V1P8SX	
	Audio Codec	38H	+V1P8S	PU +V1P8S
	Debug Port	Check Intel	+V1P8S	
I2C_2	Gyro	68H	+V1P8S	
	E-Com	0CH	+V1P8S	PU +V1P8S
	Light sensor	0x10	+V3P3S	
I2C_3	Touch Screen	20H	+V1P8S	PU +V1P8S
I2C_4	PMIC	Device 1 5EH Device 2 6EH	+V1P8S	PU +V1P8S

DDI Table

Gen	Module
DDI0	HDMI
DDI1	NC

eMMC Table

Gen	Module
MMC1	eMMC

MIPI DSI Table

Gen	Module
MDSI_A	MIPI Panel

SDIO Table

Gen	Module
SD2	WIFI/BT
SD3	Micro SD Card

MIPI CSI Table

Gen	Module
MCSI_1	Rear Camera
MCSI_2	Front Camera

USB 2.0 Table

Gen	Module
USB_0	USB Docking
USB_1	Micro USB
USB_ULPI	USB PHY

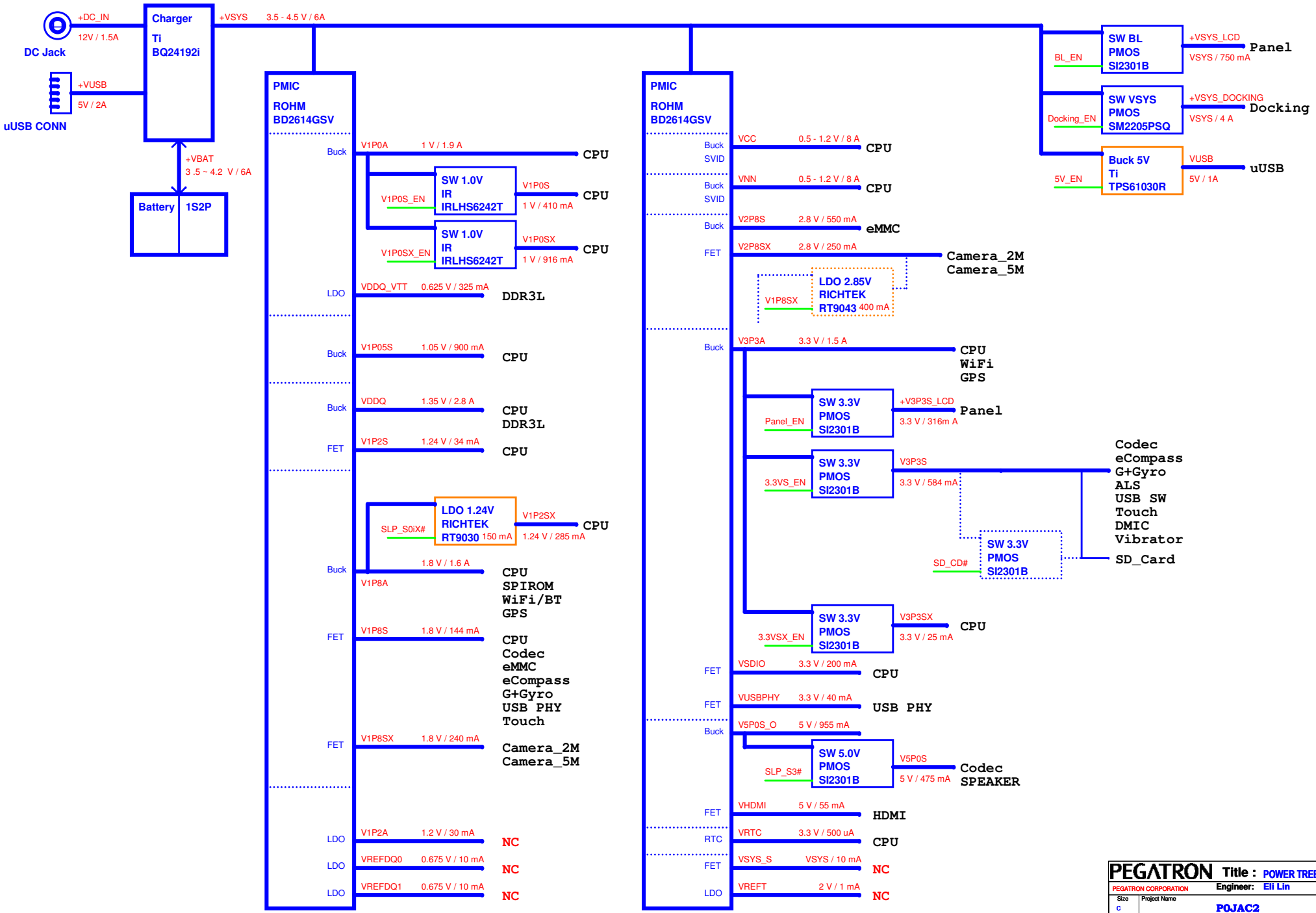
HSUART Table

Gen	Module
UART_1	WIFI/BT
UART_2	GPS

I2S Table

I2S Port	Module
I2S_0	Audio Codec
I2S_1	WLAN

Power Tree



PEGATRON Title : POWER TREE

PEGATRON CORPORATION Engineer: Eli Lin

Size	Project Name	Rev
C	P0JAC2	1.4

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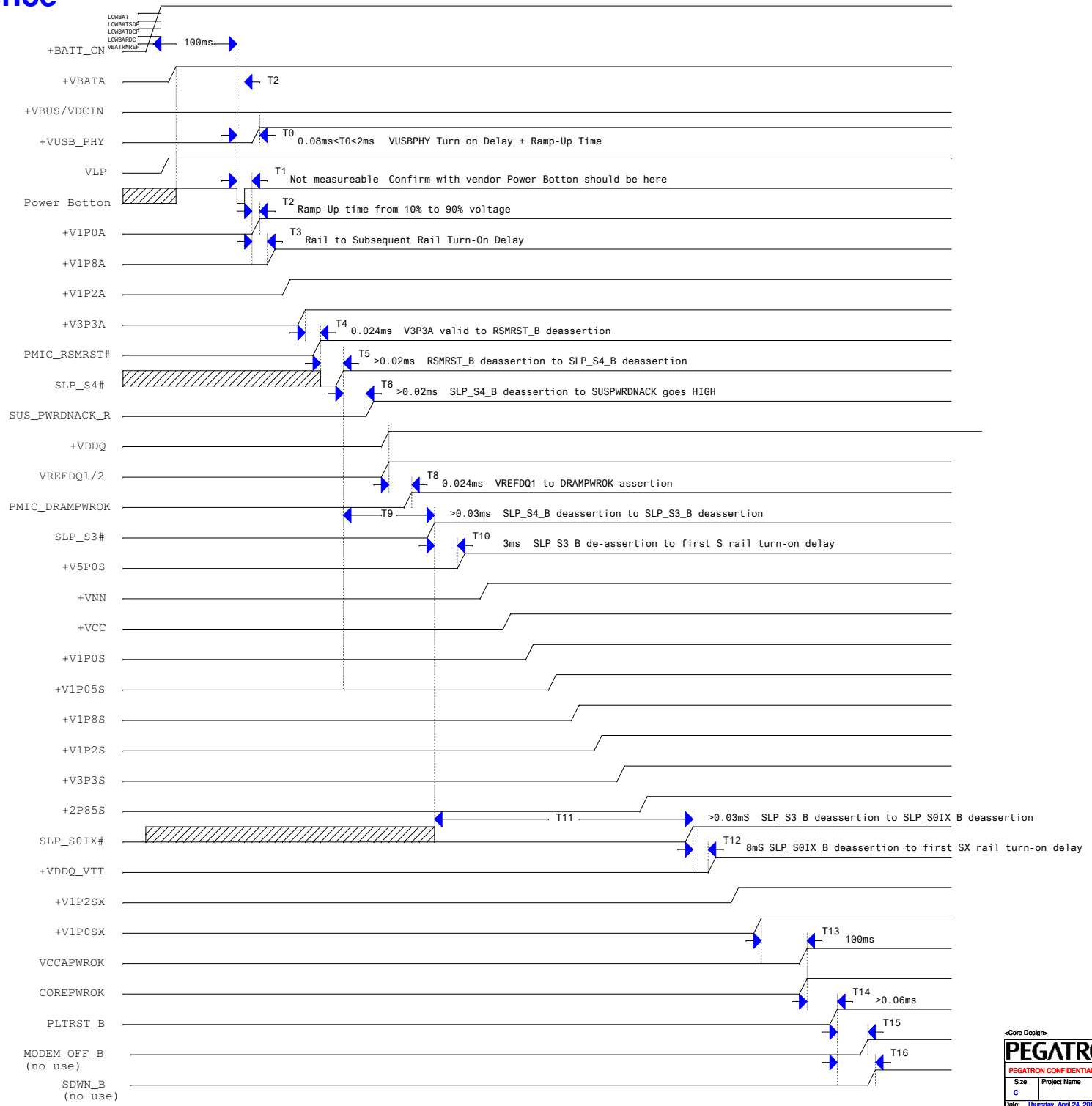
06 Power Sequence

Cold Boost Power Sequenc Diagram



<Core Design>

PEGATRON Title : Power Sequence

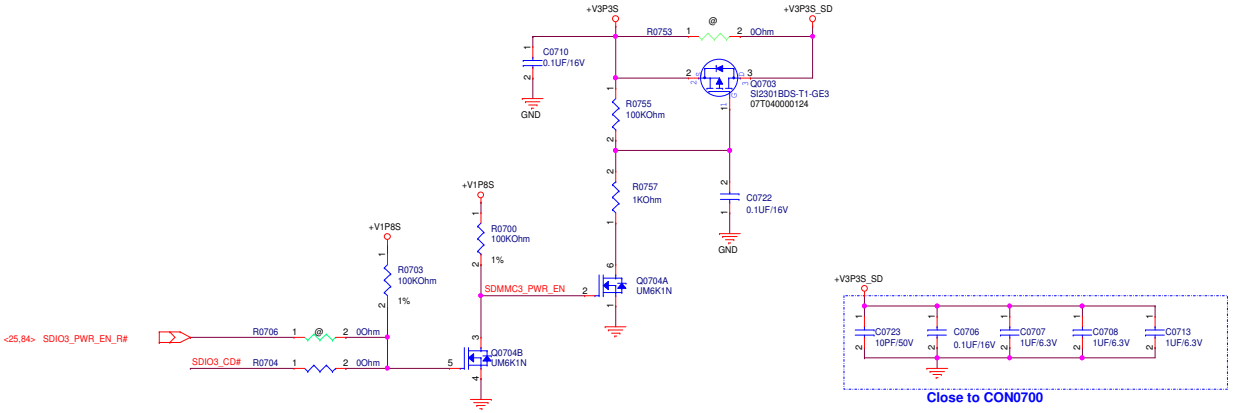
PEGATRON CONFIDENTIAL Engineer: Eli_Lin

Size	Project Name	Rev
C	P0JAC2	1.4

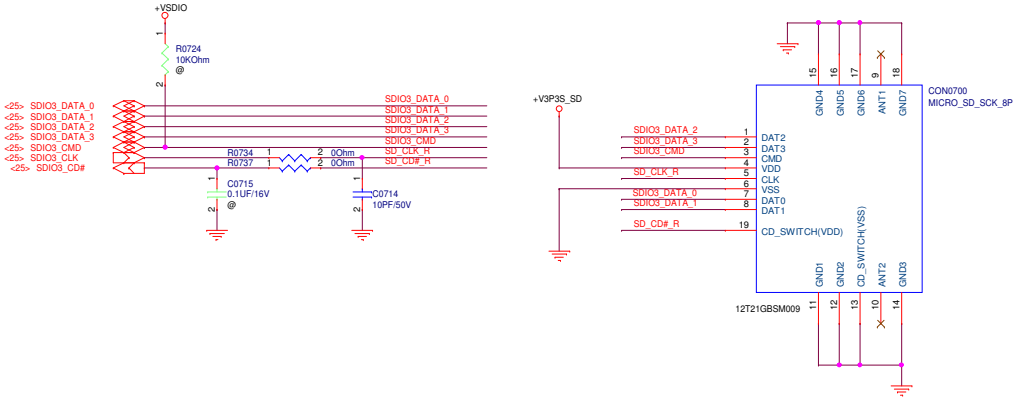
Date: Thursday, April 24, 2014 Sheet 6 of 99

Micro SD

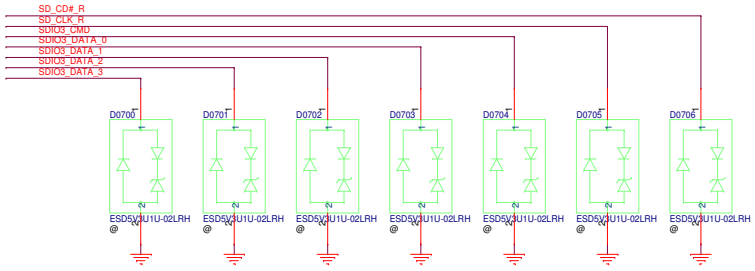
Micro SD Power



Micro SD Socket



ESD



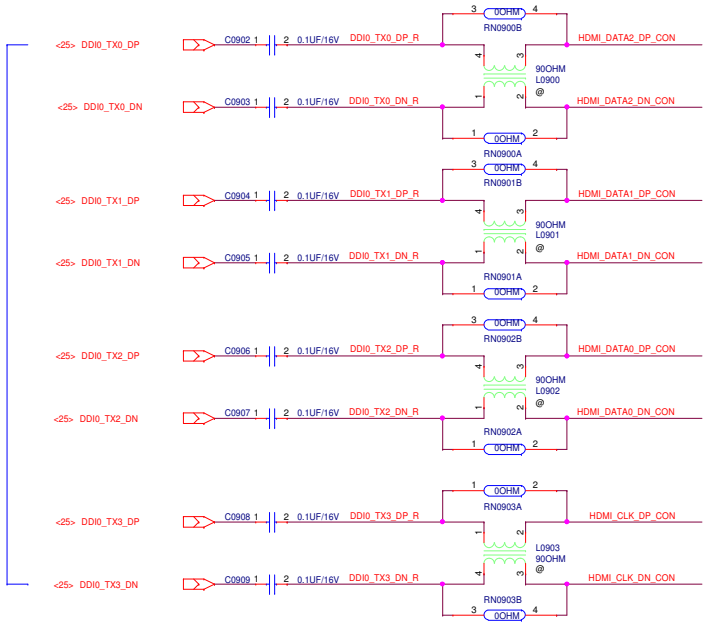
PEGATRON	Title : Micro SD	
PEGATRON CONFIDENTIAL	Engineer: Kang_Kang	
Size: C	Project Name: P0JAC2	Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 7 of 99	

09

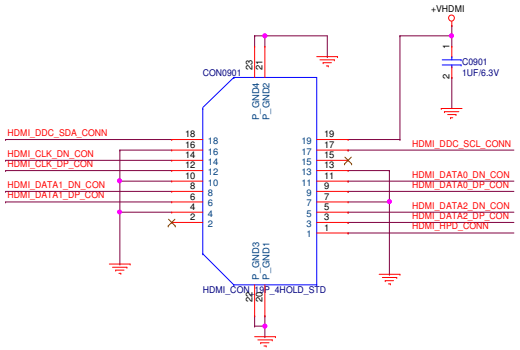
HDMI

HDMI DATA

From SOC



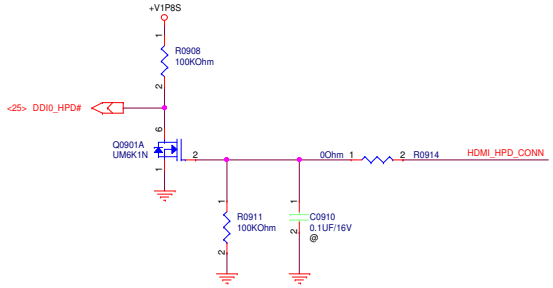
HDMI CONN



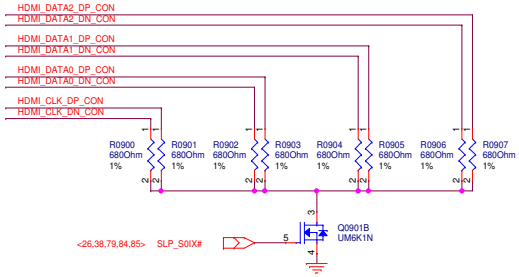
Micro HDMI CONN Pin Definition Type-D

1 : Hot Plug Detect
2 : Utility(NC)
3 : TMDS Data2+
4 : TMDS Data2 Shield
5 : TMDS Data2-
6 : TMDS Data1+
7 : TMDS Data1 Shield
8 : TMDS Data1-
9 : TMDS Data0+
10 : TMDS Data0 Shield
11 : TMDS Data0-
12 : TMDS Clock+
13 : TMDS Clock Shield
14 : TMDS Clock-
15 : CEC(NC)
16 : DDC/CEC Ground
17 : SCL
18 : SDA
19 : +5V Power

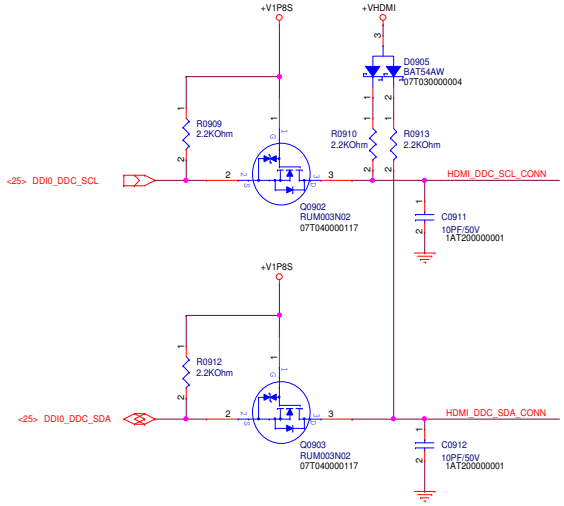
HDMI HPD



Level Shift



HDMI DDC



PEGATRON Title : HDMI
PEGATRON CONFIDENTIAL Engineer: Sam
Size C | Project Name POJAC2 | Rev 1.4
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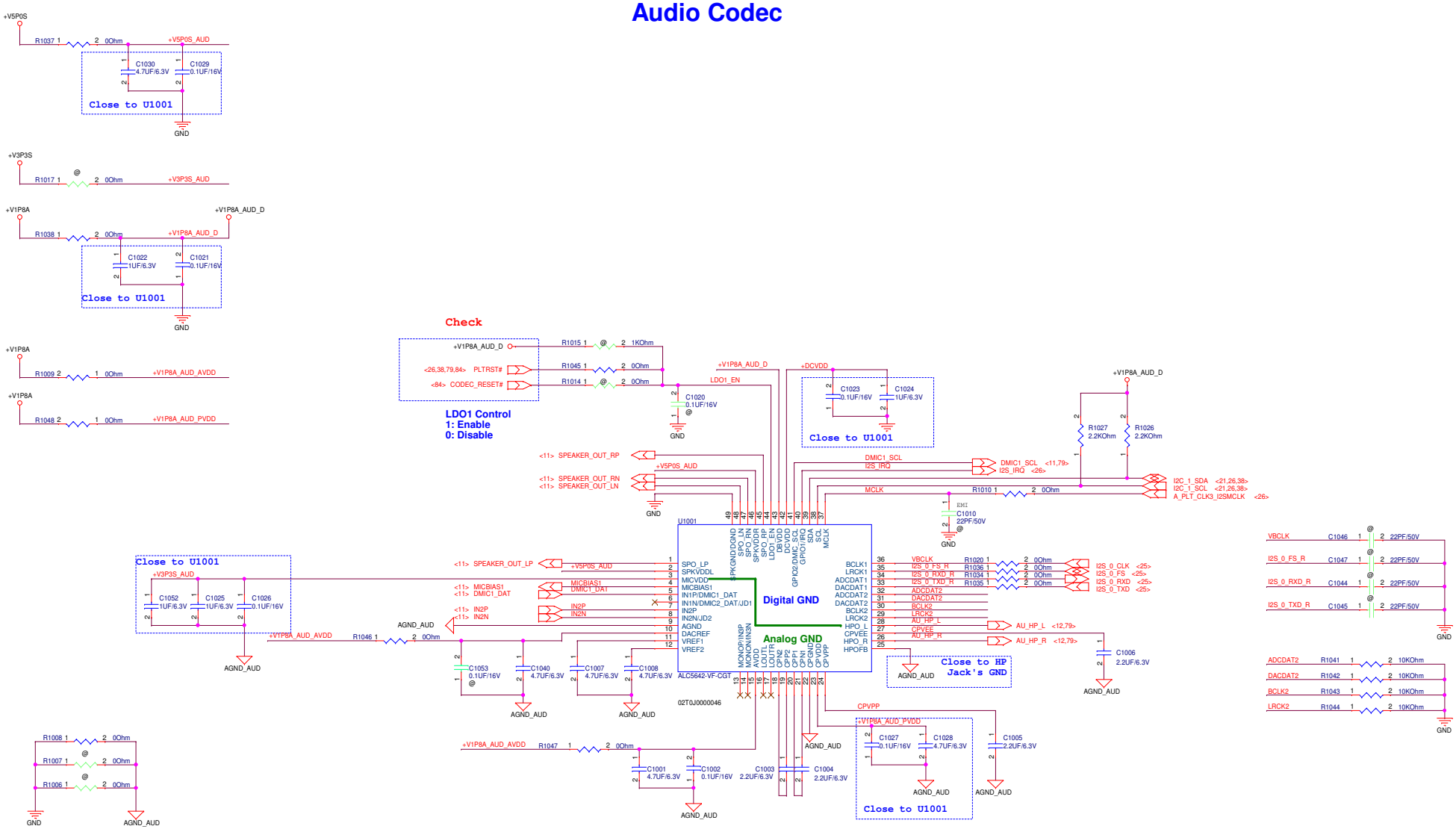
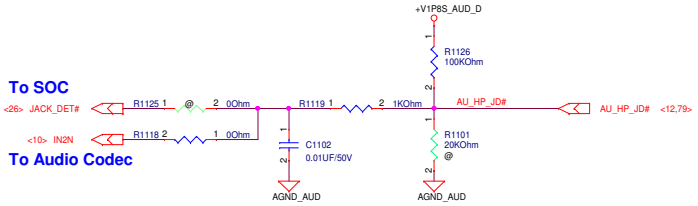
Audio Codec



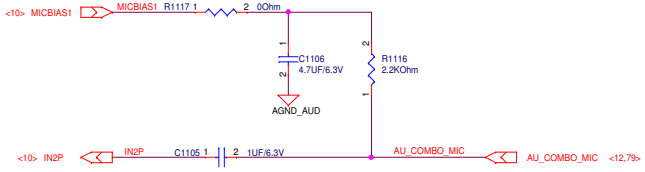
PEGATRON	Title : Audio Codec		
PEGATRON CONFIDENTIAL	Engineer: Sam		
Size: C	Project Name: POJAC2		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 10 of 99		

Audio SPK/DMIC

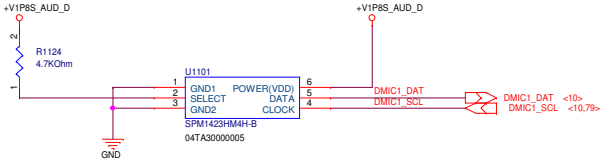
Combo JACK



AU_HP_JD#:
Voltage High : no Jack.
Voltage low: Detect Jack in.

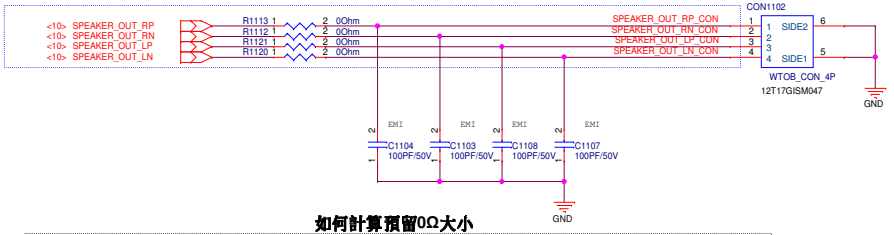


D-MIC



SPK CONN

Trace Width 20 mils



如何計算預留0Ω大小

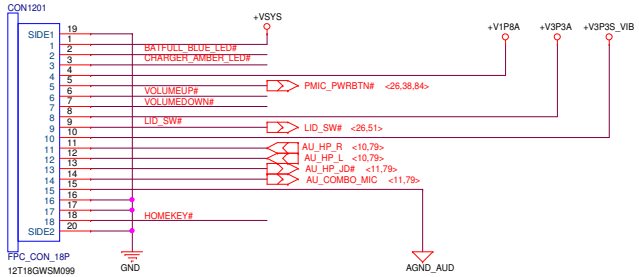
Speaker Spec. P=1W R=8Ω	P=I^2*R 1=I^2*8 Irms=0.354A Ip=0.354*1.414 =0.501A	0Ω通常用50mΩ來計算耐壓 P=Ip^2*R P=0.7515*0.7515*0.05 =0.028W
Vendor 建議電流估1.5倍	Ip=0.501*1.5 =0.7515A	

Size	阻抗值	
0805	1/8W	0.125W
0603	1/10W	0.1W
0402	1/16W	0.0625W
0201	1/20W	0.05W

PEGATRON Title : Audio_SPK/DMIC
PEGATRON CONFIDENTIAL Engineer: Sam
Size C | Project Name P0JAC2 | Rev 1.4
Date: Thursday, April 24, 2014 | Sheet 11 of 99

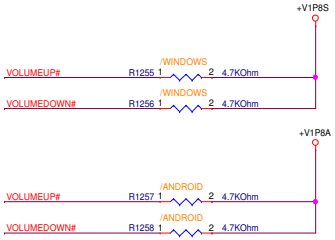
Audio_BDCONN

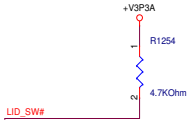
Audio Jack DB



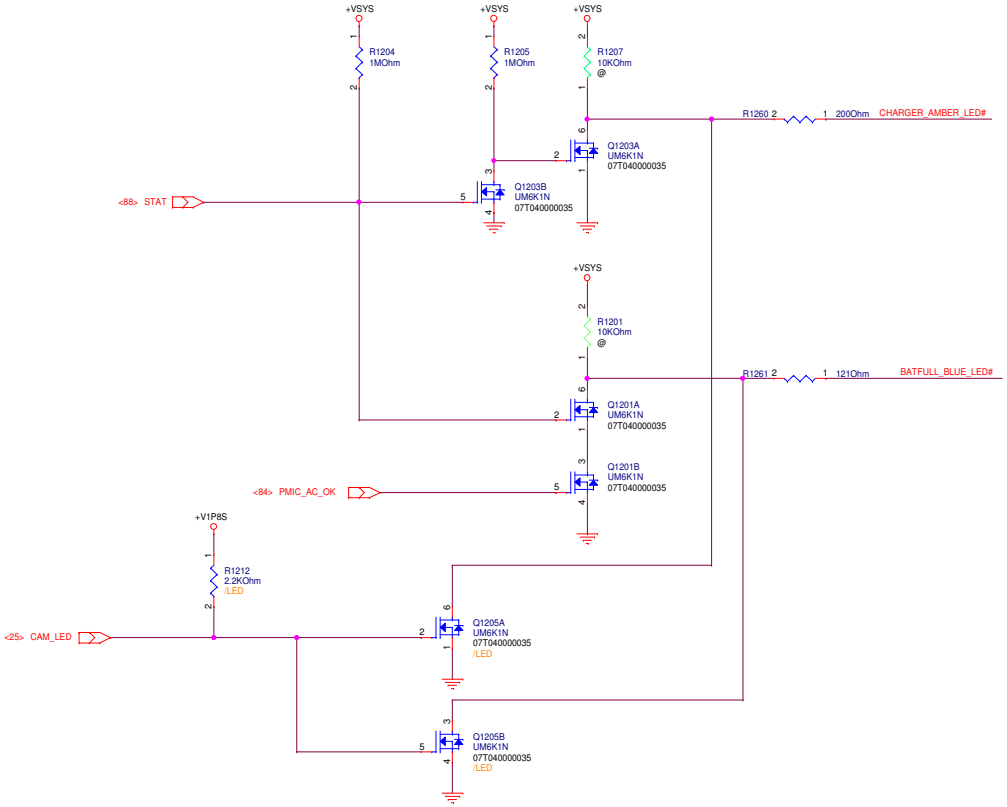
Audio CONN Pin Definition

01. VSYS
02. BATFULL_BLUE_LED#
03. CHARGER_AMBER_LED#
04. V1P8A
05. PWRBTN#
06. VOLUMEUP#
07. VOLUMEDOWN#
08. V3P3A
09. LID_SW#
10. V3P3S_VIB
11. AU_HP_R
12. AU_HP_L
13. AU_HP_JD#
14. AU_COMBO_MIC
15. AGND_AUD
16. GND
17. GND
18. HOMEKEY#

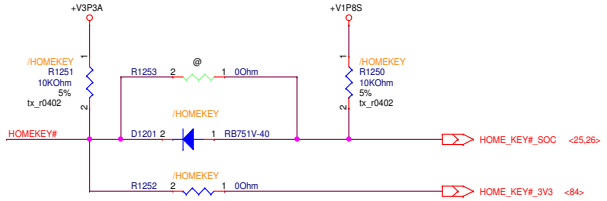




LED



Win8 Home Key



PEGATRON Title : Audio_BDCONN

PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAC2	1.4

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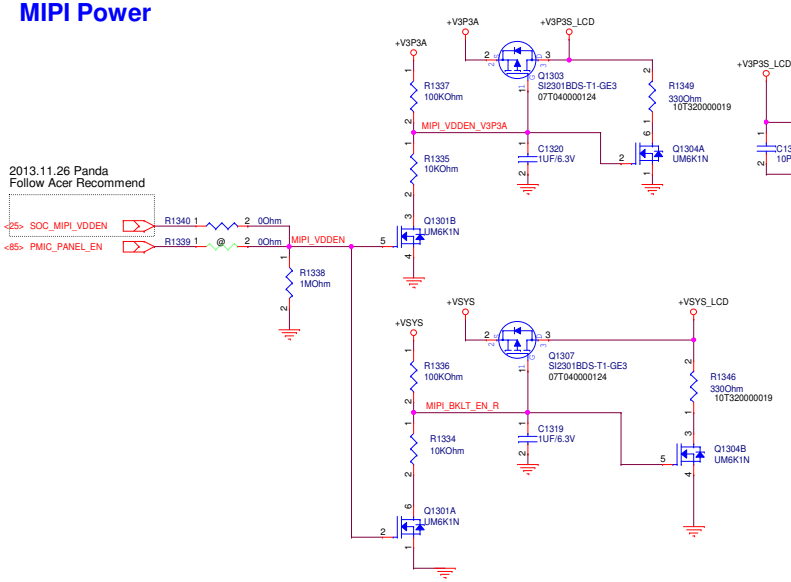
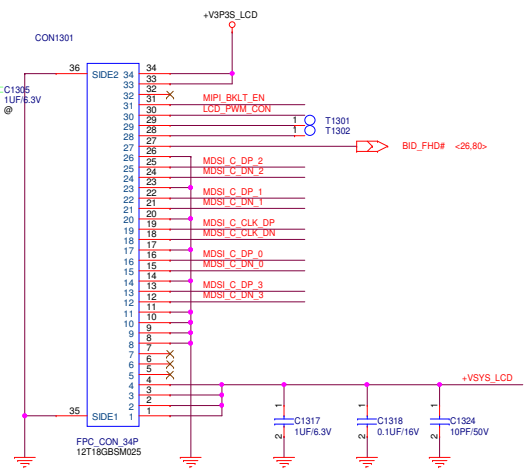
MIPI LCD CONN

MIPI Power



MIPI CONN

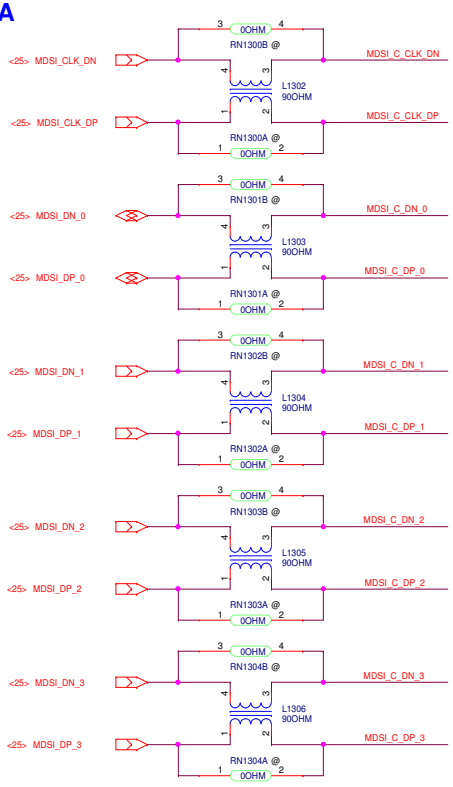


MIPI CONN Pin Definition

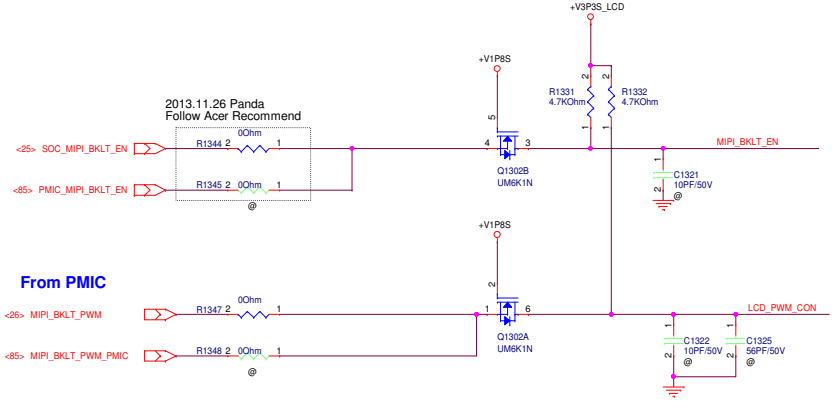
34. VDD(3.3V)
33. VDD(3.3V)
32. NC
31. LED_EN(1.8V)
30. LED_PWM
29. EDID_SDA
28. EDID_SCL
27. BID_FHD#
26. GND
25. MIPI_DSI_D2P
24. MIPI_DSI_D2N
23. GND
22. MIPI_DSI_D1P
21. MIPI_DSI_D1N
20. GND
19. MIPI_DSI_CLKP
18. MIPI_DSI_CLKN
17. GND
16. MIPI_DSI_D0P
15. MIPI_DSI_D0N
14. GND
13. MIPI_DSI_D3P
12. MIPI_DSI_D3N
11. GND
10. GND
09. GND
08. GND
07. NC
06. Aging mode power(AUO only)
05. NC
04. LED+
03. LED+
02. LED+
01. LED+

MIPI CLK, DATA

From SOC



MIPI PWM



PEGATRON CONFIDENTIAL

Title : MIPI LCD CONN

Engineer: Sam

Size	Project Name	Rev
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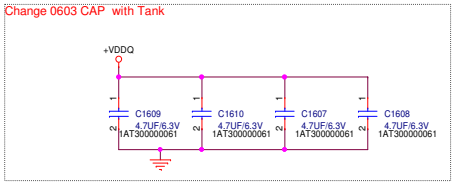
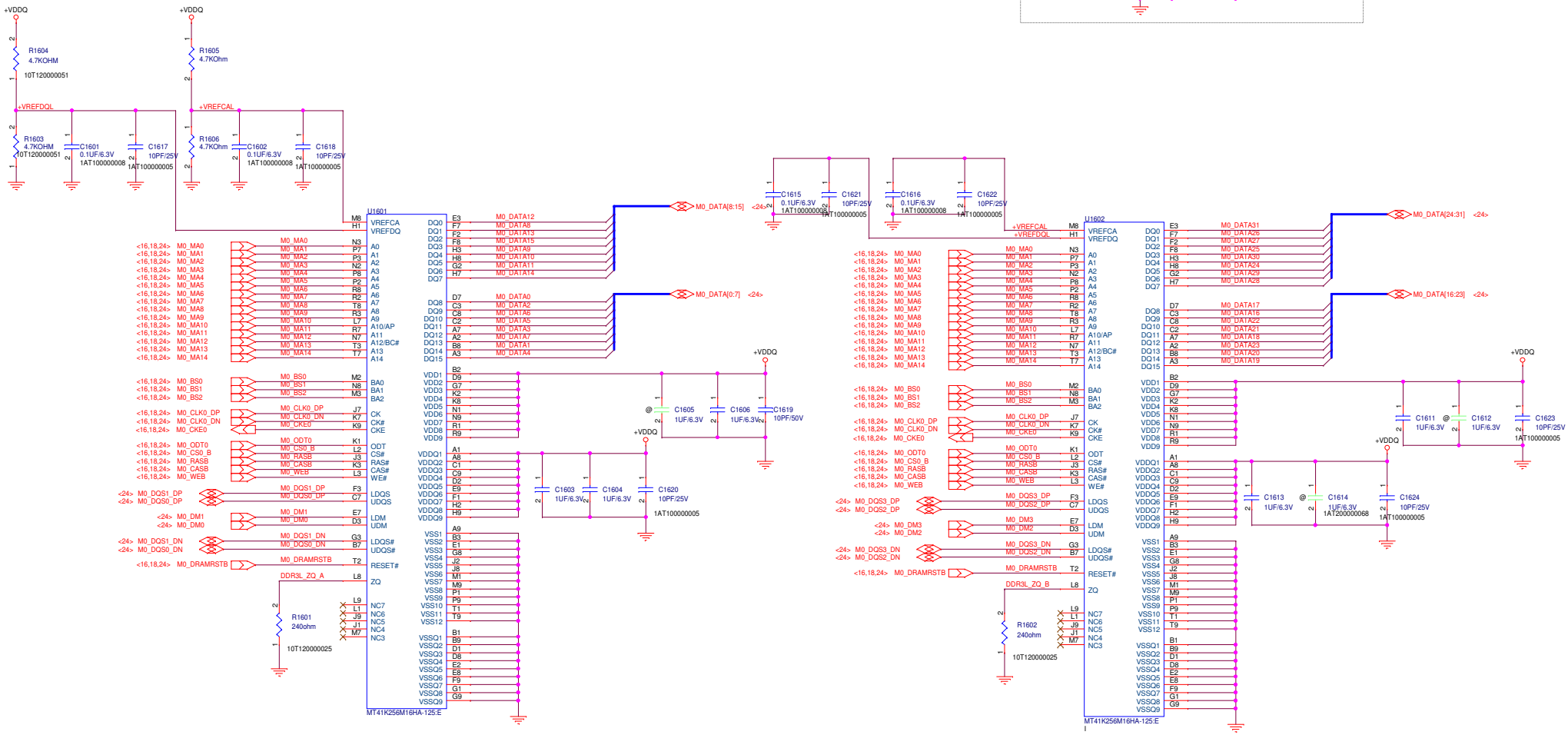
Memory DRAM 1/2

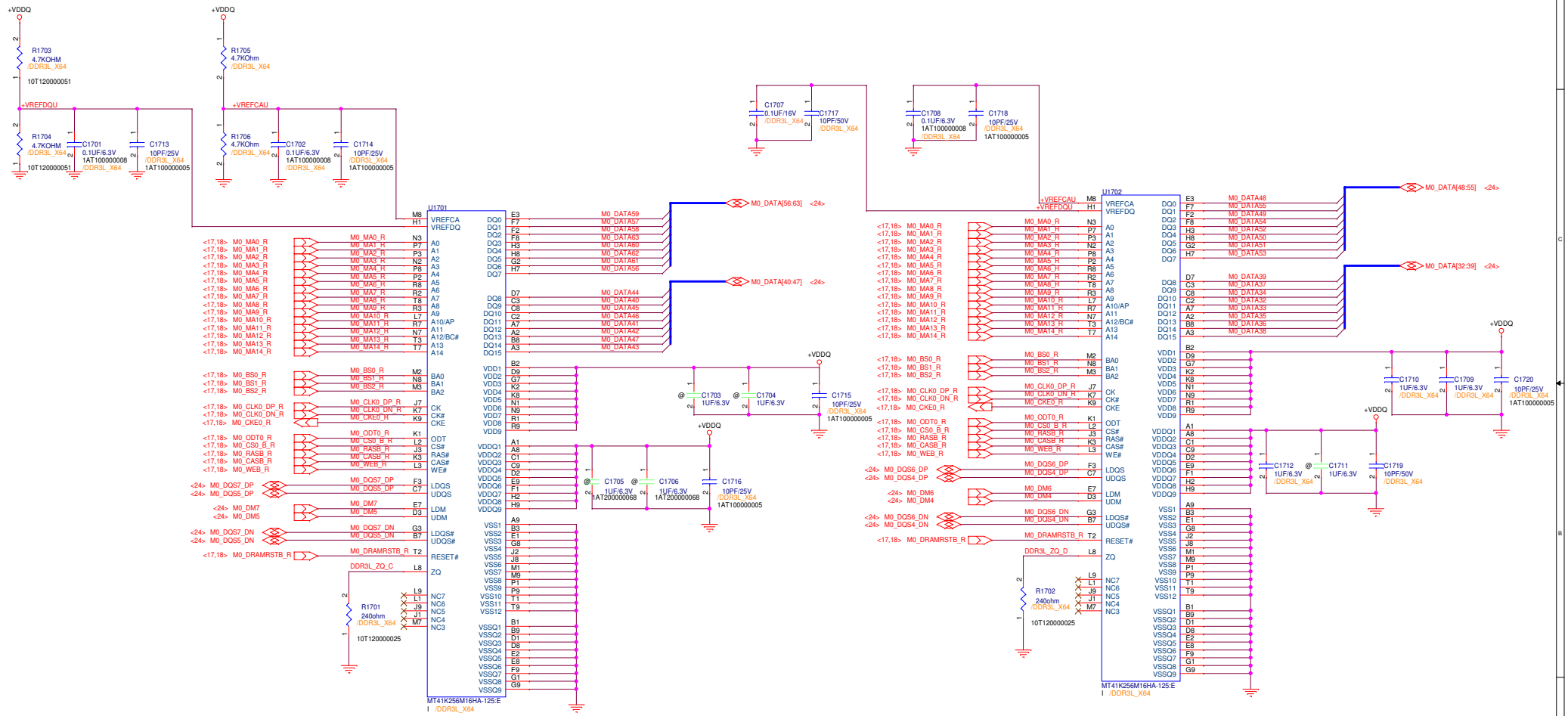
Change 0603 CAP with Tank





PEGATRON	Title : DDR3L(1)	
PEGATRON CONFIDENTIAL	Engineer: Kang_Kang	
Size: C	Project Name: POJAC2	Rev: R1.4
Date: Thursday, April 24, 2014	Sheet 16 of 99	

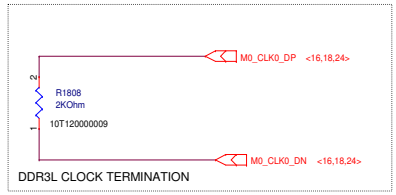
Memory DRAM 3/4



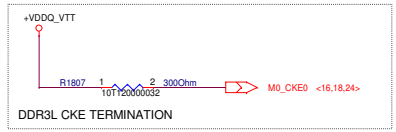
PEGATRON	Title : DDR3L(2)		
PEGATRON CONFIDENTIAL	Engineer: Kang_Kang		
Size: C	Project Name: POJAC2		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 17 of 99		

Memory DRAM Termination Resistor

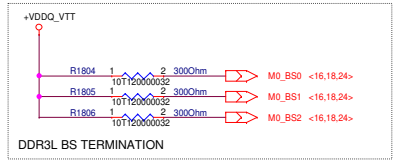
DDR3L MA TERMINATION



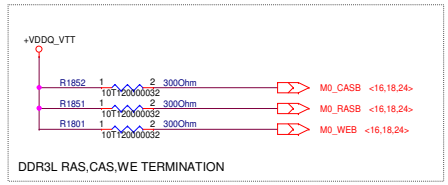
DDR3L CLOCK TERMINATION



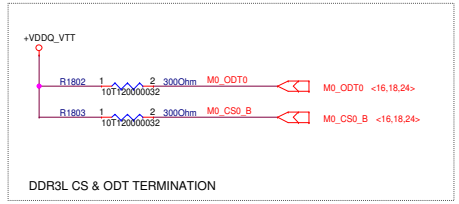
DDR3L CKE TERMINATION



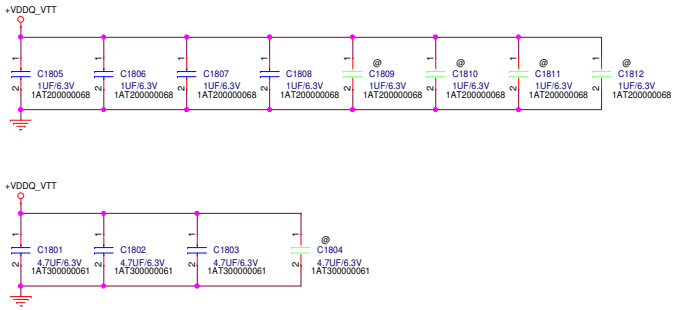
DDR3L BS TERMINATION



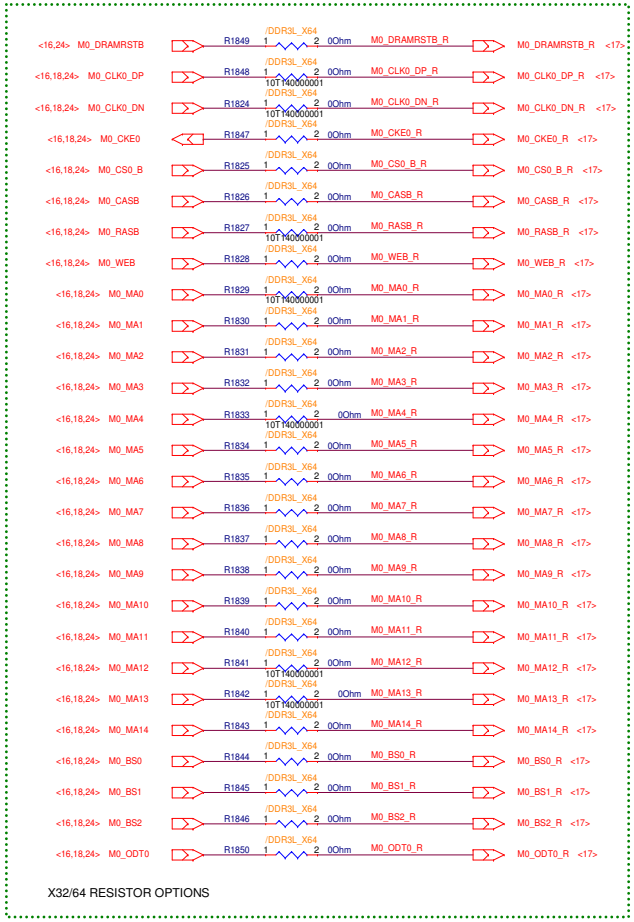
DDR3L RAS,CAS,WE TERMINATION



DDR3L CS & ODT TERMINATION



Average placed close to +VDDQ_VTT power plane



X32/64 RESISTOR OPTIONS

PEGATRON Title : DDR3L(3)

PEGATRON CONFIDENTIAL Engineer: Kang_Kang

Size	Project Name	Rev
C	P0JAC2	1.4

Date: Thursday, April 24, 2014 Sheet 18 of 99

eMMC

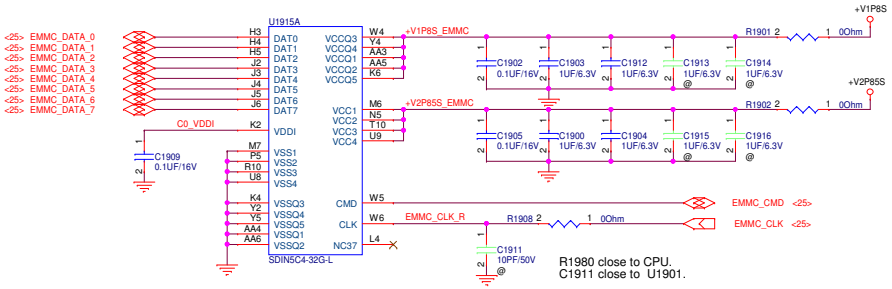
T1901 1 EMMC_DATA_0

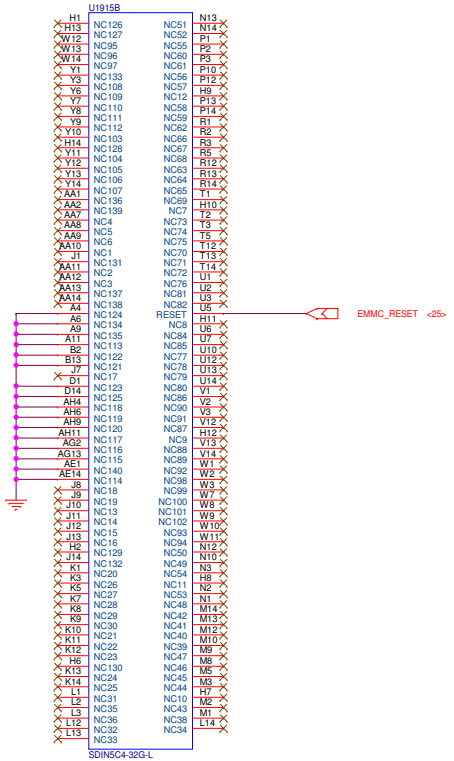
T1902 1 EMMC_RESET

T1903 1 EMMC_CMD

Table 16. Storage Control Cluster (eMMC, SDIO, SD) Interface Signals (Sheet 1 of 2)

Signal Name	Dir	Term	Plat. Power	Default Buffer State			
				S4/S5	Reset	Enter S0	S0ix
MMC1_D[7:0]†	I/O	20k(H)	V1P8S	Off	Pull-up	Pull-up	
MMC1_CMD†	I/O	20k(H)	V1P8S	Off	Pull-up	Pull-up	
MMC1_CLK†	I/O	20k(L)	V1P8S	Off	Pull-down	Pull-down	
MMC1_RST#†	I/O	20k(L)	V1P8S	Off	Pull-down	Pull-down	
MMC1_RCOMP	-	-	V1P8S				
SD2_D[3:0]†	I/O	20k(H)	V1P8S	Off	Pull-up	Pull-up	
SD2_CMD†	I/O	20k(H)	V1P8S	Off	Pull-up	Pull-up	





153 BALL / 169 BALL

eMMC Table			
Priority	eMMC Description	Part Number	TX Part Number
Main Source	SANDISK / FLASH EMMC 32GB FBGA-153	0500-01L10DE	**********
Second Source	HYNIX / FLASH EMMC 32GB FBGA-153	0500-01K50QS	**********
Second Source	SAMSUNG / FLASH EMMC 32GB FBGA-153	0500-01L20DE	**********

PEGATRON Title : eMMC

PEGATRON CONFIDENTIAL Engineer: Kang_Kang

Size	Project Name	Rev
C	P0JAC2	1.4

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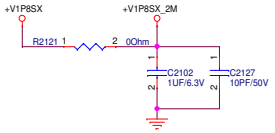
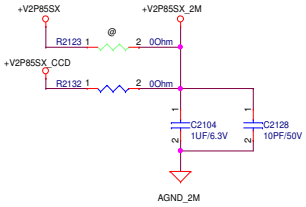
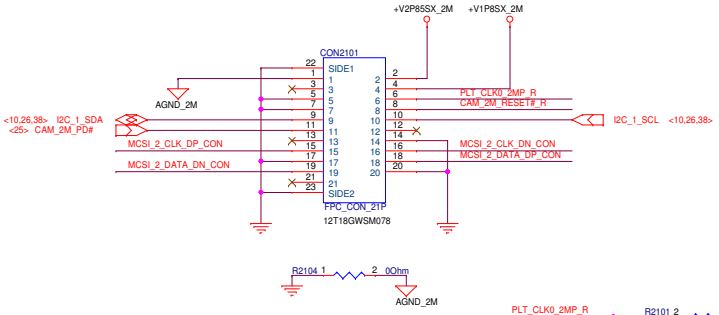
21

2M Camera (Front)



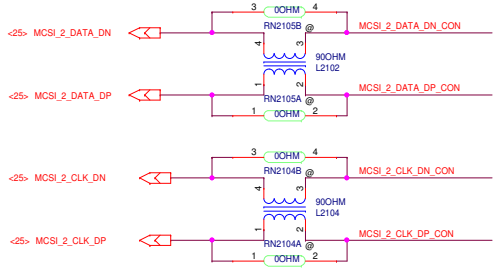


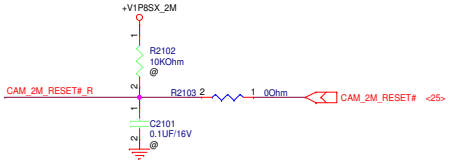
2M CAM Pin Define	
Pin1	AGND
Pin2	AVDD28
Pin3	NC
Pin4	DOVDD18
Pin5	DGND
Pin6	XVCLK
Pin7	DGND
Pin8	RESET#
Pin9	SIO_D
Pin10	SIO_C
Pin11	PWDN
Pin12	LED+(NC)
Pin13	LED+(NC)
Pin14	DGND
Pin15	MIPI_CSI_CLKP
Pin16	MIPI_CSI_CLKN
Pin17	DGND
Pin18	MIPI_CSI_D1P
Pin19	MIPI_CSI_D1N
Pin20	DGND
Pin21	NC



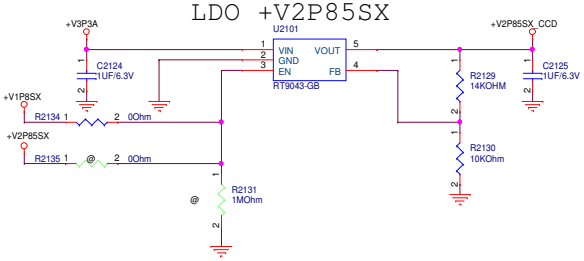
Vendor	MPN	Part Number	I2C_Address
LITEON	13P2SF205		0x6C for write 0x6D for read

CO-LAY





LDO +V2P85SX



PEGATRON Title : Camera_5M/2M

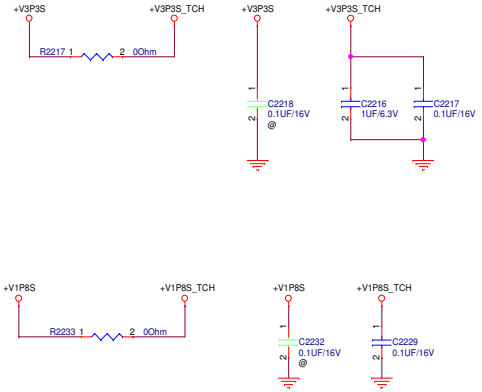
PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAC2	1.4

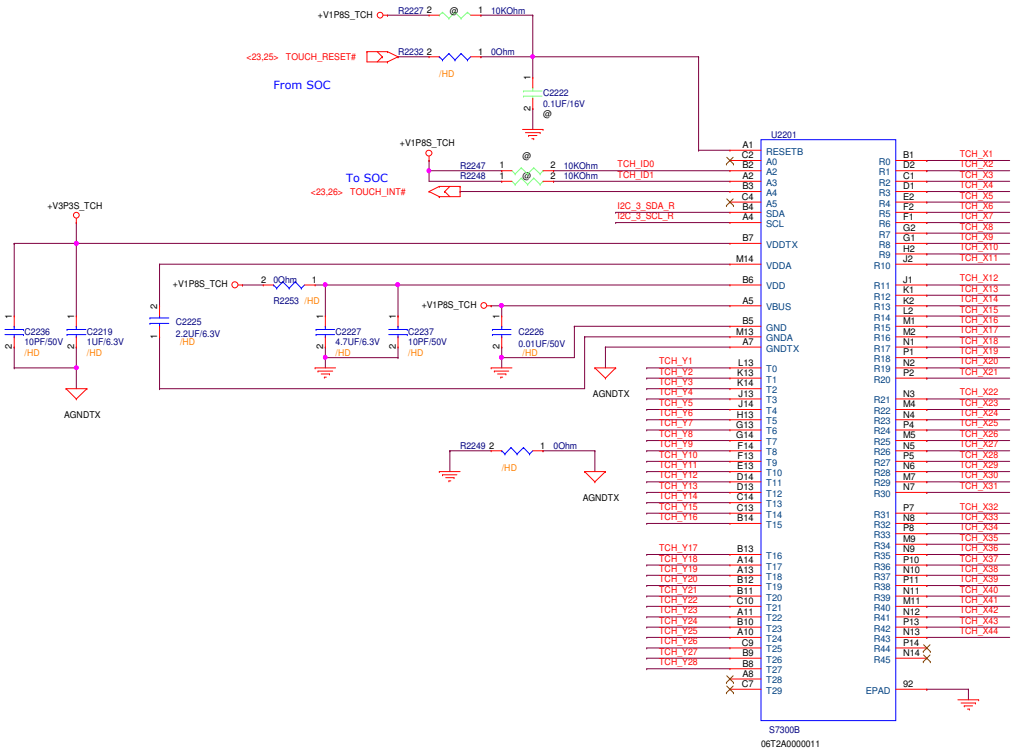
Date: Thursday, April 24, 2014 Sheet 21 of 99

Touch Screen

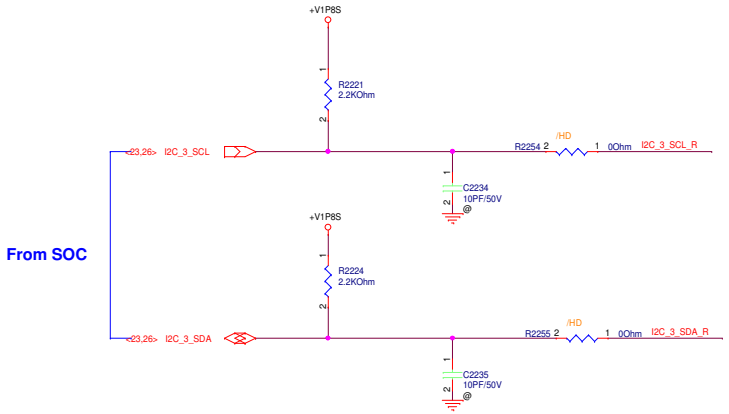
Touch Power



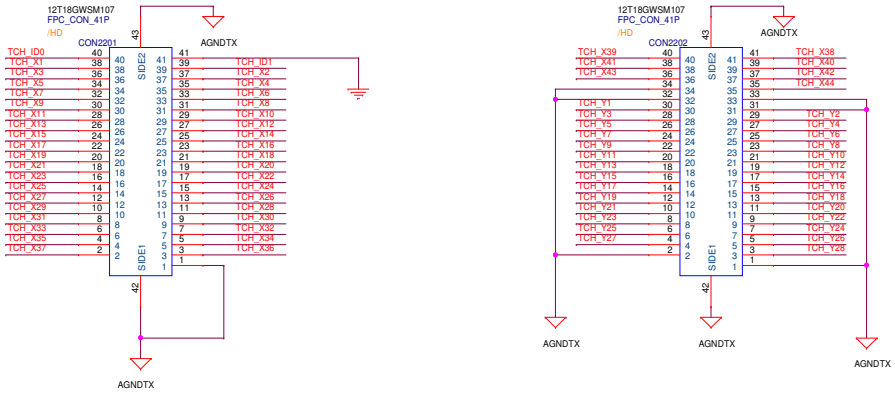
Touch Controller



Touch Panel I2C Interface



Touch Panel CONN



Touch Screen Board ID			
TCH_ID1	TCH_ID0	Panel	Touch Screen
0	0	AUO	O-Film
0	1		SOE
1	0		
1	1		No-Touch

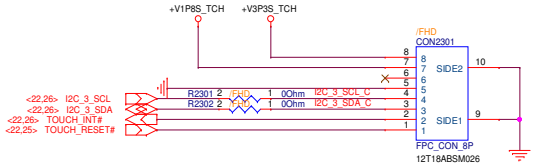
PEGATRON Title : Touch Screen

PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAC2	1.4

Date: Thursday, April 24, 2014 Sheet 22 of 99

FHD_DB CONN



FHD CONN
Pin Define

01. REST
02. INT
03. SDA
04. SCL
05. GROUND
06. NC
07. +V1P8S
08. +V3P3S

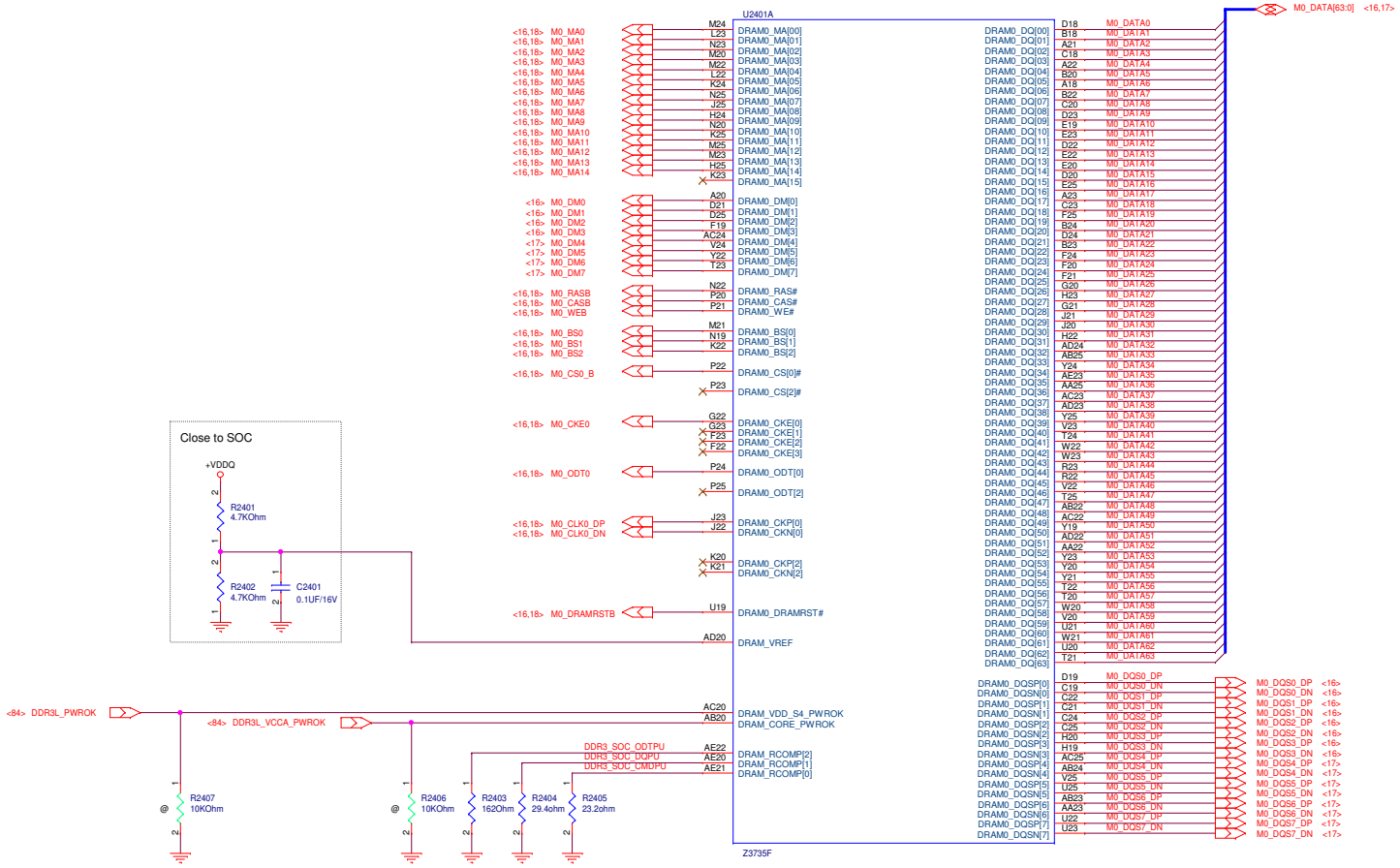
PEGATRON Title : FHD_DB CONN

PEGATRON CONFIDENTIAL Engineer: Corey

Size	Project Name	Rev
C	P0JAC2	1.4

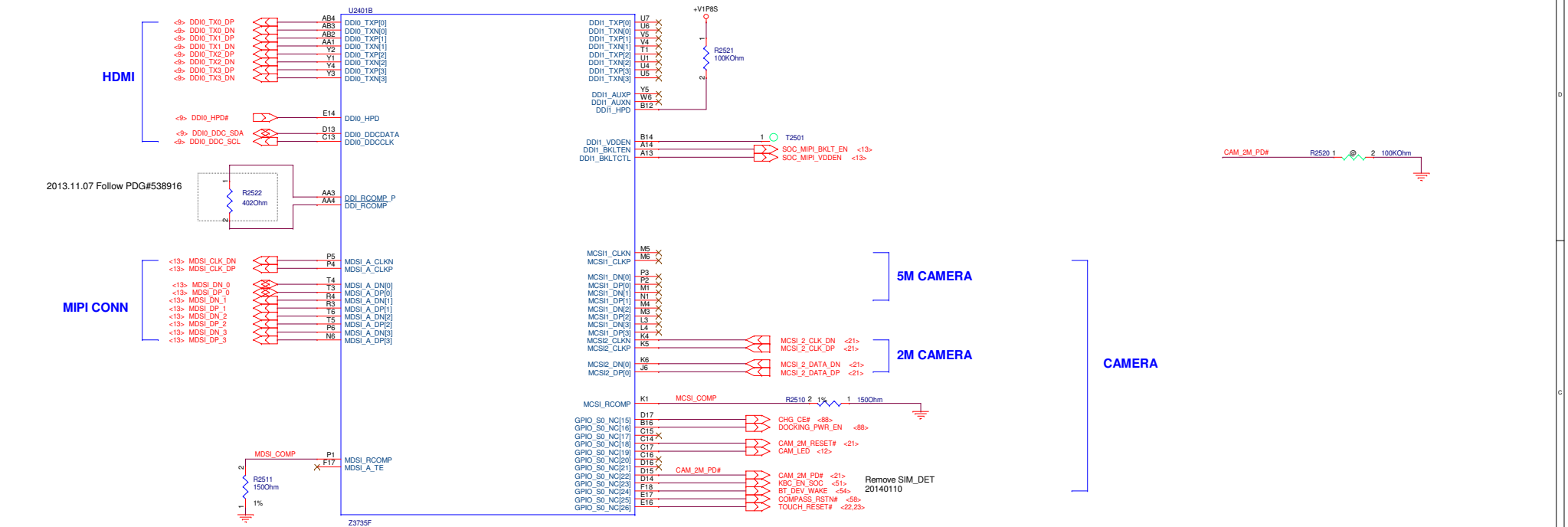
Date: Thursday, April 24, 2014 Sheet 23 of 99

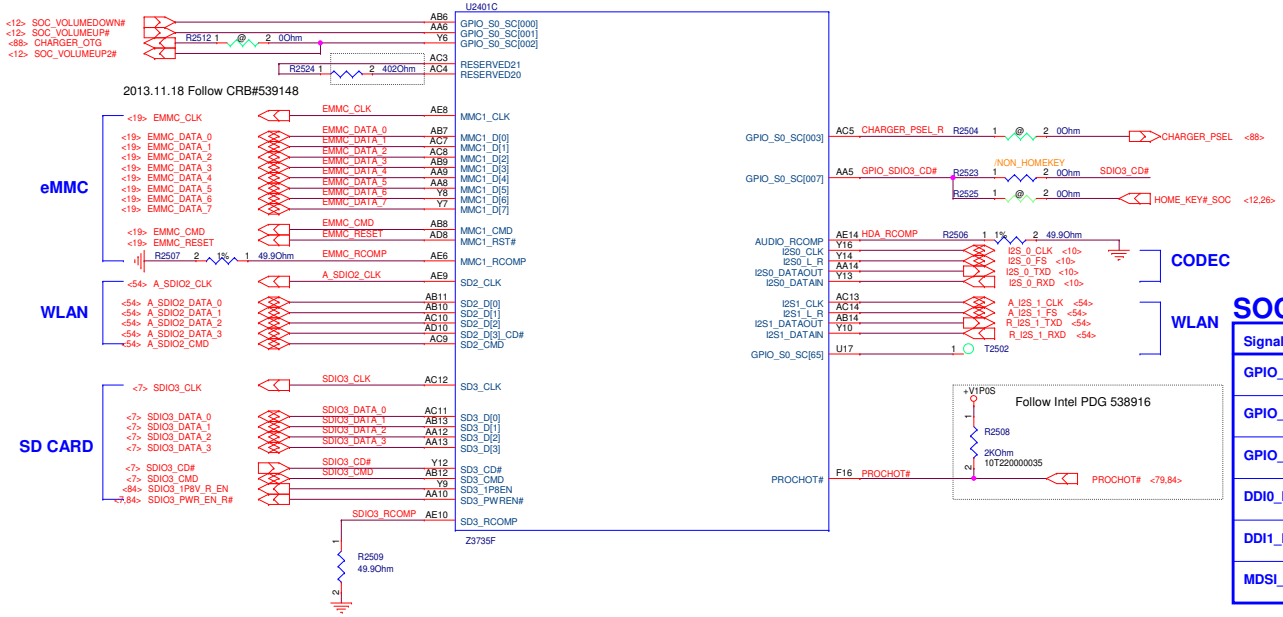
SOC Memory



PEGATRON	Title : CPU-1		
PEGATRON CONFIDENTIAL	Engineer: Kang_Kang		
Size: C	Project Name: POJAC2		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 24 of 99		

SOC DISPLAY,STORAGE,AUD





SOC Straping

Signal Name	Pin No.	Default	Strap Exit	Strap Description	Circuitry
GPIO_S0_56	BU11	1b	PMC_CORE_PWROK de-asserted	Top Swap (A16 Override) 0 = Top address bit is unchanged ☆1 = Top adderss bit is inverted	No external PU/PD
GPIO_S0_63	BT36	1b	PMC_CORE_PWROK de-asserted	BIOS Boot Selection 0 = LPC ☆1 = SPI	No external PU/PD
GPIO_S0_65	BK36	1b	PMC_CORE_PWROK de-asserted	Security Flash Descriptors 0 = Override ☆1 = Normal Operation	No external PU Has a @ 4.7KOhm PD
DDI0_DDC_SDA	H32	0b	PMC_CORE_PWROK de-asserted	DDI0 Detect 0 = DDI0 not detected ☆1 = DDI0 detected	PU to +V1P8S by 2.2KOhm
DDI1_DDC_SDA	F36	0b	PMC_CORE_PWROK de-asserted	DDI1 Detect ☆0 = DDI1 not detected 1 = DDI1 detected	Connect to GND
MDSI_DDC_SDA	B34	0b	PMC_CORE_PWROK de-asserted	MIPI DSI Detect ☆0 = DSI not detected 1 = DSI detected	NC

☆ : Circuitry State

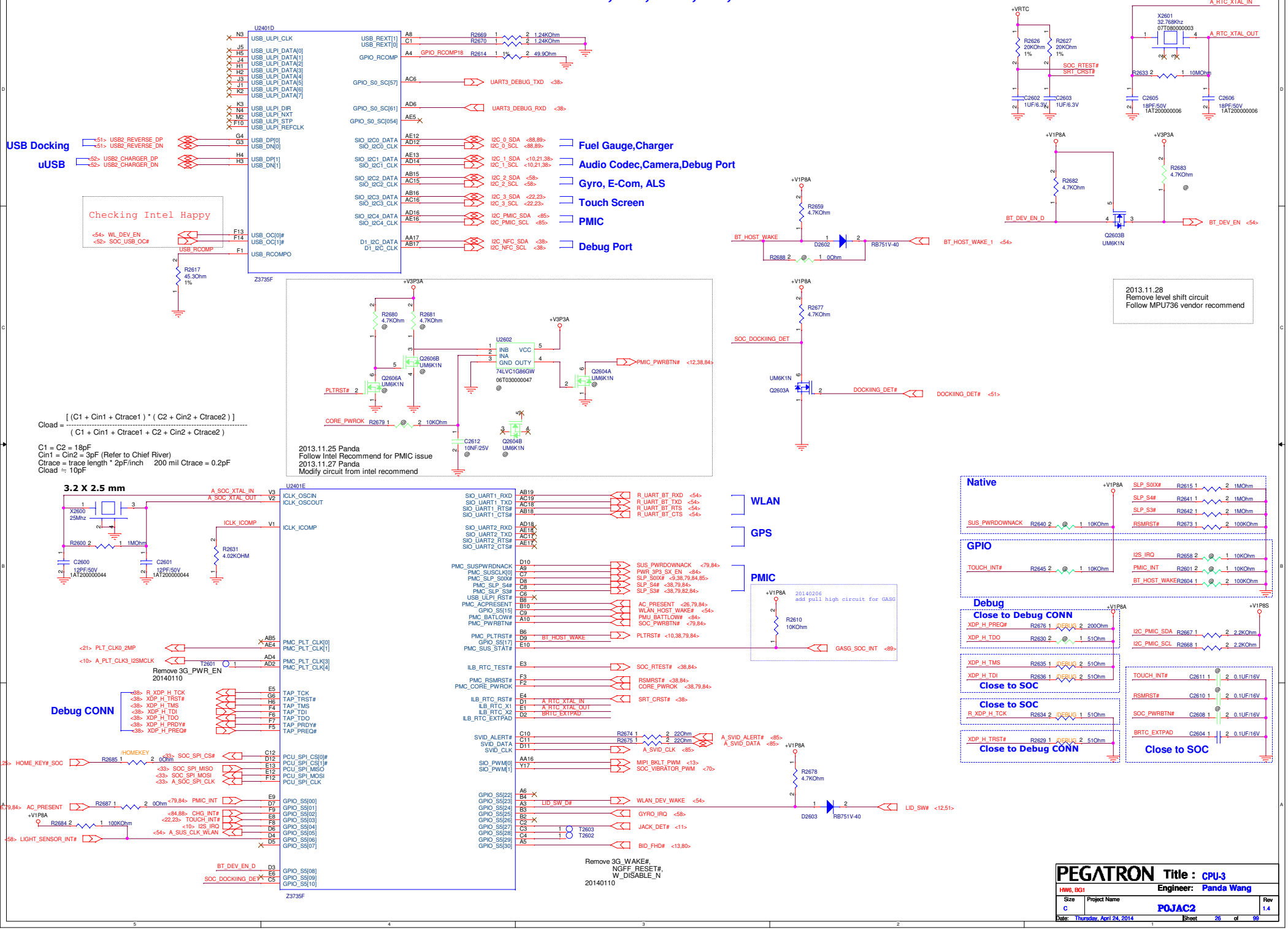
PEGATRON Title : CPU-2

PEGATRON CONFIDENTIAL Engineer: Panda_Wang

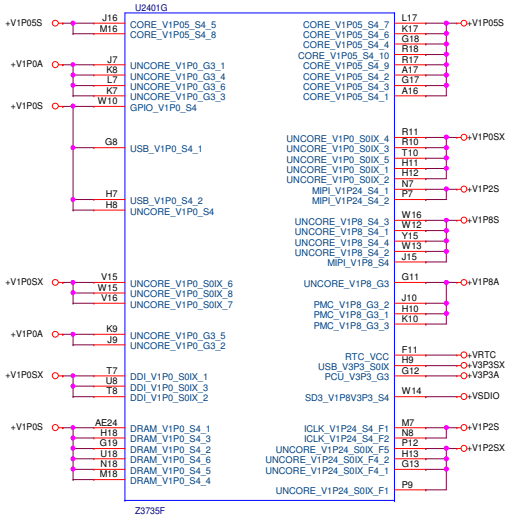
Size	Project Name	Rev
C	P0JAC2	1.4

Date: Thursday, April 24, 2014 Sheet 25 of 99

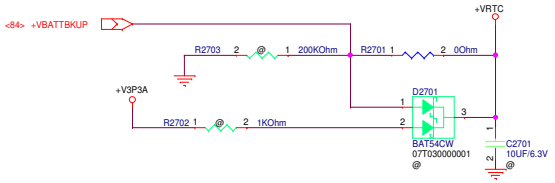
SOC USB,I2C,CLK,DB,MISC



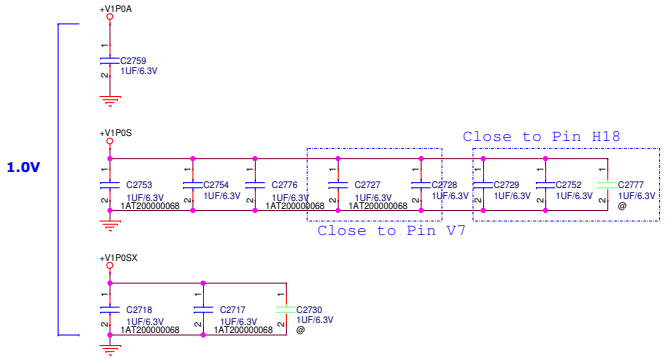
SOC POWER

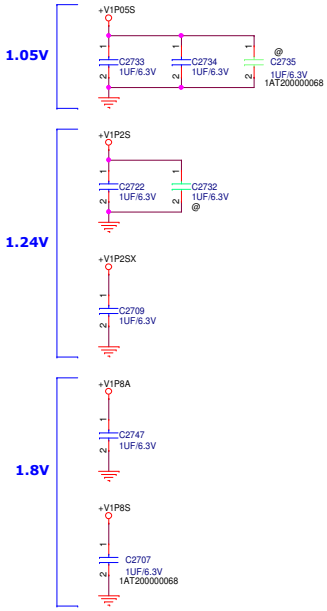


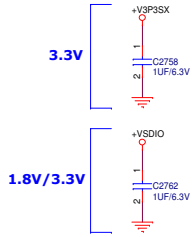
RTC Circuit



SOC DECOUPLING







PEGATRON Title : CPU-4

PEGATRON CONFIDENTIAL Engineer: Panda_Wang

Size	Project Name	Rev
C	POJAC2	1.4

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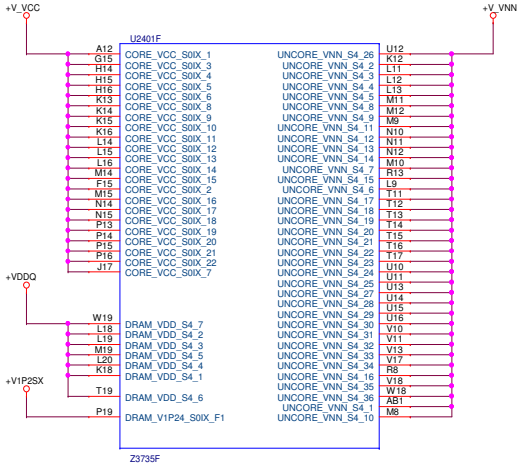
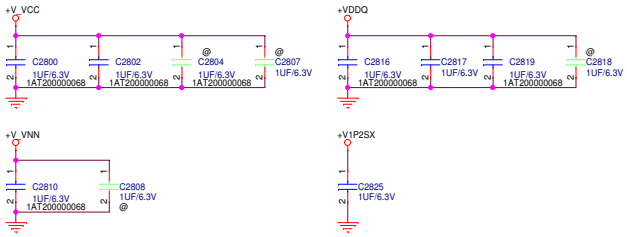
SOC Core Power

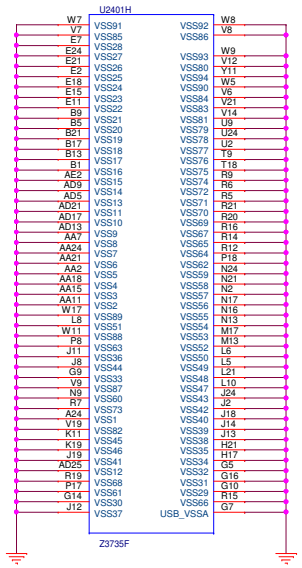


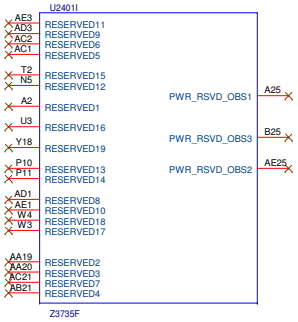
SOC DECOUPLING



PEGATRON	Title : CPU-5	
PEGATRON CONFIDENTIAL	Engineer: Panda1_Wang	
Size: C	Project Name: P0JAC2	Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 28 of 99	

SOC GND





PEGATRON Title : CPU-6

PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAC2	1.4

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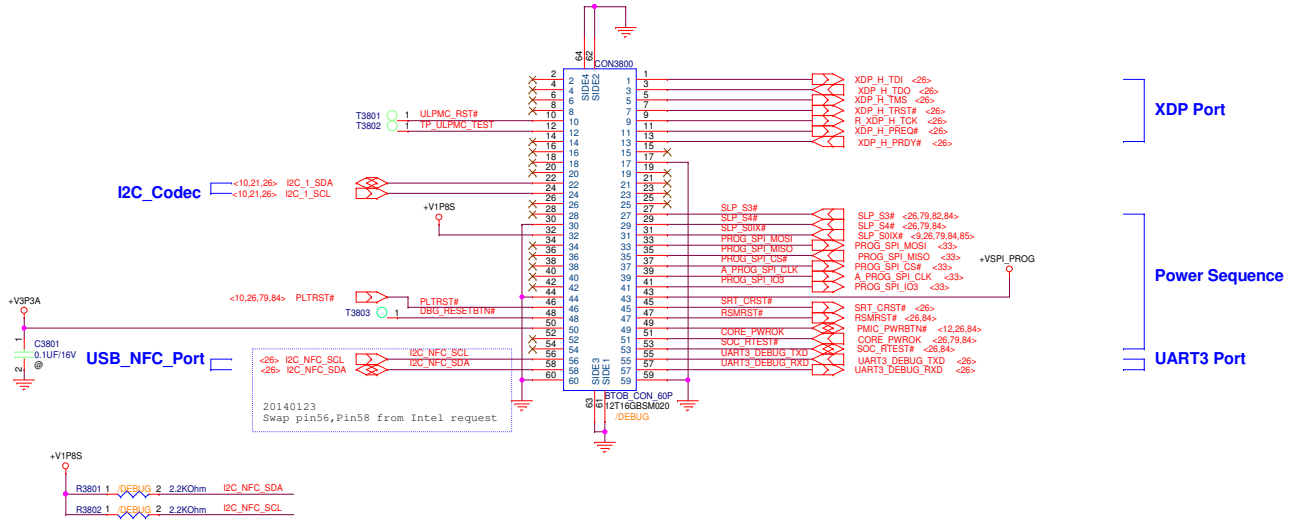
Debug Port

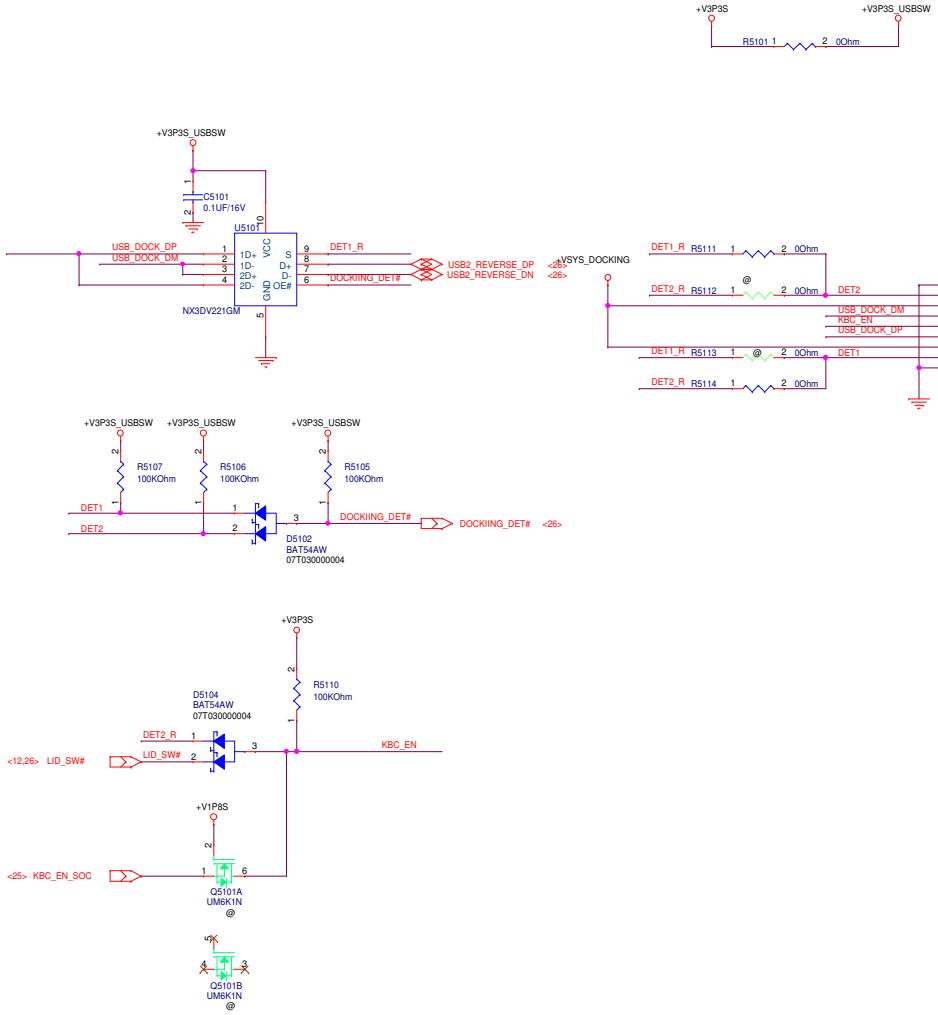


UART Debug Switch

PEGATRON	Title :	DEBUG
PEGATRON CONFIDENTIAL	Engineer:	Panda Wang
Size: C	Project Name: P0JAC2	Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 38 of 99	

POGO Docking

DOCKING Function Table				
DET1	DET2	OE#	S	
1	1	1	X	OFF
0	1	0	L	PORT1
1	0	0	H	PORT2



MB to POGO Pin Definition

1. GND
2. Detect1
3. PWR
4. USB D+
5. KBC_EN
6. USB D-
7. PWR
8. Detect2
9. GND

PEGATRON Title : USB Docking

PEGATRON CONFIDENTIAL Engineer: Panda_Wang

Size	Project Name	Rev
C	P0JAC2	1.4

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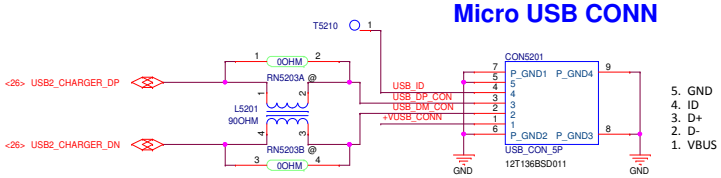
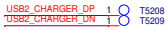
Micro USB

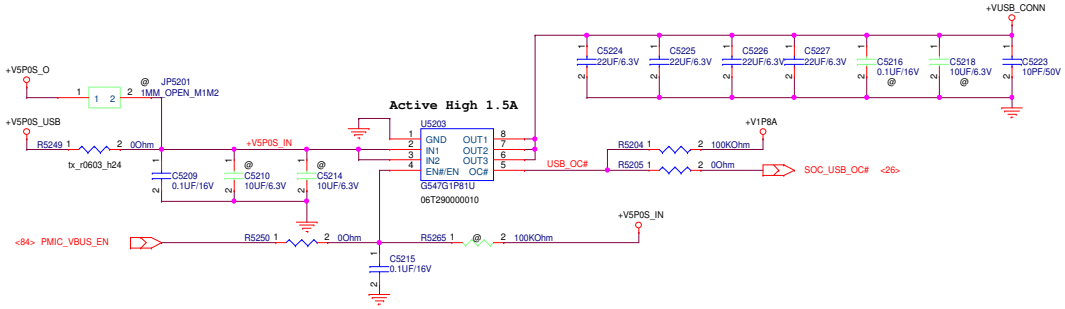
Micro USB CONN



Test Point



Power



PEGATRON CONFIDENTIAL

Title : Micro USB

Engineer: Panda Wang

Size	Project Name	Rev
C	P0JAC2	1.4

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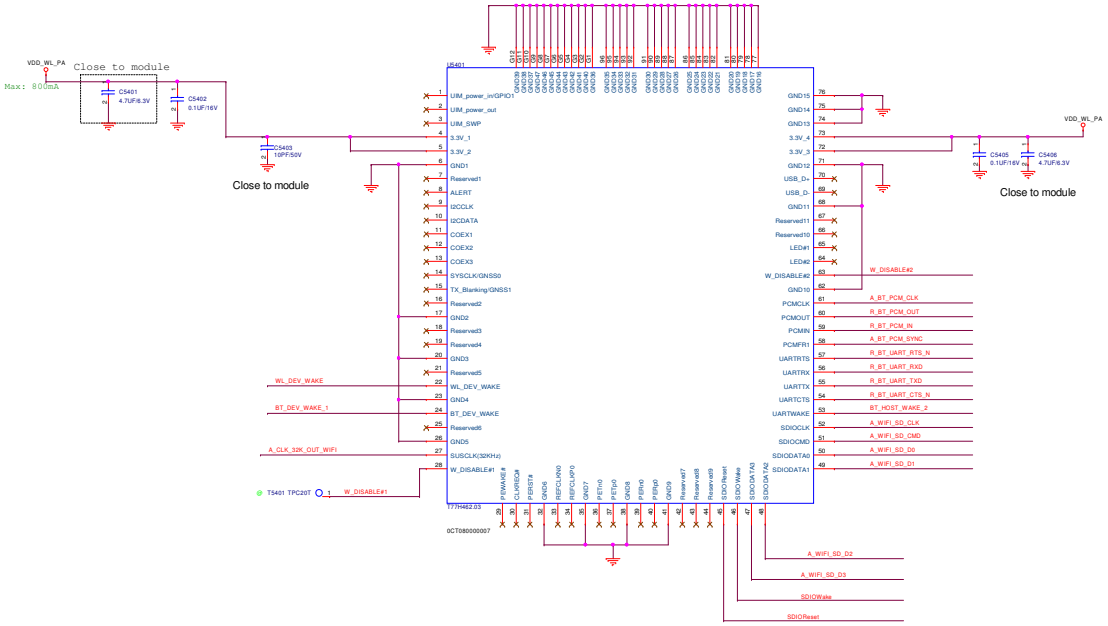
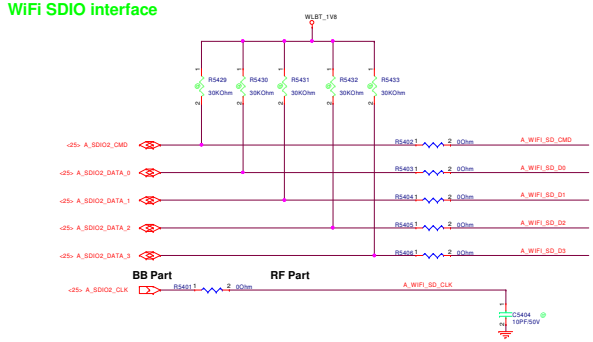
FOXCONN T77H462.03 (BCM43241) co-lay T77H546 (RTL8723BS)

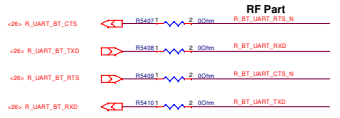


WiFi/BT Interface

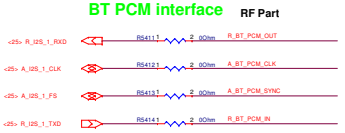
WiFi SDIO interface



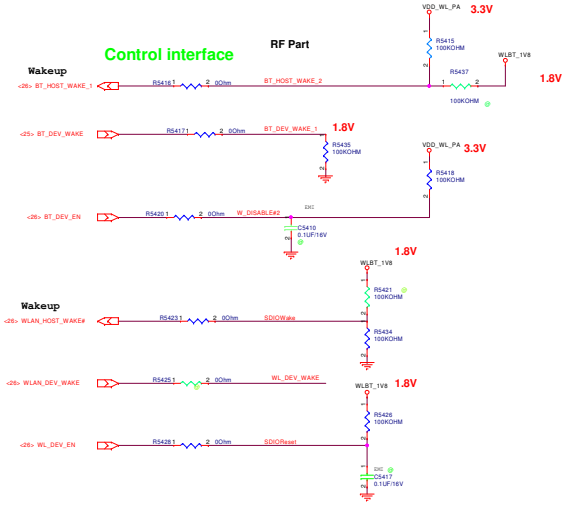
BT UART interface



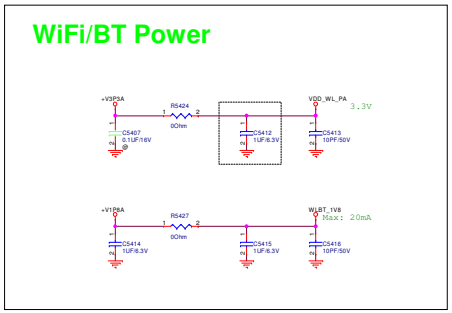
BT PCM interface



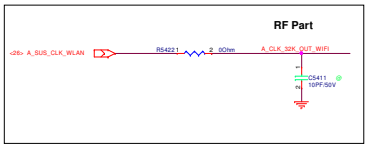
Control interface



WiFi/BT Power



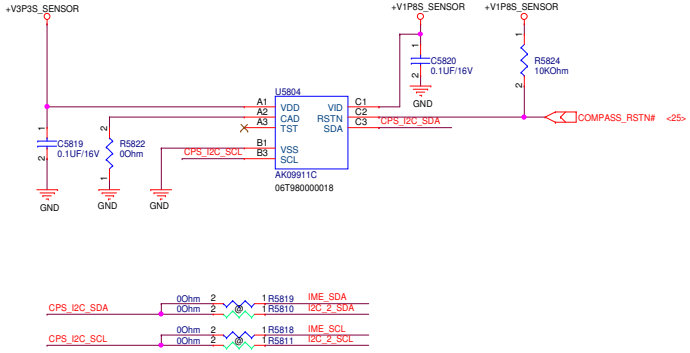
WiFi clock interface



Sensor

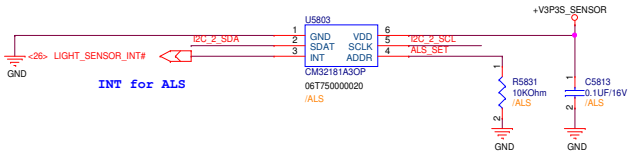
e-Compass

e-Compass I2C Address Table (Pin A2)		
1	0xD	PU 10K
0	0xC	R5822 (default)



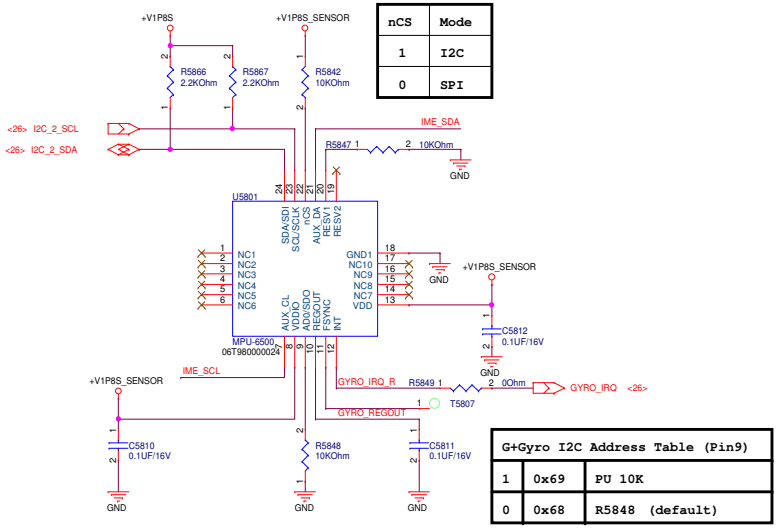
Sensor Power

Light Sensor



ALS I2C Address Table (Pin4)		
1	0x48	PU 10K
0	0x10	R5831(default)

G-Sensor + Gyro



nCS	Mode
1	I2C
0	SPI

G+Gyro I2C Address Table (Pin9)		
1	0x69	PU 10K
0	0x68	R5848 (default)

Remove P Sensor 2014.01.09

PEGATRON Title : SENSOR

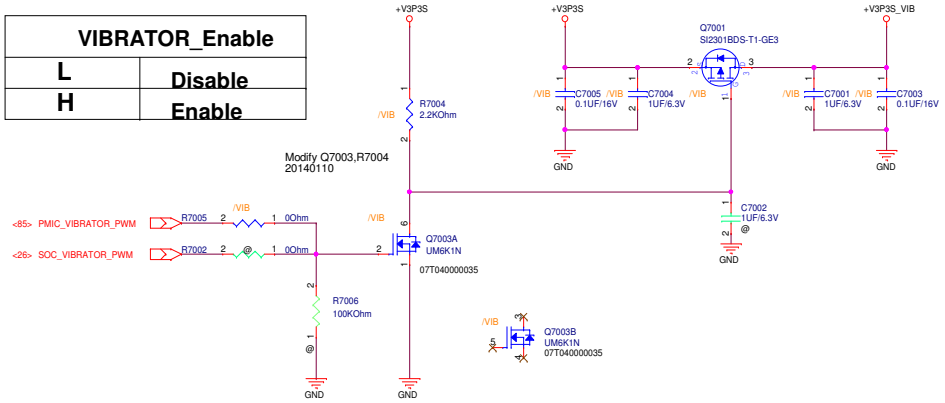
PEGATRON CONFIDENTIAL Engineer: Panda_Wang

Size	Project Name	Rev
C	P0JAC2	1.4

Date: Thursday, April 24, 2014 Sheet 58 of 99

VIBRATOR

VIBRATOR_Enable	
L	Disable
H	Enable



PEGATRON Title : VIBRATOR

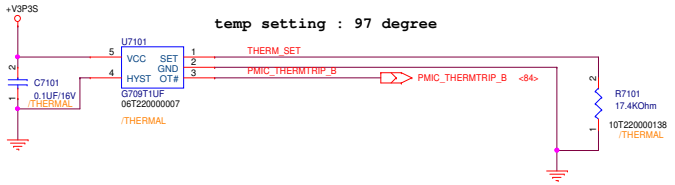
PEGATRON CORPORATION Engineer: Panda_Wang

Size	Project Name	Rev
C	P0JAC2	1.4

Date: Thursday, April 24, 2014 Sheet 70 of 99

Thermal Trip

Location check Thermal



PEGATRON	Title : Reset	
PEGATRON CORPORATION	Engineer: Sam	
Size: C	Project Name: P0JAC2	Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 71 of 99	

ME parts & TP

Test Point

POWER

測點全部放TOP面

+BATT_CN — @T7901

+AC_Adapter — @T7903

+AC_Adapter — @T7905

+V5P0S — @T7911
+V3P3A — @T7912
+V1P8A — @T7913
+V1P8S — @T7928

Memory POWER

+VDDQ — @T7919
+VDDQ_VTT — @T7921

GND

@T7907

@T7908

Battery

<80,88,89> A_BPTHERM_R — @T7909

AUDIO

<11,12> AU_COMBO_MIC — @T7929
<10,12> AU_HP_L — @T7931
<10,12> AU_HP_R — @T7933
<11,12> AU_HP_JD# — @T7934
<10,11> DMIC1_SCL — @T7930

Button

<26,84> SOC_PWRBTN# — @T7935

PWR STATE

<10,26,38,84> PLTRST# — @T7936
<9,26,38,84,85> SLP_S0IX# — @T7938
<26,38,84> SLP_S4# — @T7939
<26,38,82,84> SLP_S3# — @T7940

<26,38,84> CORE_PWROK — @T7943
<26,84> SUS_PWRDOWNACK — @T7923

<25,84> PROCHOT# — @T7947
<26,84> AC_PRESENT — @T7958
<26,84> PMIC_INT — @T7959

SHIELDING FRAME

CPU SHIELDING FRAME

SC7901

NP_NC1, NP_NC2

GND1 … GND11

GND12 … GND27

GND28 … GND40

SHIELDING_59P

POWER SHIELDING FRAME

SC7902

NP_NC1, NP_NC2

GND1 … GND56

GND30 … GND45

GND57 … GND59

SHIELDING_56P

SUPPORT MATEL

SC7905

P0JAB-SUPPORT-MATEL

TOUCH IC SHIELDING FRAME

SC7903

NP_NC1, NP_NC2

GND1 … GND18

SHIELDING_17P

ME Screw

Screw A ,E

H7902

RT279X227CB217D71

Screw B

H7901

C217D71

Screw F None PTH

H7904

C106D106N

Screw G None PTH

H7910

DO106x146N

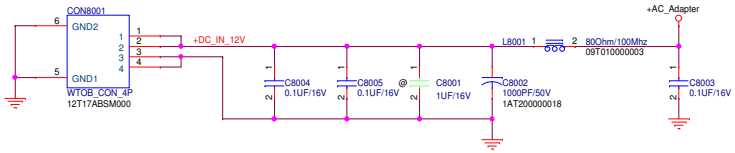
PEGATRON Title : ME/TP

PEGATRON CONFIDENTIAL Engineer: Panda Wang

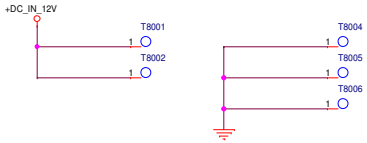
Size	Project Name	Rev
C	P0JAC2	1.1

Date: Thursday, April 24, 2014 Sheet 79 of 99

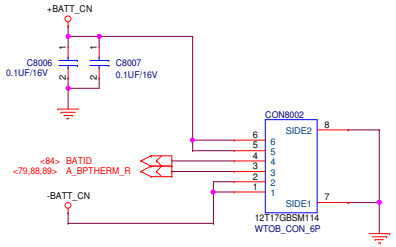
DC JACK



Test Point



BATT CONN



ID Definition

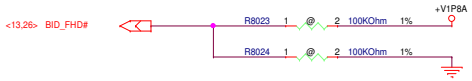
Function	WIFI
Pin#	F8
Net name	BOARD_ID_3
N/A	0
WIFI	1

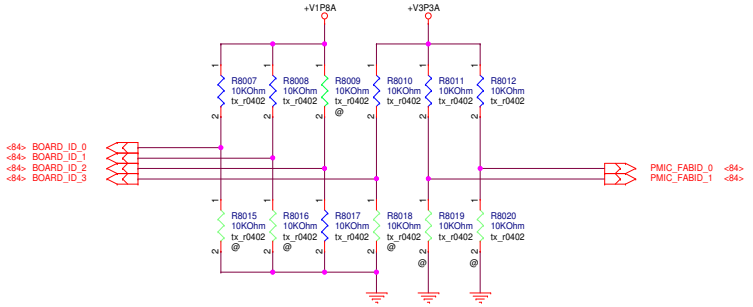
Function	HD/FHD LCM
Pin#	F9
Net name	BID_FHD#
FHD	0
HD	1

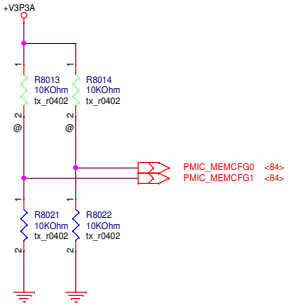
At FHD FPC, BID_FHD# will connected to GND directly.

Function	PCB Version			Project Stage
Pin#	J9	J10	J11	
Net name	BOARD_ID_2	BOARD_ID_1	BOARD_ID_0	
Bit	bit2	bit1	bit0	
Rev 1.0	0	0	0	SR
Rev 1.2	0	0	1	
Rev 1.3	0	1	0	ER
Rev 1.4	0	1	1	PR

MB ID







Function		Memory				Note
Pin#		G9	E10	G8	E11	
Net name		PMIC_MEMCFG0	PMIC_MEMCFG1	PMIC_FABID_0	PMIC_FABID_1	
Bit		bit3	bit2	bit1	bit0	
Micron	1G (X32)	0	0	0	0	MT41K256M16HA-125 M:E
	2G (X64)	0	0	0	1	
Hynix	1G (X32)	0	0	1	0	H5TC4G63AFR-PBA
	2G (X64)	0	0	1	1	

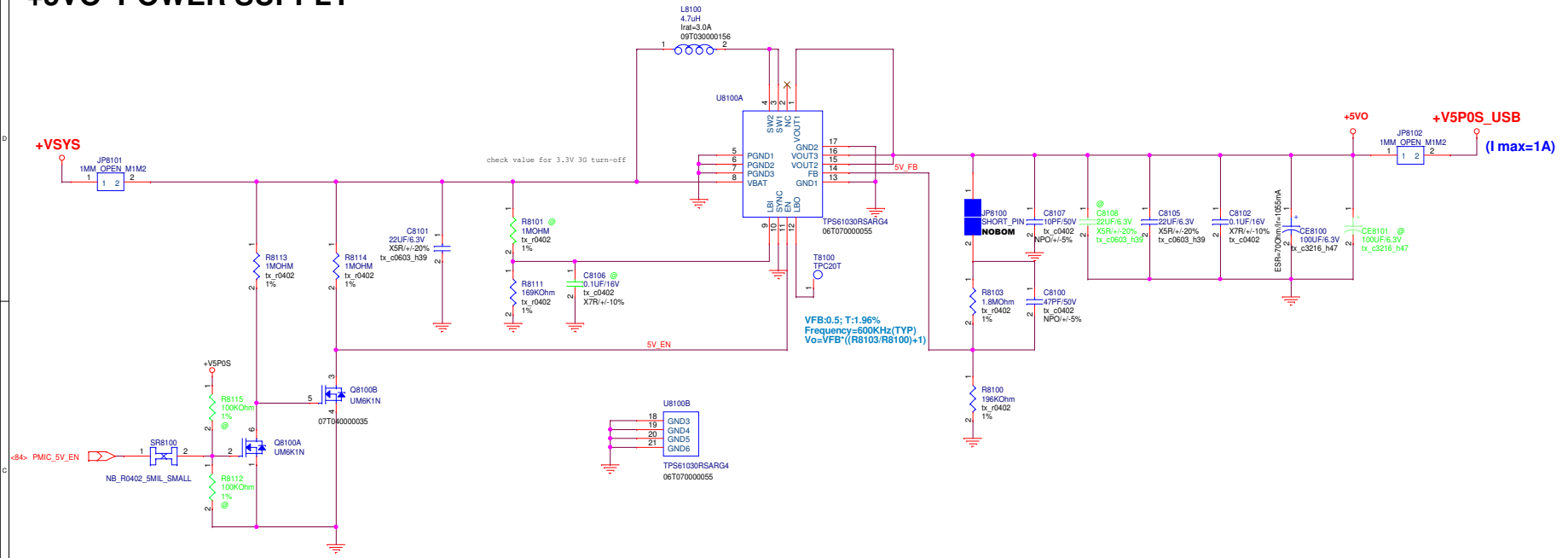
PEGATRON Title : DC JACK

PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAB	1.1

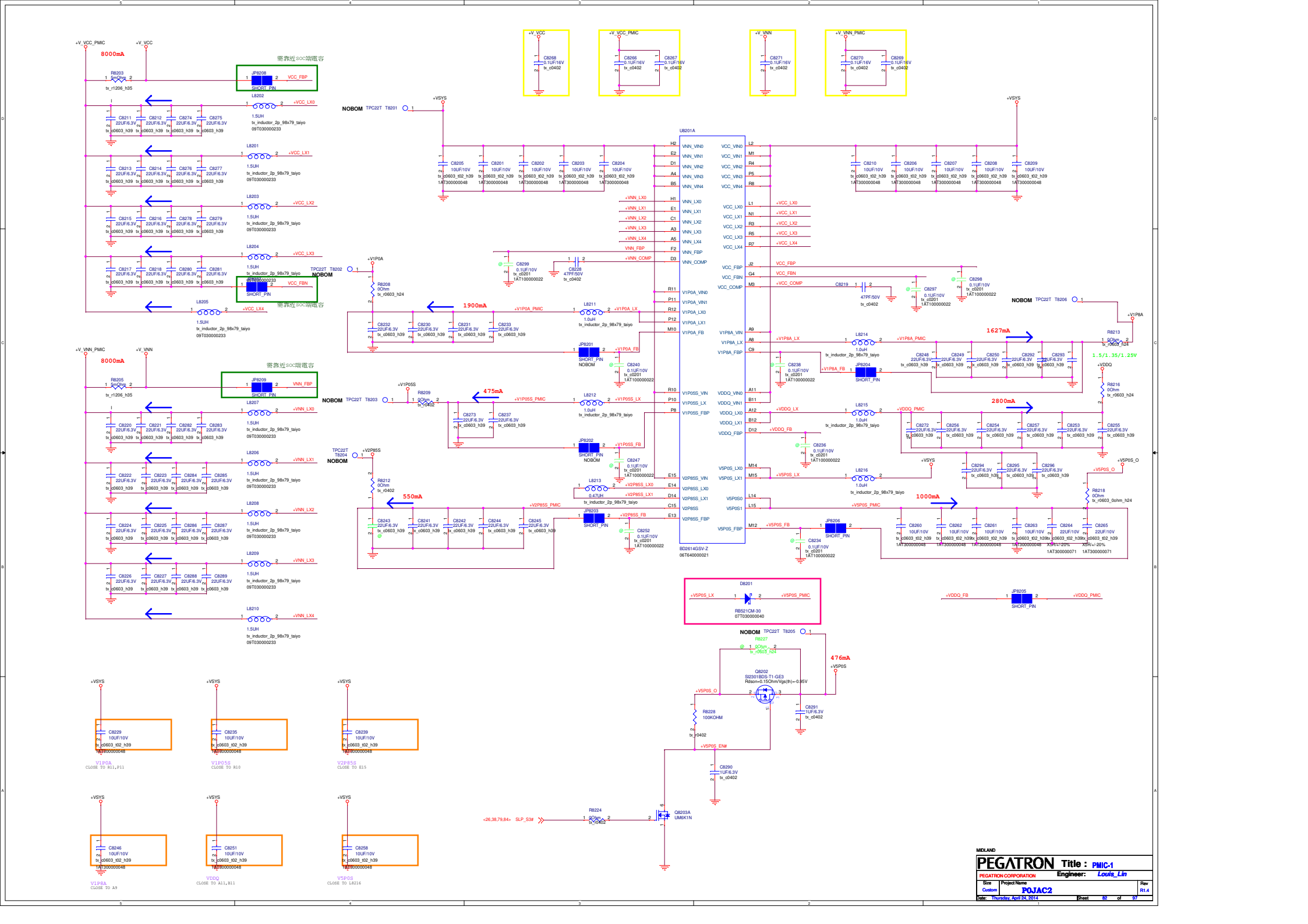
Date: Thursday, April 24, 2014 Sheet 80 of 99

+5VO POWER SUPPLY



<Variant Name>

PEGATRON	Title : DCDC POWER	
PEGATRON CONFIDENTIAL	Engineer: HANK WANG	
Size: C	Project Name: P0JAC2	Rev: R1.4
Date: Thursday, April 24, 2014	Sheet 81 of 97	


+V_VCC_PMIC
+V_VCC
8000mA
需靠近SOC端電容
R8203
5mOhm
tx_r1206_h35
JP8208
SHORT_PIN
VCC_FBP
L8202
+VCC_LX0
1.5UH
tx_inductor_2p_98x79_taiyo
09T030000233
C8211 22UF/6.3V
C8212 22UF/6.3V
C8274 22UF/6.3V
C8275 22UF/6.3V
tx_c0603_h39
L8201
+VCC_LX1
C8213 22UF/6.3V
C8214 22UF/6.3V
C8276 22UF/6.3V
C8277 22UF/6.3V
L8203
+VCC_LX2
C8215 22UF/6.3V
C8216 22UF/6.3V
C8278 22UF/6.3V
C8279 22UF/6.3V
L8204
+VCC_LX3
C8217 22UF/6.3V
C8218 22UF/6.3V
C8280 22UF/6.3V
C8281 22UF/6.3V
TPC22T T8202
NOBOM
JP8207
SHORT_PIN
VCC_FBN
L8205
+VCC_LX4
+V_VNN_PMIC
+V_VNN
8000mA
需靠近SOC端電容
R8205
5mOhm
tx_r1206_h35
JP8209
SHORT_PIN
VNN_FBP
L8207
+VNN_LX0
C8220 22UF/6.3V
C8221 22UF/6.3V
C8282 22UF/6.3V
C8283 22UF/6.3V
L8206
+VNN_LX1
C8222 22UF/6.3V
C8223 22UF/6.3V
C8284 22UF/6.3V
C8285 22UF/6.3V
L8208
+VNN_LX2
C8224 22UF/6.3V
C8225 22UF/6.3V
C8286 22UF/6.3V
C8287 22UF/6.3V
L8209
+VNN_LX3
C8226 22UF/6.3V
C8227 22UF/6.3V
C8288 22UF/6.3V
C8289 22UF/6.3V
L8210
+VNN_LX4
+V_VCC
C8268 0.1UF/16V tx_c0402
+V_VCC_PMIC
C8266 0.1UF/16V tx_c0402
C8267 0.1UF/16V tx_c0402
+V_VNN
C8271 0.1UF/16V tx_c0402
+V_VNN_PMIC
C8270 0.1UF/16V tx_c0402
C8269 0.1UF/16V tx_c0402
NOBOM TPC22T T8201
+VSYS
U8201A
VNN_VIN0
VNN_VIN1
VNN_VIN2
VNN_VIN3
VNN_VIN4
VNN_LX0
VNN_LX1
VNN_LX2
VNN_LX3
VNN_LX4
VNN_FBP
VNN_COMP
VCC_VIN0
VCC_VIN1
VCC_VIN2
VCC_VIN3
VCC_VIN4
VCC_LX0
VCC_LX1
VCC_LX2
VCC_LX3
VCC_LX4
VCC_FBP
VCC_FBN
VCC_COMP
V1P0A_VIN0
V1P0A_VIN1
V1P0A_LX0
V1P0A_LX1
V1P0A_FB
V1P8A_VIN
V1P8A_LX
V1P8A_FBP
VDDQ_VIN0
VDDQ_VIN1
VDDQ_LX0
VDDQ_LX1
VDDQ_FBP
V1P05S_VIN
V1P05S_LX
V1P05S_FBP
V2P85S_VIN
V2P85S_LX0
V2P85S_LX1
V2P85S
V2P85S_FBP
V5P0S_LX0
V5P0S_LX1
V5P0S0
V5P0S1
V5P0S_FBP
BD2614GSV-Z
06T640000021
C8205 10UF/10V tx_c0603_t02_h39 1AT300000048
C8201 10UF/10V tx_c0603_t02_h39 1AT300000048
C8202 10UF/10V tx_c0603_t02_h39 1AT300000048
C8203 10UF/10V tx_c0603_t02_h39 1AT300000048
C8204 10UF/10V tx_c0603_t02_h39 1AT300000048
C8210 10UF/10V tx_c0603_t02_h39 1AT300000048
C8306 10UF/10V tx_c0603_t02_h39 1AT300000048
C8207 10UF/10V tx_c0603_t02_h39 1AT300000048
C8208 10UF/10V tx_c0603_t02_h39 1AT300000048
C8209 10UF/10V tx_c0603_t02_h39 1AT300000048
C8299 0.1UF/10V tx_c0201 1AT100000022
C8228 47PF/50V tx_c0402
C8219 47PF/50V tx_c0402
C8297 0.1UF/10V tx_c0201 1AT100000022
C8298 0.1UF/10V tx_c0201 1AT100000022
NOBOM TPC22T T8206
+V1P8A
1627mA
R8213 0Ohm tx_r0603_h24
1.5/1.35/1.25V
+VDDQ
R8216 0Ohm tx_r0603_h24
+V1P0A
1900mA
R8208 0Ohm tx_r0603_h24
+V1P0A_PMIC
L8211 1.0uH tx_inductor_2p_98x79_taiyo
C8232 22UF/6.3V tx_c0603_h39
C8230 22UF/6.3V tx_c0603_h39
C8231 22UF/6.3V tx_c0603_h39
C8233 22UF/6.3V tx_c0603_h39
JP8201 SHORT_PIN NOBOM
+V1P0A_LX
+V1P0A_FB
C8240 0.1UF/10V tx_c0201 1AT100000022
+V1P8A_PMIC
L8214 1.0uH tx_inductor_2p_98x79_taiyo
+V1P8A_FB
JP8204 SHORT_PIN
C8238 0.1UF/10V tx_c0201 1AT100000022
C8248 22UF/6.3V tx_c0603_h39
C8249 22UF/6.3V tx_c0603_h39
C8250 22UF/6.3V tx_c0603_h39
C8292 22UF/6.3V tx_c0603_h39
C8293 22UF/6.3V tx_c0603_h39
+V1P05S
475mA
R8209 0Ohm tx_r0402
NOBOM TPC22T T8203
+V1P05S_PMIC
L8212 1.0uH tx_inductor_2p_98x79_taiyo
C8273 22UF/6.3V tx_c0603_h39
C8237 22UF/6.3V tx_c0603_h39
+V1P05S_LX
JP8202 SHORT_PIN NOBOM
+V1P05S_FB
C8247 0.1UF/10V tx_c0201 1AT100000022
TPC22T T8204
NOBOM
+V2P85S
R8212 0Ohm tx_r0402
550mA
+V2P85S_PMIC
L8213 0.47UH tx_inductor_2p_98x79_taiyo
+V2P85S_LX0
+V2P85S_LX1
C8243 22UF/6.3V tx_c0603_h39
C8241 22UF/6.3V tx_c0603_h39
C8242 22UF/6.3V tx_c0603_h39
C8244 22UF/6.3V tx_c0603_h39
C8245 22UF/6.3V tx_c0603_h39
JP8203 SHORT_PIN
+V2P85S_FB
C8252 0.1UF/10V tx_c0201 1AT100000022
+VDDQ_PMIC
L8215 1.0uH tx_inductor_2p_98x79_taiyo
+VDDQ_LX
+VDDQ_FB
C8236 0.1UF/10V tx_c0201 1AT100000022
2800mA
C8272 22UF/6.3V tx_c0603_h39
C8256 22UF/6.3V tx_c0603_h39
C8254 22UF/6.3V tx_c0603_h39
C8257 22UF/6.3V tx_c0603_h39
C8253 22UF/6.3V tx_c0603_h39
C8255 22UF/6.3V tx_c0603_h39
+V5P0S_O
R8218 0Ohm tx_r0603_0ohm_h24
+V5P0S_PMIC
L8216 1.0uH tx_inductor_2p_98x79_taiyo
+V5P0S_LX
+V5P0S_FB
JP8206 SHORT_PIN
C8234 0.1UF/10V tx_c0201 1AT100000022
1000mA
C8260 10UF/10V tx_c0603_t02_h39 1AT300000048
C8262 10UF/10V tx_c0603_t02_h39 1AT300000048
C8261 10UF/10V tx_c0603_t02_h39 1AT300000048
C8263 10UF/10V tx_c0603_t02_h39 1AT300000048
C8264 22UF/10V tx_c0603_t02_h39 1AT300000071
C8265 22UF/10V tx_c0603_t02_h39 1AT300000071
C8294 22UF/6.3V tx_c0603_h39
C8295 22UF/6.3V tx_c0603_h39
C8296 22UF/6.3V tx_c0603_h39
D8201
RB521CM-30
07T030000040
JP8205 SHORT_PIN
NOBOM TPC22T T8205
R8227 0Ohm tx_r0603_h24
476mA
+V5P0S
Q8202
Si2301BDS-T1-GE3
Rdson=0.15Ohm Vgs(th)=-0.95V
R8228 100KOHM tx_r0402
C8291 1UF/6.3V tx_c0402
+V5P0S_EN#
C8290 1UF/6.3V tx_c0402
R8224 0Ohm tx_r0402
<26,38,79,84> SLP_S3#
Q8203A
UM6K1N
C8229 10UF/10V tx_c0603_t02_h39 1AT300000048
V1P0A
CLOSE TO R11,P11
C8235 10UF/10V tx_c0603_t02_h39 1AT300000048
V1P05S
CLOSE TO R10
C8239 10UF/10V tx_c0603_t02_h39 1AT300000048
V2P85S
CLOSE TO E15
C8246 10UF/10V tx_c0603_t02_h39 1AT300000048
V1P8A
CLOSE TO A9
C8251 10UF/10V tx_c0603_t02_h39 1AT300000048
VDDQ
CLOSE TO A11,B11
C8258 10UF/10V tx_c0603_t02_h39 1AT300000048
V5P0S
CLOSE TO L8216
MIDLAND
PEGATRON
Title : PMIC-1
PEGATRON CORPORATION
Engineer: Louis_Lin
Size Custom
Project Name POJAC2
Rev R1.4
Date: Thursday, April 24, 2014
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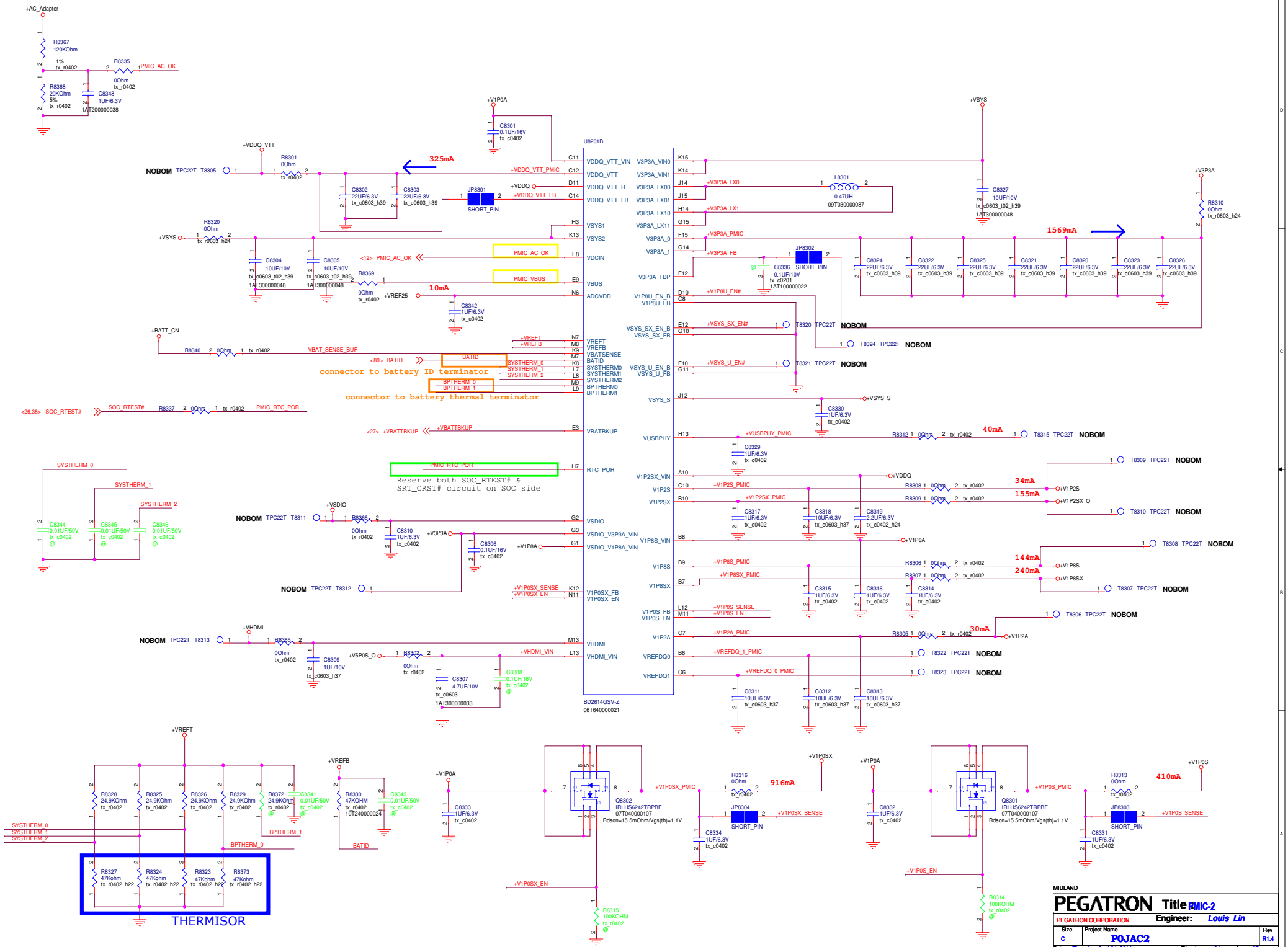
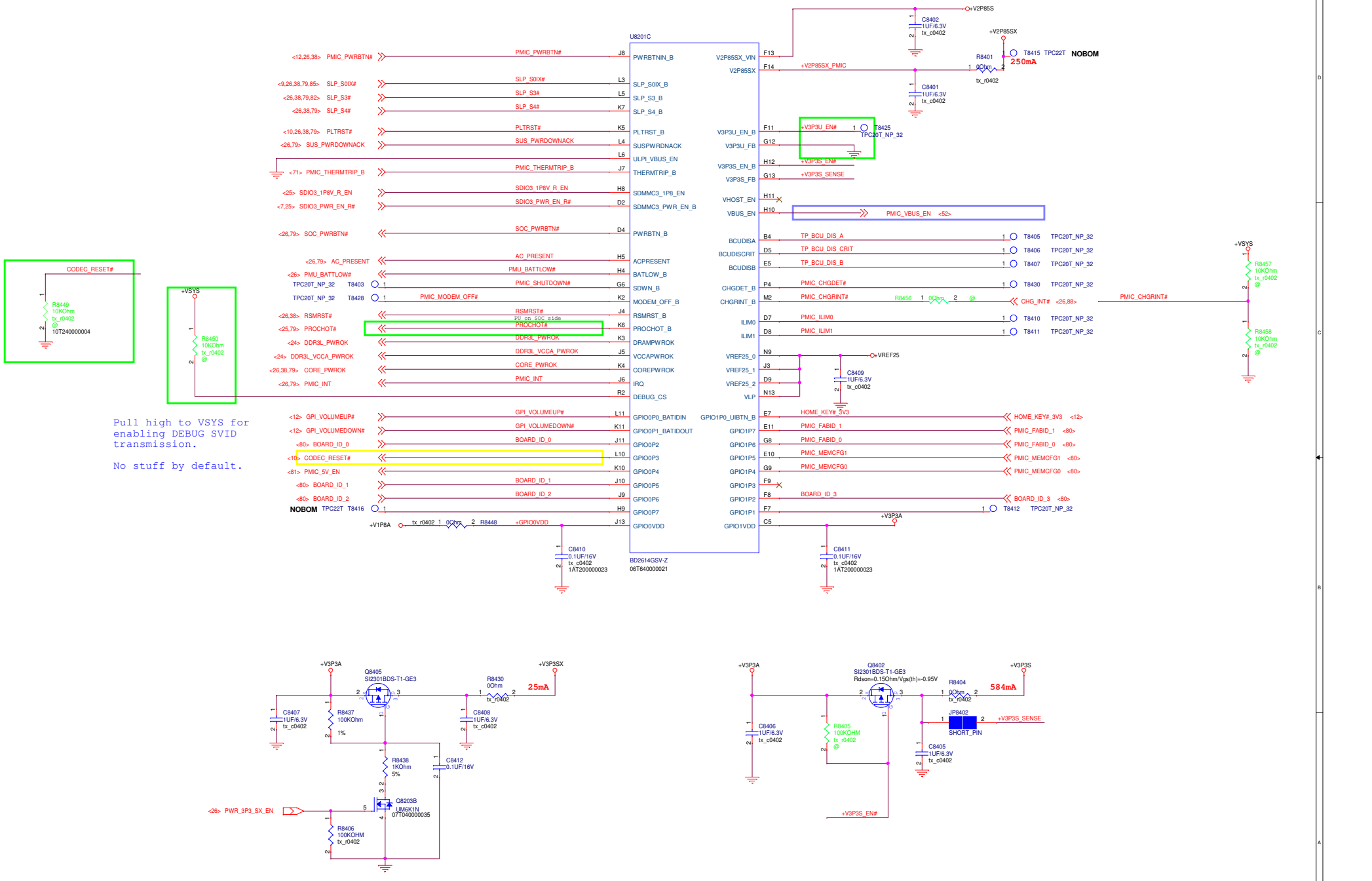

+AC_Adapter
R8367 120KOhm 1% tx_r0402
R8368 20KOhm 5% tx_r0402
R8335 0Ohm tx_r0402
PMIC_AC_OK
C8346 1UF/6.3V 1AT200000038
U8201B
BD2614GSV-Z 06T640000021
+V1P0A
C8301 0.1UF/16V tx_c0402
+VDDQ_VTT
R8301 0Ohm tx_r0402
NOBOM TPC22T T8305
325mA
C8302 22UF/6.3V tx_c0603_h39
C8303 22UF/6.3V tx_c0603_h39
JP8301 SHORT_PIN
+VDDQ_VTT_PMIC
+VDDQ_VTT_FB
+VDDQ
+VSYS
R8320 0Ohm tx_r0603_h24
C8304 10UF/10V tx_c0603_t02_h39 1AT300000048
C8305 10UF/10V tx_c0603_t02_h39 1AT300000048
<12> PMIC_AC_OK
PMIC_VBUS
R8369 0Ohm tx_r0402
10mA
+VREF25
C8342 1UF/6.3V tx_c0402
+BATT_CN
R8340 0Ohm tx_r0402
VBAT_SENSE_BUF
<80> BATID
connector to battery ID terminator
BPTHERM_0
BPTHERM_1
connector to battery thermal terminator
<26,38> SOC_RTEST#
R8337 0Ohm tx_r0402
PMIC_RTC_POR
Reserve both SOC_RTEST# & SRT_CRST# circuit on SOC side
<27> +VBATTBKUP
SYSTHERM_0
SYSTHERM_1
SYSTHERM_2
C8344 0.01UF/50V tx_c0402 @
C8345 0.01UF/50V tx_c0402 @
C8346 0.01UF/50V tx_c0402 @
+VSDIO
NOBOM TPC22T T8311
R8366 0Ohm tx_r0402
C8310 1UF/6.3V tx_c0402
+V3P3A
C8306 0.1UF/16V tx_c0402
+V1P8A
NOBOM TPC22T T8312
+V1P0SX_SENSE
+V1P0SX_EN
+VHDMI
NOBOM TPC22T T8313
R8365 0Ohm tx_r0402
C8309 1UF/10V tx_c0603_h37
+V5P0S_O
R8302 0Ohm tx_r0402
+VHDMI_VIN
C8307 4.7UF/10V tx_c0603 1AT300000033
C8308 0.1UF/16V tx_c0402 @
+VREFT
+VREFB
R8328 24.9KOhm tx_r0402
R8325 24.9KOhm tx_r0402
R8326 24.9KOhm tx_r0402
R8329 24.9KOhm tx_r0402
R8372 24.9KOhm tx_r0402 @
C8341 0.01UF/50V tx_c0402 @
R8330 47KOHM tx_r0402 16T240000024
C8343 0.01UF/50V tx_c0402 @
BPTHERM_1
BPTHERM_0
BATID
R8327 47Kohm tx_r0402_h22
R8324 47Kohm tx_r0402_h22
R8323 47Kohm tx_r0402_h22
R8373 47Kohm tx_r0402_h22
THERMISOR
+V1P0A
C8333 1UF/6.3V tx_c0402
Q8302 IRLHS6242TRPBF 07T040000107 Rdson=15.5mOhm/Vgs(th)=1.1V
+V1P0SX_PMIC
R8316 0Ohm tx_r0402
916mA
+V1P0SX
JP8304 SHORT_PIN
+V1P0SX_SENSE
C8334 1UF/6.3V tx_c0402
+V1P0SX_EN
R8315 100KOHM tx_r0402 @
+V1P0A
C8332 1UF/6.3V tx_c0402
Q8301 IRLHS6242TRPBF 07T040000107 Rdson=15.5mOhm/Vgs(th)=1.1V
+V1P0S_PMIC
R8313 0Ohm tx_r0402
410mA
+V1P0S
JP8303 SHORT_PIN
+V1P0S_SENSE
C8331 1UF/6.3V tx_c0402
+V1P0S_EN
R8314 100KOHM tx_r0402 @
+VSYS
C8327 10UF/10V tx_c0603_t02_h39 1AT300000048
L8301 0.47UH 09T030000087
+V3P3A
R8310 0Ohm tx_r0603_h24
1569mA
JP8302 SHORT_PIN
C8336 0.1UF/10V tx_c0201 1AT100000022
C8324 22UF/6.3V tx_c0603_h39
C8322 22UF/6.3V tx_c0603_h39
C8325 22UF/6.3V tx_c0603_h39
C8321 22UF/6.3V tx_c0603_h39
C8320 22UF/6.3V tx_c0603_h39
C8323 22UF/6.3V tx_c0603_h39
C8326 22UF/6.3V tx_c0603_h39
T8320 TPC22T NOBOM
T8324 TPC22T NOBOM
T8321 TPC22T NOBOM
C8330 1UF/6.3V tx_c0402
40mA
R8312 0Ohm tx_r0402
T8315 TPC22T NOBOM
C8329 1UF/6.3V tx_c0402
T8309 TPC22T NOBOM
34mA
155mA
R8308 0Ohm tx_r0402
R8309 0Ohm tx_r0402
+V1P2S
+V1P2SX_O
T8310 TPC22T NOBOM
C8317 1UF/6.3V tx_c0402
C8318 10UF/6.3V tx_c0603_h37
C8319 22UF/6.3V tx_c0402_h24
T8308 TPC22T NOBOM
144mA
240mA
R8306 0Ohm tx_r0402
R8307 0Ohm tx_r0402
+V1P8S
+V1P8SX
T8307 TPC22T NOBOM
C8315 1UF/6.3V tx_c0402
C8316 1UF/6.3V tx_c0402
C8314 1UF/6.3V tx_c0402
T8306 TPC22T NOBOM
30mA
R8305 0Ohm tx_r0402
+V1P2A
T8322 TPC22T NOBOM
T8323 TPC22T NOBOM
C8311 10UF/6.3V tx_c0603_h37
C8312 10UF/6.3V tx_c0603_h37
C8313 10UF/6.3V tx_c0603_h37
MIDLAND
PEGATRON
Title RMIC-2
PEGATRON CORPORATION
Engineer: Louis_Lin
Size C
Project Name POJAC2
Rev R1.4
Date: Thursday, April 24, 2014
Sheet 84 of 97



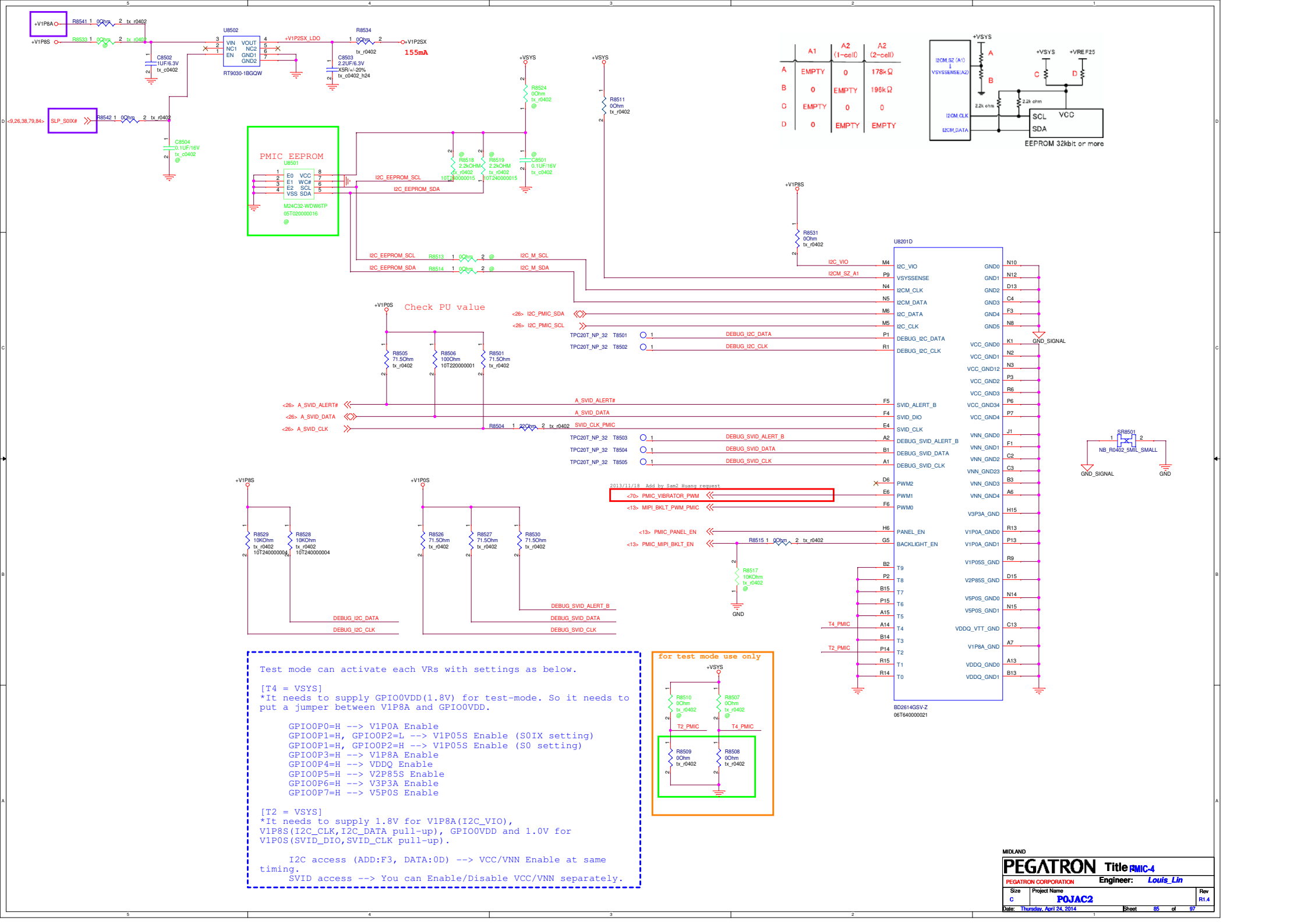
MIDLAND

PEGATRON Title RMIC-3

PEGATRON CORPORATION Engineer: Louis_Lin

Size	Project Name	Rev
C	POJAC2	R1.4

Date: Thursday, April 24, 2014 Sheet 84 of 97


+V1P8A
+V1P8S
R8541 1 0Ohm 2 tx_r0402
R8533 1 0Ohm 2 tx_r0402
U8502
VIN NC1 EN
VOUT NC2 GND1 GND2
RT9030-1BGQW
+V1P2SX_LDO
C8502 1UF/6.3V tx_c0402
C8503 2.2UF/6.3V X5R/+/-20% tx_c0402_h24
R8534 0Ohm tx_r0402
+V1P2SX
155mA
<9,26,38,79,84> SLP_S0IX#
R8542 1 0Ohm 2 tx_r0402
C8504 0.1UF/16V tx_c0402
PMIC EEPROM
U8501
E0 E1 E2 VSS VCC WC# SCL SDA
M24C32-WDW6TP
05T020000016
I2C_EEPROM_SCL
I2C_EEPROM_SDA
R8518 2.2kOHM tx_r0402 10T240000015
R8519 2.2kOHM tx_r0402 10T240000015
C8501 0.1UF/16V tx_c0402
+VSYS
R8524 0Ohm tx_r0402
R8511 0Ohm tx_r0402
A1 A2 (1-cell) A2 (2-cell)
A EMPTY 0 178kΩ
B 0 EMPTY 196kΩ
C EMPTY 0 0
D 0 EMPTY EMPTY
I2CM_SZ (A1) / VSYSSENSE(A2)
I2CM_CLK
I2CM_DATA
2.2k ohm
+VREF25
SCL VCC SDA
EEPROM 32kbit or more
+V1P8S
R8531 0Ohm tx_r0402
U8201D
I2C_VIO
I2CM_SZ_A1
VSYSSENSE
I2CM_CLK
I2CM_DATA
I2C_DATA
I2C_CLK
DEBUG_I2C_DATA
DEBUG_I2C_CLK
GND_SIGNAL
I2C_EEPROM_SCL R8513 0Ohm I2C_M_SCL
I2C_EEPROM_SDA R8514 0Ohm I2C_M_SDA
+V1P0S
Check PU value
R8505 71.5Ohm tx_r0402
R8506 100Ohm 10T220000001
R8501 71.5Ohm tx_r0402
<26> I2C_PMIC_SDA
<26> I2C_PMIC_SCL
TPC20T_NP_32 T8501 1 DEBUG_I2C_DATA
TPC20T_NP_32 T8502 1 DEBUG_I2C_CLK
<26> A_SVID_ALERT#
<26> A_SVID_DATA
<26> A_SVID_CLK
A_SVID_ALERT#
A_SVID_DATA
R8504 1 22Ohm 2 tx_r0402 SVID_CLK_PMIC
SVID_ALERT_B
SVID_DIO
SVID_CLK
TPC20T_NP_32 T8503 1 DEBUG_SVID_ALERT_B
TPC20T_NP_32 T8504 1 DEBUG_SVID_DATA
TPC20T_NP_32 T8505 1 DEBUG_SVID_CLK
DEBUG_SVID_ALERT_B
DEBUG_SVID_DATA
DEBUG_SVID_CLK
SR8501
NB_R0402_5MIL_SMALL
GND_SIGNAL
GND
2013/11/18 Add by Sam2 Huang request
<70> PMIC_VIBRATOR_PWM
<13> MIPI_BKLT_PWM_PMIC
<13> PMIC_PANEL_EN
<13> PMIC_MIPI_BKLT_EN
R8515 1 0Ohm 2 tx_r0402
PWM2
PWM1
PWM0
PANEL_EN
BACKLIGHT_EN
R8517 10KOhm tx_r0402
R8529 10KOhm tx_r0402 10T240000004
R8528 10KOhm tx_r0402 10T240000004
R8526 71.5Ohm tx_r0402
R8527 71.5Ohm tx_r0402
R8530 71.5Ohm tx_r0402
T4_PMIC
T2_PMIC
BD2614GSV-Z
06T640000021
Test mode can activate each VRs with settings as below.
[T4 = VSYS]
*It needs to supply GPIO0VDD(1.8V) for test-mode. So it needs to put a jumper between V1P8A and GPIO0VDD.
GPIO0P0=H --> V1P0A Enable
GPIO0P1=H, GPIO0P2=L --> V1P05S Enable (S0IX setting)
GPIO0P1=H, GPIO0P2=H --> V1P05S Enable (S0 setting)
GPIO0P3=H --> V1P8A Enable
GPIO0P4=H --> VDDQ Enable
GPIO0P5=H --> V2P85S Enable
GPIO0P6=H --> V3P3A Enable
GPIO0P7=H --> V5P0S Enable
[T2 = VSYS]
*It needs to supply 1.8V for V1P8A(I2C_VIO), V1P8S(I2C_CLK,I2C_DATA pull-up), GPIO0VDD and 1.0V for V1P0S(SVID_DIO,SVID_CLK pull-up).
I2C access (ADD:F3, DATA:0D) --> VCC/VNN Enable at same timing.
SVID access --> You can Enable/Disable VCC/VNN separately.
for test mode use only
R8510 0Ohm tx_r0402
R8507 0Ohm tx_r0402
R8509 0Ohm tx_r0402
R8508 0Ohm tx_r0402
MIDLAND
PEGATRON
Title RMIC-4
PEGATRON CORPORATION
Engineer: Louis_Lin
Size C
Project Name POJAC2
Rev R1.4
Date: Thursday, April 24, 2014
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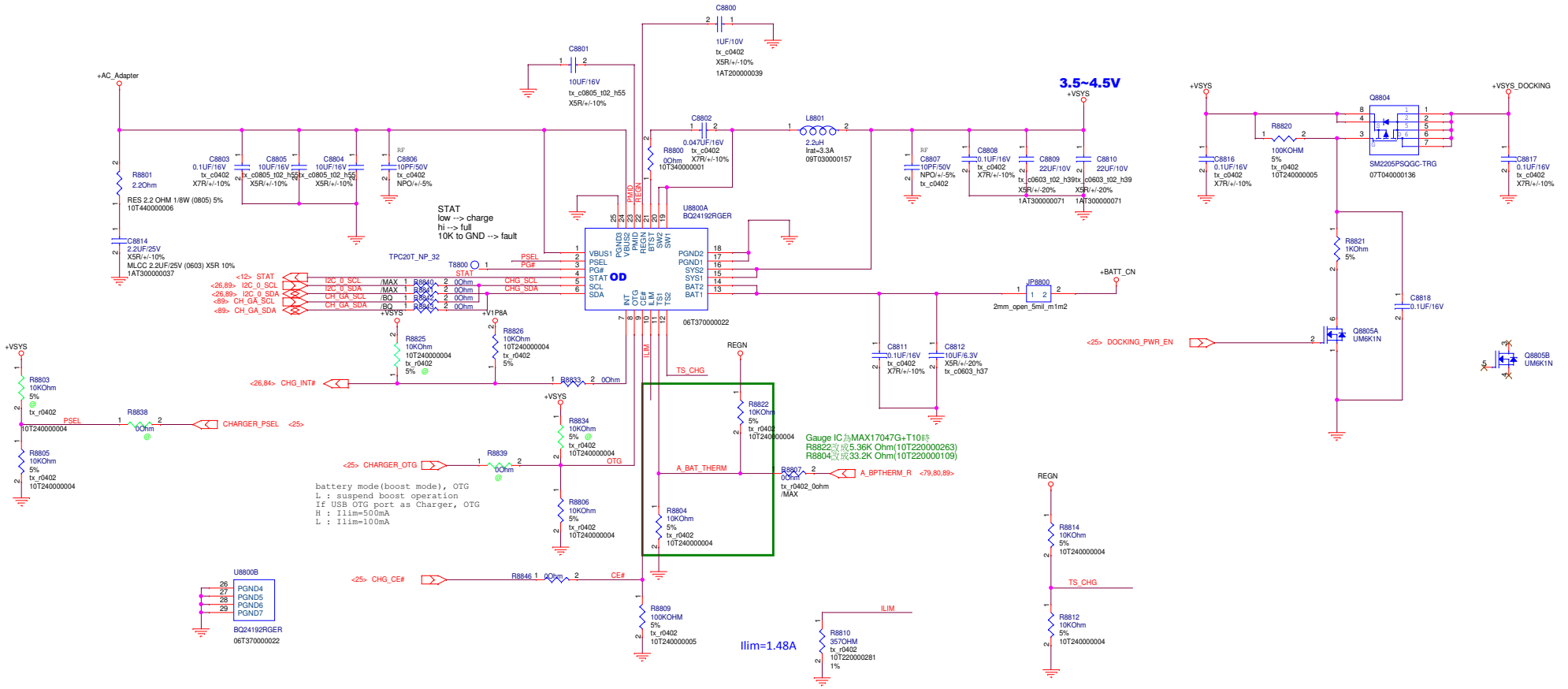
Battery Charger



PEGATRON Title : Charger

HW6, BG1 Engineer: Louis_Lin

Size	Project Name	Rev
C	POJAC2	R1.4

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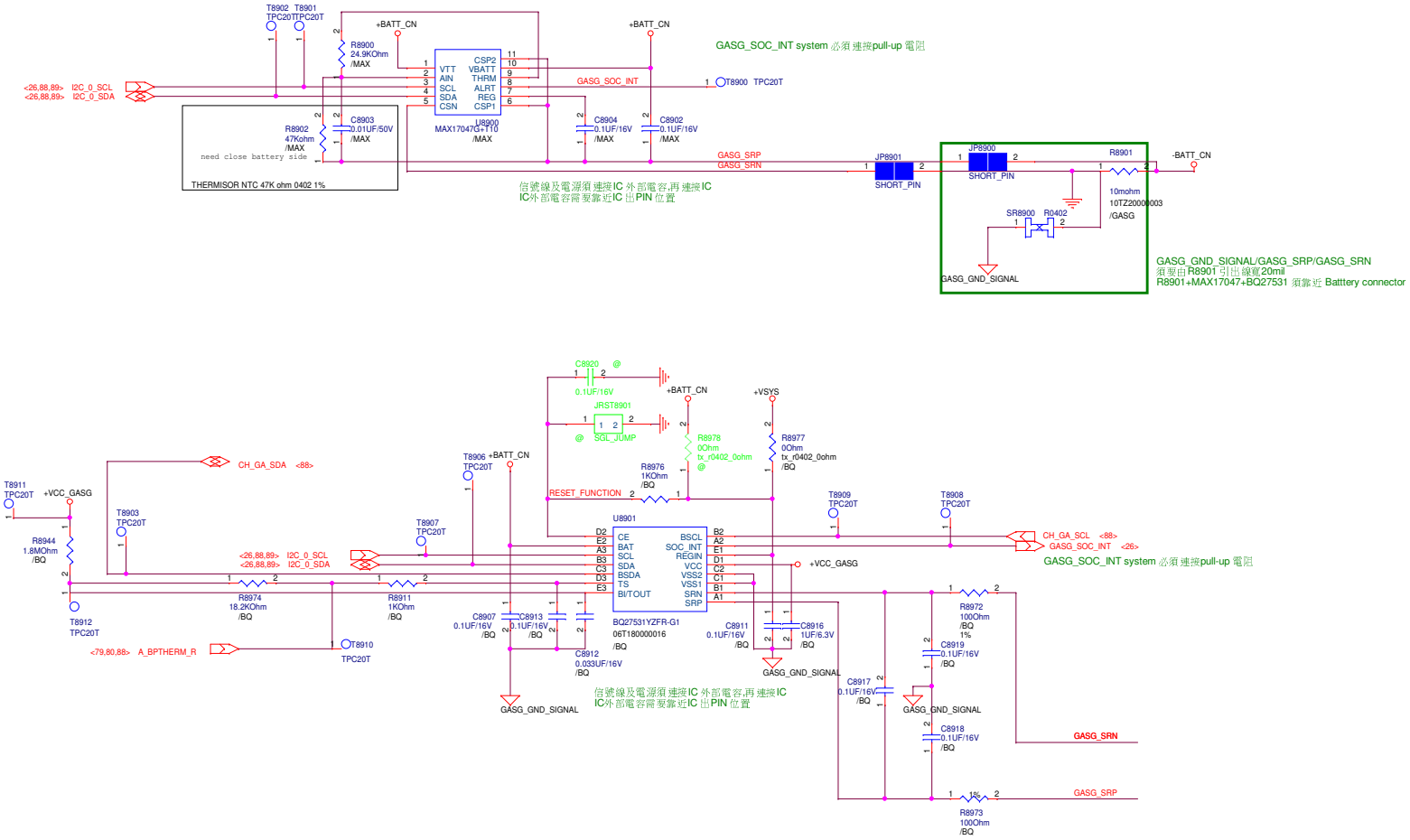
Gas Gauge

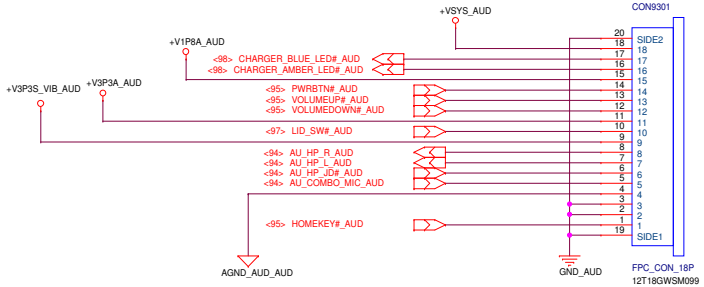


PEGATRON	Title : Gas Gauge	
HW6, BG1	Engineer: Luke_Lee	
Size: C	Project Name: POJAC2	Rev: R1.4
Date: Thursday, April 24, 2014	Sheet 89 of 97	

FFC CONN

FFC CONN
Pin Definition

18. +VSYS
17. CHARGER_BLUE_LED#
16. CHARGER_AMBER_LED#
15. V1P8A
14. PWRBTN#
13. VOLUMEUP#
12. VOLUMEDOWN#
11. V3P3A
10. LID_SW#
09. V3P3S_VIB
08. AU_HP_R
07. AU_HP_L
06. AU_HP_JD#
05. AU_COMBO_MIC
04. AGND_AUD
03. GND
02. GND
01. HOMEKEY#



PEGATRON Title : FFC CONN

PEGATRON CONFIDENTIAL Engineer: Sam

Size	Project Name	Rev
C	POJAC2 Audio DB	1.4

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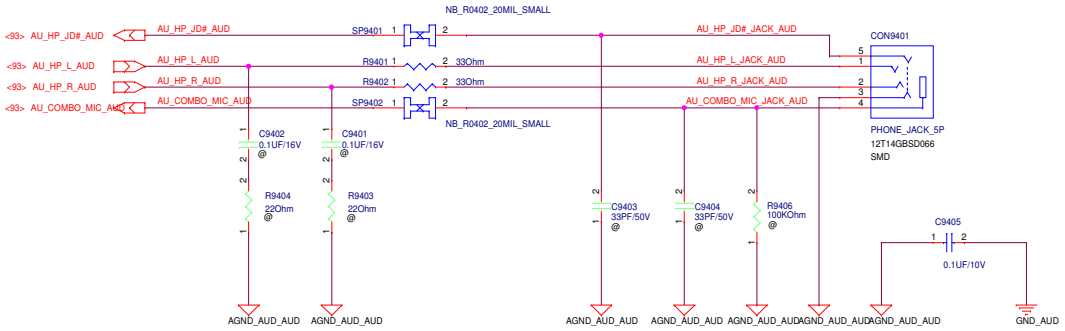
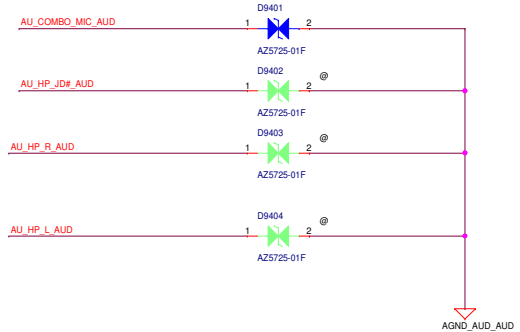
Audio Combo Jack



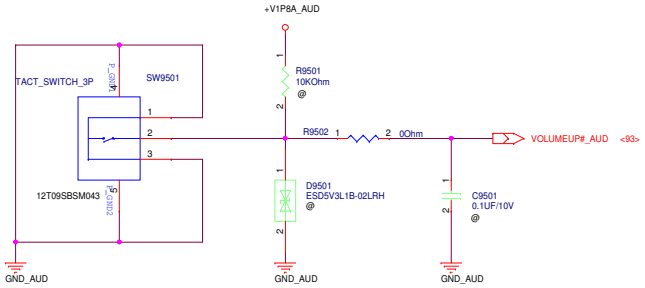
ESD



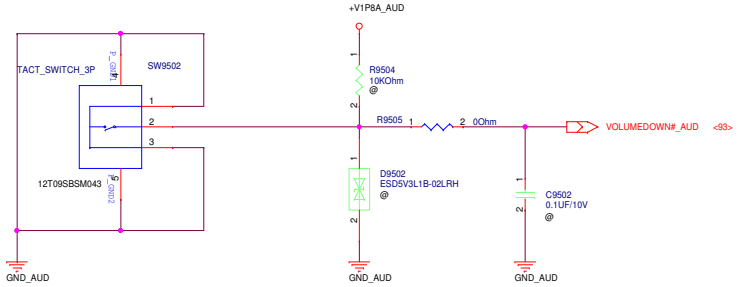
PEGATRON	Title : Audio_Combo Jack		
PEGATRON CONFIDENTIAL	Engineer: Sam		
Size: C	Project Name: POJAC2 Audio DB		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 94 of 99		

Button

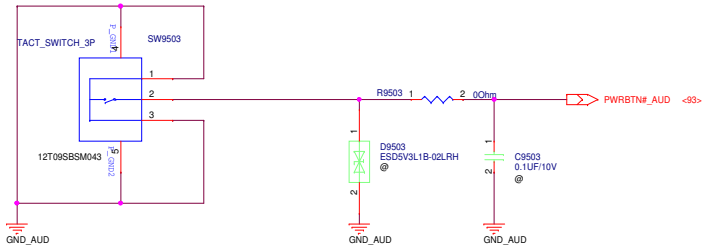
Volume Up



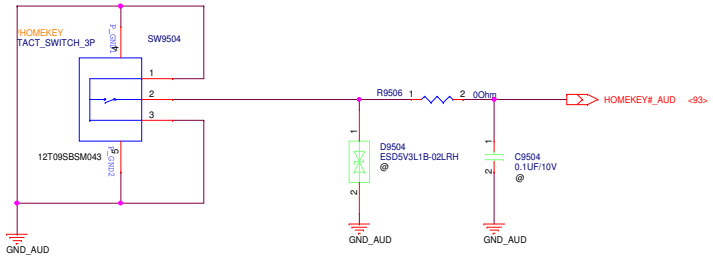
Volume Down



Power Key

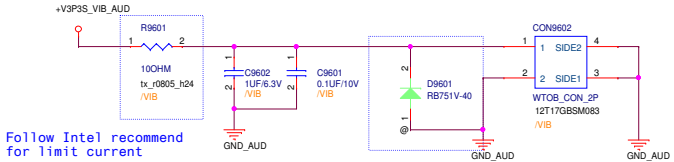


HOME KEY



PEGATRON	Title : Button	
PEGATRON CONFIDENTIAL	Engineer: Sam	
Size: C	Project Name: POJAC2 Audio DB	Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 95 of 99	

VIBRATOR

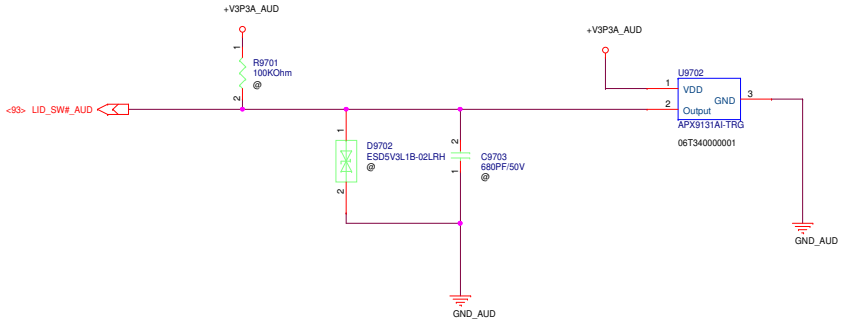


Follow Intel recommend
for limit current

Reserve protection circuit
20140115

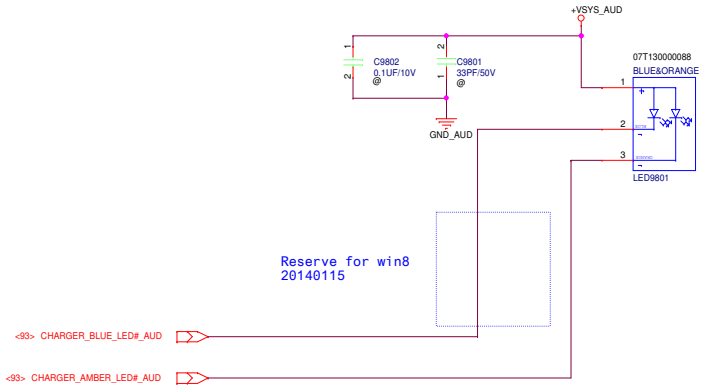
PEGATRON	Title : VIBRATOR		
PEGATRON CORPORATION	Engineer: Sam		
Size: C	Project Name: POJAC2 Audio DB		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 96 of 99		

LID Switch



PEGATRON	Title :	LID Switch	
PEGATRON CONFIDENTIAL	Engineer:	Sam	
Size: C	Project Name: POJAC2 Audio DB		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 97 of 99		

LED

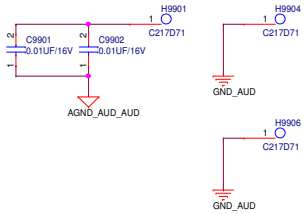


PEGATRON	Title :	LED
PEGATRON CONFIDENTIAL	Engineer:	Sam
Size: C	Project Name:	POJAC2 Audio DB
Rev: 1.4		
Date: Thursday, April 24, 2014	Sheet 98 of 99	

Test Point

Screw Hole

Screw A



Screw D

Screw E

PEGATRON	Title :	Screw / TP	
PEGATRON CONFIDENTIAL	Engineer:	Sam	
Size: C	Project Name: POJAC2 Audio DB		Rev: 1.4
Date: Thursday, April 24, 2014	Sheet 99 of 99		