Model Name:GA-P41T-D3P *Revision 1.31*

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B,D
06	P4_LGA775_C
07	P4_LGA775_E,F,G,H
08	Eaglelake HOST
09	Eaglelake DDR
10	Eaglelake PCI_EXP_16,DMI
11	Eaglelake VGA
12	Eaglelake GND
13	Eaglelake PWR
14	PCI EXPRESS*16 SLOT
15	DDRII CHANNEL A
16	DDRII CHANNEL B
17	DDRII TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY
24	ITE 8718 GB/HX
25	COM_LPT
26	CI,HWM,KB/MS,DUAL BIOS
27	ALC888

SHEET	TITLE
28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM_ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	REALTEK RTL8111D
34	PCI SLOT3,PCIE*2,3

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Title: Cover Sheet		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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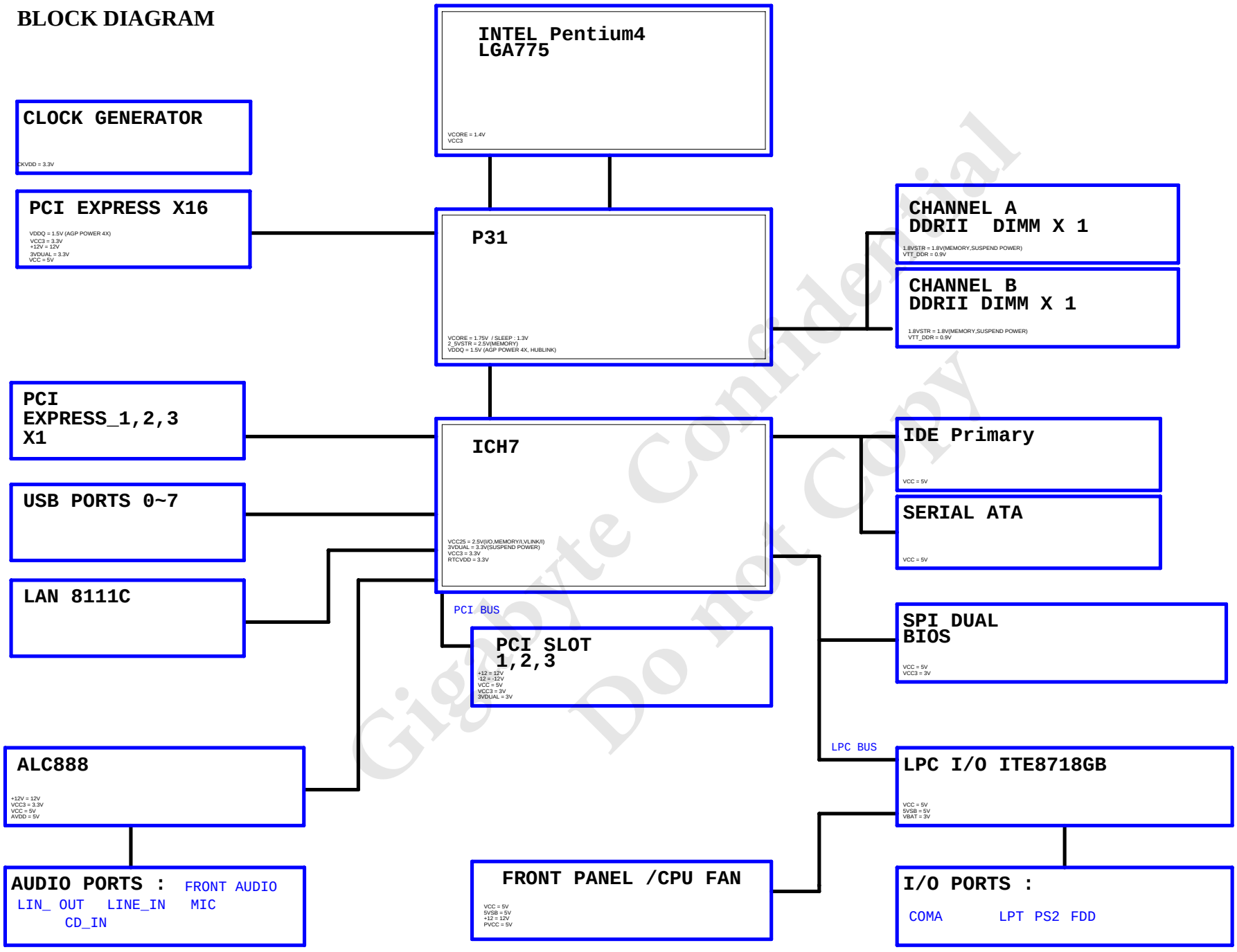
BLOCK DIAGRAM



Gigabyte Technology		
Title: BOM & PCB MODIFY HISTORY		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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Model Name:GA-P41T-D3

Version: 1.31

Component value change history

2007/08/02

Data	Change Item	Reason
2007/08/03		EBOM:9MG31MS2L-00-10B
2007/08/22 EBOM:10A	1.G31 CHIP -->P31 CHIP, remove VGA function,add comb port	
	2.ADD 1 PCI SLOT,2 PCIE SLOT	
2007/09/10 PBOM:10A	1.R375 8.2K-->1K ,R1951 1K-->8.2K	
	2.R327 2.67K-->649 ,R325 8.45K-->1M,DR37 1.4K-->1.54K,DR30 330-->365	
	3.R2913 1.18K-->2.21K,R2914 360-->931	
2008/05/14 PBOM:11A	1.ADD DES LITE ,FERRITE CHOKE	
	2.B0 CHIP , SMD POLY FUSE	
2008/06/26 EBOM:20B	1.20A BOM為 PCB2.0使用,故以20B FOR PCB:2.01	
	2.修改為RTM587 CLOCK GEN,RTL8111C,ALC883,DUAL BIOS,FORNT USB ADD PROTECT DIODE	
2008/07/21 PBOM:10A	1.ALC883--->888; DR30 316-->365 ;BC747 0.1U\Y5V-->X7R;IDE red--->green	
	2.ADD C1908 10PF / R1896 1K-->330 ohm add c1908 22pf ,22ohm--->0ohm for bios CS0 glitch issue	
	3.DEL R2935, ADD R2936 FOR MAIN POWER BIOS---rechard 說的	
2008/07/31 PBOM:10B	1.DQ13 2N7002--->MMBT2222A FOR CPU FSB LATCH ERROR ISSUE	
2008/08/21 PBOM:10C	1.CLOCK GEN POWER CAP 0.1U/Y5V---->0.1U/X7R FOR CPU INFORMATION FAIL ISSUE	
	2.ADD PCB CR ,CL	
P41T-ES3G-10A	1. SC1~SC8轉背板	
P41T-ES3G-13A	2. F4,F5 10FP1-06160B-C1R -> 10FP1-06350B-10R	
P41T-D3-14A	1. 電解改固態電容	
	2. ALC892R	
0929-14B	1. ALC892R --> ALC892	
P41T-D3P-13A	1. 3 PHASE TO252 -->4 PHASE POWER-PACK	
	2. ALC892 --> ALC889	
13B	3. MOSFET --> NEC+ON	
	5. Add USB_LAN "11NR6-702009-92R_93R"	
13C	1. DR80 200/6/1 --> 200/4/1	
13D	1. RTL8111E-VB --> RTL8111E-VL	
13E	1. aALC889 line-in CBC10,CBC11 10u --> 22u	

Circuit or PCB layout change for next version

DATE	Change Item	Reason
2007/08/08 PCB:1.0	1.由GA-G31M-S2L-1.01修改	
	2.ADD 1 PCI SLOT,2 PCIE SLOT	
2007/09/07 PCB:1.0	1.由GA-P31-S3E-1.0 rename GA-P31-S3G-1.0	
2008/05/14 PCB:1.1	1.由 GA-P31-S3G-1.0修改,ADD DES LITE ,FERRITE CHOKE	
2008/06/12 PCB:2.01	1.由 GA-P31-S3G-1.1修改	
	2.ALC662--->ALC883,ADD RCA IN REAR-->ALC888;RTL8111C	
	3.DUAL BIOS 4M;DUAL BIOS	
	4.SUPPORT CPU FSB 1600	
2008/07/18 PCB:1.0	1.由 GA-P31-S3G-2.01 rename GA-P31-ES3G 1.0	
	2.移除EASY SAVER文字面	
2009/08/27 PCB:1.0	1.由 GA-P31-ES3G 1.0改成P41-ES3G-1.0,更換北橋	
2009/10/01 PCB:1.1	1.ADD EUP FUNCTION	
	2.更換螺孔FOOTPRINT	
	3.更換DDR18V_OV2 GPIO PIN	
P41T-ES3G 0.1	1. EVT Release	
	2. DDR3 1333+ --> DDR3 1333 OC	
P41T-ES3G 1.0	1. PVT Release	
P41T-ES3G 1.3	1. 文字面Rev1.0 --> Rev1.3 for USB 3X Power	
P41T-ES3G 1.31	1. RTL8111D --> RTL8111E 2. R2985~R2988 Short Wire --> 0/4 ohm 3. SATA2_0~SATA2_3 Update footprint "H1X7-SATA2- HS-MASK" 4. Add Prochot control circuit (GPO21:MB_ID , ICH7_GPO0 prochot) 5. 定位孔移除	

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Title: BOM & PCB MODIFY HISTORY

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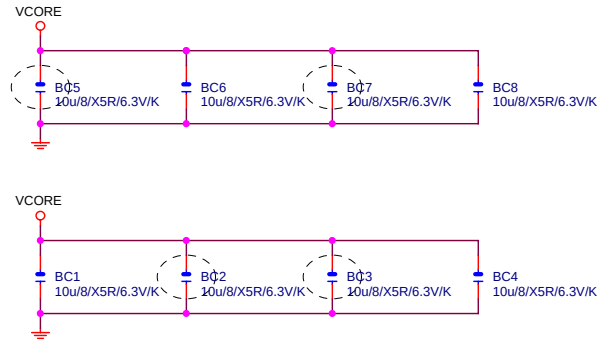
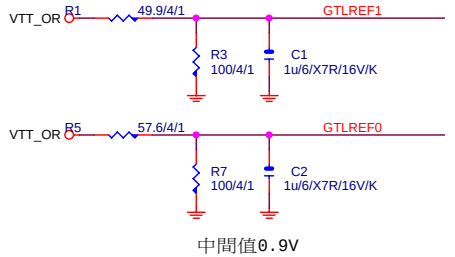
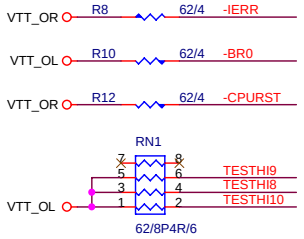
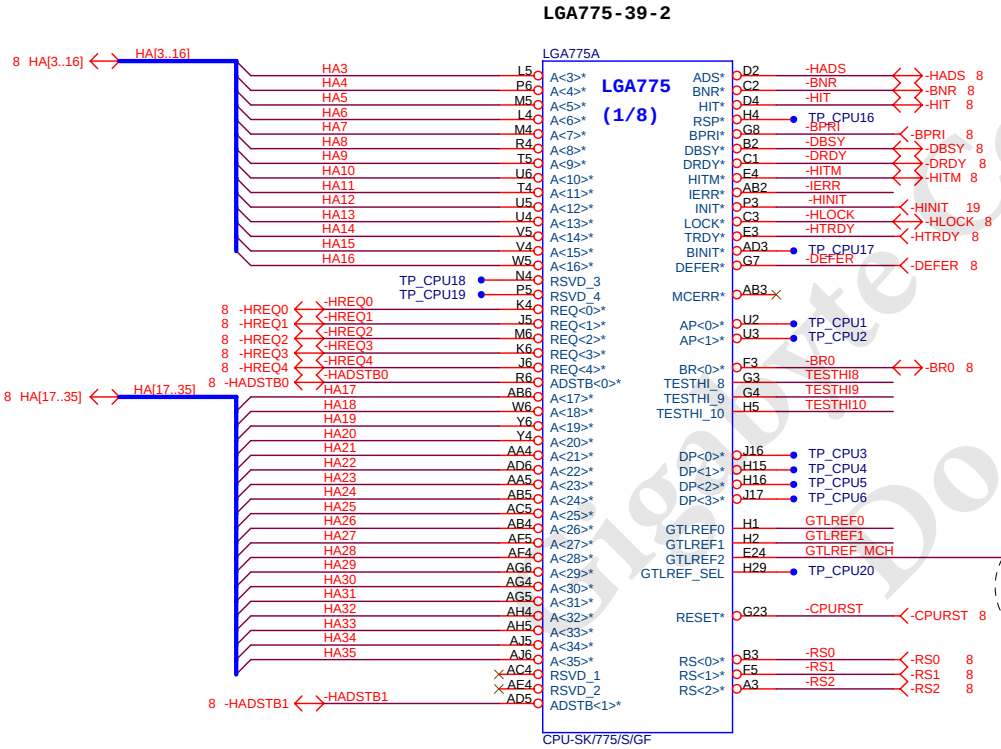
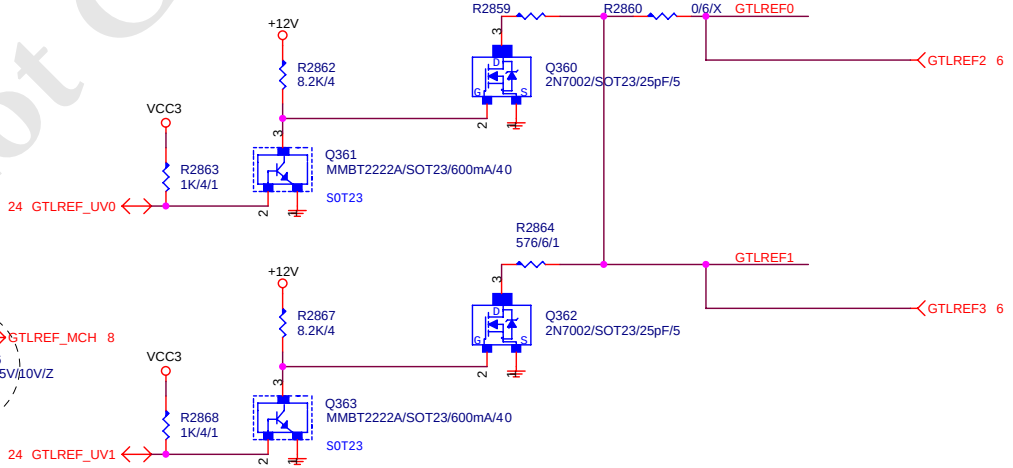




中間值0.9V







Gigabyte Technology			
Title: P4_LGA775-A			
Size: B	Document Number: GA-P41T-D3P		Rev: 1.31
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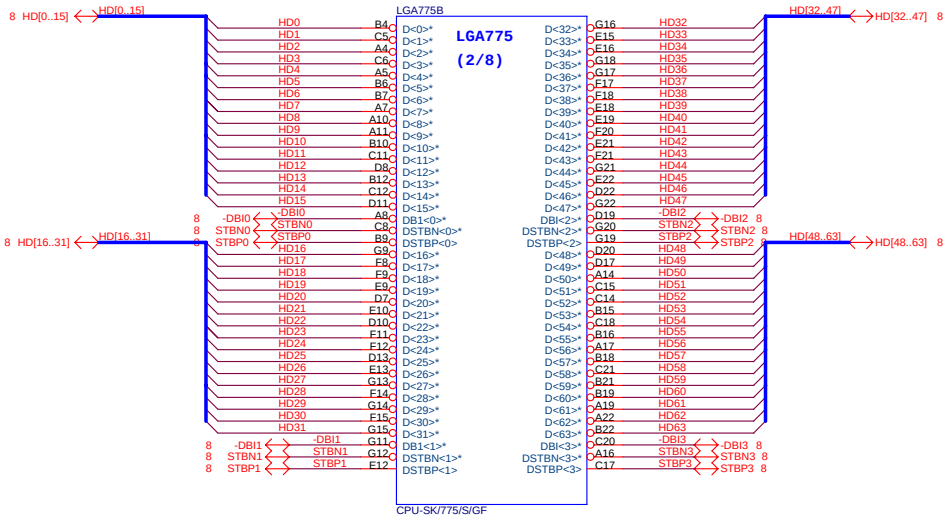
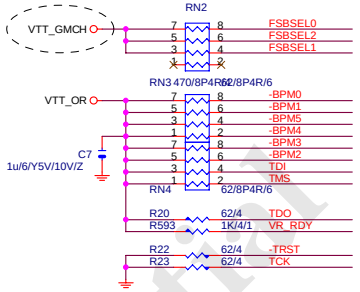
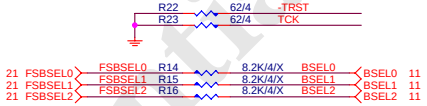


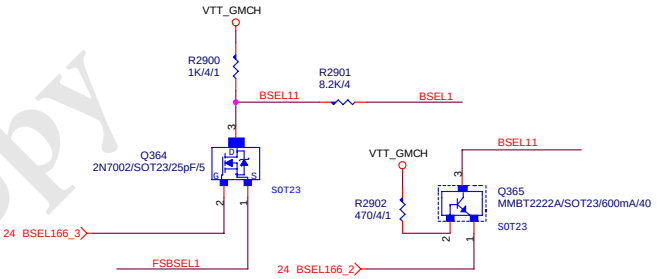


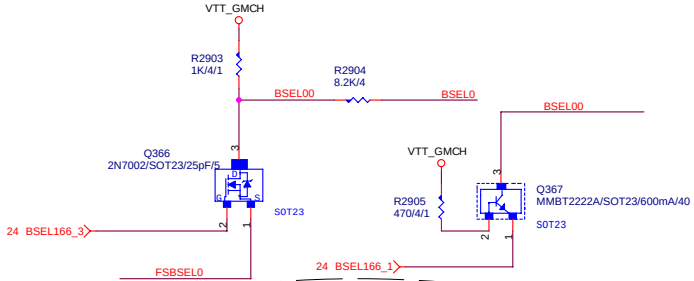
TO CLK GEN

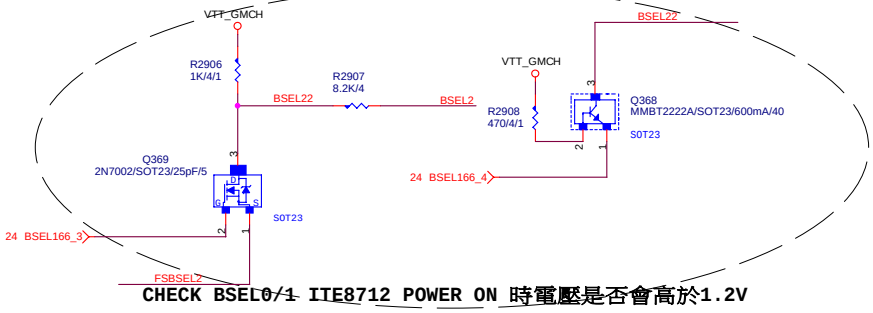


TO NB

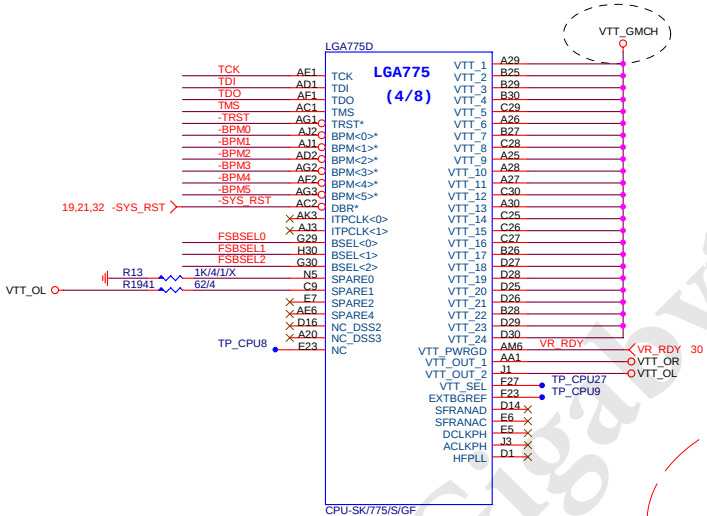
FOR ALL DDR CLK RATIO

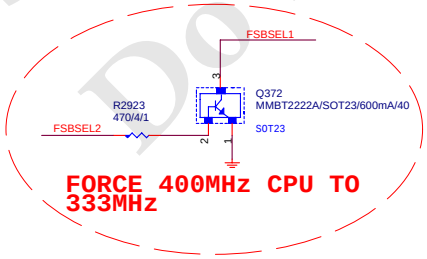






CHECK BSEL0/1 IIE8712 POWER ON 時電壓是否會高於1.2V





FORCE 400MHz CPU TO 333MHz

	FSA	FSB	FSC		
	FSBSEL0	FSBSEL1	FSBSEL2	Clock	
?	1	0	1	100MHz	
?	1	0	0	133MHz	3/4
G31	0	1	0	200MHz	2/2.66/3.33/4
G31	0	0	0	266MHz	2/2.5/3/4
G31	0	0	1	333MHz	2/2.4
	0	1	1	400MHz	

400/533
400/533/667/800
533/667/800/1066
667/800

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Title	P4_LGA775-B,D	
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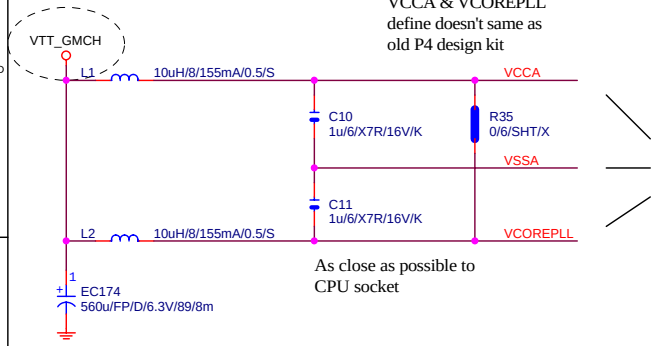
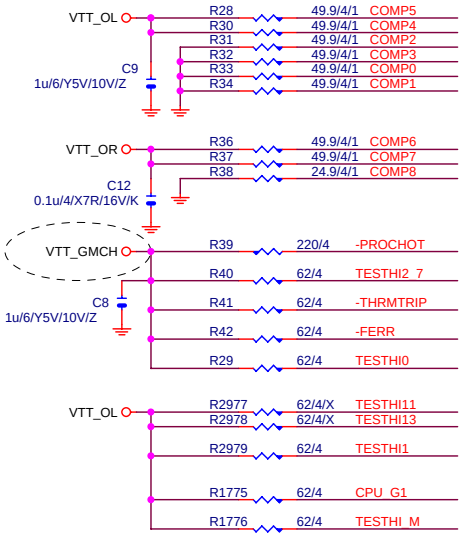
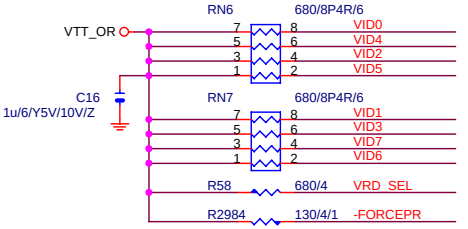
Note:

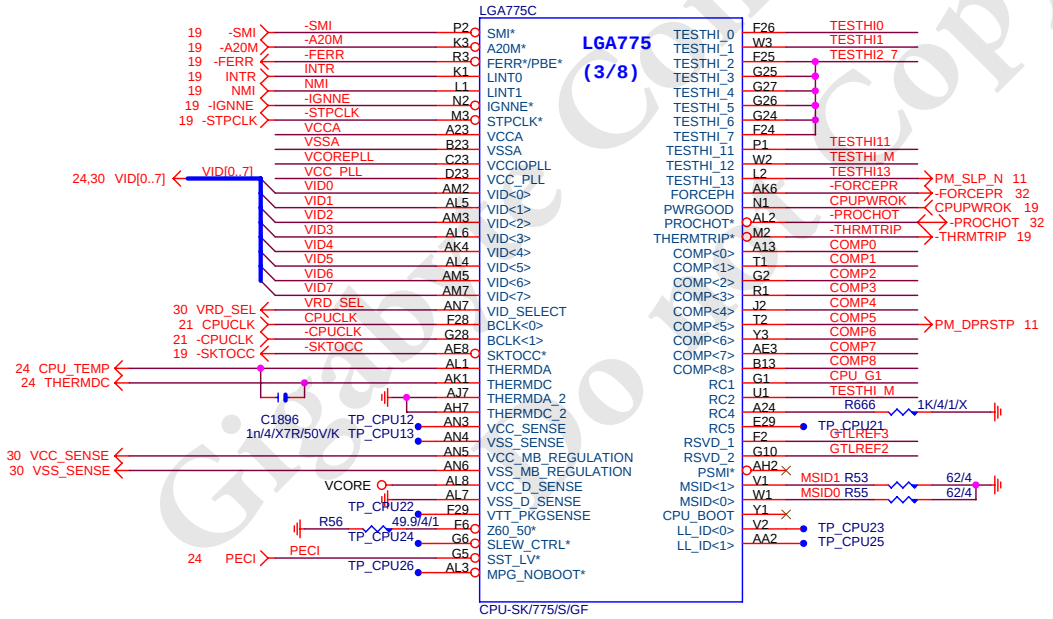
VCCA & VCOREPLL define doesn't same as old P4 design kit

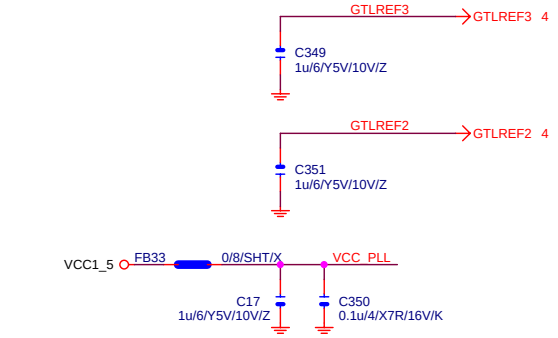


Place outside of CPU socket









PECI:Platform Environment Control Interface

Gigabyte Technology		
Title: P4_LGA775-C		
Size: B	Document Number: GA-P41T-D3P	Rev: 1.31
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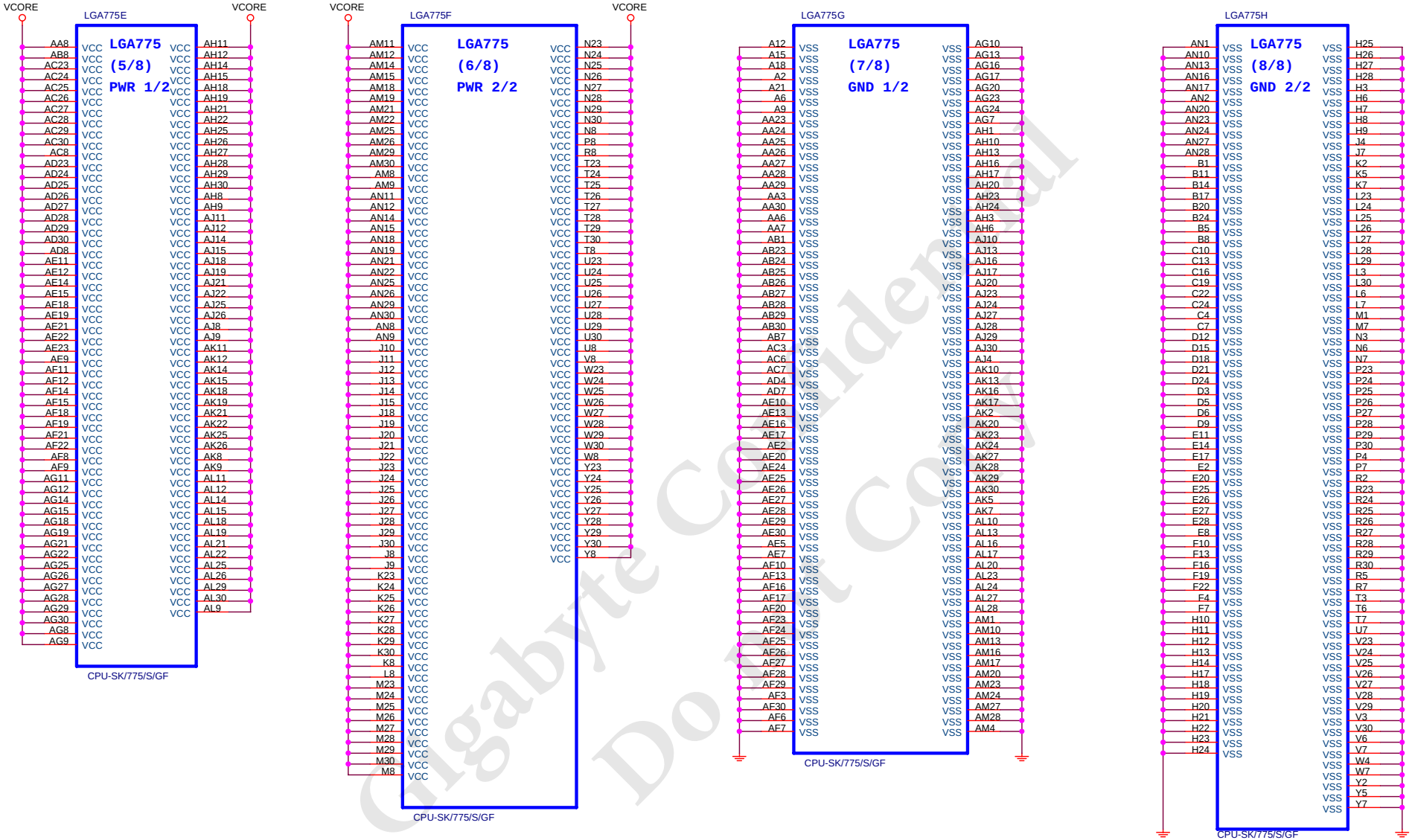

Gigabyte Technology			
Title: P4_LGA775-E,F,G,H			
Size: B	Document Number: GA-P41T-D3P		Rev: 1.31
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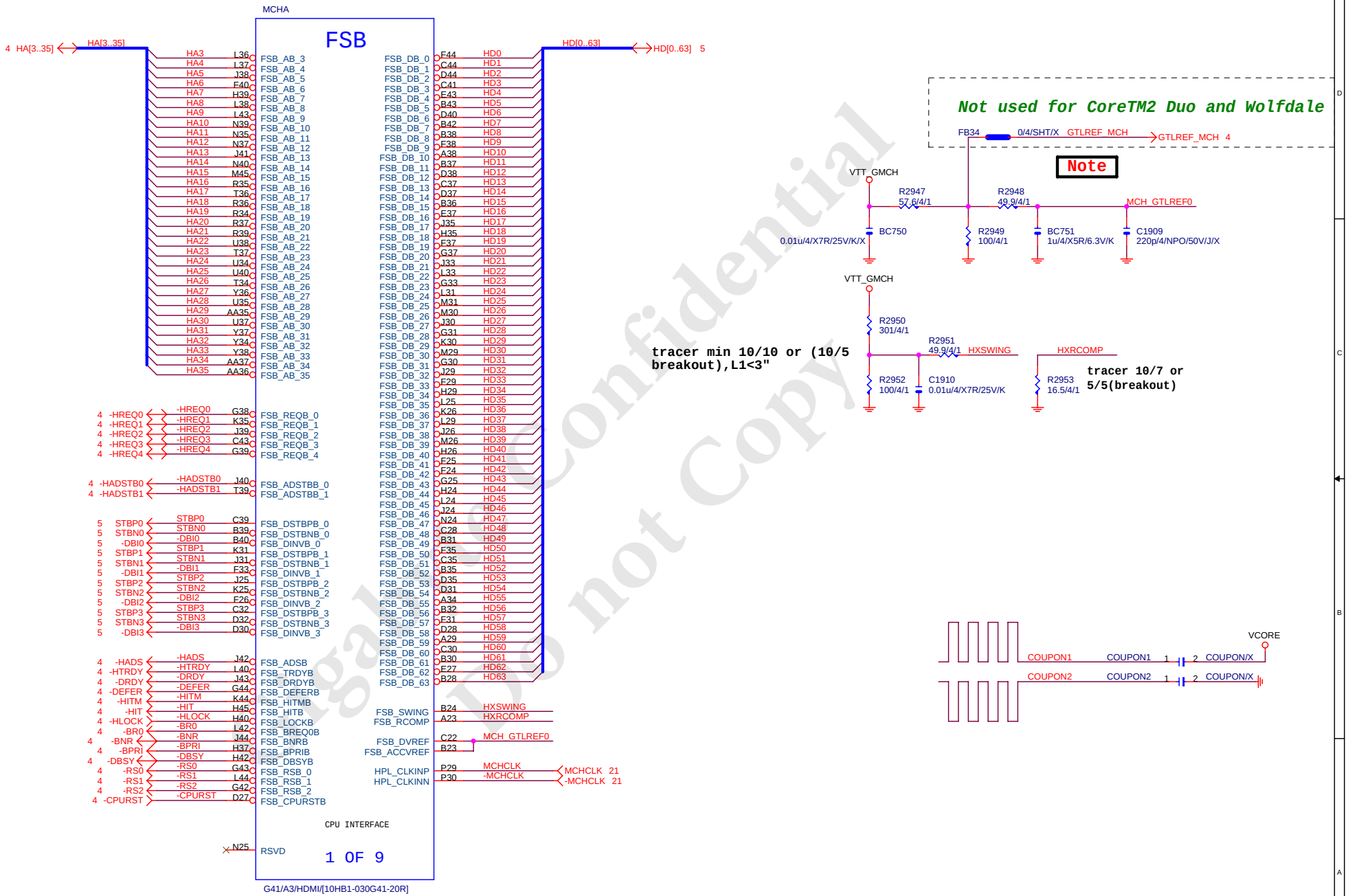
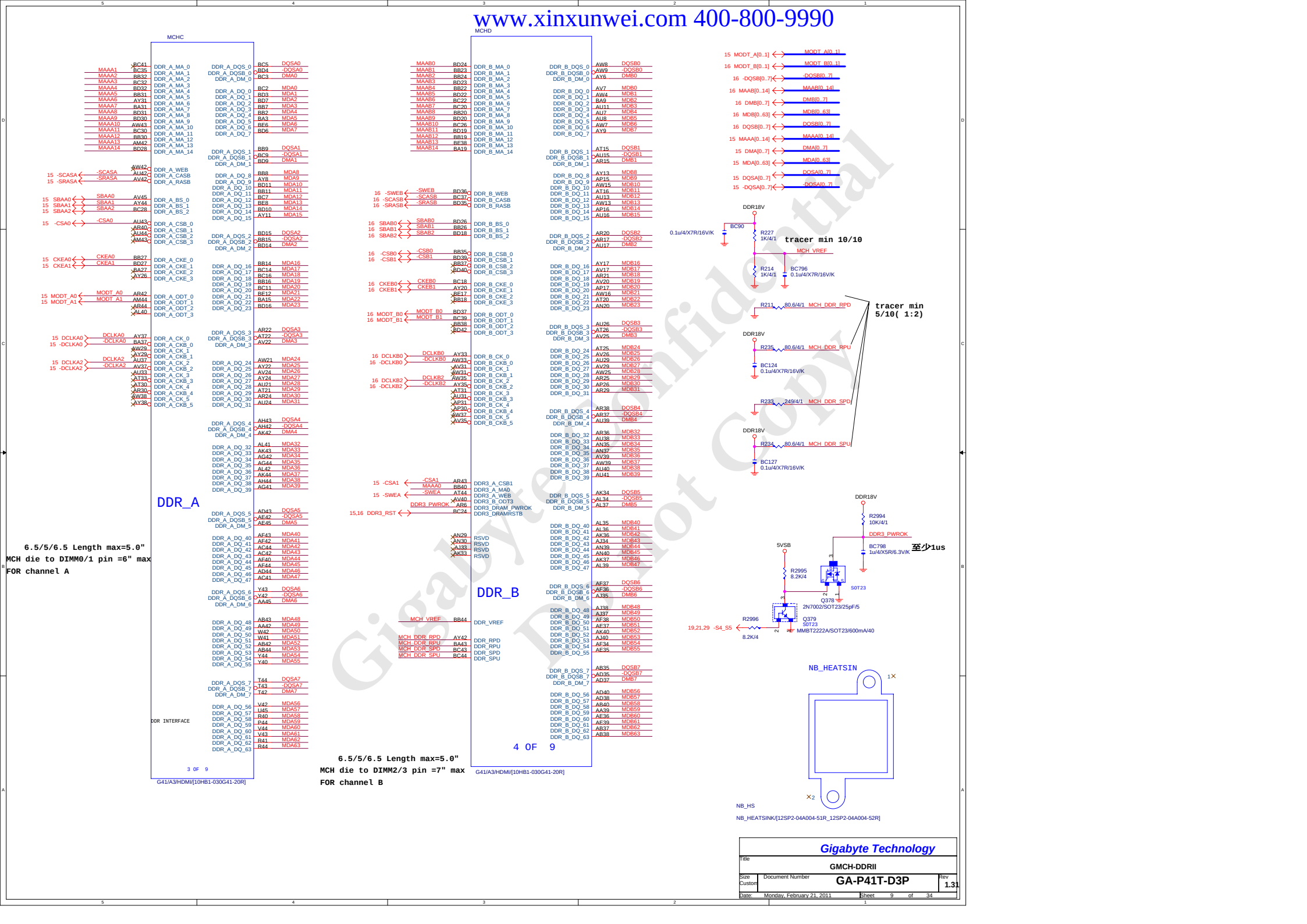


Gigabyte Technology		
Title: GMCH-HOST		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
Date: Monday, February 21, 2011	Sheet 8 of 34	



Gigabyte Technology		
Title: GMCH-DDRII		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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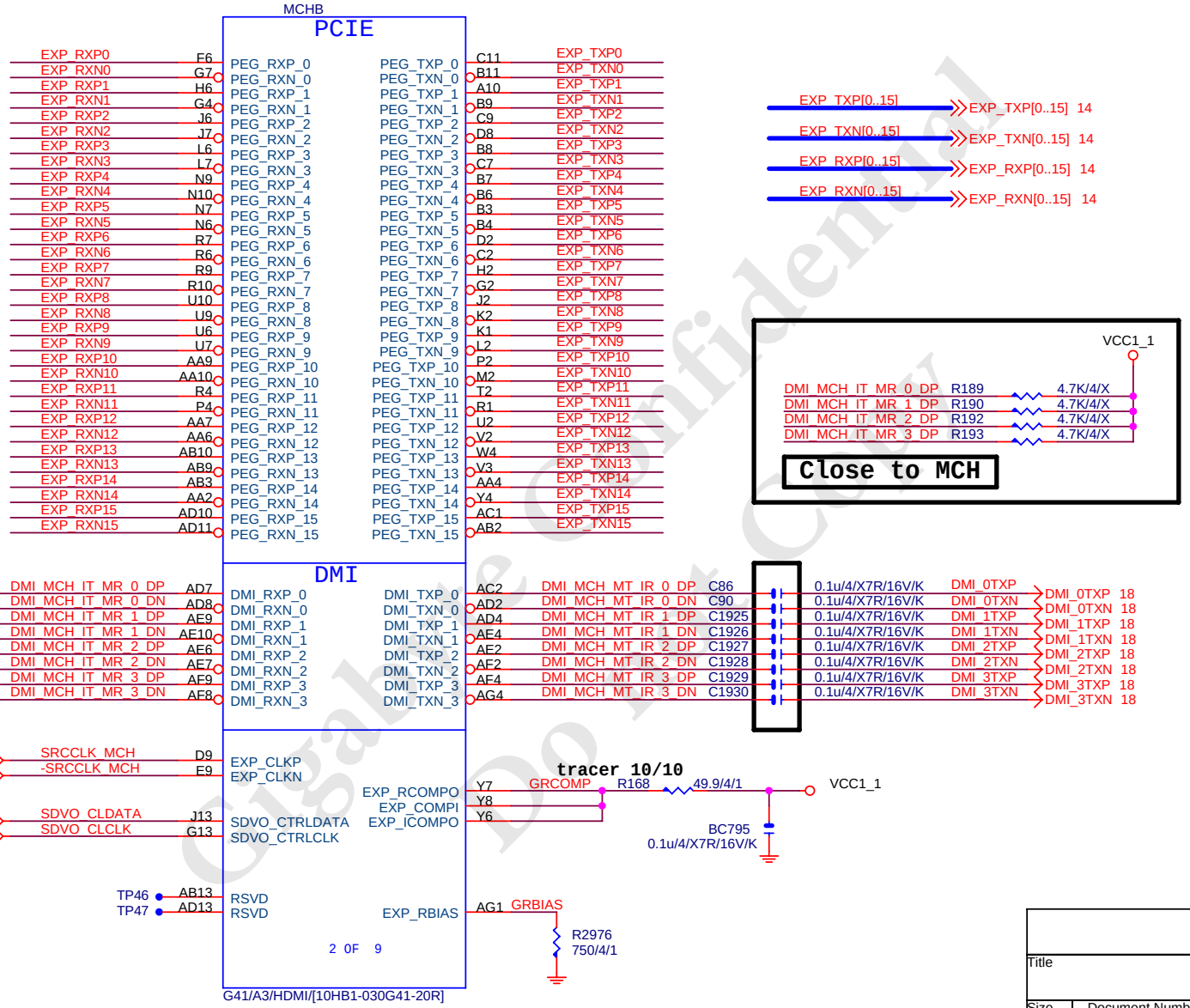
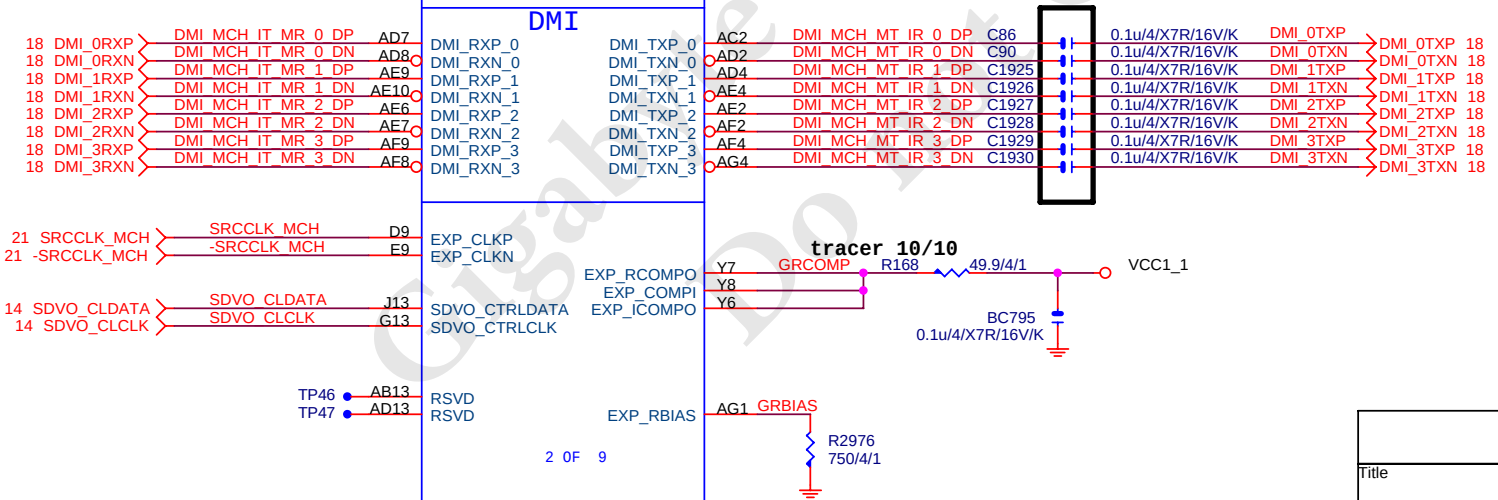
PCIEX16:16/5/5/5/16(breakout min 8/4/5/4/8)

Impedance=85 +- 17.5%



DMI:12/4/8/4/12

Impedance=95 +- 17.5%



G41/A3/HDMI/[10HB1-030G41-20R]

Gigabyte Technology

Title: GMCH-PCI E & DMI

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EXP_SM
0:SDVO OR PCIE
1:BOTH SDVO AND PCIE
EXP_SLR:
0:BTX PCIE are reversed
1:ATX PCIE normal

MCHE

4/10//15<500 MILS

1th RGB:7.5/6 < 300 MILS 2th RGB:4/20 <5"~8" inch

ON-BOARD VGA 75ohm
non-ON-BOARD VGA 0ohm

4/6//12

ON-BOARD VGA 1.02k
non-ON-BOARD VGA 0ohm

VGA

MISC

CL_VREF:4/10

0.349V

ITPM:
0:ENABLE ITPM
1:DISABLE ITPM

CEN:
0:DISABLE TLS
1:ENABLE TLS

DualX8_Enable:
0 = 2x8 PCI Express Ports
1 = 1x16 PCI Express Port

BW+ICH8 不要上(SAMPLE)
BW+ICH7 要上

G41CHIP 都要上

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G41/A3/HDMI/[10HB1-030G41-20R]

Title	GMCH-INTERNAL VGA
Size B	Document Number GA-P41T-D3P Rev 1.31
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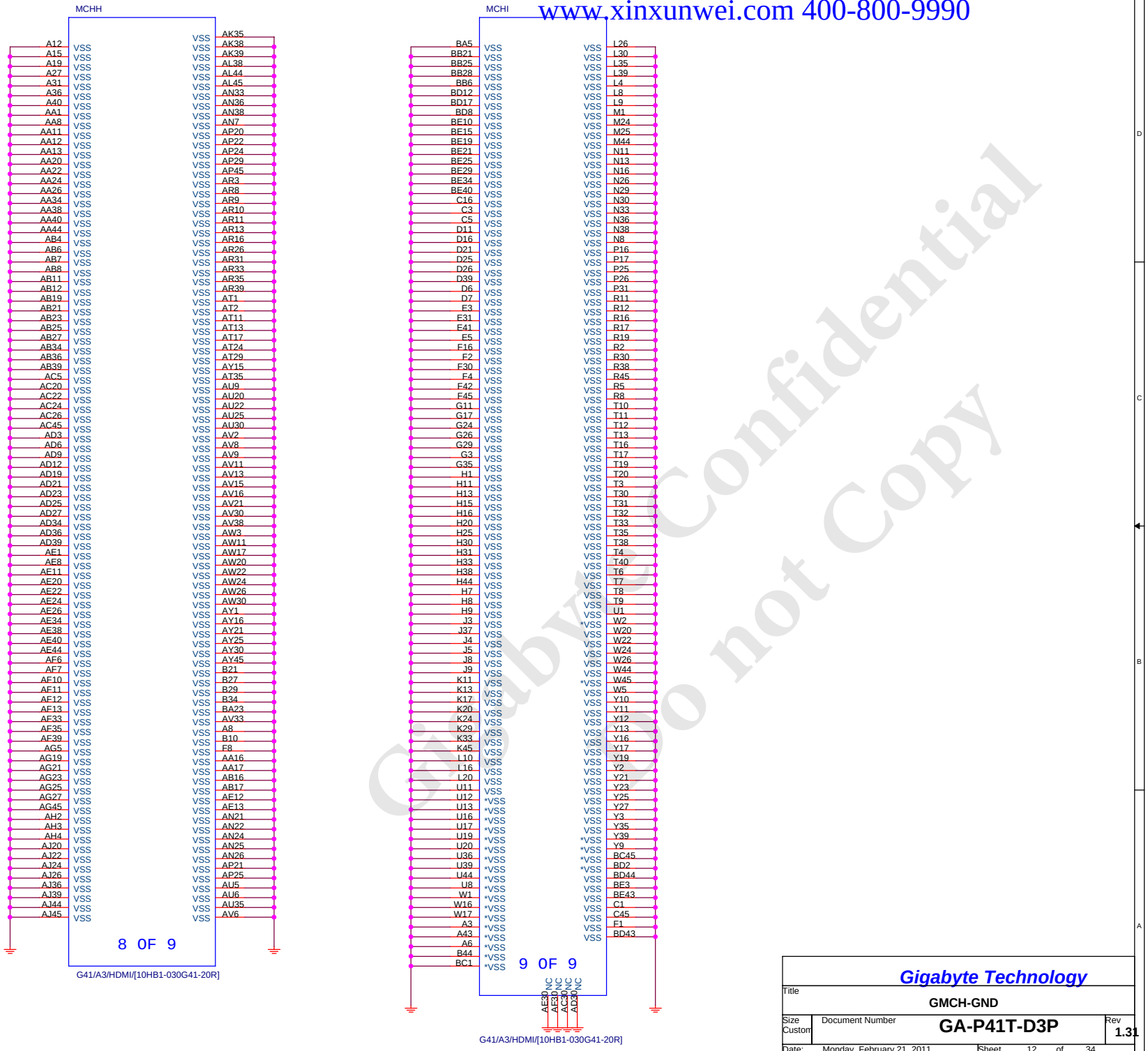
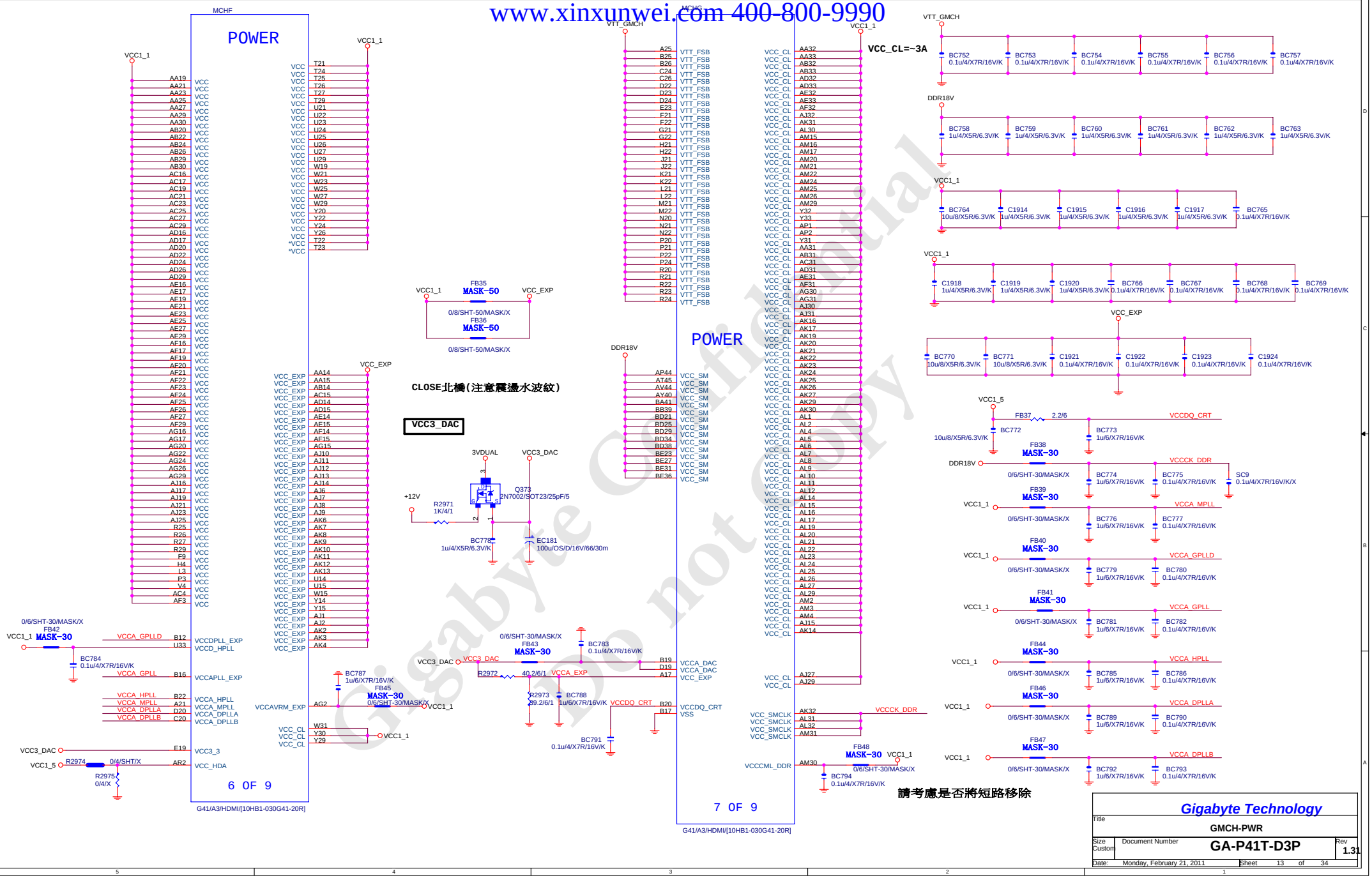
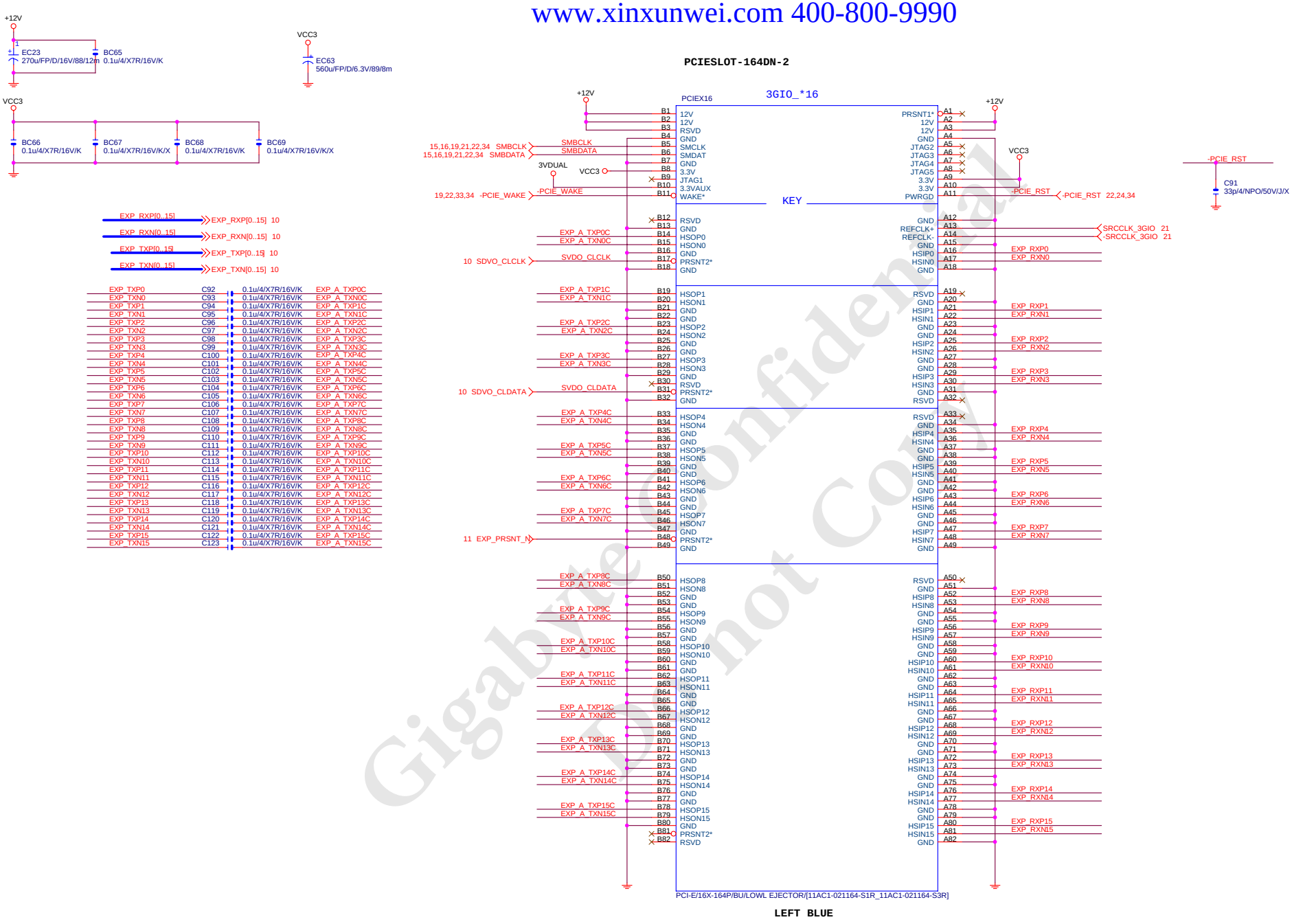
Gigabyte Technology



Gigabyte Technology		
Title	GMCH-GND	
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
Date: Monday, February 21, 2011	Sheet 12 of 34	





Gigabyte Technology			
Title	PCI EXPRESS * 16		
Size: Custom	Document Number: GA-P41T-D3P		Rev: 1.31
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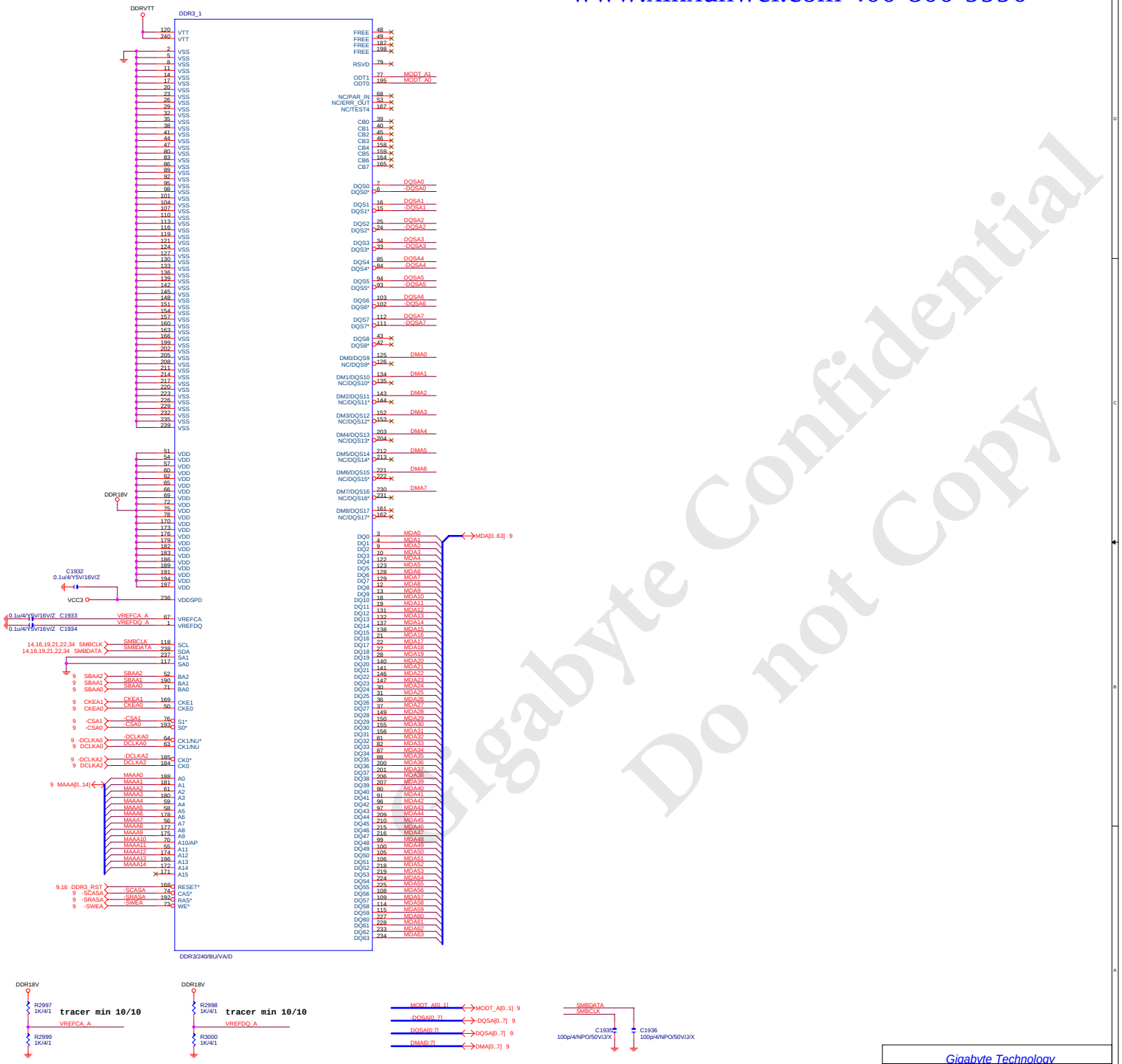

Gigabyte Technology			
Title: DDR3 CHANNEL A			
Size: Custom	Document Number: GA-P41T-D3P		Rev: 1.31
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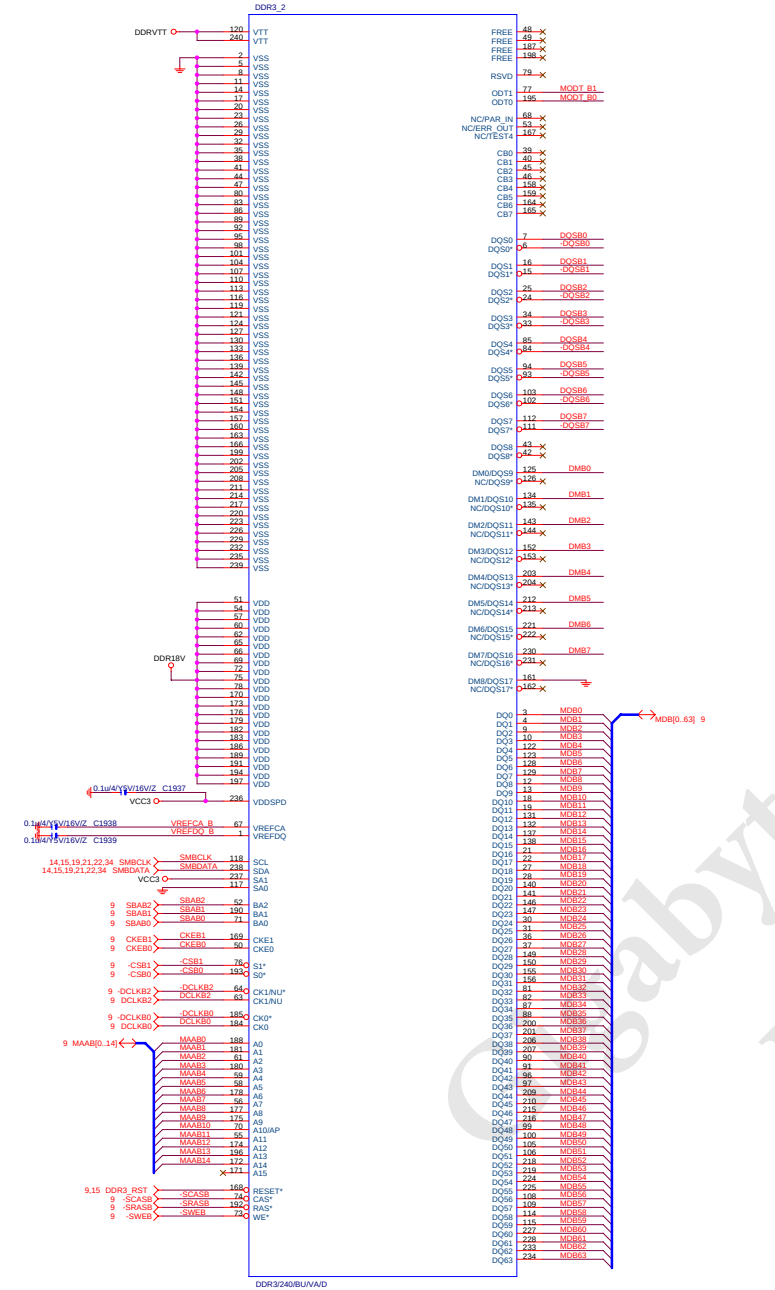

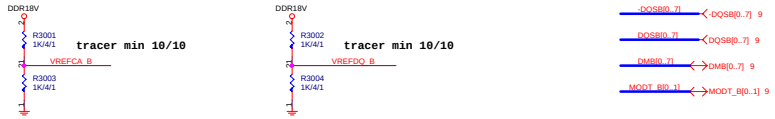

Gigabyte Technology			
Title: DDR3 CHANNEL B			
Size: Custom	Document Number: GA-P41T-D3P		Rev: 1.31
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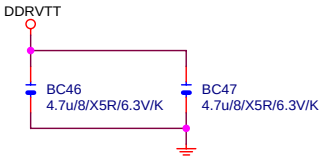
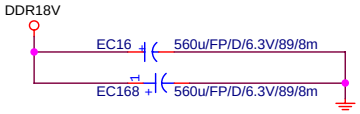
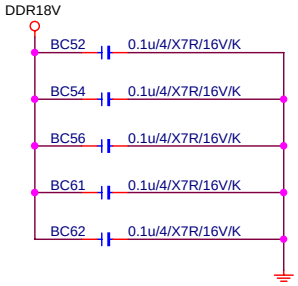
DDR TERMINATION CHANNEL A

DDRVTT Decouple

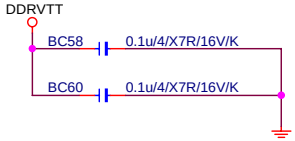




DDR18V Decouple

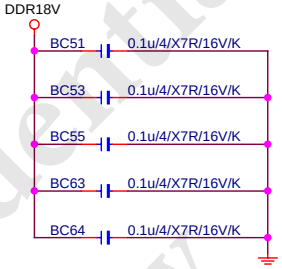


DDRVTT Decouple

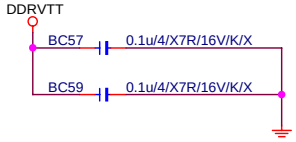


DDR TERMINATION CHANNEL B

DDR18V Decouple



DDRVTT Decouple



Gigabyte Technology		
Title: DDRII TERMINATOR		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
Date: Monday, February 21, 2011	Sheet 17 of 34	

FOR ICH7R POWER ON 瞬間會HIGH 到1.8V 之後0V,必須PULL DOWN 1K/6

H/W RESET

24 -RST_BTN
Q371 MMBT2222A/SOT23/600mA/40
Q370 MMBT2222A/SOT23/600mA/40
S0T23
S0T23
19 GPO16
R2915 100K/4/1
R2916 1K/4/1
BC744 1u/6/Y5V/10V/Z
BC745 1U/6/Y/10V/X/X

A_D[0..31] 22,34

U9A

22,34 PAR	PAR	E10	PAR	AD_0	E18 A_D0
22,34 -DEVSEL	-DEVSEL	A12	DEVSEL#	AD_1	C18 A_D1
21 ICH33	ICH33	A9	PCICLK	AD_2	A16 A_D2
R171 22/4		B18	PCIRST#	AD_3	F18 A_D3
22,34 -IRDY	-IRDY	A7	IRDY#	AD_4	F16 A_D4
22,34 -PCIPME	-PCIPME	B19	PME#	AD_5	A18 A_D5
22,34 -SERR	-SERR	B10	SERR#	AD_6	F17 A_D6
22,34 -STOP	-STOP	F15	STOP#	AD_7	A17 A_D7
22,34 -PLOCK	-PLOCK	E11	PLOCK#	AD_8	A15 A_D8
22,34 -TRDY	-TRDY	F14	TRDY#	AD_9	C14 A_D9
22,34 -PERR	-PERR	C9	PERR#	AD_10	E14 A_D10
22,34 -FRAME	-FRAME	F16	FRAME#	AD_11	D14 A_D11
				AD_12	B12 A_D12
				AD_13	C13 A_D13
22 -GNT0	-GNT0	E7	GNT0#	AD_14	G15 A_D14
22 -GNT1	-GNT1	D16	GNT1#	AD_15	G13 A_D15
34 -GNT2	-GNT2	D17	GNT2#	AD_16	E12 A_D16
		F13	GNT3#	AD_17	C11 A_D17
	-GNT4	A14	GNT4#_GPIO48	AD_18	D11 A_D18
	-GNT5	D8	GPIO17/GNT5#	AD_19	A11 A_D19
				AD_20	A10 A_D20
				AD_21	F11 A_D21
22 -REQ0	-REQ0	D7	REQ0#	AD_22	F10 A_D22
22 -REQ1	-REQ1	C16	REQ1#	AD_23	E9 A_D23
22,34 -REQ2	-REQ2	C17	REQ2#	AD_24	D9 A_D24
22 -REQ3	-REQ3	E13	REQ3#	AD_25	B9 A_D25
22 -REQ4	-REQ4	A13	REQ4#/GPIO22	AD_26	A8 A_D26
22 -REQ5	-REQ5	C8	GPIO1/REQ5#	AD_27	A6 A_D27
				AD_28	C7 A_D28
				AD_29	B6 A_D29
22,34 -PIRQA	-PIRQA	A3	PIRQA#	AD_30	E6 A_D30
22 -PIRQB	-PIRQB	B4	PIRQB#	AD_31	D6 A_D31
22,34 -PIRQC	-PIRQC	C5	PIRQC#		
22,34 -PIRQD	-PIRQD	B5	PIRQD#		
22,34 -PIRQE	-PIRQE	G8	GPIO2/PIRQE#	C/BE0#	B15 -C_BE0 -C_BE0 22,34
22 -PIRQF	-PIRQF	F7	GPIO3/PIRQF#	C/BE1#	C12 -C_BE1 -C_BE1 22,34
22 -PIRQG	-PIRQG	F8	GPIO4/PIRQG#	C/BE2#	D12 -C_BE2 -C_BE2 22,34
22 -PIRQH	-PIRQH	G7	GPIO5/PIRQH#	C/BE3#	C15 -C_BE3 -C_BE3 22,34

ICH7/A1/[10HB1-032801-N2R]

22,34 -PCIRST
C124 100p/4/NPO/50V/J/X

Pb-Free

SB_HEATSIN
SB_HS/[12SP2-030005-42R_12SP2-030005-43R]
SB_HS
NEW SB_HS 對邊

01=SPI
10=PCI
11=LPC(DEFAULT)
-GNT4 R1744 1K/4/1/X
-GNT5 R1745 1K/4/1

Close to ICH
as short as possible

U9B

10 DMI_0TXN	DMI_0TXN	V26	DMI_0RXN	USBP_0N	F1 -USBP0 -USBP0 23
10 DMI_0TXP	DMI_0TXP	V25	DMI_0RXP	USBP_0P	F2 +USBP0 +USBP0 23
10 DMI_0RXN C1863	DMI_ICH_IT_MR_0_DN	U28	DMI_0TXN	USBP_1N	G4 -USBP1 -USBP1 23
10 DMI_0RXP C1864	0.1u/4/X7R/16V/K DMI_ICH_IT_MR_0_DP	U27	DMI_0TXP	USBP_1P	G3 +USBP1 +USBP1 23
	0.1u/4/X7R/16V/K		DMI_1RXN	USBP_2N	H1 -USBP2 -USBP2 23
10 DMI_1TXN	DMI_1TXN	Y26	DMI_1RXP	USBP_2P	H2 +USBP2 +USBP2 23
10 DMI_1TXP	DMI_1TXP	Y25	DMI_1TXN	USBP_3N	J4 -USBP3 -USBP3 23
10 DMI_1RXN C1865	DMI_ICH_IT_MR_1_DN	W28	DMI_1TXP	USBP_3P	J3 +USBP3 +USBP3 23
10 DMI_1RXP C1866	0.1u/4/X7R/16V/K DMI_ICH_IT_MR_1_DP	W27	DMI_2RXN	USBP_4N	K1 -USBP4 -USBP4 33
	0.1u/4/X7R/16V/K		DMI_2RXP	USBP_4P	K2 +USBP4 +USBP4 33
10 DMI_2TXN	DMI_2TXN	AB26	DMI_2TXN	USBP_5N	L4 -USBP5 -USBP5 33
10 DMI_2TXP	DMI_2TXP	AB25	DMI_2TXP	USBP_5P	L5 +USBP5 +USBP5 33
10 DMI_2RXN C1867	DMI_ICH_IT_MR_2_DN	AA28	DMI_3RXN	USBP_6N	M1 -USBP6 -USBP6 23
10 DMI_2RXP C1868	0.1u/4/X7R/16V/K DMI_ICH_IT_MR_2_DP	AA27	DMI_3RXP	USBP_6P	M2 +USBP6 +USBP6 23
	0.1u/4/X7R/16V/K		DMI_3TXN	USBP_7N	N4 -USBP7 -USBP7 23
10 DMI_3TXN	DMI_3TXN	AD25	DMI_3TXP	USBP_7P	N3 +USBP7 +USBP7 23
10 DMI_3TXP	DMI_3TXP	AD24			
10 DMI_3RXN C1869	DMI_ICH_IT_MR_3_DN	AC28			
10 DMI_3RXP C1870	0.1u/4/X7R/16V/K DMI_ICH_IT_MR_3_DP	AC27			
	0.1u/4/X7R/16V/K				

22 PCIE_IN0		F26	PE0RXN	OC_0#	D3 -USBOC_F 23
22 PCIE_IP0		F25	PE0RXP	OC_1#	C4
22 PCIE_ON0 C1874	0.1u/4/X7R/16V/K	E28	PE0TXN	OC_2#	D5
22 PCIE_OP0 C1875	0.1u/4/X7R/16V/K	E27	PE0TXP	OC_3#	D4
33 ML_IN		H26	PE1RXN	OC_4#	E5 -USBOC_R 33
33 ML_IP		H25	PE1RXP	GPIO29_OC_5#	C3
33 ML_ON C1894	0.1u/4/X7R/16V/K	G28	PE1TXN	GPIO30_OC_6#	A2
33 ML_OP C1895	0.1u/4/X7R/16V/K	G27	PE1TXP	GPIO31_OC_7#	B3
34 PCIE_IN1		K26	PE2RXN		
34 PCIE_IP1		K25	PE2RXP		
34 PCIE_ON1 C1897	0.1u/4/X7R/16V/K	J28	PE2TXN		
34 PCIE_OP1 C1898	0.1u/4/X7R/16V/K	J27	PE2TXP	USBRBIAS	D1 R172 22.6/6/1
34 PCIE_IN2		M26	PE3RXN	USBRBIAS#	D2
34 PCIE_IP2		M25	PE3RXP		
34 PCIE_ON2 C1899	0.1u/4/X7R/16V/K	L28	PE3TXN		
34 PCIE_OP2 C1900	0.1u/4/X7R/16V/K	L27	PE3TXP	CLK48	B2 USBCLK48 USBCLK48 21
		P26	PE4RXN		
		P25	PE4RXP		
		N28	PE4TXN		
		N27	PE4TXP		
		T25	PE5RXN		
		T24	PE5RXP		
		R28	PE5TXN		
		R27	PE5TXP		

ICH7 ONLY 4 LANE

VCC1_5 R173 24.9/4/1
C25 DMI_ZCOMP
D25 DMI_IRCOMP

21 -SRCCLK_ICH -SRCCLK_ICH AE28 DMICLKN
21 SRCCLK_ICH SRCCLK_ICH AE27 DMICLKP

ICH7/A1/[10HB1-032801-N2R]

Gigabyte Technology

Title: ICH7-PCI, DMI, LAN, USB

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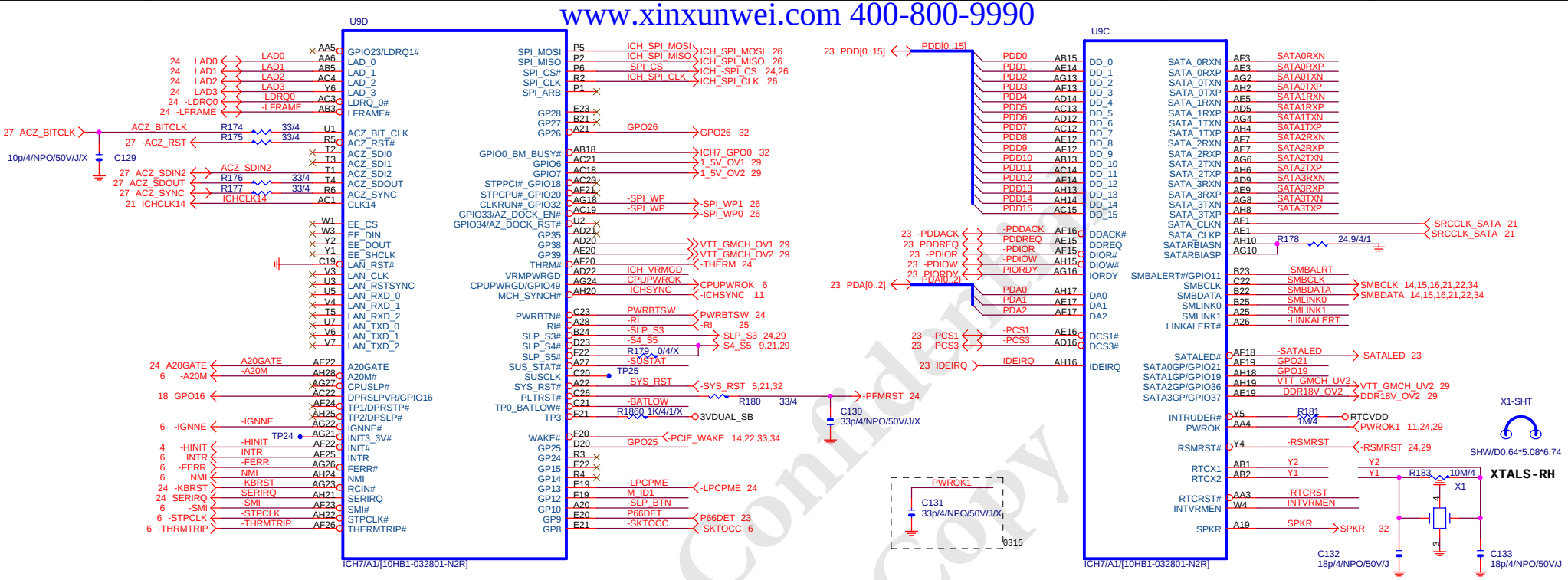
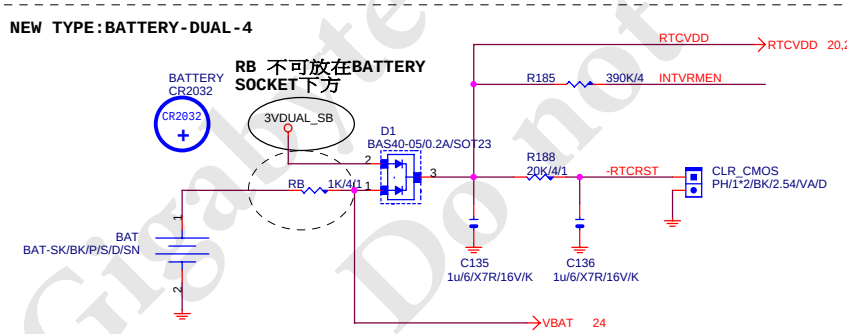
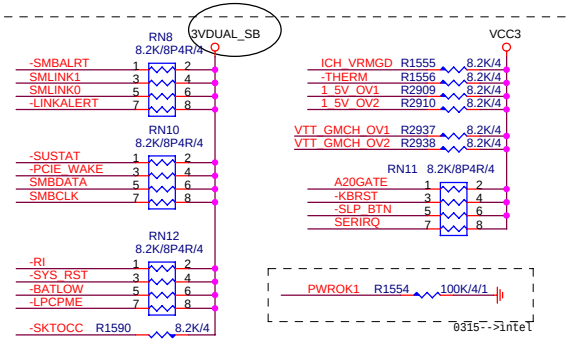
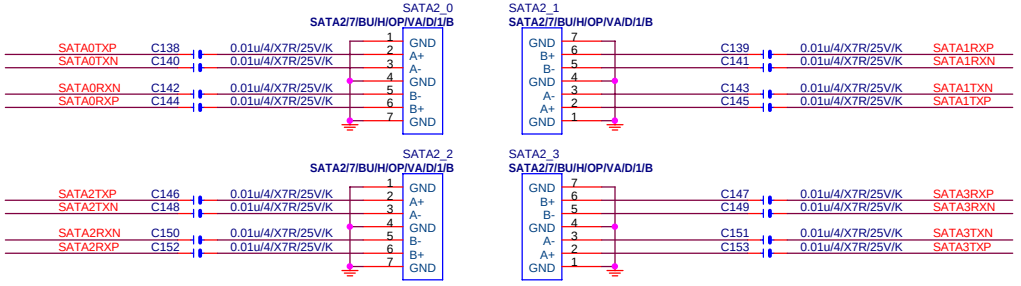
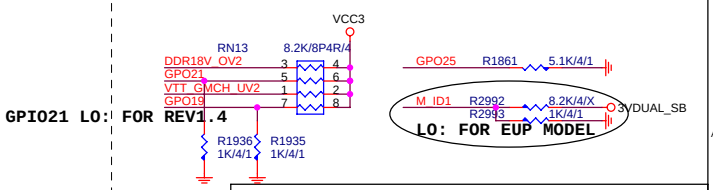


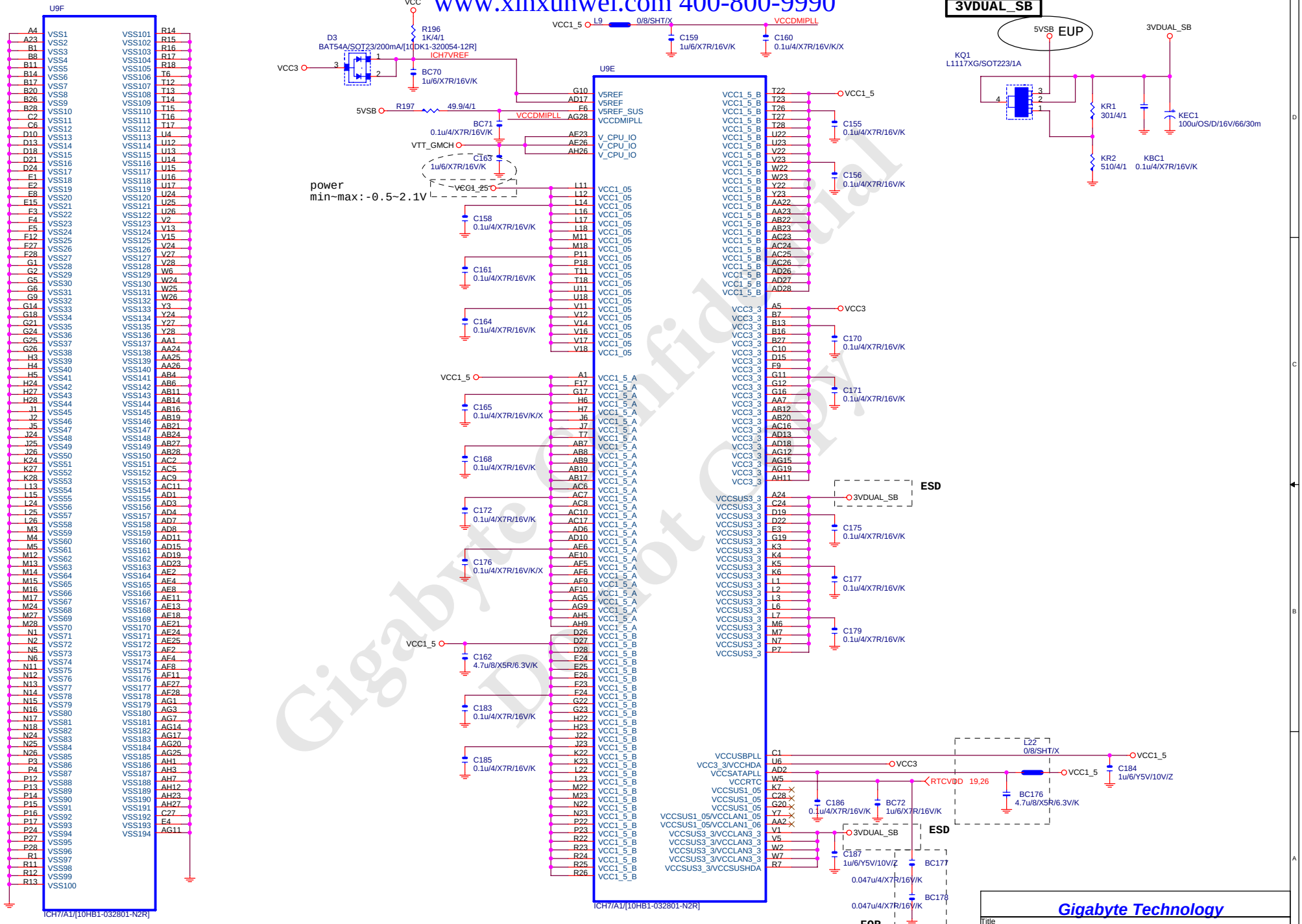








Gigabyte Technology	
Title	ICH7-IDE, SATA, GPIO, CTRL
Size: Custom	Document Number: GA-P41T-D3P
Rev: 1.31	
Date: Monday, February 21, 2011	Sheet 19 of 34



CLK GEN CK505

U10

RTM875T-587-VC-GRT/S

3VDUAL

CKVDD

EUP

Q377 2N7002/SOT23/25pF/5

GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from 17/18

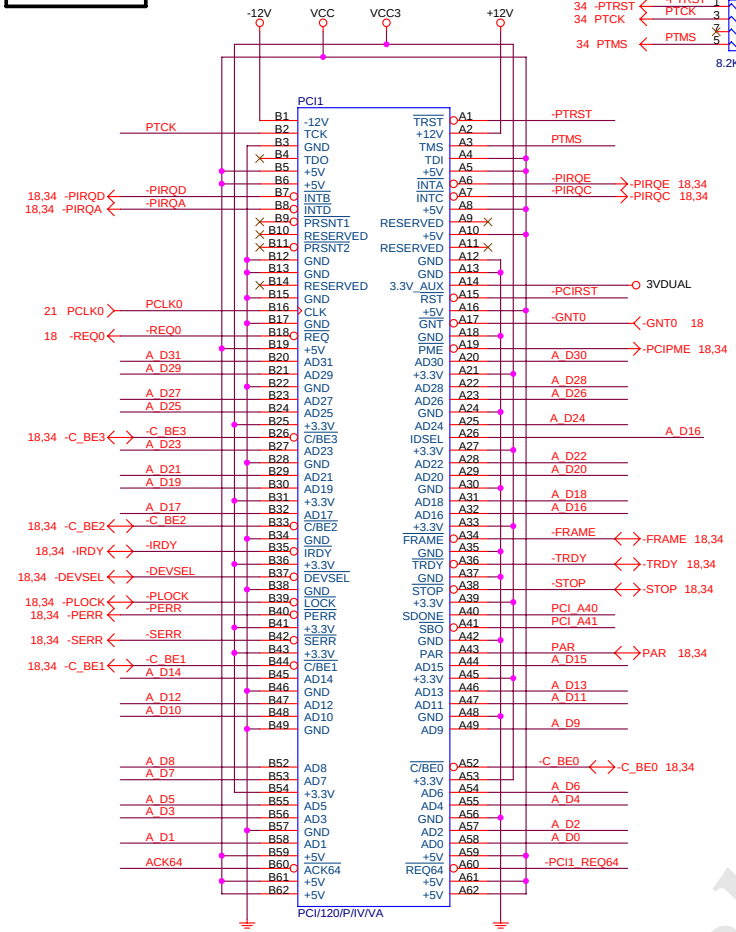
Trusted Mode

overclocking

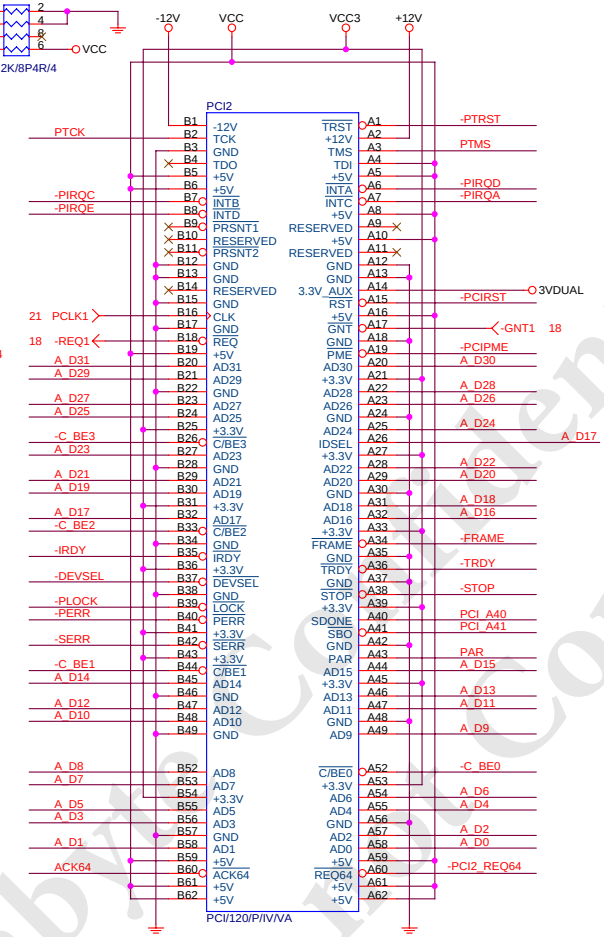
FOR REALTEK CLOCK PULL HIGH

Gigabyte Technology	
Title	CK505 CLK GEN
Size: Custom	Document Number: GA-P41T-D3P
Rev	1.31
Date: Monday, February 21, 2011	Sheet 21 of 34

PCI1,2 SLOT

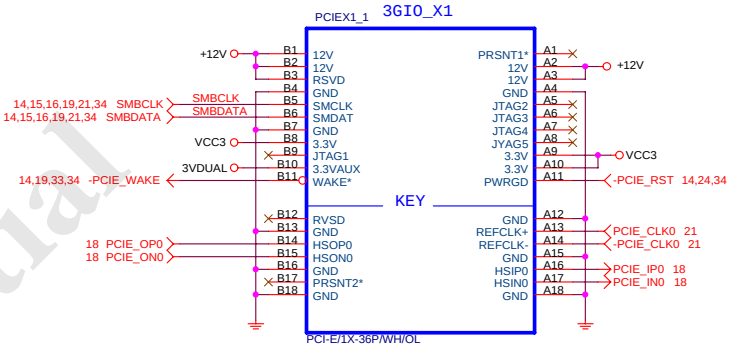


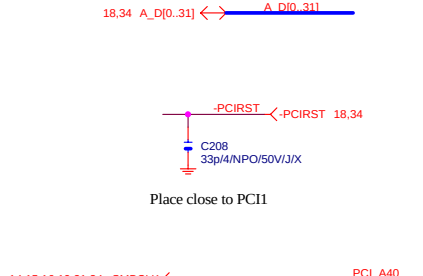
AD_16/-PIRQ(E-D-C-A)/-REQ0/-GNT0

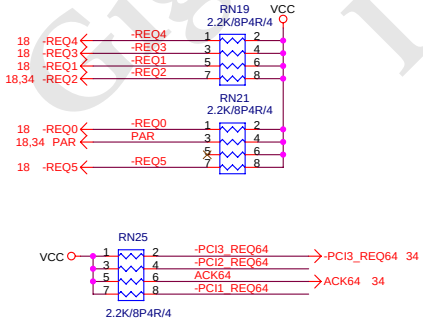


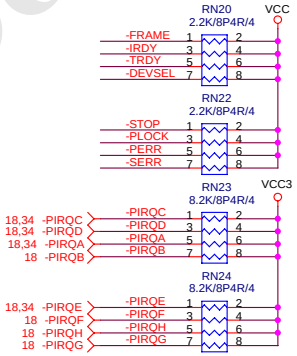
AD_17/-PIRQ(D-C-A-E)/-REQ1/-GNT1

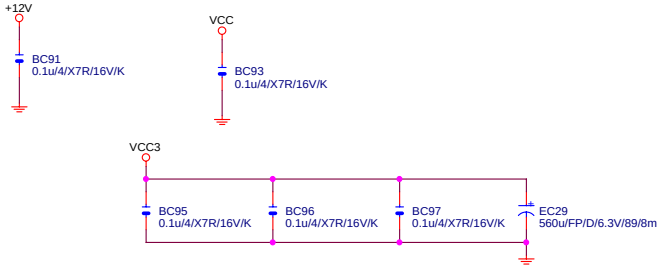
PCIE*1











Gigabyte Technology

Title	PCI SLOT 1, 2/PCIEX1	
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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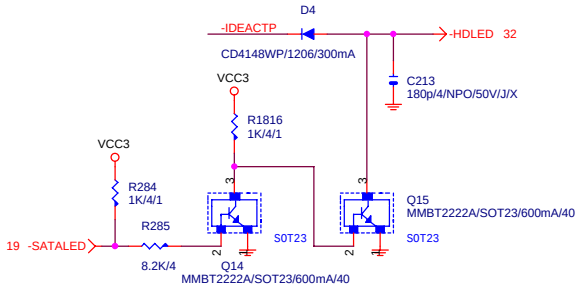
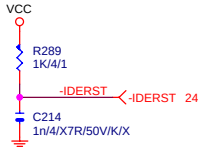
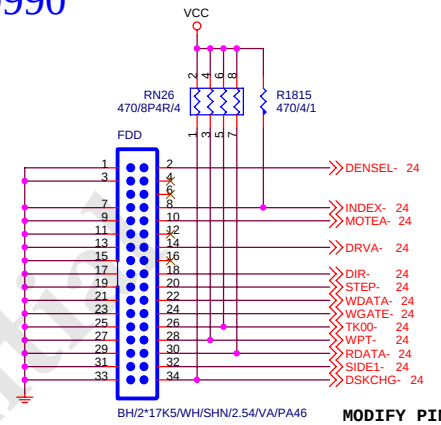
IDE/SATA LED



IDE RESET

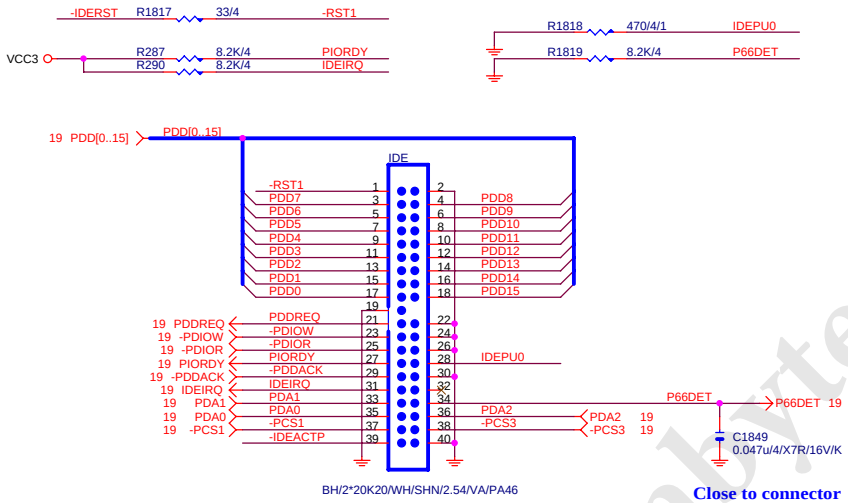


FLOPPY



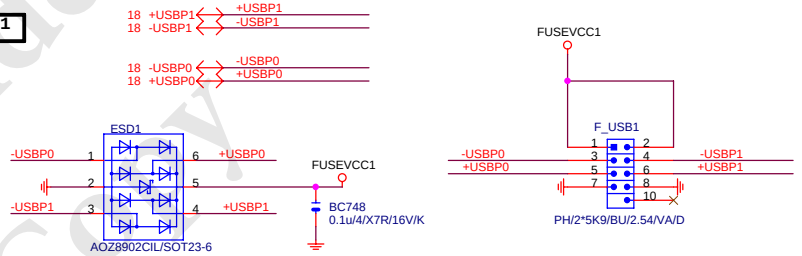
MODIFY PIN HEADER

IDE

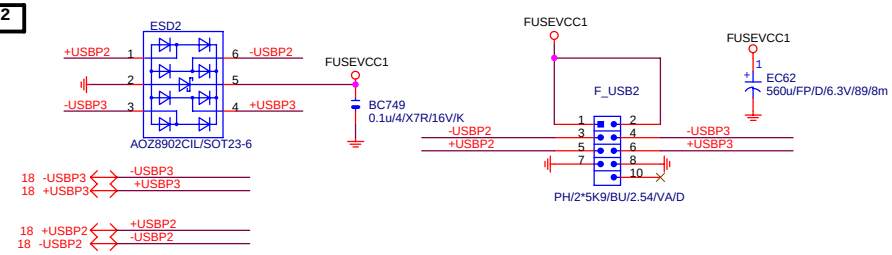


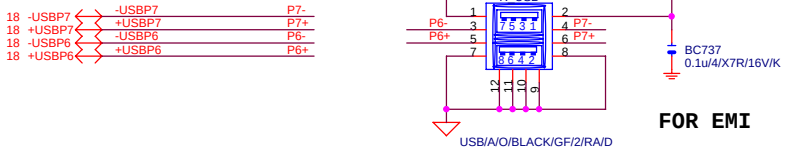
Close to connector

FRONT USB1

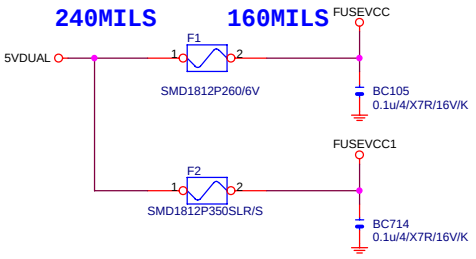


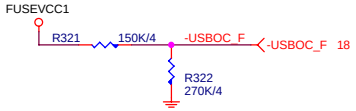
FRONT USB2





FOR EMI





Gigabyte Technology		
Title: IDE,FDD,F_USB		
Size: B	Document Number: GA-P41T-D3P	Rev: 1.31
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RTS2- ==LOW CPU FAN 50%
==HIGH 100%
DEFAULT 50%

VCC
R1561 8.2K/4/X DTR2-
R1562 8.2K/4/X RTS2-
R1587 680/4
0302-UP

SOUT2		
	1	VID pins threshold voltage select: Vih / Vil : 2.0 / 0.8V
	0	VID pins threshold voltage select: Vih / Vil : 0.8 / 0.4V

PUSH-PULL
D68 BAT54A/SOT23/200mA/[10DK1-320054-12R]/X
33 GP64
R2920 1K/4/1/X DTR2- RTS2-
R347 680/4 DTR2-
R348 680/4 TXD2
FOR LAN 8111C

IT8712F/[IT8718F]

PD[0..7] PD[0..7] 25
STB- AFD- ERR- INIT- SLIN- ACK-

U15

JP7 : HIGH DUAL BIOS ENABLE
LOW DUAL BIOS DISABLE

如果BSEL0/1 直接拉TO N/B,R264/R265 不上

RN140 1K/8P4R/4
RN163 8.2K/8P4R/4

Pop to disable Dual BIOS
-SIO_SPI_CS0 R2926 0/4/X ICH_-SPI_CS
26 -SIO_SPI_CS1 R2927 22/4 CEB_N
26 -SIO_SPI_CS0 R2928 0/4/SHT/X -RST_BT

Dual BIOS:
GB logo :Pin 61 (GP15/CSA)
GB logo :Pin 59 (GP17/CSB)

Pin 59 Dual BIOS ,Power On Strapping:
H ==>Dual BIOS function Enable
L ==>Dual BIOS function Disable

1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接3.3V
LPCPD#=VIDVCC

Gigabyte Technology		
Title	ITE 8718 LPC IO	
Size B	Document Number GA-P41T-D3P	Rev 1.31
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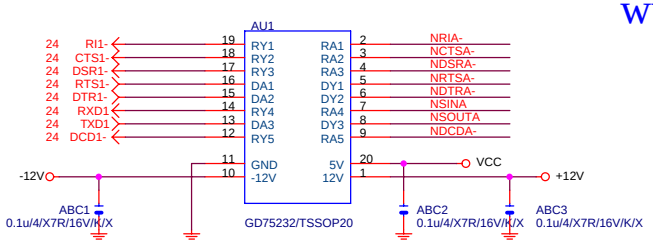
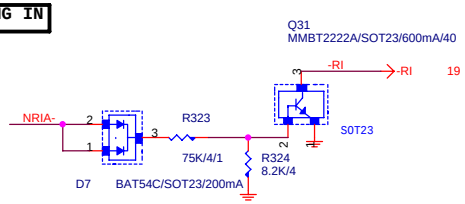
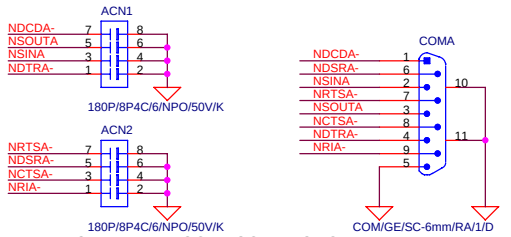
COMA



RING IN

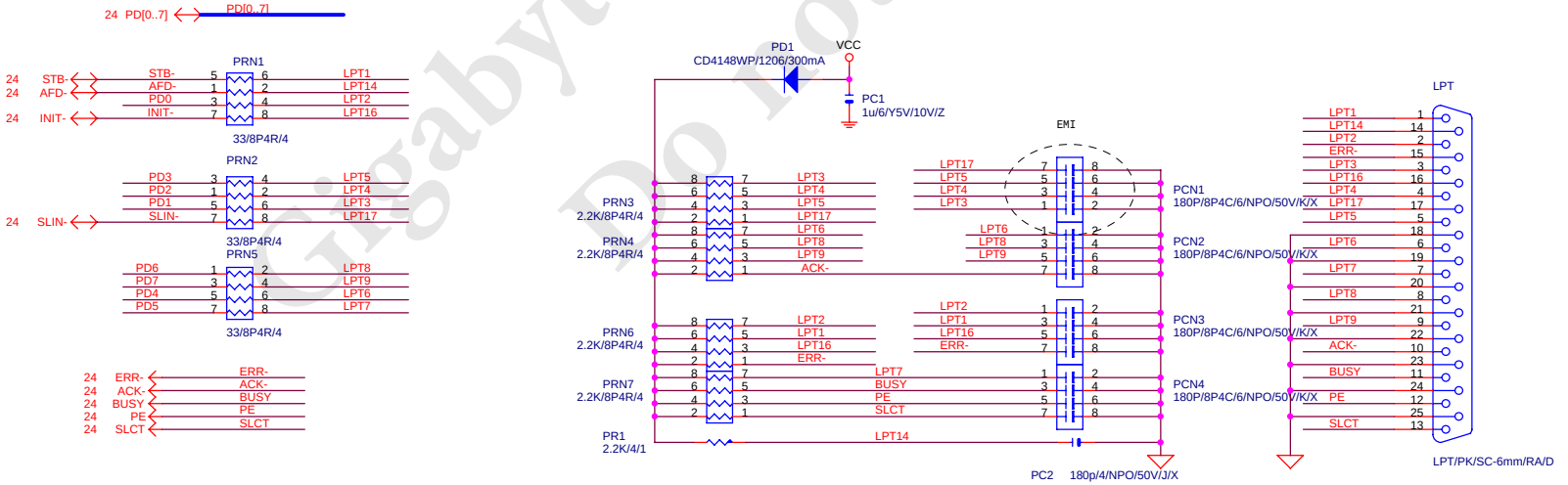


EXTERNAL COMA



PLACE NEAR COM CONNECTOR

LPT PORT



Gigabyte Technology		
Title: COM & LPT PORT		
Size: B	Document Number: GA-P41T-D3P	Rev: 1.31
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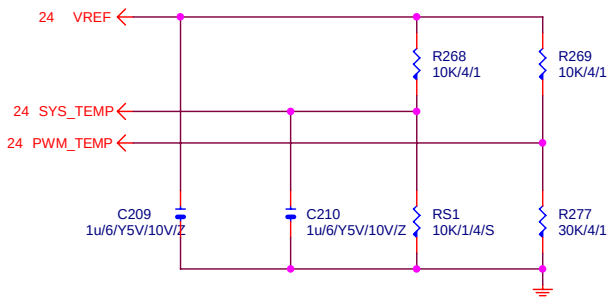
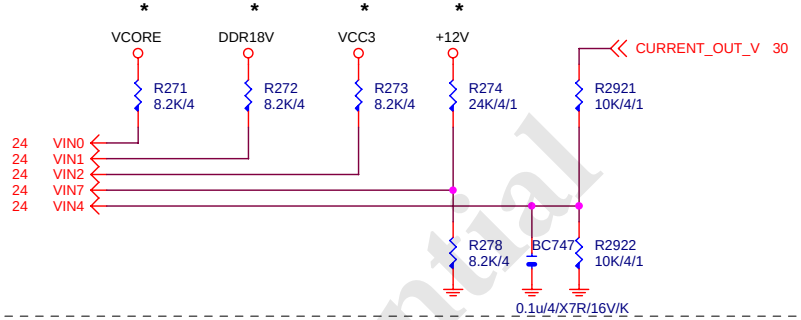
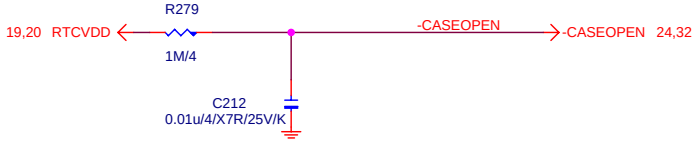
TEMP H/W MONITOR



VOLTAGE-- H/W MONITOR

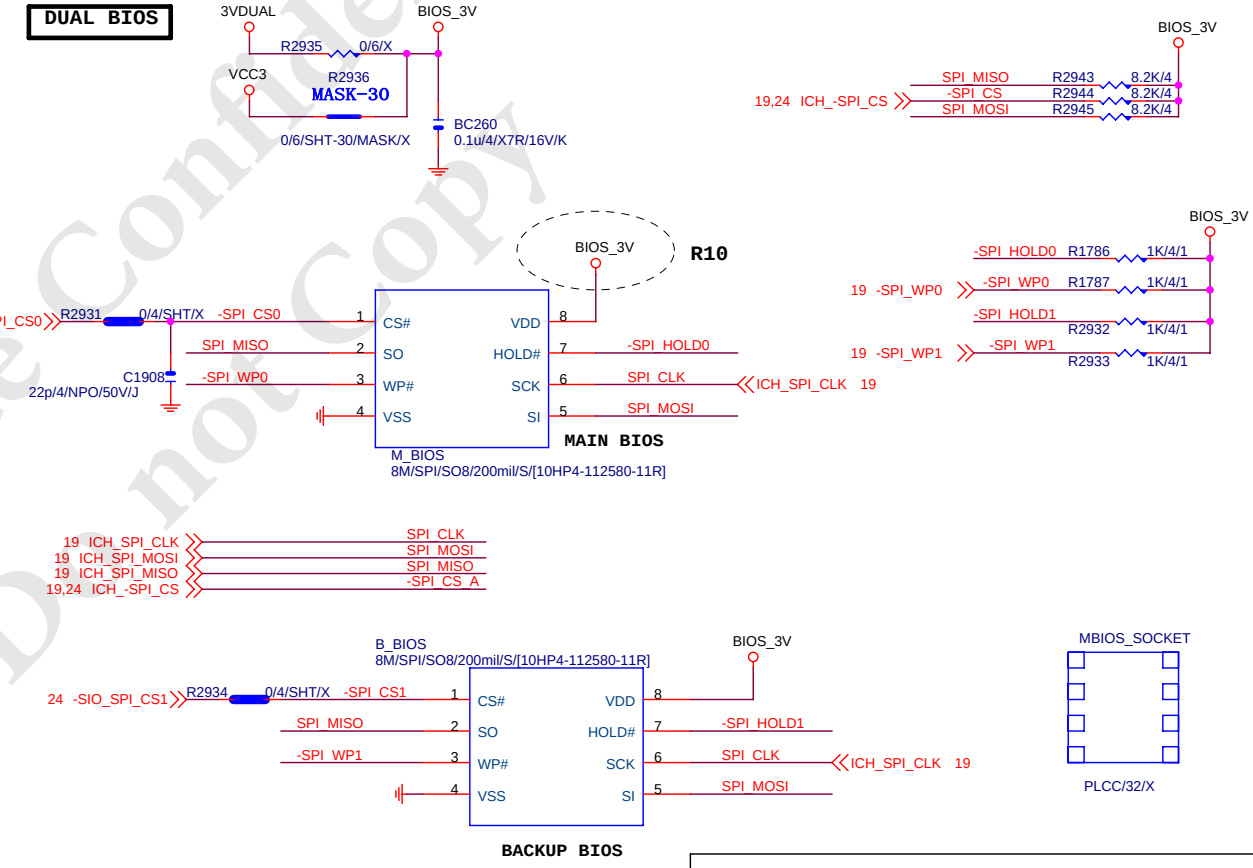


CASE OPEN

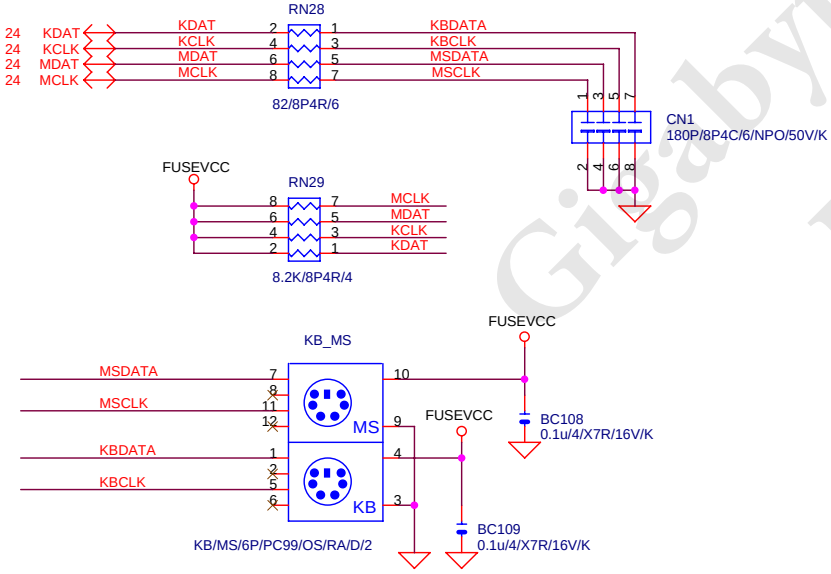


Case Open Circuits

DUAL BIOS



KB/MS



Gigabyte Technology		
Title: HW-MONITOR/CI/KB/MS/BIOS		
Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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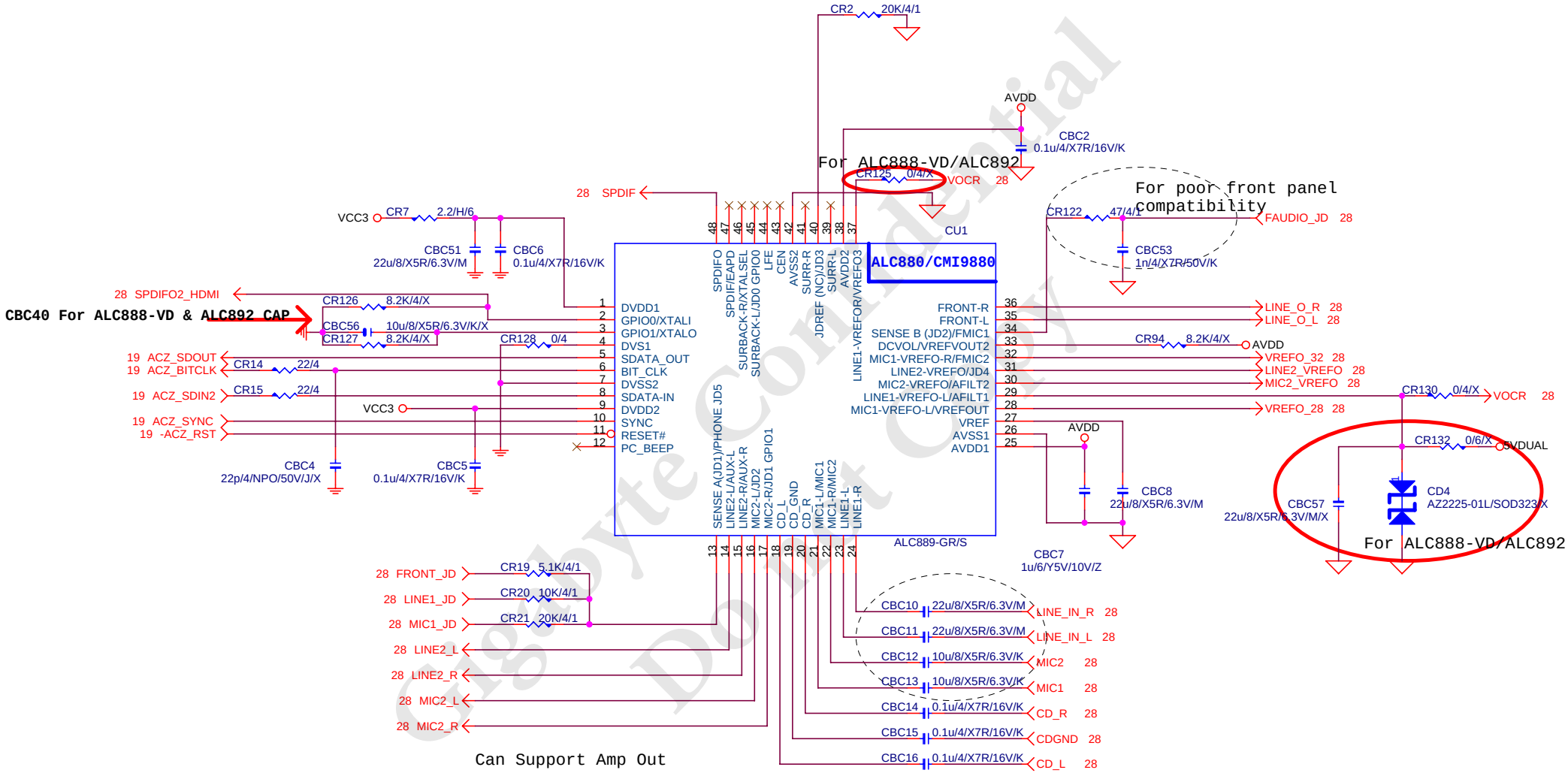
AZALIA CODEC



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Size: Custom	Document Number: GA-P41T-D3P	Rev: 1.31
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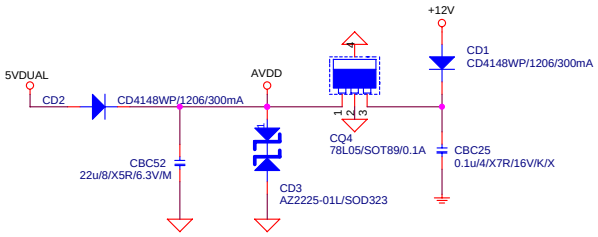
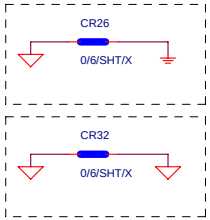
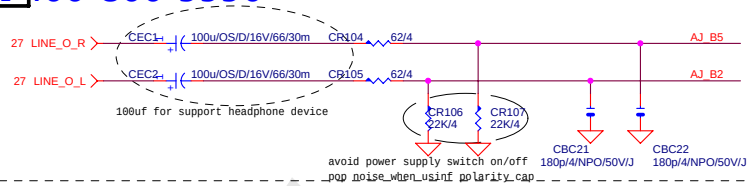
CODEC POWER/EMI PAD

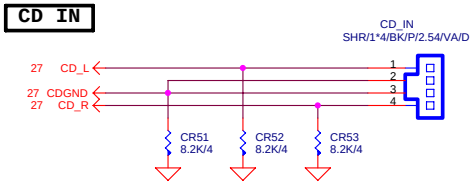




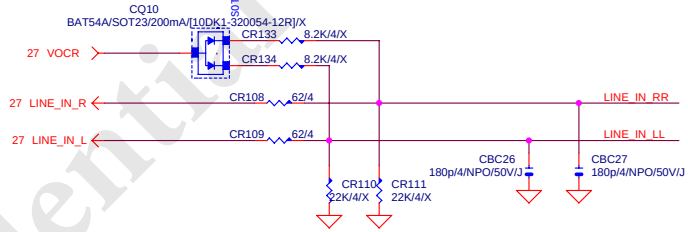
LINE-OUT



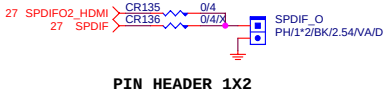
CD IN



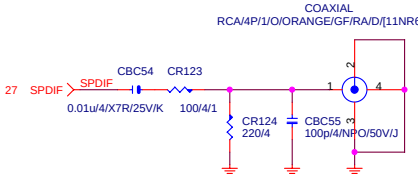
LINE-IN



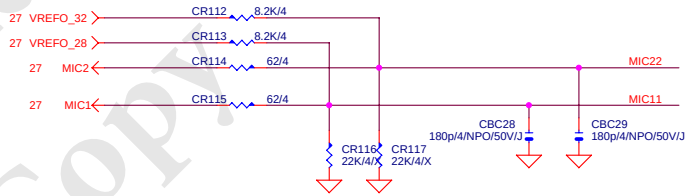
SPDIF



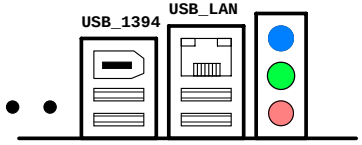
PIN HEADER 1X2

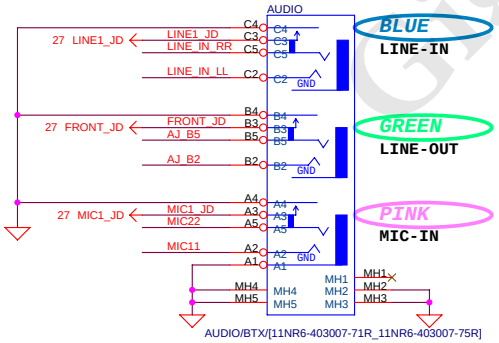


MIC-IN

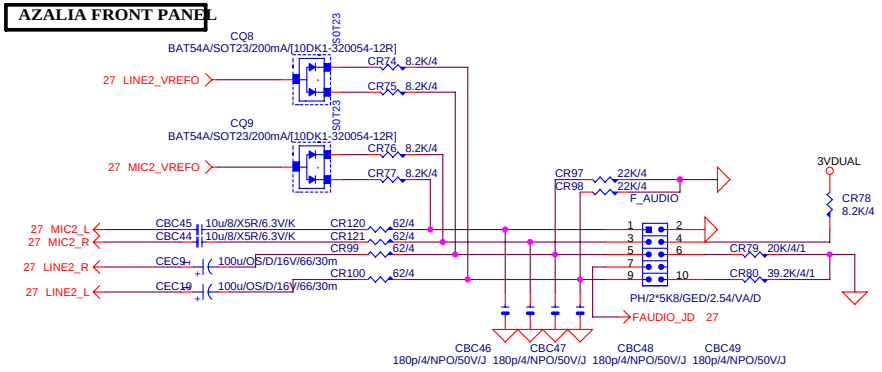


AZALIA JACK





AZALIA FRONT PANEL



Gigabyte Technology		
Title: AUDIO JACK		
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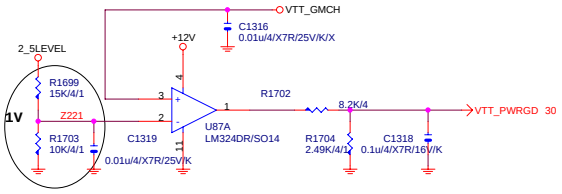
VTT_PWRGD



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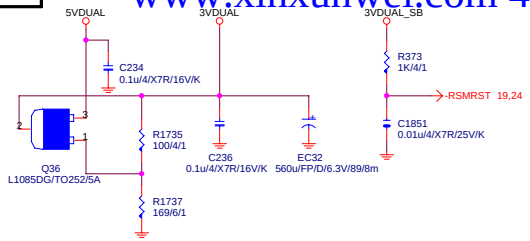
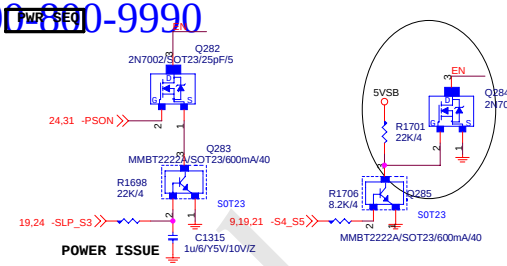
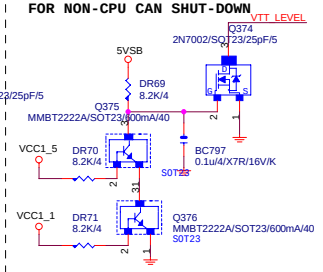


PWR SEQ



FOR NON-CPU CAN SHUT-DOWN



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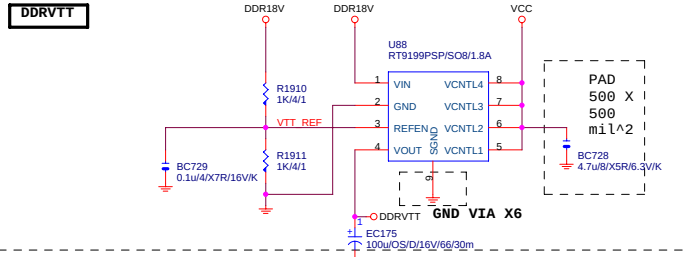

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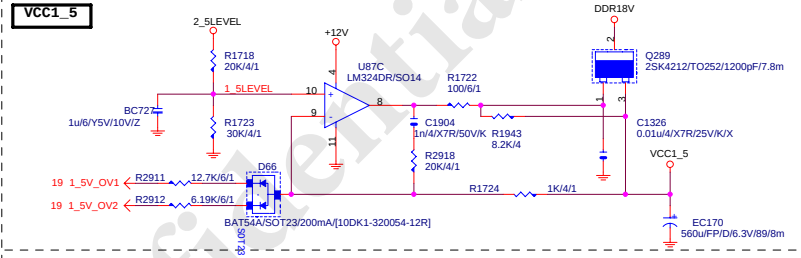
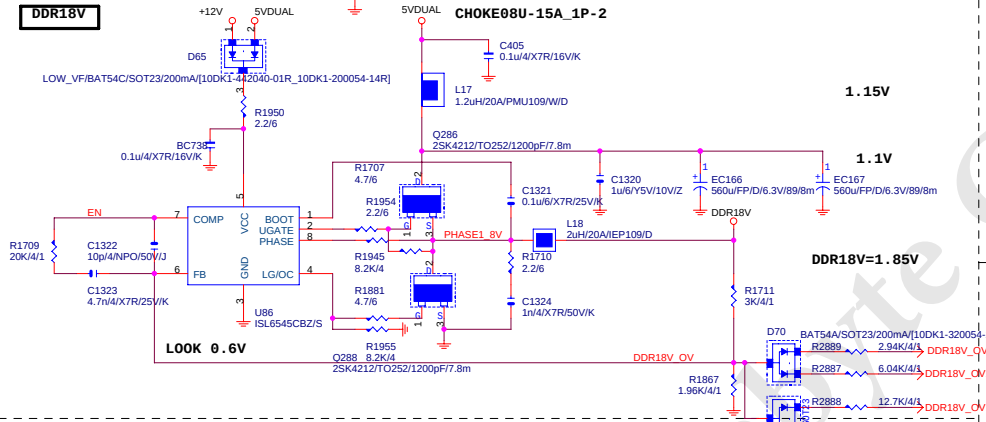
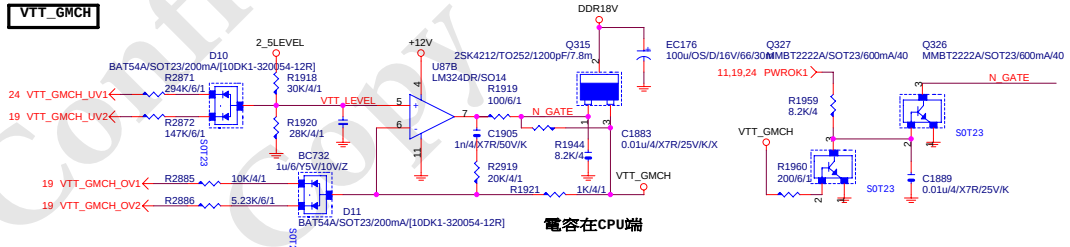


DDR18V



VTT_GMCH



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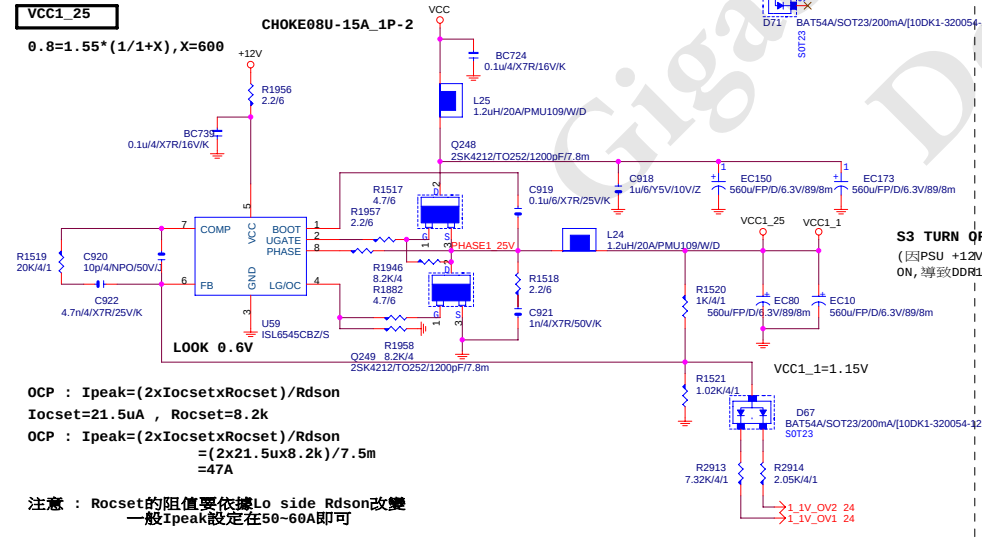

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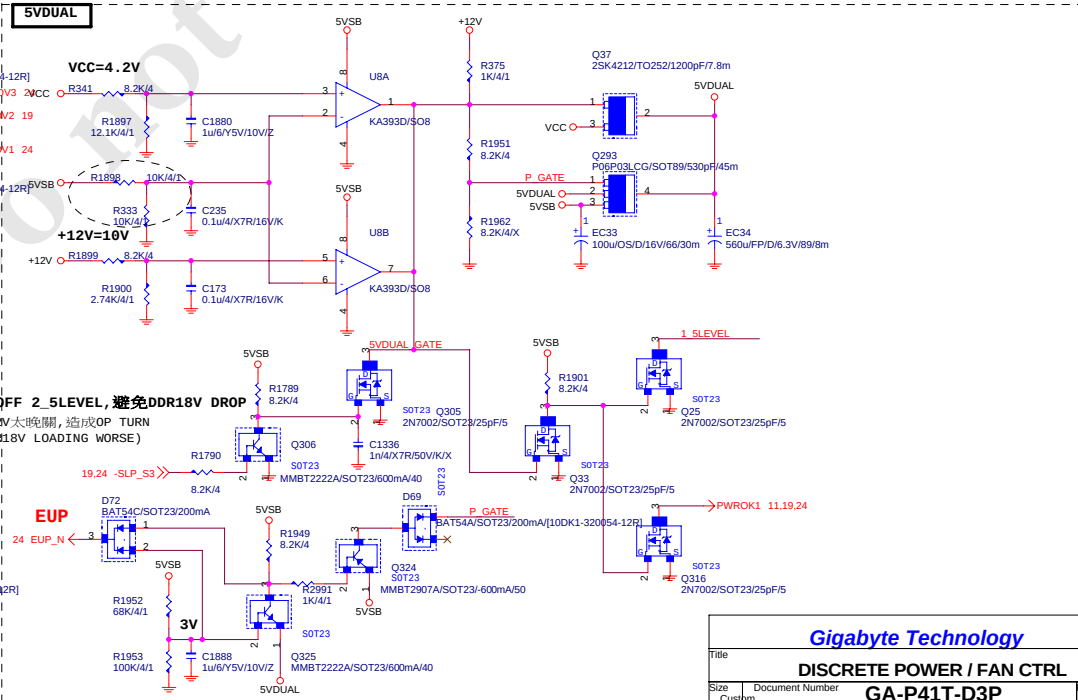

Gigabyte Technology		
Title	DISCRETE POWER / FAN CTRL	
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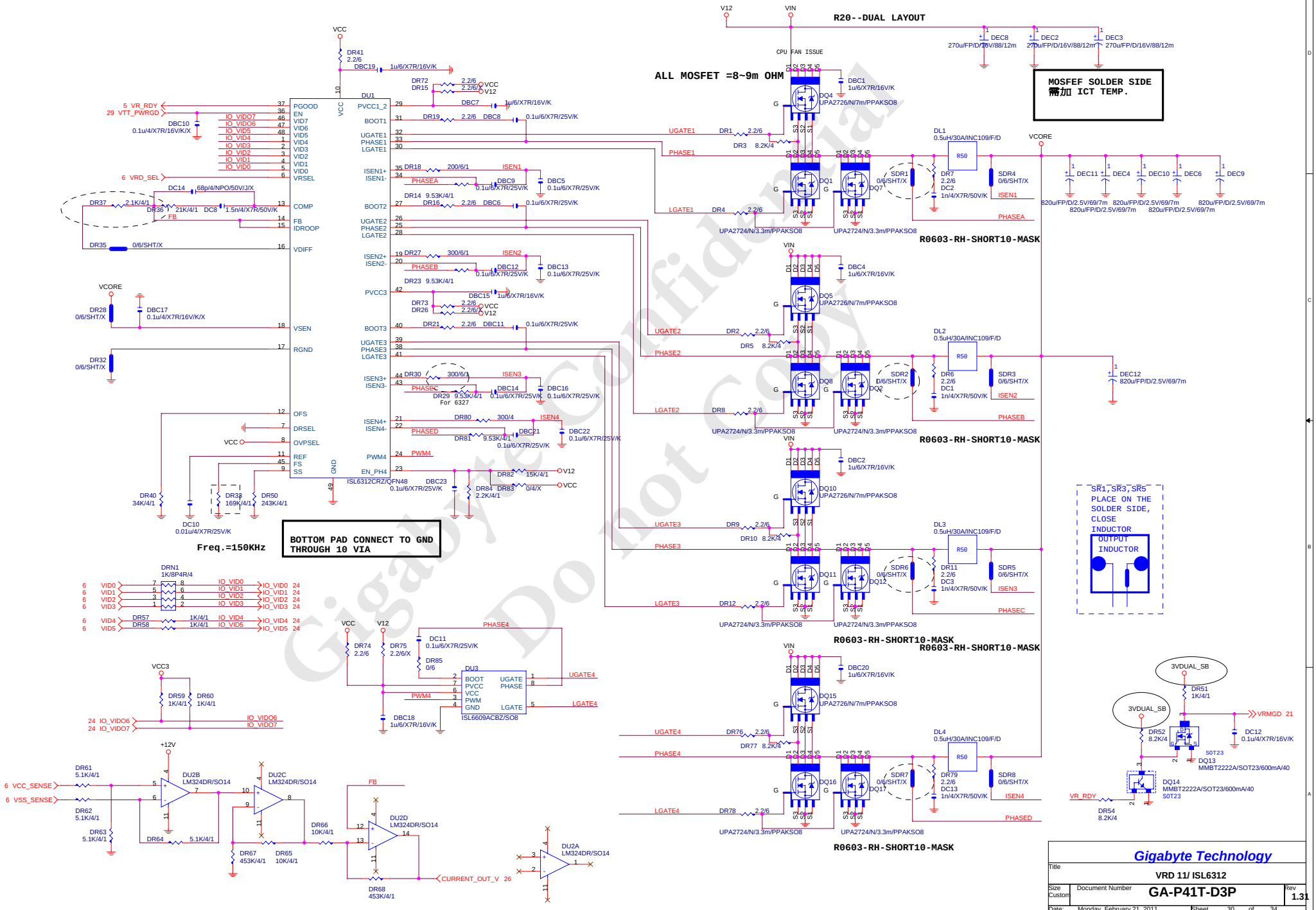

Gigabyte Technology			
Title	VRD 11/ ISL6312		
Size: Custom	Document Number: GA-P41T-D3P		Rev: 1.31
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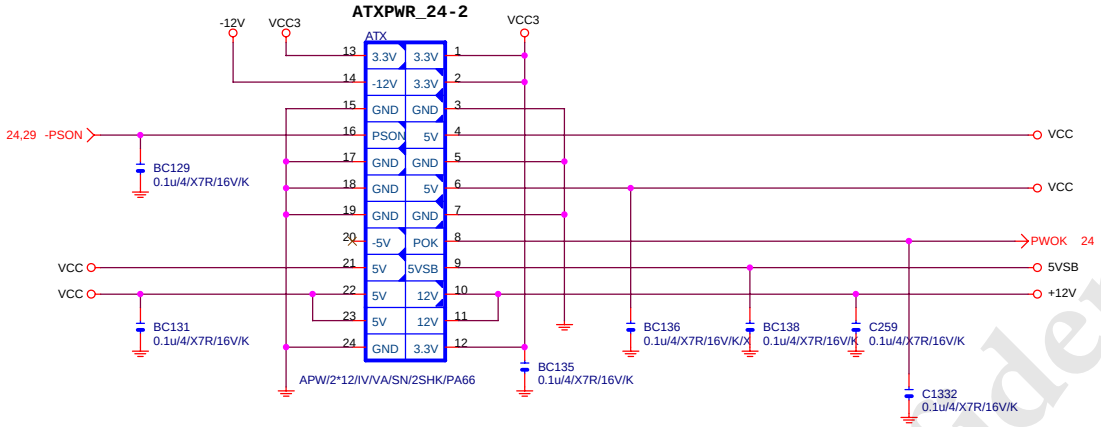
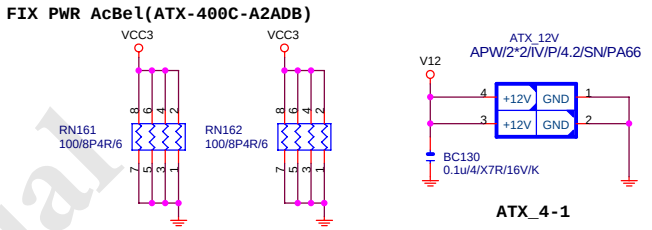
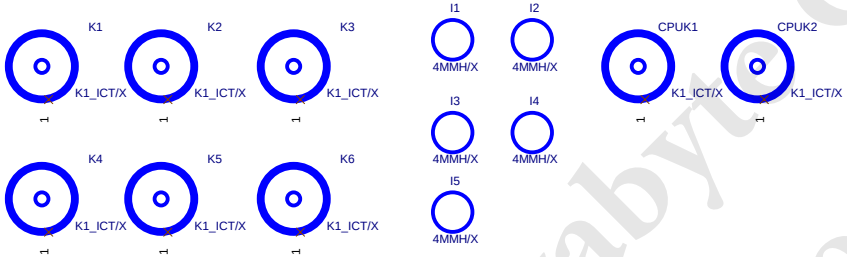
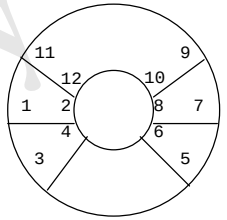
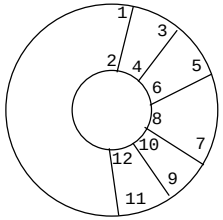
ATX POWER CONNECTOR

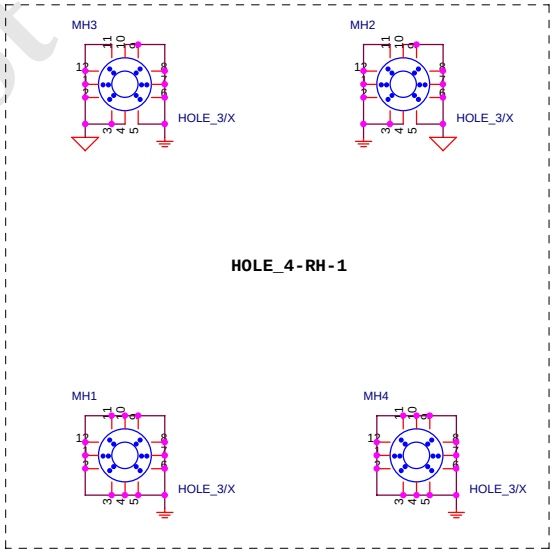


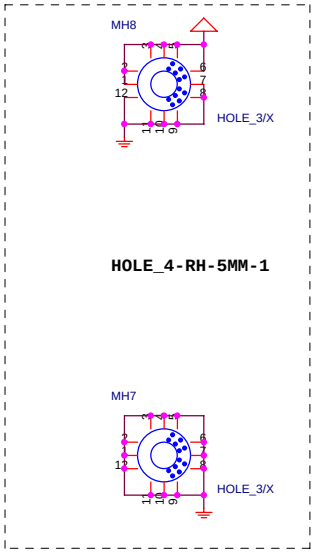


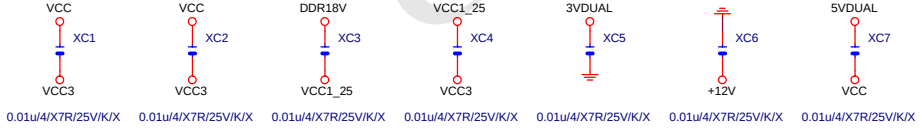












Gigabyte Technology		
Title: ATX POWER CONNECTOR		
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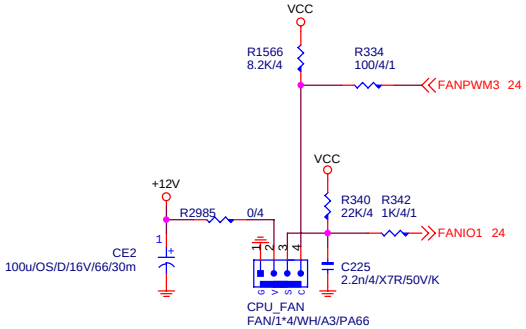
CPU SMART FAN

SMART FAN

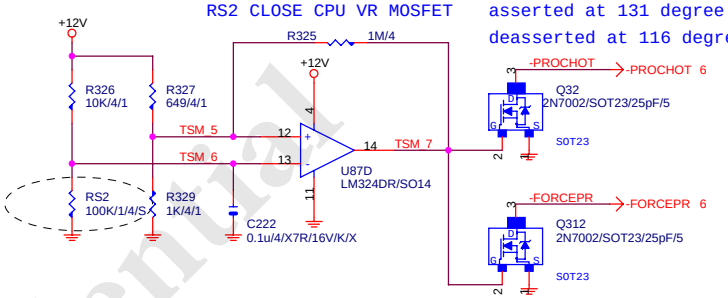


PROCESSOR HOT

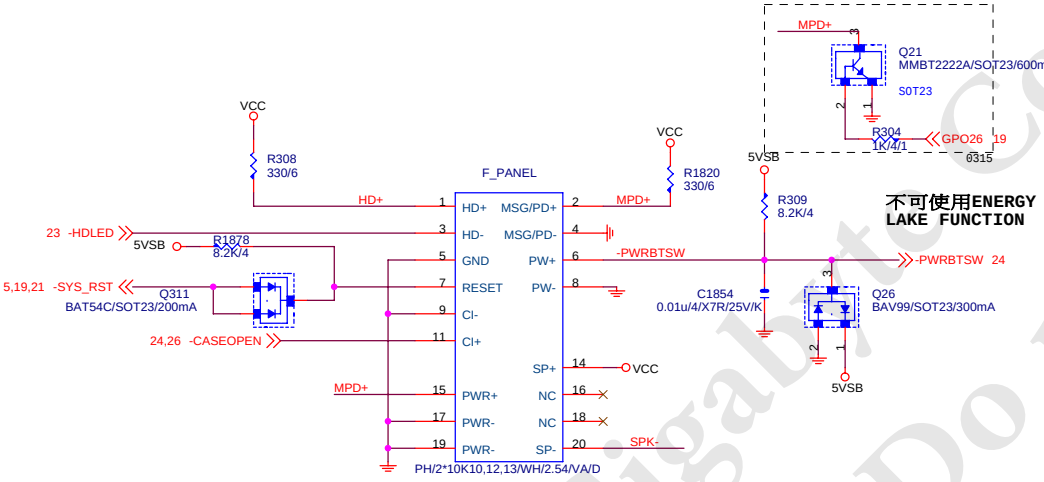
RS2 CLOSE CPU VR MOSFET

asserted at 131 degree

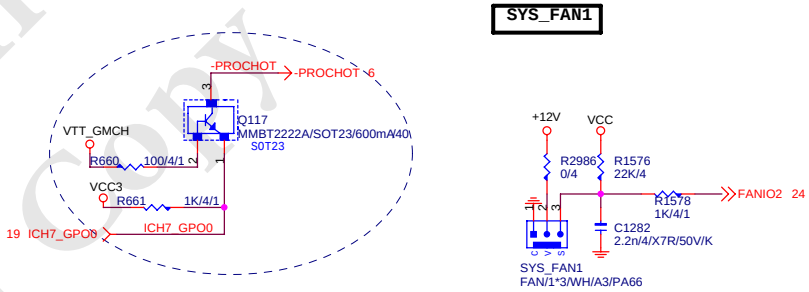
deasserted at 116 degree



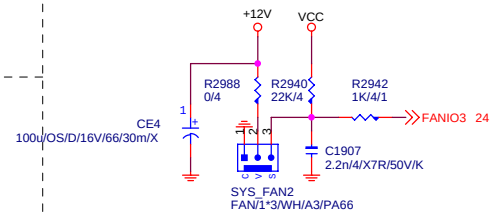
INTEL FRONT PANEL



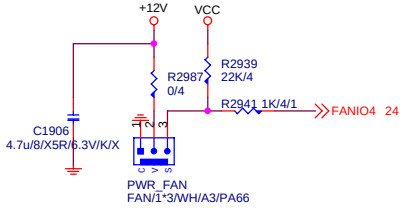
SYS_FAN1



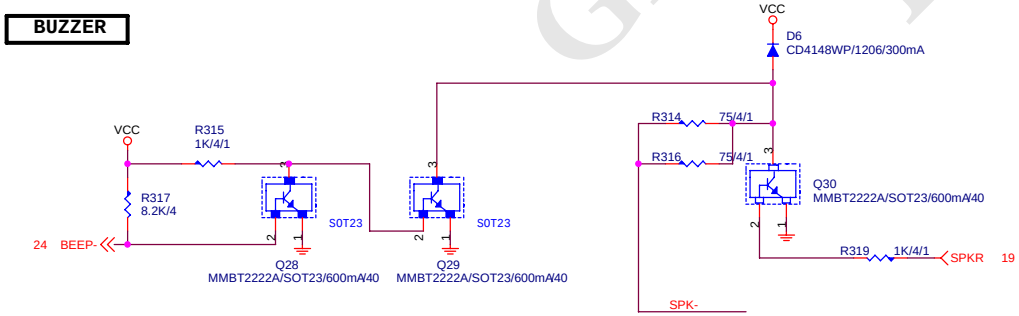
SYS_FAN2



PWR FAN



BUZZER



Gigabyte Technology

Title: FRONT PANEL / FAN CTRL

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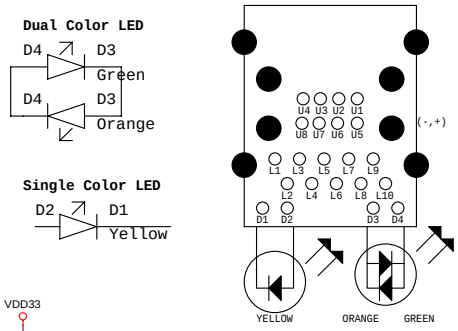
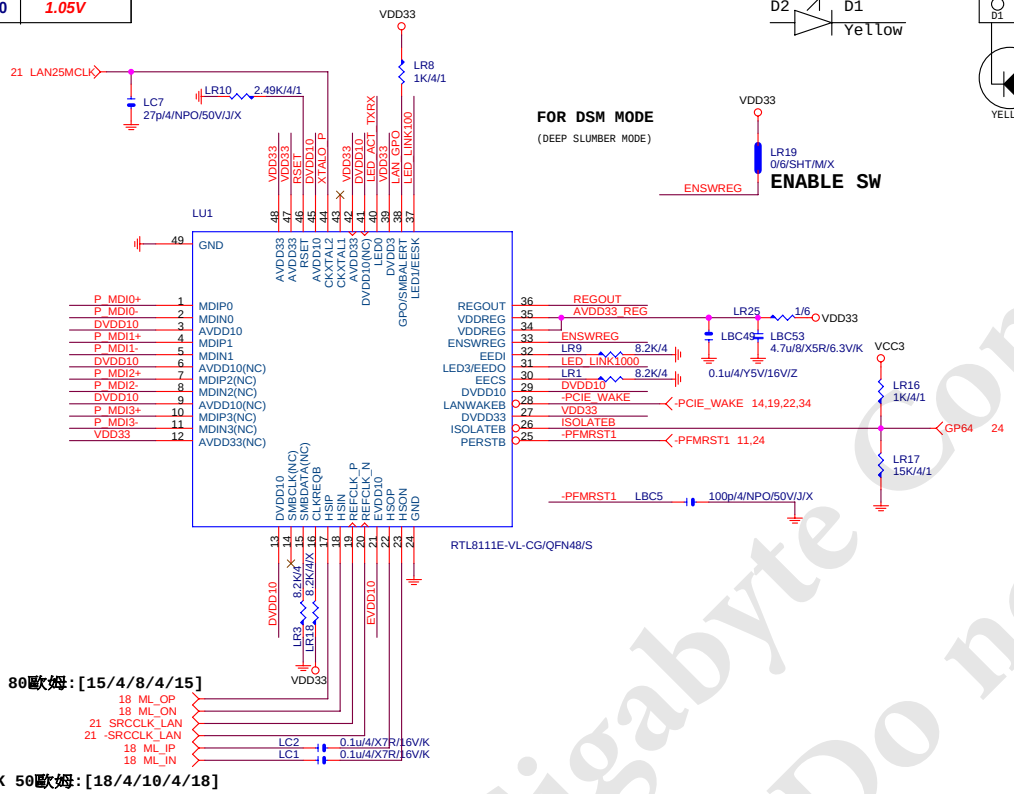
PCIE-1G LAN

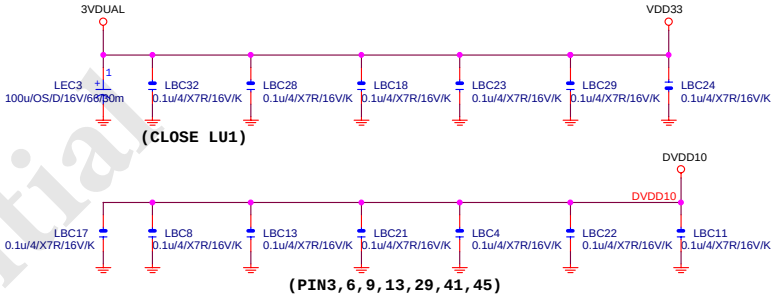
Power domain chart

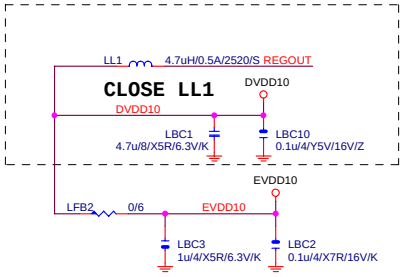
	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V





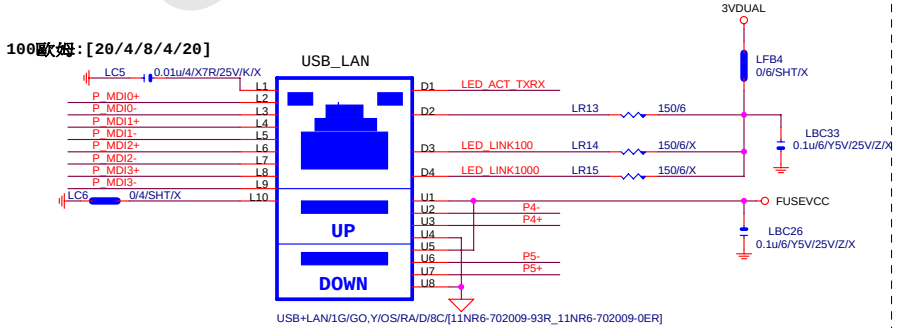
FOR DSM MODE
(DEEP SLUMBER MODE)





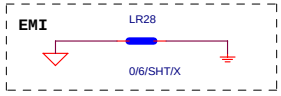
USB_LAN CONNECTOR

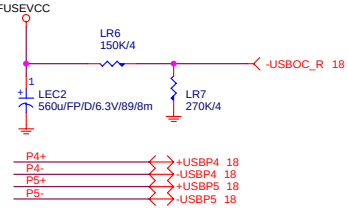
RTL8101E :L1+L10-->AVDD18+0.1U(BIOS DISABLE MDI-X FUNCTION)
1G :USB+LAN/1G/GO,Y/OS/RA/D/1
100M:USB+LAN/100/GO,Y/OS/RA/D/1



USB+LAN/1G/GO,Y/OS/RA/D/8C/[11NR6-702009-93R_11NR6-702009-0ER]

USB_LAN





Gigabyte Technology

Title: REALTEK RTL8111E

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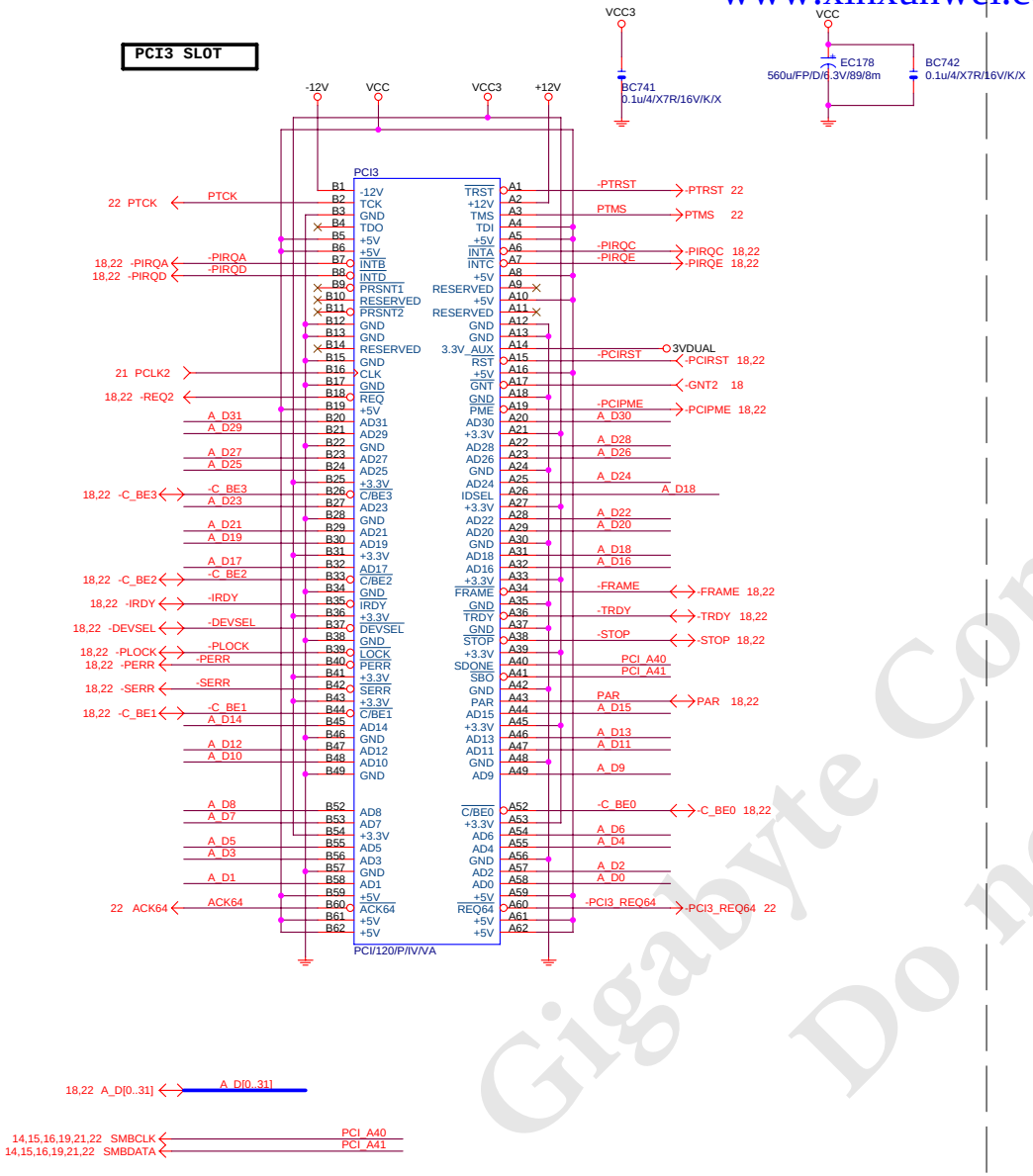
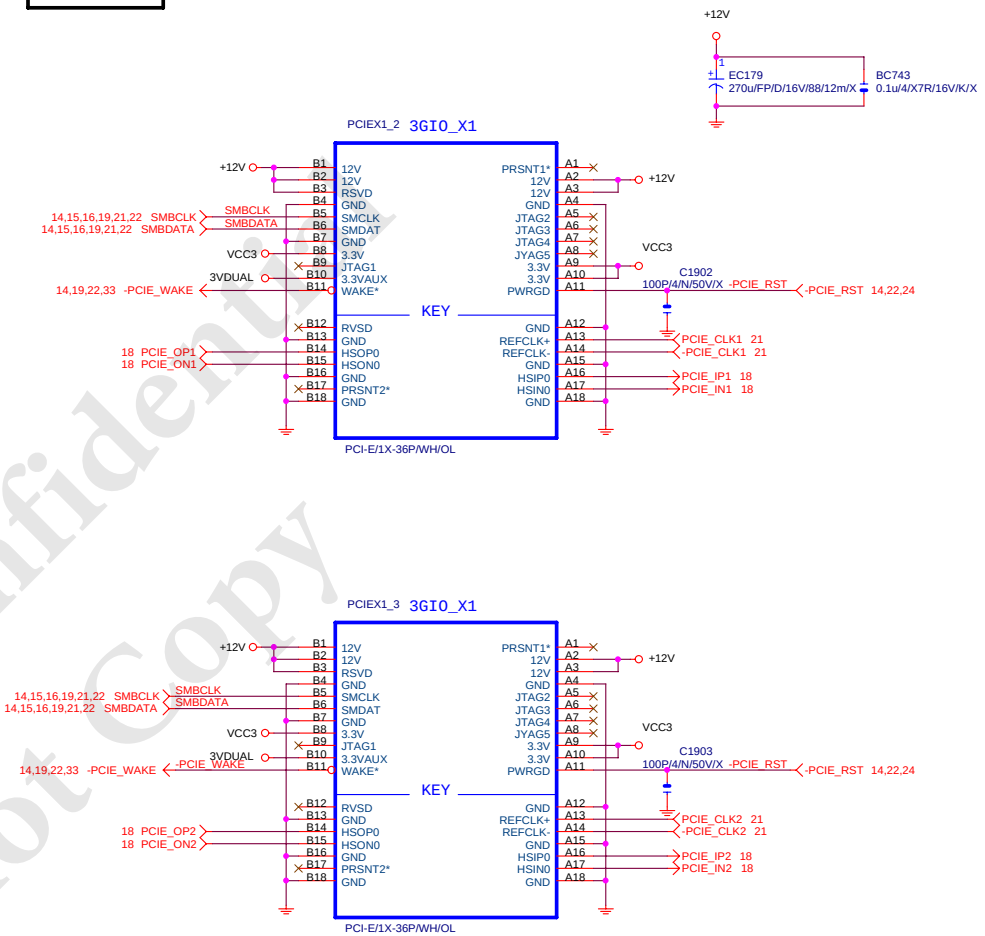
PCI3 SLOT



PCIE_2/3



Intel Confidential		
Title	PCI SLOT 3,PCIE2,3	
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