

Project Name :

Platform : Bay trail(For Valleyview-D/I/M System On Chip)

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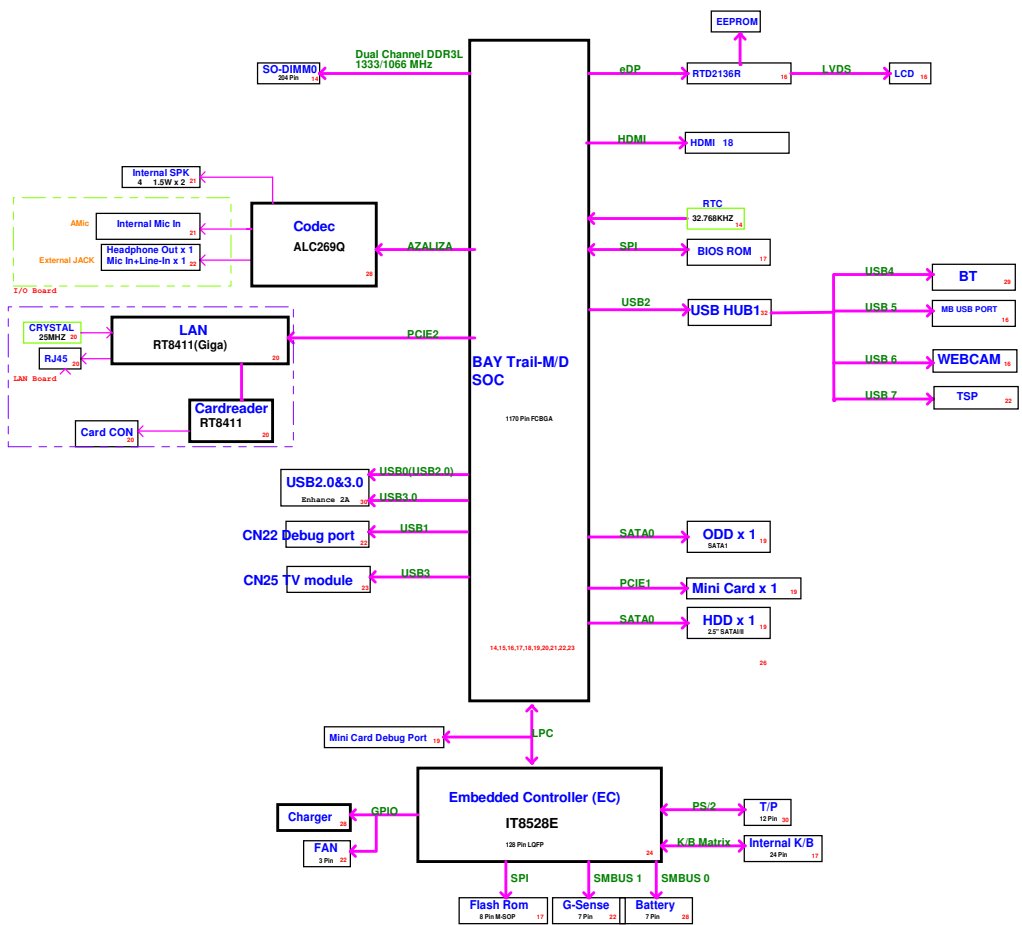
M/B Schematic Version Change List

Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note
2013-12-13	A				

Daughter Board Schematic Version Change List

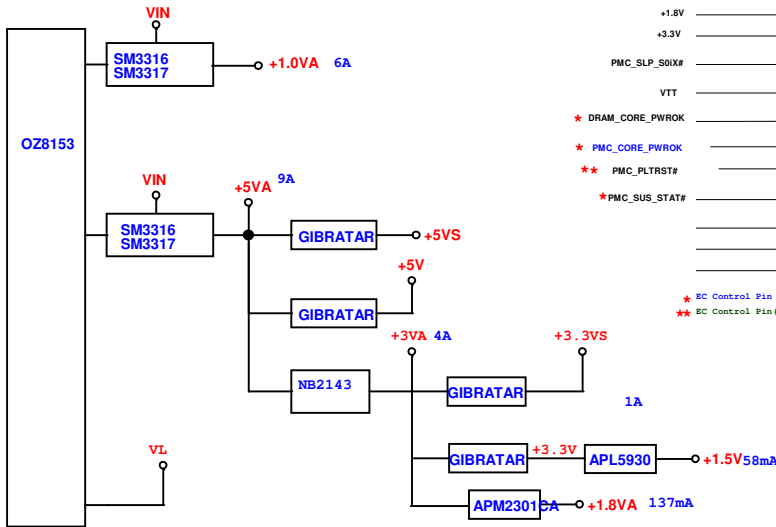
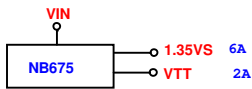
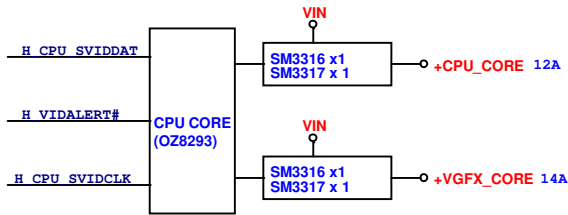
Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note

SYSTEM BLOCK DIAGRAM

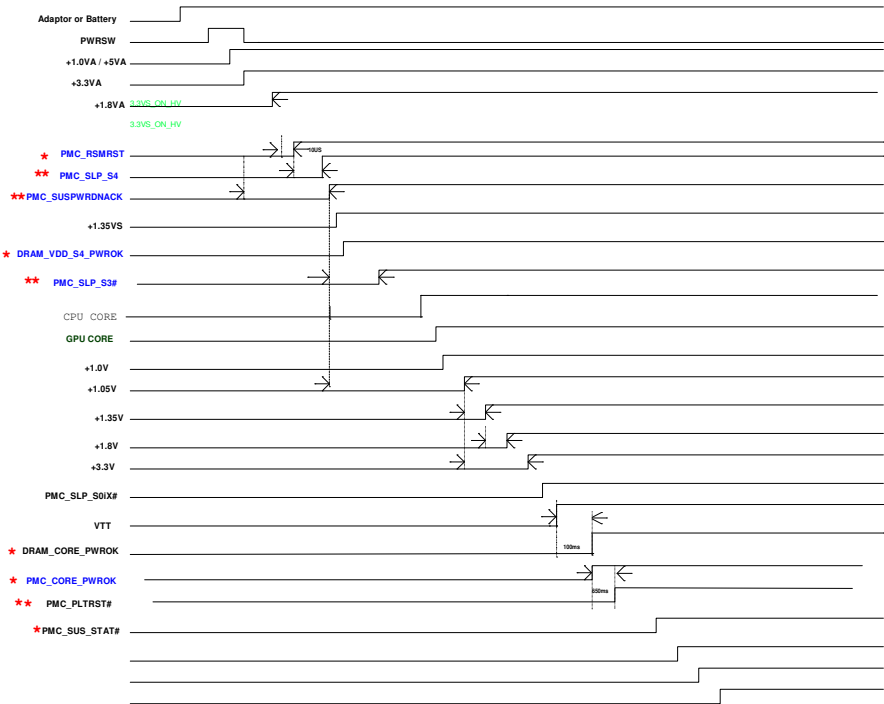


USB0	USB3.0/2.0 ENHANCE
USB1	CN22 Debug port
USB2	USB HUB1
USB3	CN25 TV module
USB4	BT
USB5	MB USB PORT
USB6	Webcam
USB7	TSP

POWER BLOCK DIAGRAM



System Poewr On Sequence



* EC Control Pin (O/P)
** EC Control Pin (I/P)

SOC	
GPI00	PM_RM_BUSY#
GPI01	EC_EXTSMI#
GPI02	INT_PIRQ#
GPI03	INT_PIRQF#
GPI04	INT_PIRQG#
GPI05	INT_PIRQH#
GPI06	BIOS_REC
GPI07	N.C (TACH3)
GPI08	N.C
GPI09	N.C (WOL_EN)
GPI010	N.C (ALERT#)
GPI011	SMB_ALERT#
GPI012	LAN_PHYPC
GPI013	N.C (GLAN_DOCK#)
GPI014	N.C (HDETDETECT)
GPI015	PM_STPPCI#
GPI017	N.C (TACH0)
GPI018	N.C
GPI019	SATA1GP
GPI021	SATA0GP
GPI022	N.C (SCLOCK)
GPI023	LDRQ1#
GPI024	CRB_SV_DET
GPI025	PM_STPCP0#
GPI026	PM_SLP_S4_STATE#
GPI027	QRT_STATE0
GPI028	QRT_STATE1
GPI029	USB_OC#5
GPI030	USB_OC#6
GPI031	USB_OC#7
GPI032	PM_CLKRUN#
GPI033	HDA_DOCK_EN
GPI034	N.C (HDA_DOCK_RST)
GPI035	CLK_SATA_GE#
GPI036	SATA2GP
GPI037	SATA3GP
GPI038	ODD_DET
GPI039	ICH_GPI039
GPI040	USB_OC#1
GPI041	USB_OC#2
GPI042	USB_OC#3
GPI043	USB_OC#4
GPI048	MFQ_MODE
GPI049	R_PWRGD
GPI050	PCL_REQ#1
GPI051	PCL_GNT#1
GPI052	PCL_REQ#2
GPI053	PCL_GNT#2
GPI054	PCL_REQ#3
GPI055	PCL_GNT#3

ITE8528		Default
GPA0	PID_3_RF_LED_ON#	UP / GPI
GPA1	BATT_VA_OFF#	UP / GPI
GPA2	BT1_BEEP	UP / GPI
GPA3	WLAN_PWR#	UP / GPI
GPA4	+1.05V_ON	UP / GPI
GPA5	SENBAT_V	UP / GPI
GPA6	PM_RSMRST#	UP / GPI
GPA7	EC_BL_PWM	UP / GPI
GPB0	PM_SLP_S4#	UP / GPI
GPB1	PM_SLP_S3#	UP / GPI
GPB2	3G_PWR#	Dn / GPI
GPB3	SMBCLK	/ GPI
GPB4	SMB_ID	/ GPI
GPB5	H_AZOGATE	UP / Func01
GPB6	H_RCIN#	Dn / GPI
GPB7	SAFTY_PROTECT	Dn / GPI
GPC0	+1.5V_ON	Dn / GPI
GPC1	SMB_CLK_EC	/ GPI
GPC2	SMB_DAT_EC	/ GPI
GPC3	PID_0_CHG_B_LED	Dn / GPI
GPC4	PWRBTN#	Dn / GPI
GPC5	PANEL_DETECT_2	Dn / GPI
GPC6	VCCSA_ON	Dn / GPI
GPC7	+1.5VS_ON	UP / GPI
GPC00	ADAP_IN	UP / GPI
GPC1	PWRBTN#	UP / GPI
GPC2	PLT_RST#	UP / Func1
GPC3	PM_SUS_STAT#	UP / GPI
GPC4	EC_EXTSMI#	UP / GPI
GPC5	Fastcharge_EN	UP / GPI
GPC6	+5V_ON	Dn / GPI
GPC7	SET_V	Dn / GPI
GPC0	LID#	Dn / GPI
GPE1	PWR_USB_LED	Dn / GPI
GPE2	ALL_SYS_PGD	Dn / GPI
GPE3	Vcore_ON	UP / GPI
GPE4	PWRSW	UP / GPI
GPE5	LVDS_VIN	Dn / GPI
GPE6	WLAN_ON	Dn / GPI
GPE7	AMP_MUTE#	UP / GPI
GPFO	PCH_BL_EN	UP / GPI
GPFI	+1.8V_ON	UP / GPI
GPFI	BT_ON	UP / GPI
GPFI	N.C	UP / GPI
GPFI	TP_CLK	UP / GPI
GPFI	TP_DATA	UP / GPI
GPFI	EC PECL	UP / GPI
GPFI	CHG_HI_VOLT#	UP / GPI
GPFO	PWRBTN#	Dn/GPO/TH
GPFI	+3.3VS_ON	Dn/GPO/ID7
GPFI	EC PORST	
GPFI	WEBCAN_ON	Dn / GPI
GPFI	PM_CLKRUN#	Dn/GPI/ID0
GPFI	PID_1_CHG_R_LED	Dn/GPI/ID1
GPFI	PID_2_PWR_LED	Dn/GPI/ID2
GPFI	EC_HSCS0#	Dn/GPI/ID3
GPFI	EC_HSC	Dn/GPI/ID4
GPFI	EC_HMISO	Dn/GPI/ID5
GPFI	EC_HMOSI	Dn/GPI/ID6

ITE8528		Default
GPI0	CRT_DETECT	/GPI/ADC
GPI1	PANEL_DETECT	/GPI/ADC
GPI2	PLATFORM_ID	/GPI/ADC
GPI3	CPPE#	/GPI/ADC
GPI4	BAT_I	/GPI/ADC
GPI5	BATT_TEMP	/GPI/ADC
GPI6	ADAPTOR_I	/GPI/ADC
GPI7	BAT_V	/GPI/ADC
GPJ0	EC_BL_ON	/GPI/DAC
GPJ1	EC_PROCHOT	/GPI/DAC
GPJ2	FAN_CTRL0	/GPI/DAC
GPJ3	CHG_REF	/GPI/DAC
GPJ4	CHG_I	/GPI/DAC
GPJ5	PWR_USB#	/GPI/DAC

BAY TRAIL CPU				
	CPU CORE(V)	ICC(A)	W	TEMP ()
Bay trail D-J1750	1.05	12	10	
Bay trail H-R2805	1.05	12	4.5	

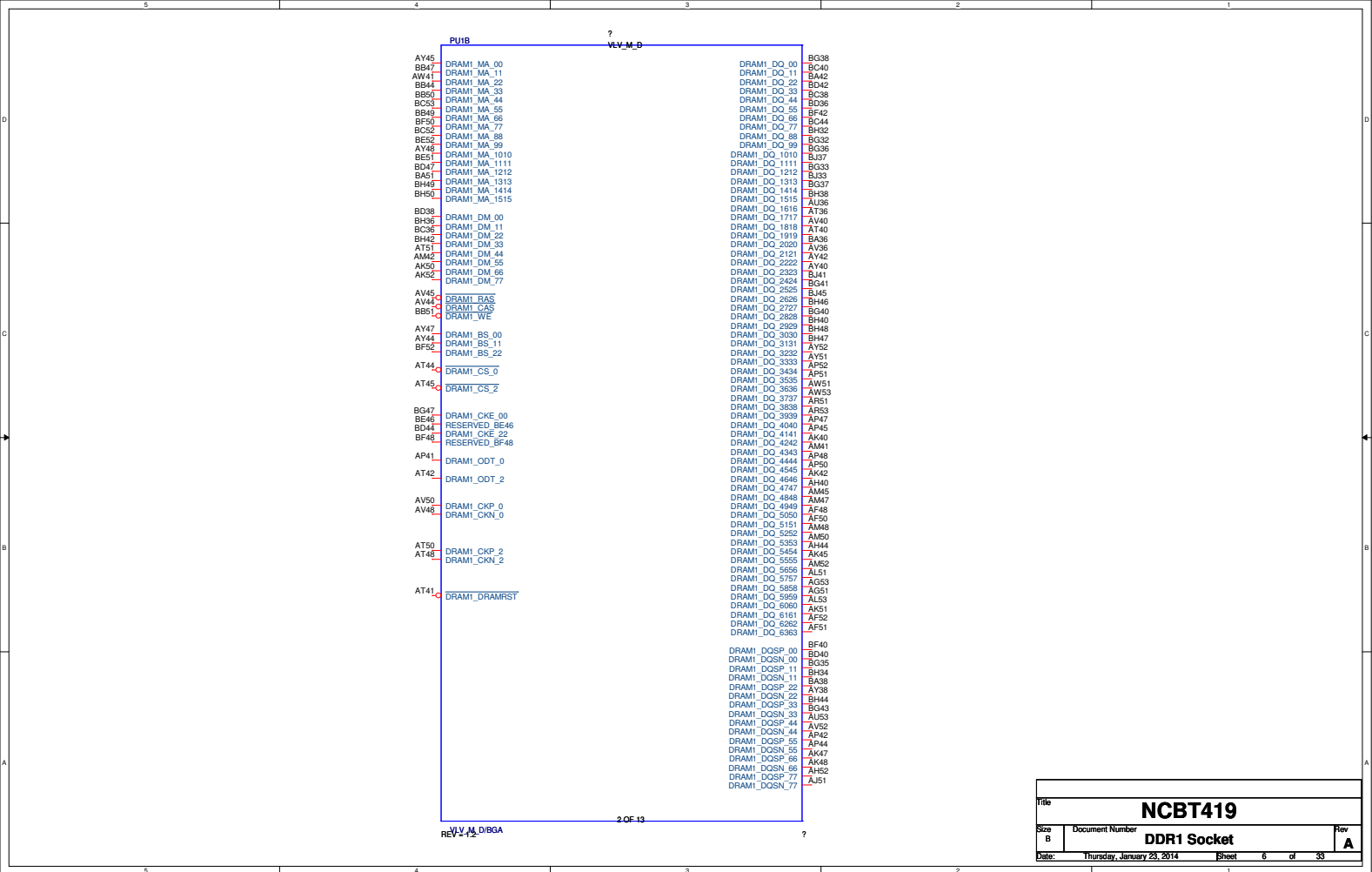
SOC				
VCC	ICC (mA)	W	TEMP ()	
+1.0VA	202	0.2	105	
+1.05V	720	0.756		
+1.0V	5487	5.487		
+1.2A	35	0.04		
+1.35VS	10000	1.43	TEMP ()	
+1.8VA	53	0.1		
+1.8V	84	0.15		
+1.5V_1.8V	58	0.09		
+3.3VA	10	0.033		
+3.3V	13	0.04		
CPU CORE1.1	12000	16.5		
GPU CORE1.0	14000	10		
RTCS.3	0.0016			

ITE8528			
VCC	ICC (mA)	mW	TEMP ()
+3.3V	100	330	70

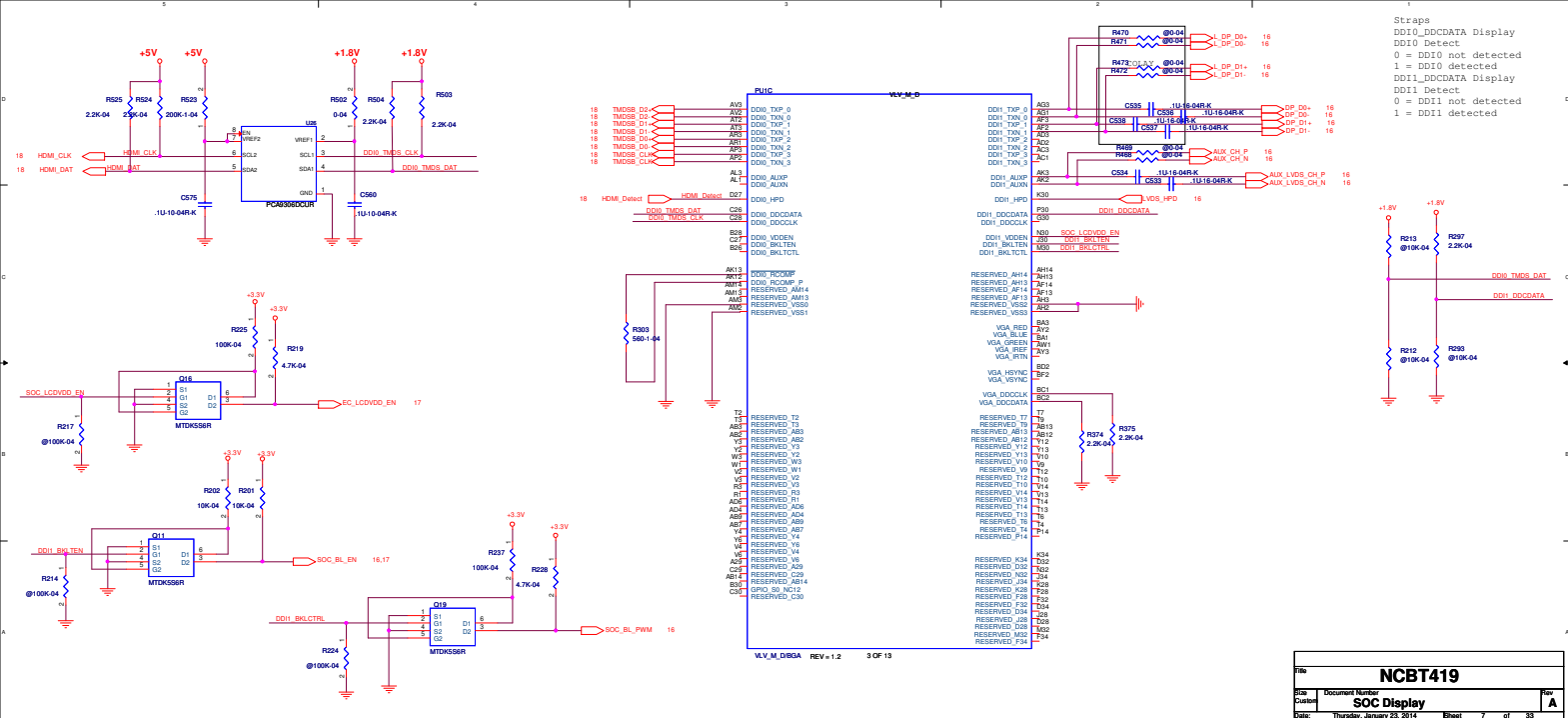
ALC269			
VCC	ICC (mA)	mW	TEMP ()
+3.3V(DVDD)	200	660	70
+5V(AVDD)	1000	5000	

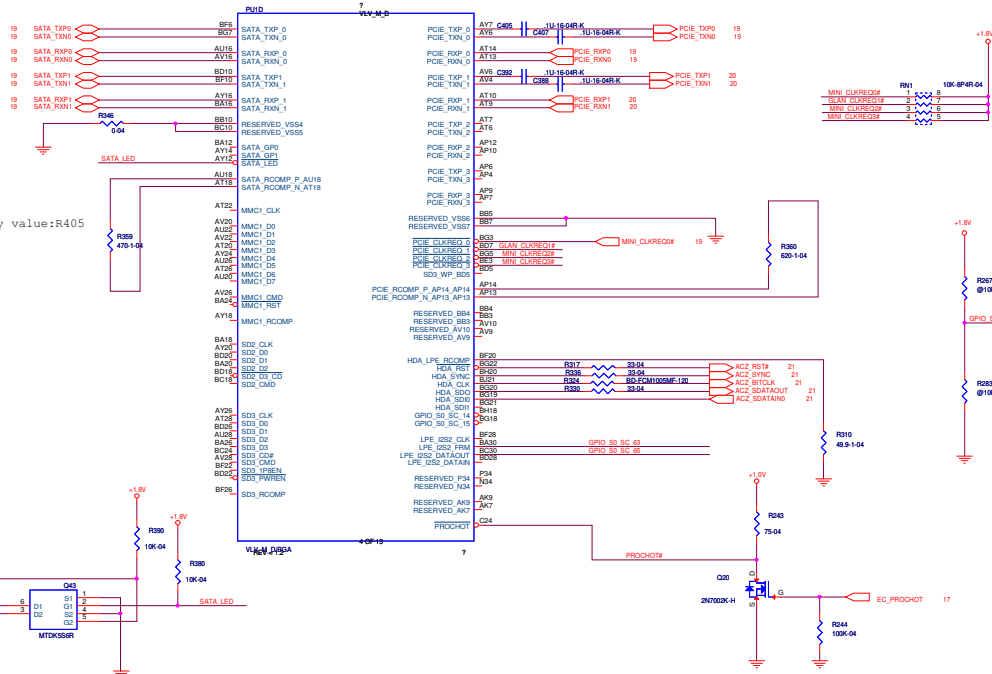
ADM1032			
VCC	ICC	mW	TEMP ()
+3.3V	170uA	0.56	150

RT8411			
VCC	ICC (mA)	mW	TEMP ()
+3.3VS	300	990	70
+1.2VS	150	180	



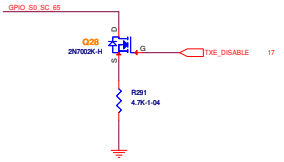
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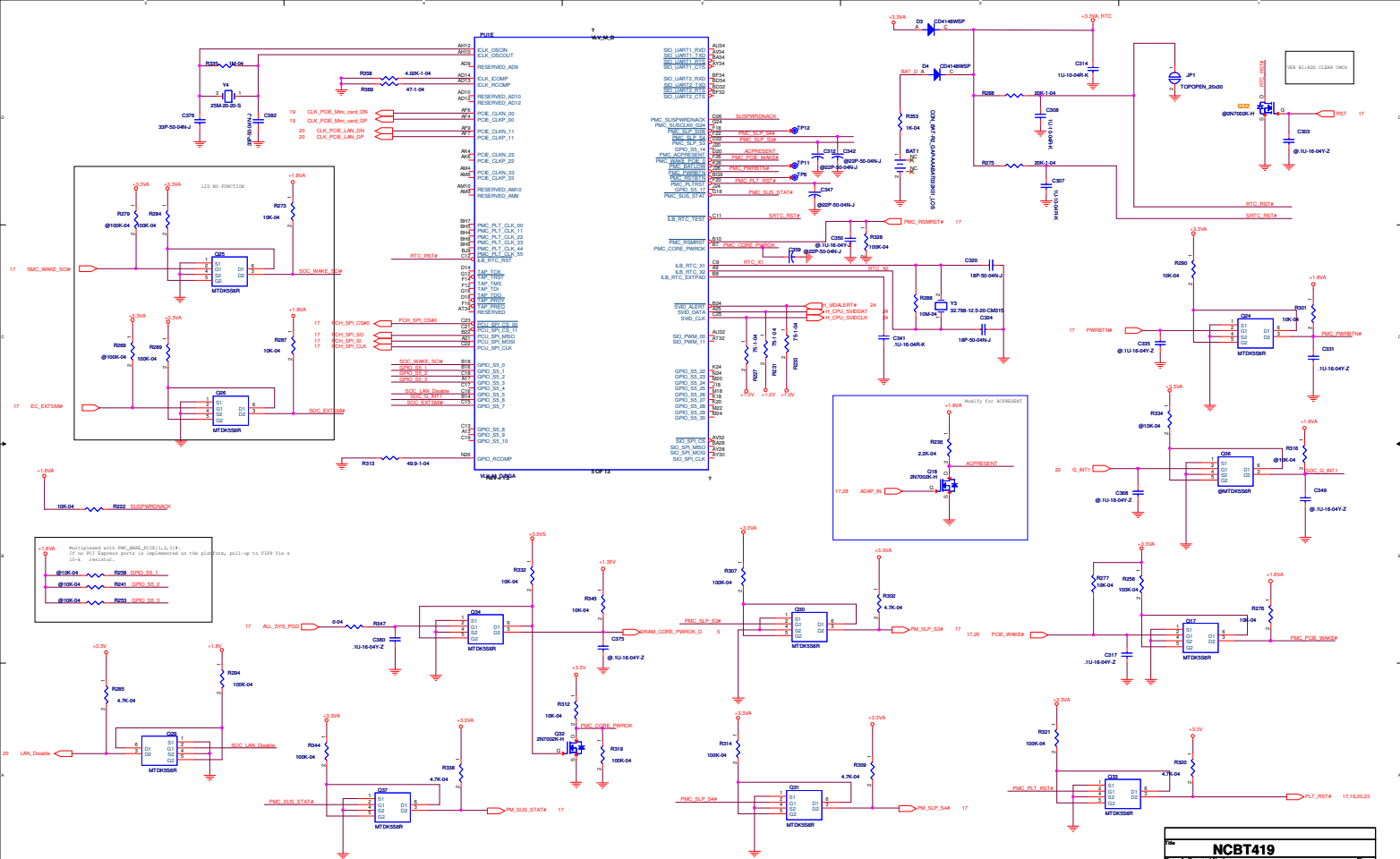




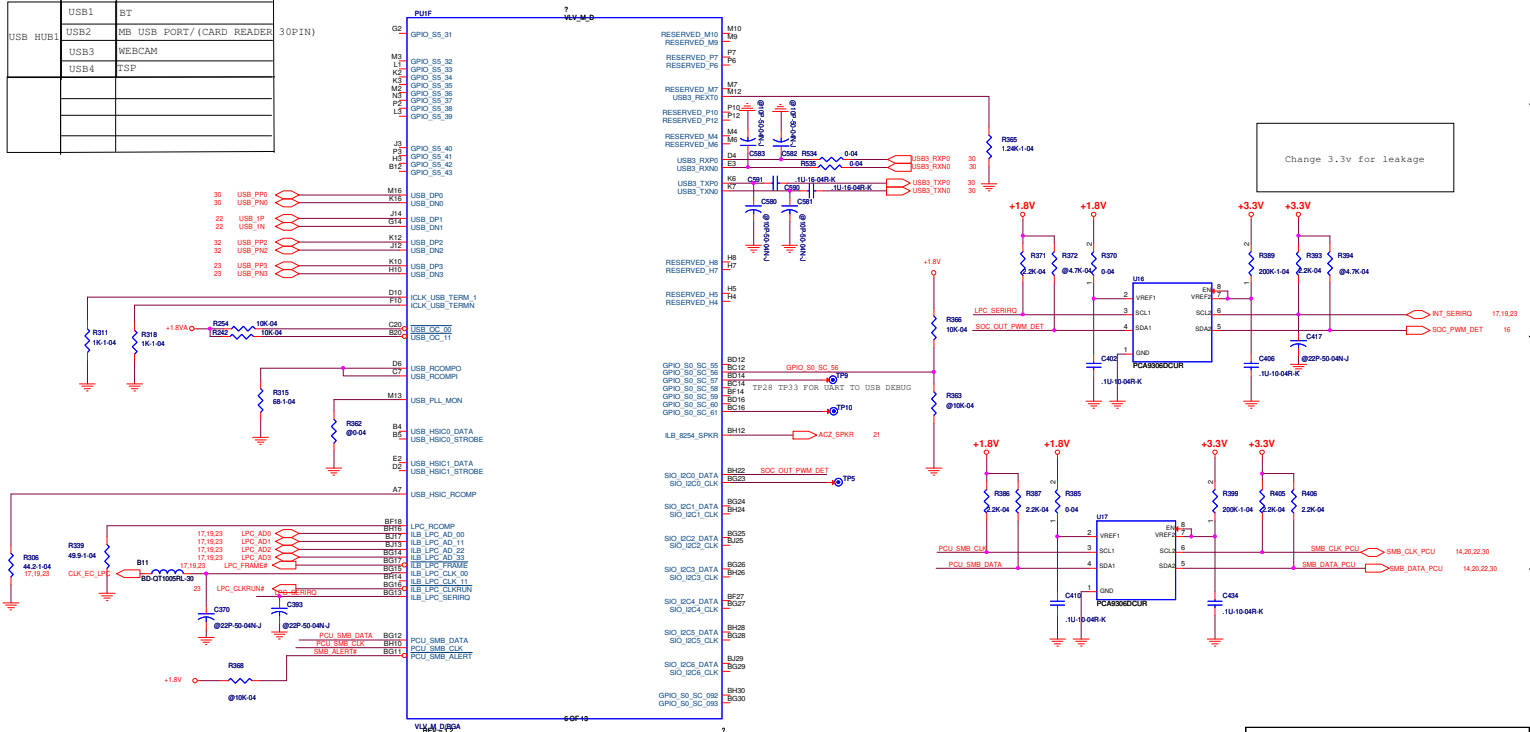
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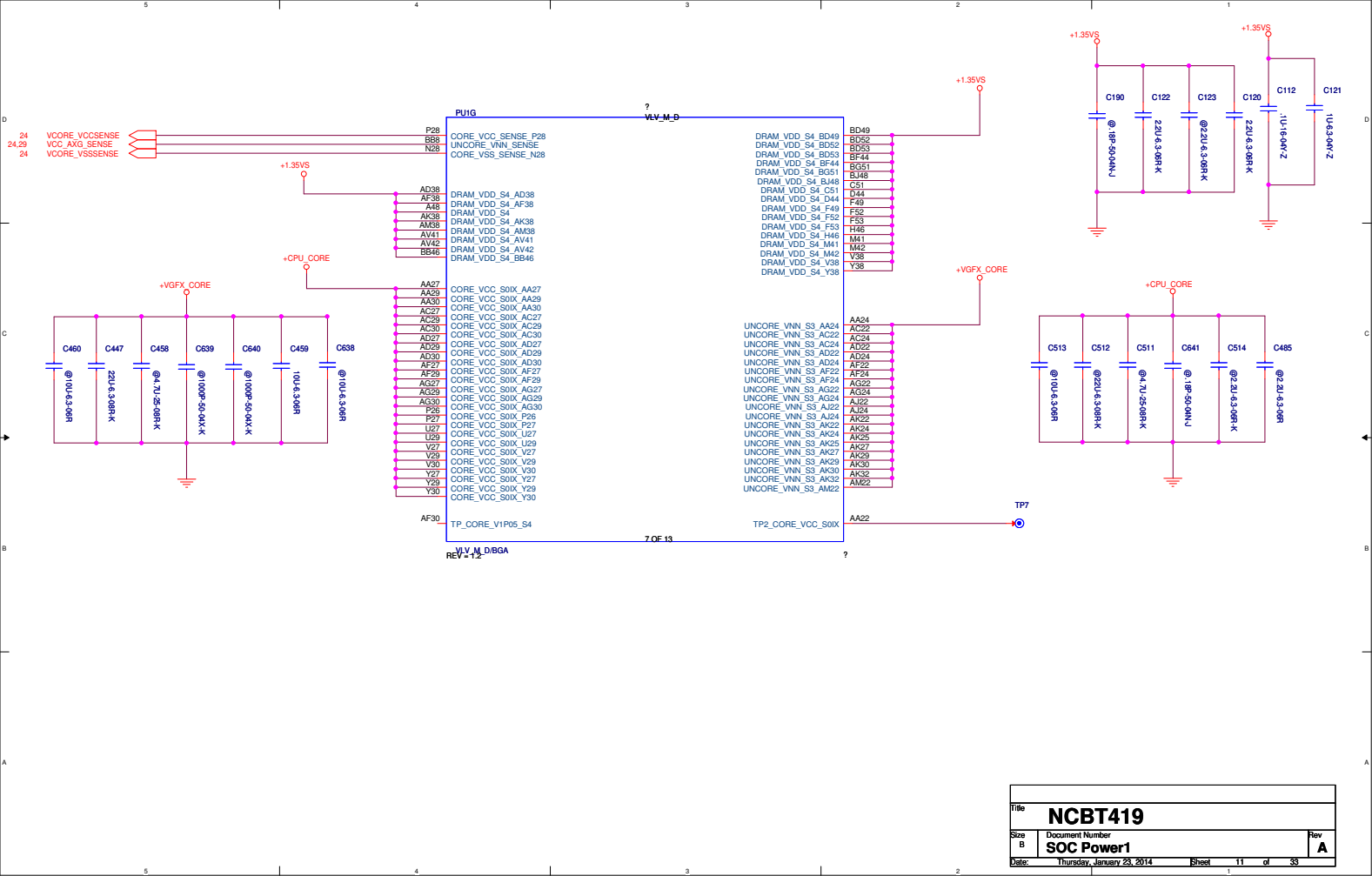
Straps
 GPIO_S0_SC[63]
 BIOS Boot Selection
 0 = LPC
 1 = SPI
 GPIO_S0_SC[65]
 Security Flash Descriptors
 0 = Override
 1 = Normal Operation

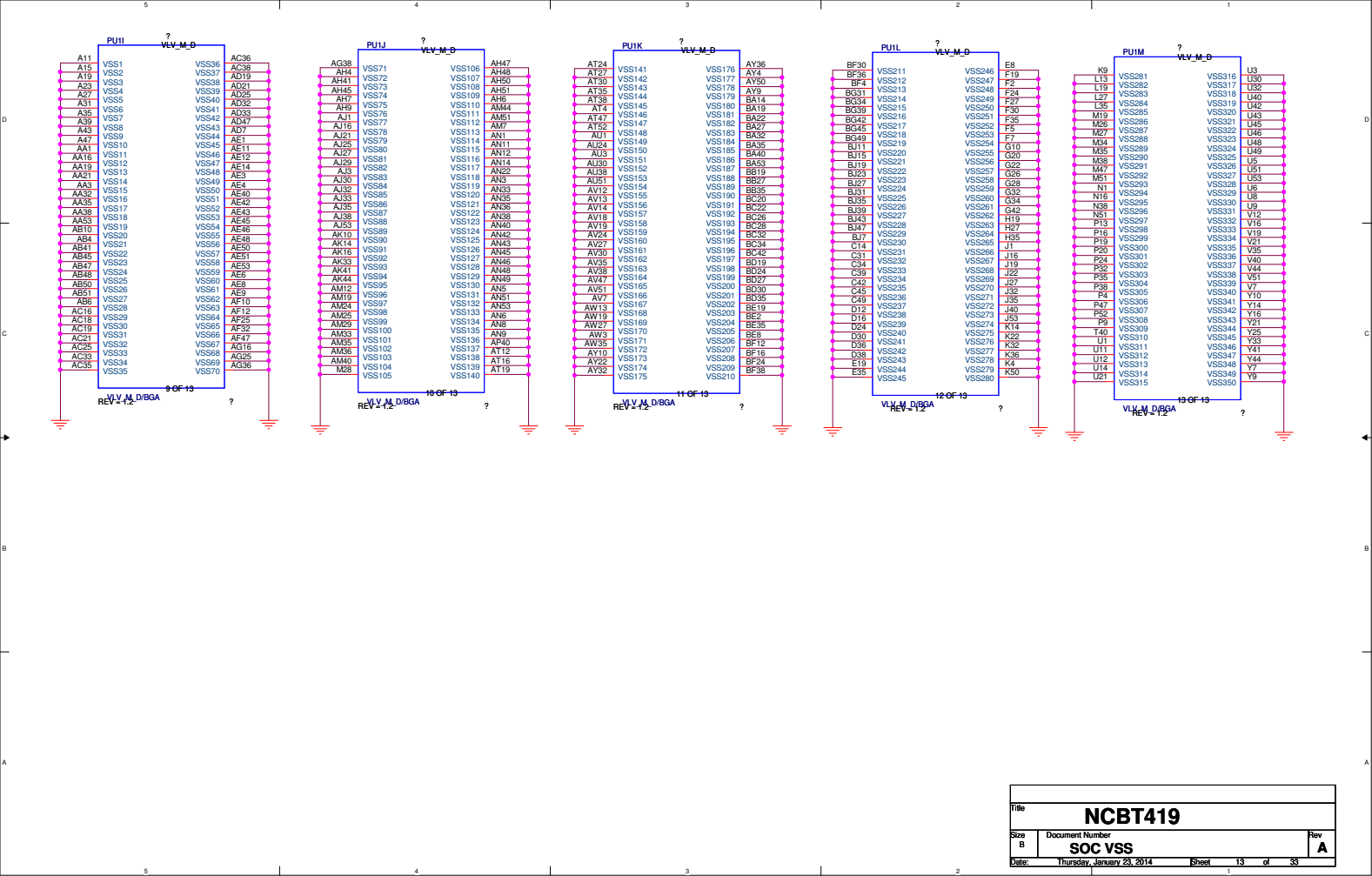




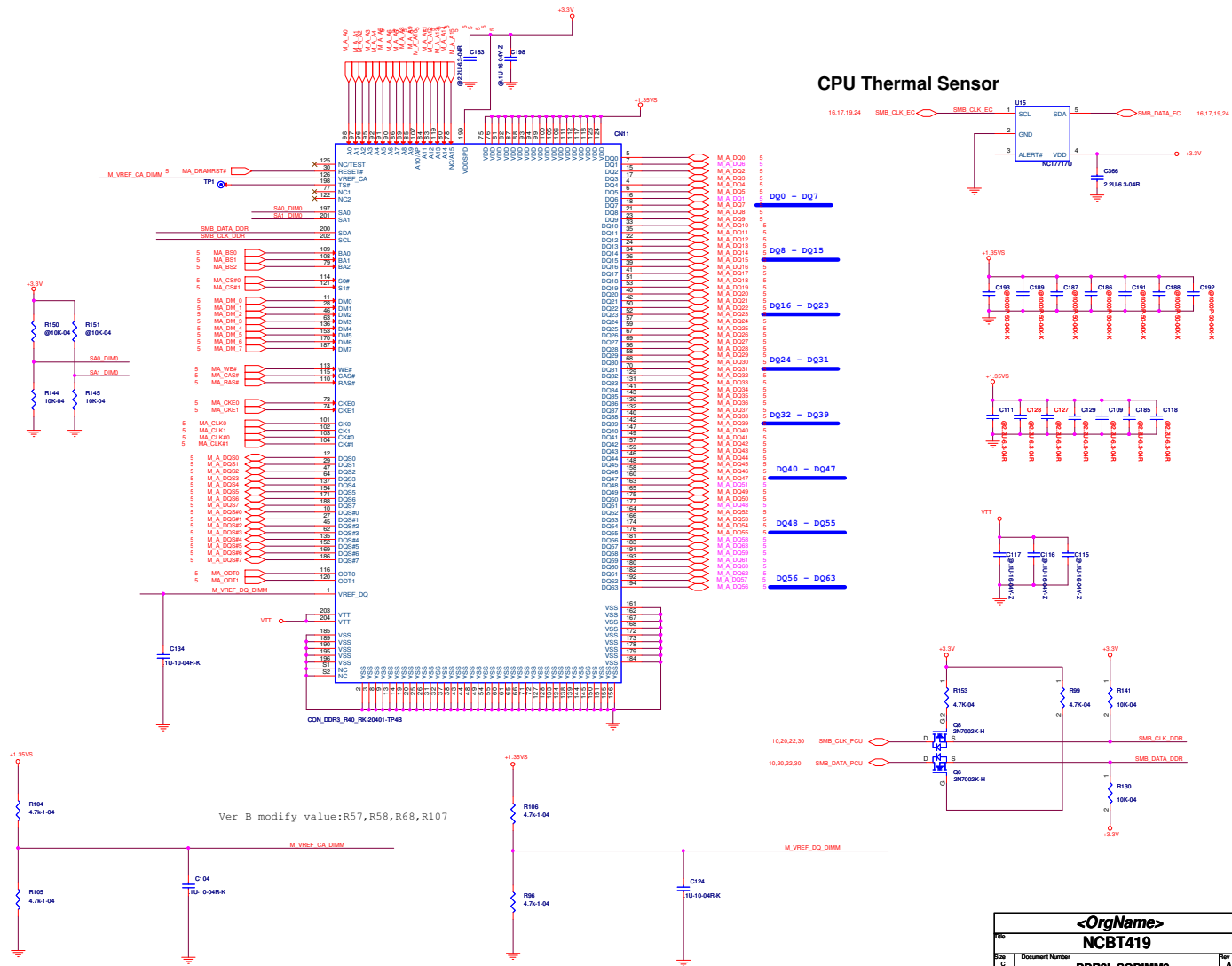
USB HUB1	USB1	BT	30PIN)
	USB2	MB USB PORT/ (CARD READER	
	USB3	WEBCAM	
	USB4	TSP	

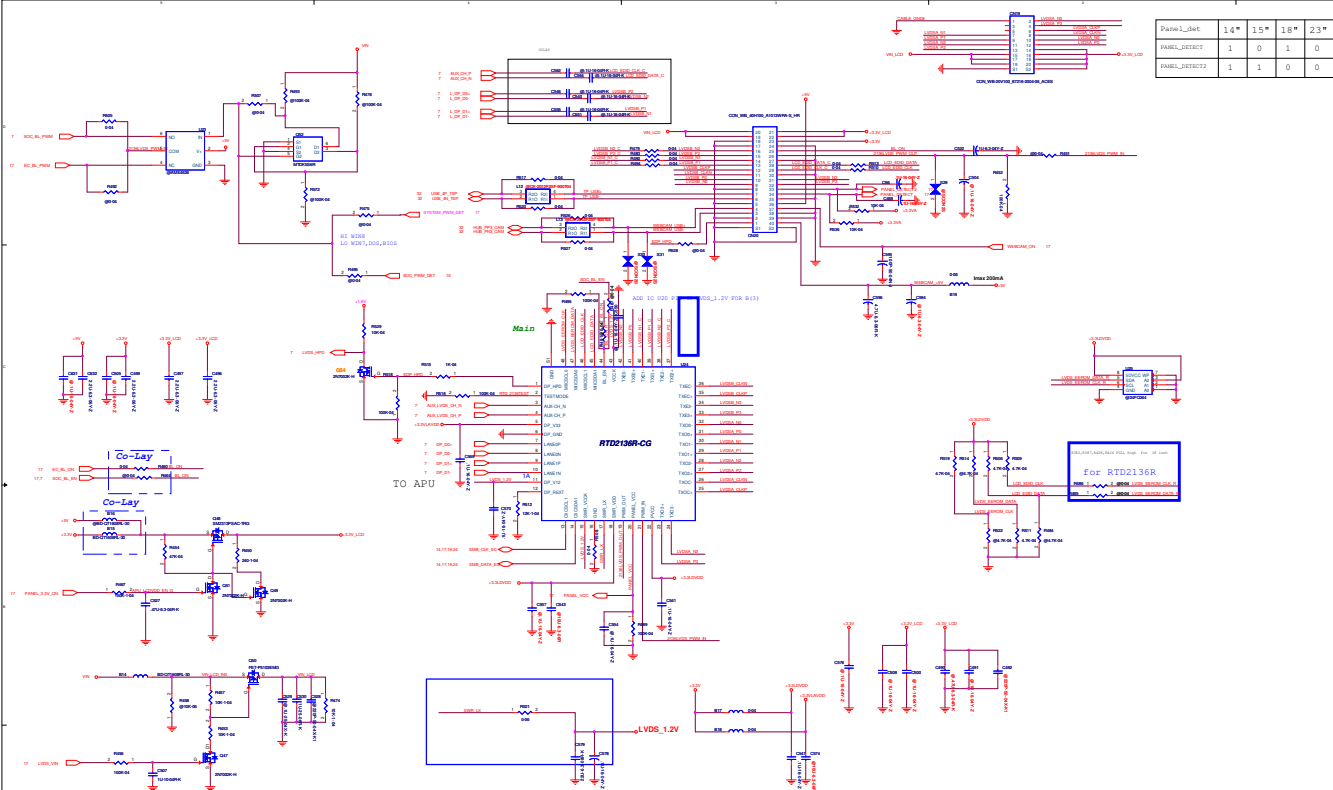






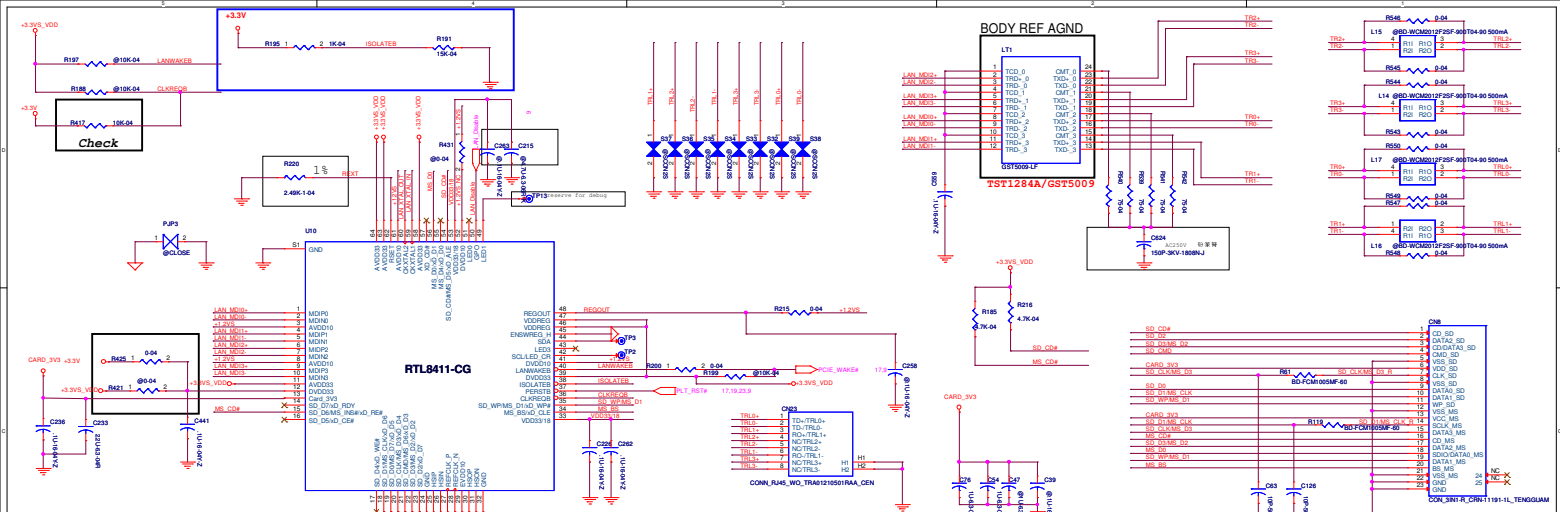
CPU Thermal Sensor



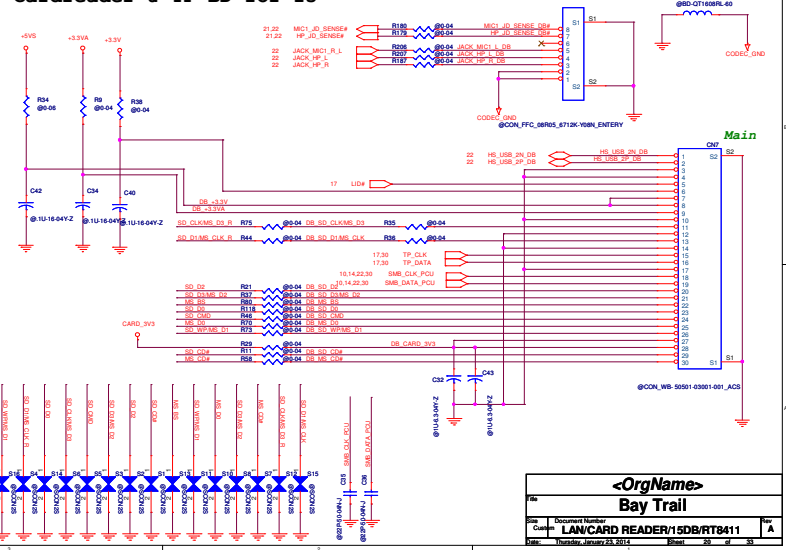


[illegible]

The schematic diagram illustrates the Zynq-7010 SoC architecture. It features two main processing blocks: the **Main** core (labeled 'Main' in green) and the **QRT** core (labeled 'QRT' in blue). The Main core is connected to the QRT core via a **QRT CONN.** interconnect. The Main core is also connected to a **PS7** block, which includes a **PS7_HBM** memory controller and a **PS7_CLK** clock signal. The QRT core is connected to a **QRT_HBM** memory controller and a **QRT_CLK** clock signal. The diagram shows various input/output signals, including **HOMOUT_D0**, **HOMOUT_D1**, **HOMOUT_D2**, **HOMOUT_D3**, **HOMOUT_D4**, **HOMOUT_D5**, **HOMOUT_D6**, **HOMOUT_D7**, **HOMOUT_D8**, **HOMOUT_D9**, **HOMOUT_D10**, **HOMOUT_D11**, **HOMOUT_D12**, **HOMOUT_D13**, **HOMOUT_D14**, **HOMOUT_D15**, **HOMOUT_D16**, **HOMOUT_D17**, **HOMOUT_D18**, **HOMOUT_D19**, **HOMOUT_D20**, **HOMOUT_D21**, **HOMOUT_D22**, **HOMOUT_D23**, **HOMOUT_D24**, **HOMOUT_D25**, **HOMOUT_D26**, **HOMOUT_D27**, **HOMOUT_D28**, **HOMOUT_D29**, **HOMOUT_D30**, **HOMOUT_D31**, **HOMOUT_D32**, **HOMOUT_D33**, **HOMOUT_D34**, **HOMOUT_D35**, **HOMOUT_D36**, **HOMOUT_D37**, **HOMOUT_D38**, **HOMOUT_D39**, **HOMOUT_D40**, **HOMOUT_D41**, **HOMOUT_D42**, **HOMOUT_D43**, **HOMOUT_D44**, **HOMOUT_D45**, **HOMOUT_D46**, **HOMOUT_D47**, **HOMOUT_D48**, **HOMOUT_D49**, **HOMOUT_D50**, **HOMOUT_D51**, **HOMOUT_D52**, **HOMOUT_D53**, **HOMOUT_D54**, **HOMOUT_D55**, **HOMOUT_D56**, **HOMOUT_D57**, **HOMOUT_D58**, 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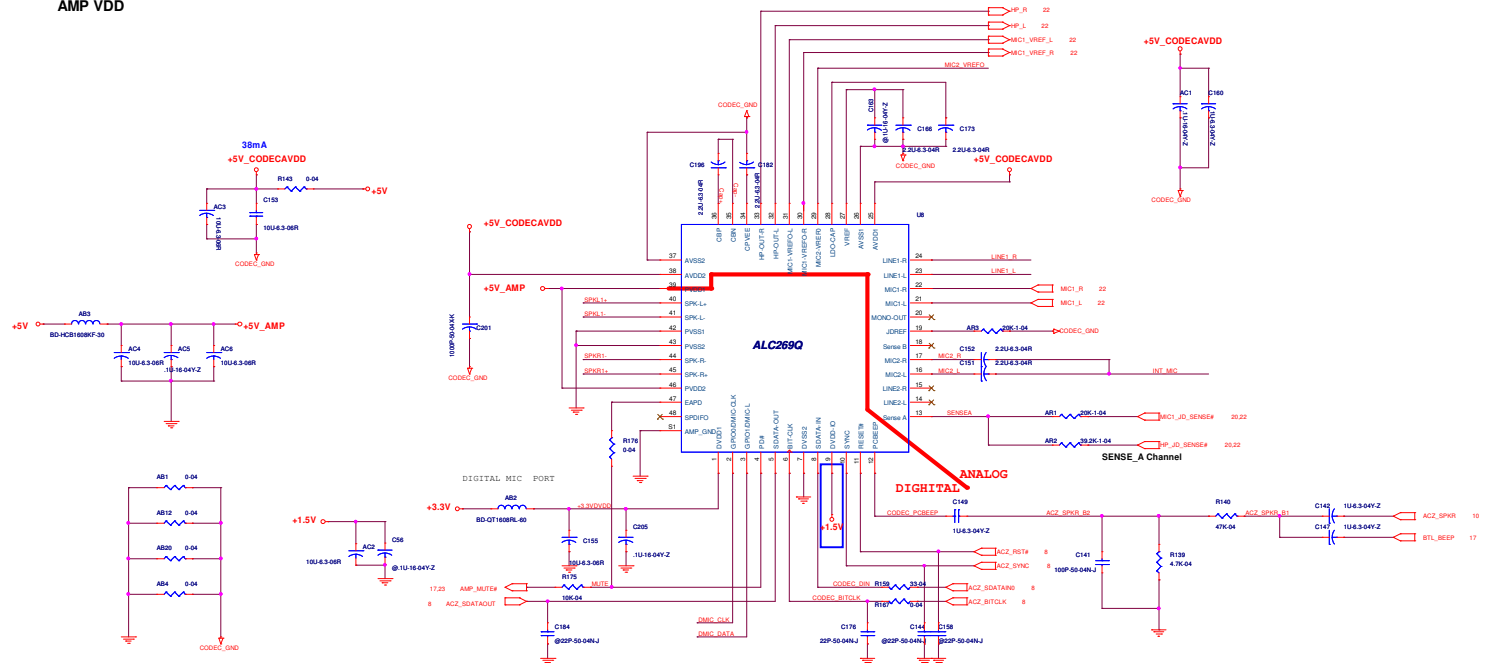


Cardreader & TP BD for 15''

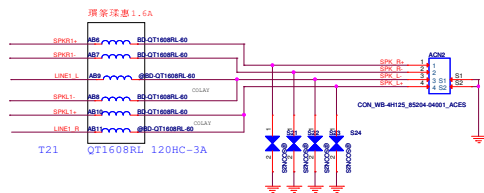
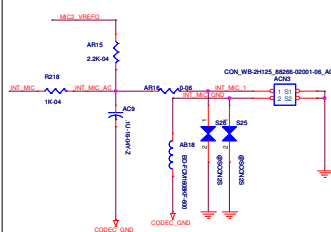


CODEC ALC269Q

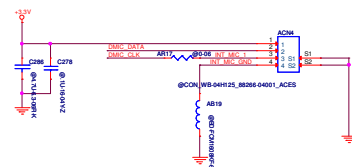
AMP VDD



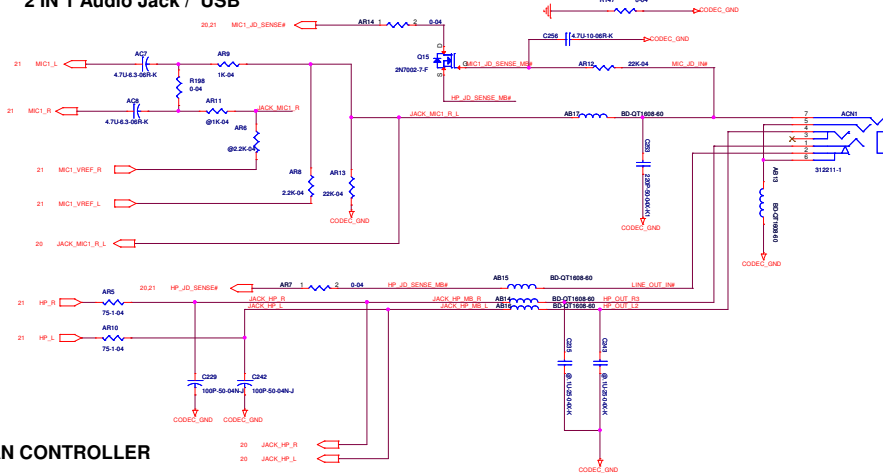
INT SPEAKER

**INT MIC**

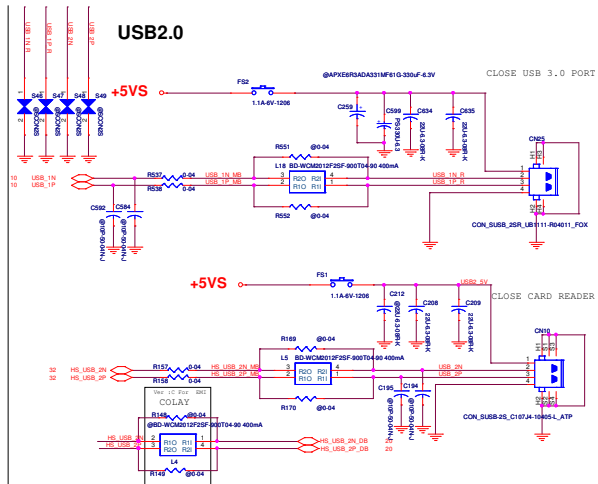
Digital Mic



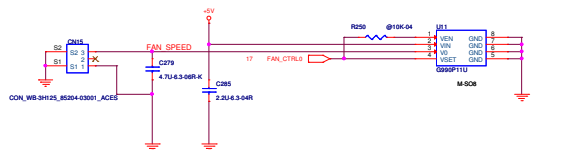
2 IN 1 Audio Jack / USB



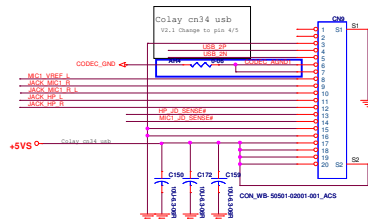
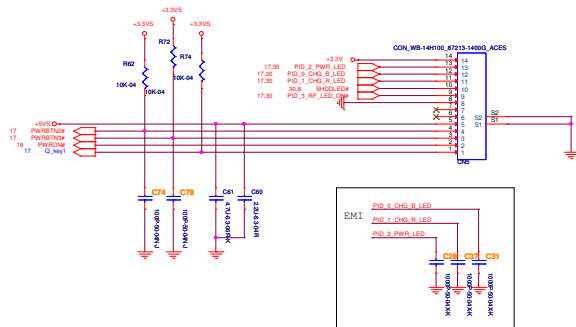
USB2.0



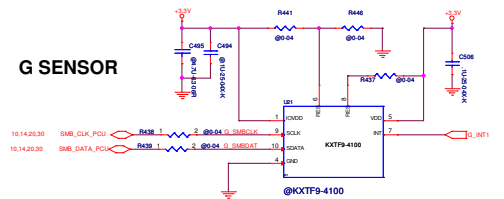
FAN CONTROLLER



EXT USB PORT 4

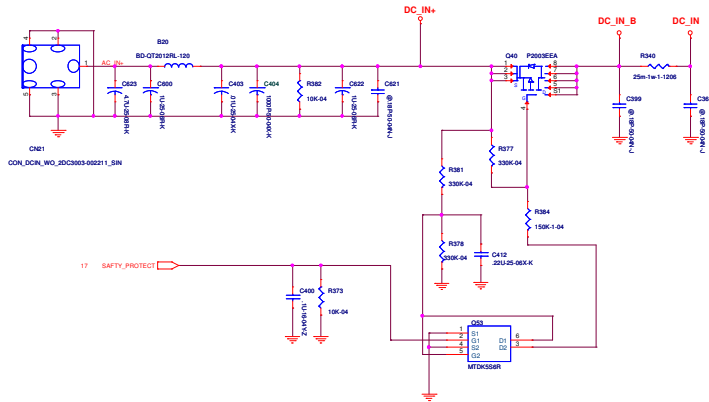


G SENSOR

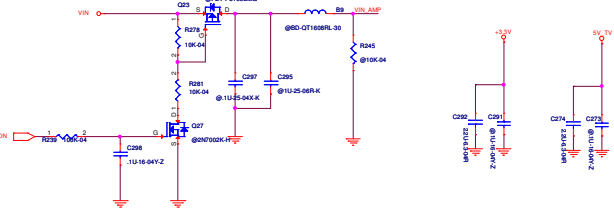
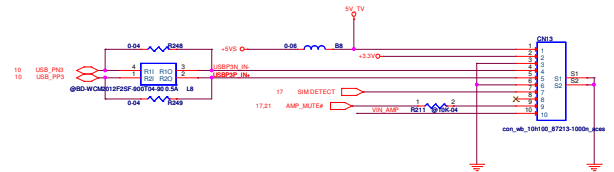
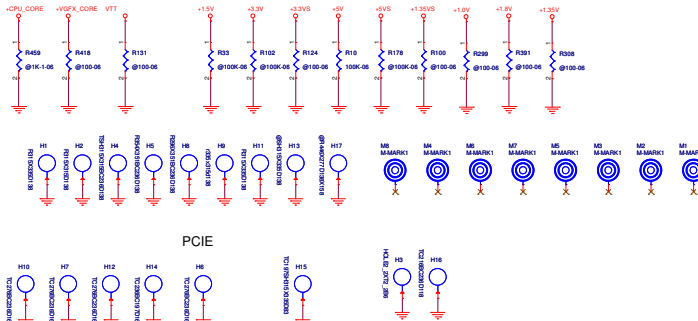
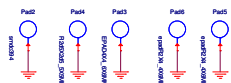


DC IN

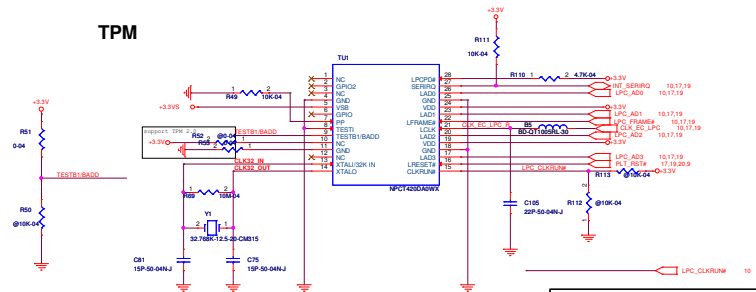
PROJECT	TN440U		
Adaptor	40W		
Resense	25m Ohm		
Stop Charger			



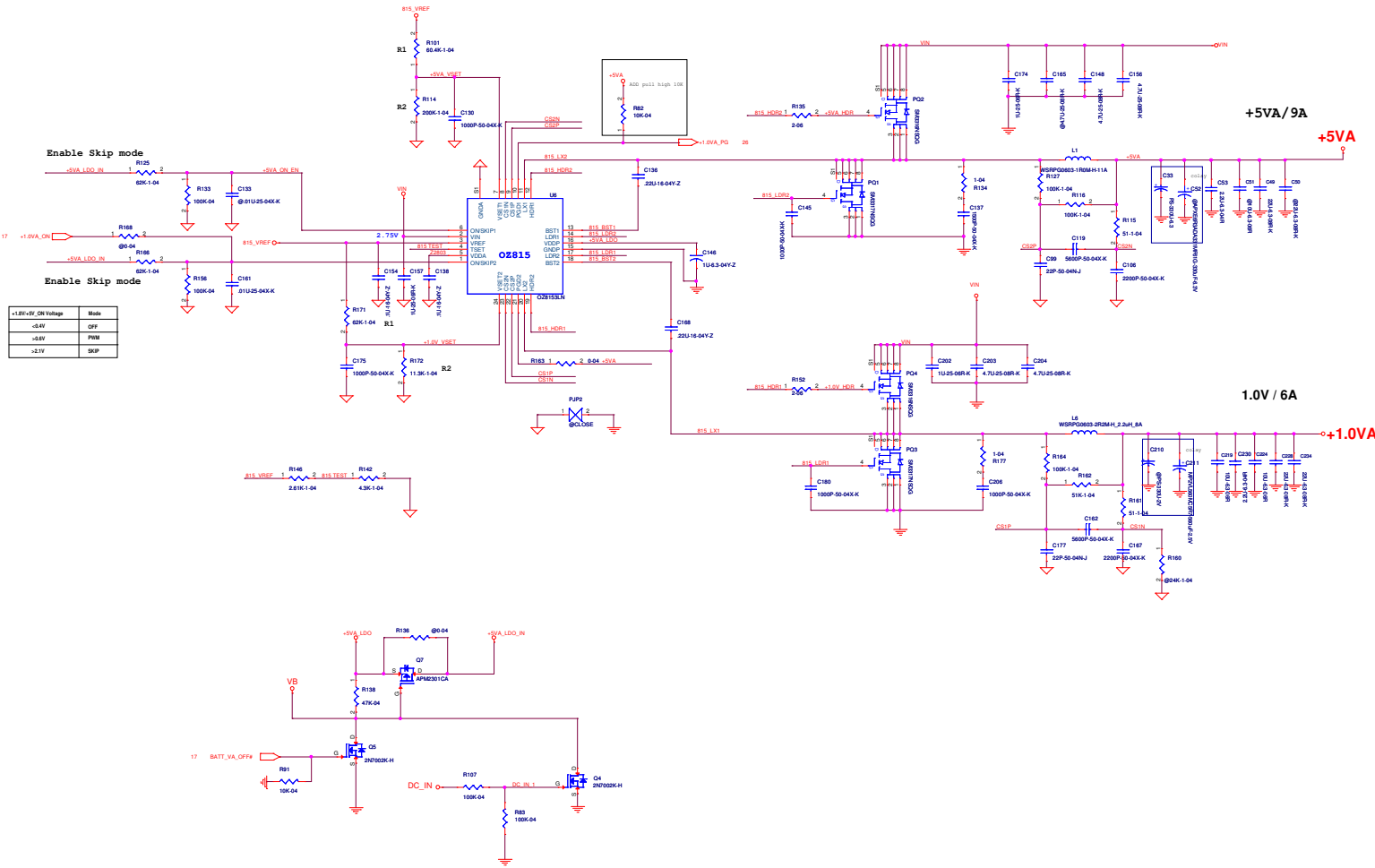
Discharge Resistor



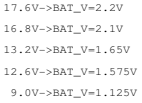
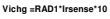
TPM



$$\text{Output Voltage} = [V_{\text{ref}} \times R2 / (R1 + R2)] \times 2$$



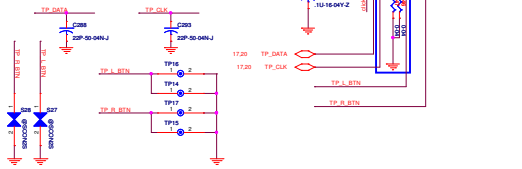
IAD MAX
40W=>R266=34.8K
65W=>R266=71.5K



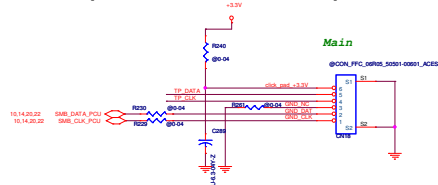
Touch Pad

R_BTN

L_BTN

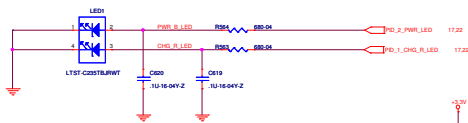


Click-pad / Co-LAY with Touch-pad



H-LED

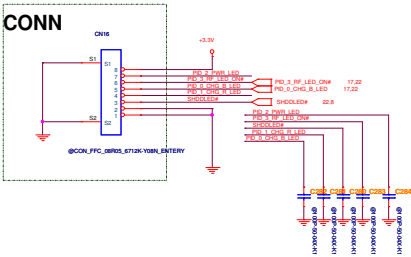
Charge LED



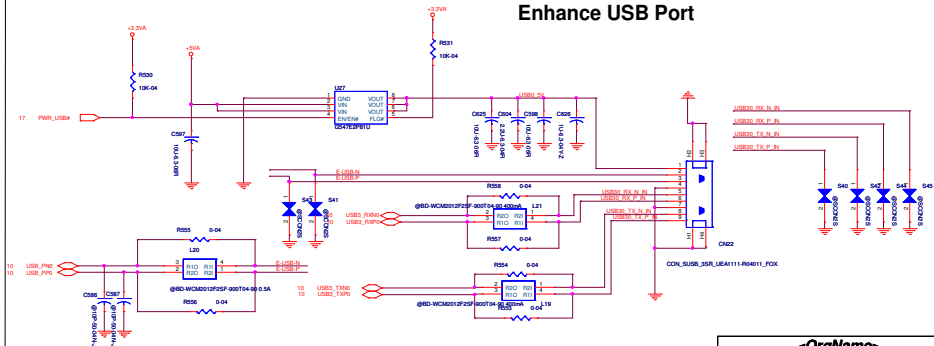
WLAN LED

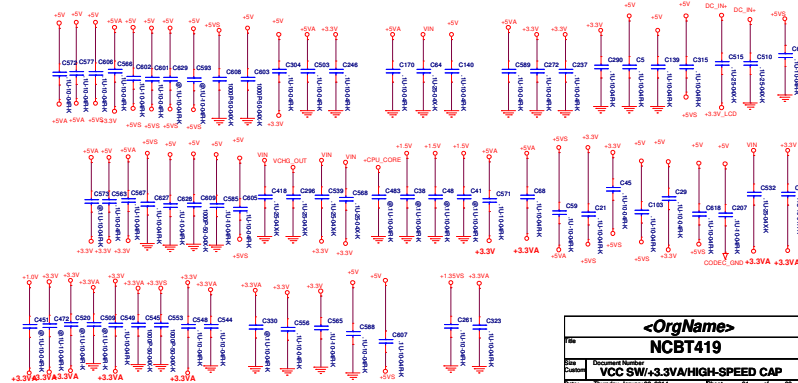
VER 3.1 (A3) WLAN LED

LED CONN



Enhance USB Port





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R466 Value:
 14'' -->604ohm
 15'' -->464ohm
 18'' -->464ohm
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