Page	Title of schematic page	Rev.	Date
01	Page List	1A	
02	Block Diagram	1A	
03	Change List	1A	
04	SNB 1/4(HOST&PCIE)	1A	
05	SNB 2/4(DDR3 I/F)	1A	
06	SNB 3/4(POWER)	1A	
07	SNB 4/4(GND/Strap)	1A	
08	PCH 1/6(DMI/FDI/VIDEO)	1A	
09	PCH 2/6(SATA/RTC/HDA/LPC)	1A	
10	PCH 3/6(PCIE/USB/CLK/NV)	1A	
11	PCH 4/6(GPIO/CPU/STRAP)	1A	
12	PCH 5/6(POWER)	1A	
13	PCH 6/6 (GND)	1A	
14	DDR3L MEMORY DOWN(1RX8)	1A	
15	DDR3 DIMM-1-STD(5.2H)	1A	
16	NPCE985E & FLASH	1A	
17	LVDS/TS/NFC	1A	
18	CARD READER(RTS5209)	1A	
19	HDMI/THERMAL	1A	
20	USB	1A	
21	LAN RTL8111GS	1A	
22	WLAN/KB-BL	1A	
23	HDD/ODD/GSENSOR/TP/FAN	1A	
24	AUDIO(ALC233-CG)	1A	
25	LED/PS	1A	
26	POWER +VCC_CORE (ISL95837)	1A	
27	POWER 3VPCU&RVCC5(TPS51427)	1A	
28	POWER 1.35VSUS/VTT_MEM	1A	
29	POWER +1.05V(G5602R41U)-15A	1A	
30	POWER VCCSA/VCCIO	1A	
31	POWER VCC1.8/Thermal	1A	
32	POWER(BAT IN / ADA IN/ UL)	1A	
33	POWER CHARGER (ISL88731C)	1A	
34	POWER VGA_CORE/1.0(RT8812A)	1A	
35	POWER VCC1.5_VRAM/1.05V	1A	
36	NVIDIA N14 GB2-64 PCIE 1/4	1A	
37	NVIDIA N14 GB2-64 TMDS 2/4	1A	
38	NVIDIA N14 GB2-64 VRAM 3/4	1A	
39	NVIDIA N14 GB2-64 VRAM 4/4	1A	

Page	Title of schematic page	Rev.	Date
40	HOLE/EMI/KB	1A	
41	IO PORT LIST	1A	

1

* : No mount
E@ : For DIS GFX only
I@ : For INT GFX only

Chief River ULV BLOCK DIAGRAM

Memory Down(Channel A)

X8 X8 X8 X8

X8 X8 X8 X8

DDR3L : 1.35V

Dual Channel

P14~P15

DDR3L : 1.35V
DDR3 : 1.5V

SO-DIMM(Channel B)

intel

Ivy Bridge

BGA1023

(31mm X 24mm)

DDR SYSTEM MEMORY

PCI-E X16

FDI DMI

P4~P7

PCIE3

5GT/s

Nvidia

N14M-LP P36 ~ P39

VRAM

1GB / 2GB

P38~P39

X8 2.7GT/s FDI

DMI X4 5GT/s

intel

<PCH>

Panther Point

mBGA 989

(25mm X 25mm)

SATA Gen3

SATA Gen2

USB

HDA

SPI

iGFX Interfaces

P8~P13

TOUCH SCREEN

PORT8

Camera P17

PORT10

WiMax/BT P21

PORT12

USB I/O P20

PORT3

USB I/O P20

PORT2

USB I/O P20

PORT1

USB I/O P20

PORT0

USB 2.0

USB 3.0

P21 HDD SATA0 6Gb/s

P21 ODD SATA3 3Gb/s

LVDS/eDP

P17

HDMI

P19

5GT/s PCI-Express Gen2

PCIE6 PCIE1 PCIE4

Giga-LAN

RTL8111GS P21

RJ45

P21

WLAN/BT P21

Card Reader

RTS5209 P18

SD-XC

NFC

I2C

Azalia

Audio CODEC

ALC233-CG P24

SPI ROM

8MB P9

LPC

EC

NPCE985L P16

Power LED

Sleep LED

SATA LED

Battery LED

CAPSLED

NUMLED

Button

ASSIST#

Power SW

Battery Detach

Combo Jack

SPK

DMIC

P24

P24

P24

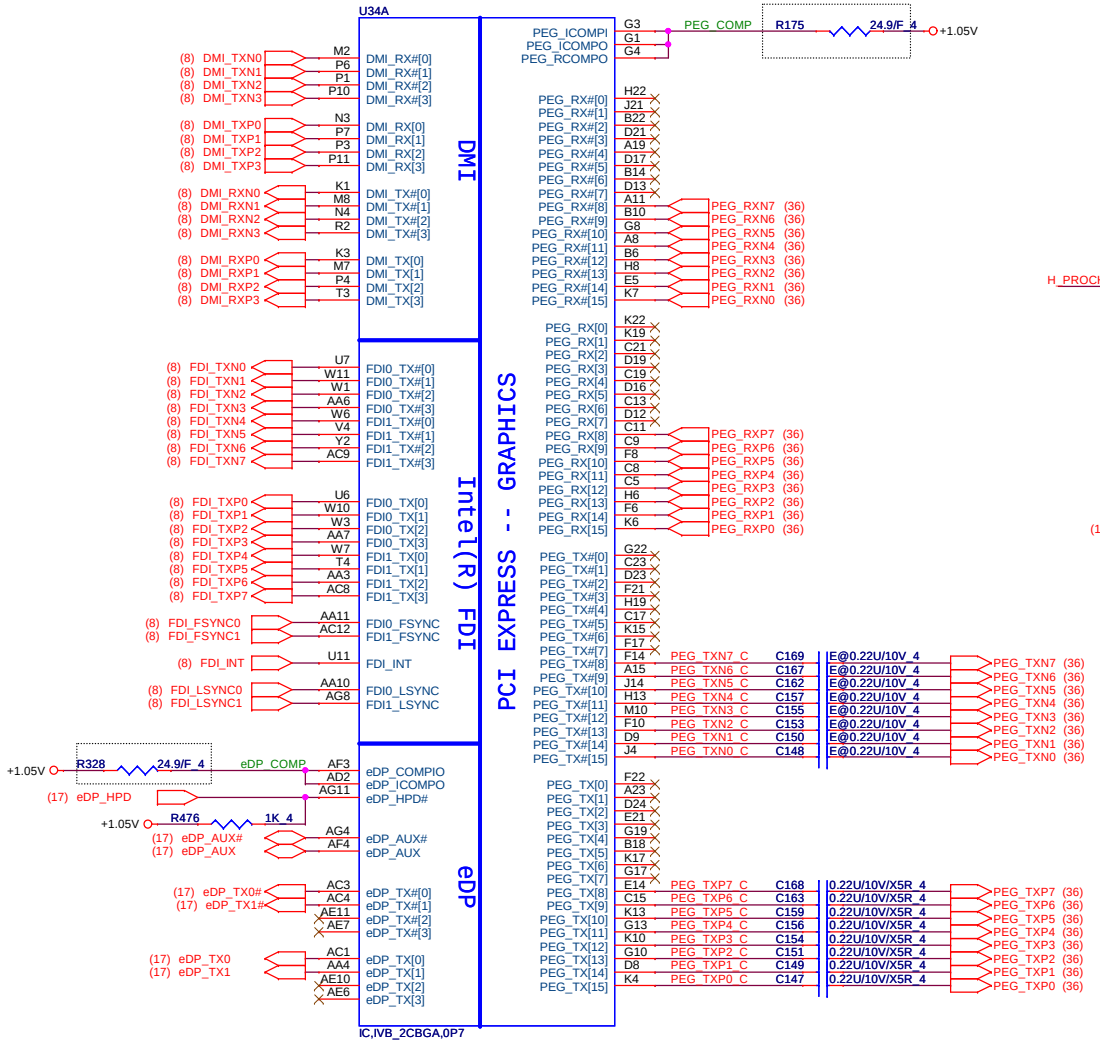
Touch Pad P20

Keyboard P25

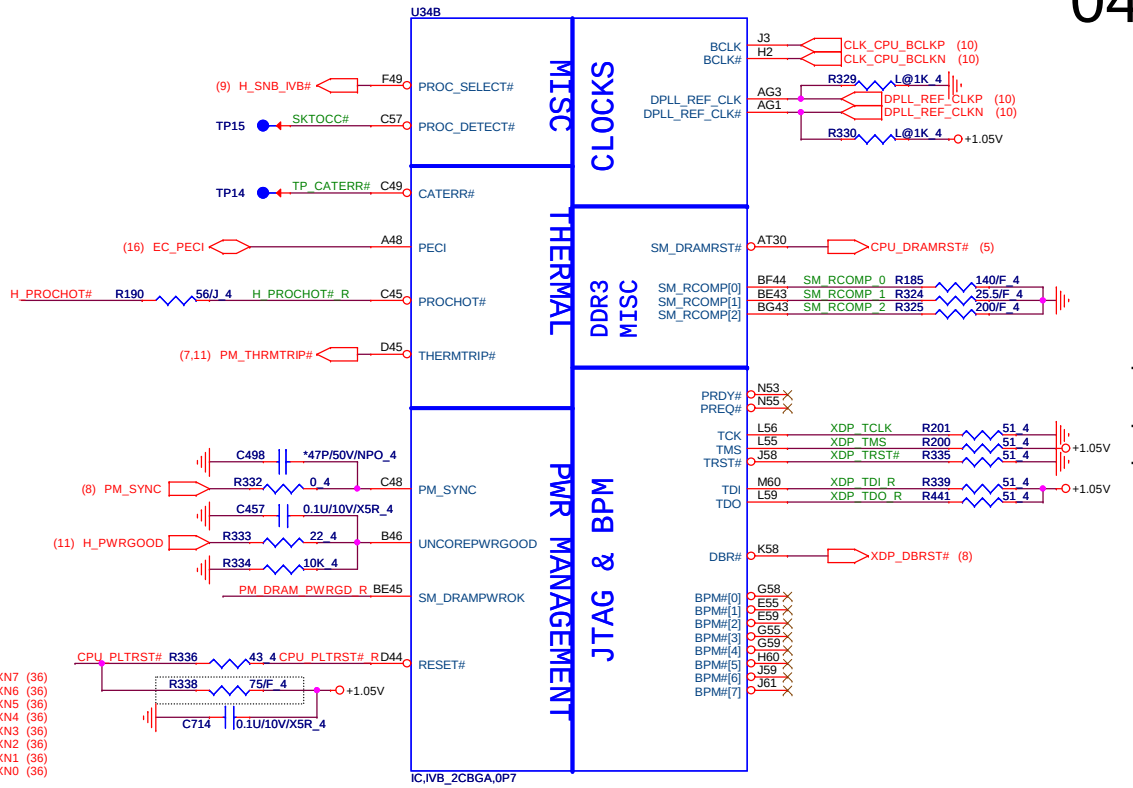
Change List

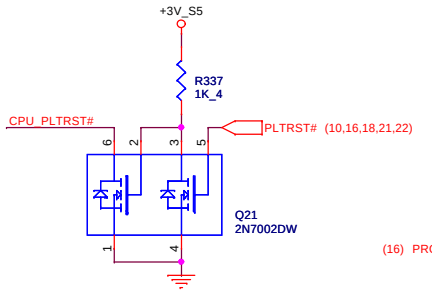
GD56_MB_SCH_EVT_001

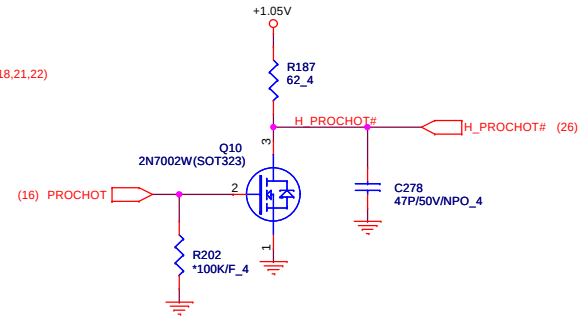
Ivy Bridge Processor (DMI,PEG,FDI)

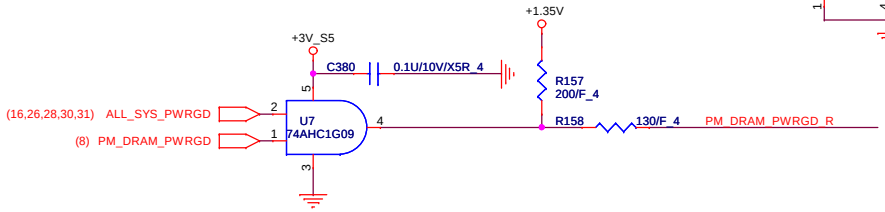


Ivy Bridge Processor (CLK,MISC,JTAG)









SNB_IVB#:
- It is NC when using Sandy Bridge.(1.05V)
- For next generation processor it will be grounded in package.(1.0V)

FDI Disabling (Discrete Only)

-FDI_FSYNC (J18/J17/J19/H17) can gang all these 4 signals together and tie them with only one 1K resistor to GND (DG V0.5 Ch2.2.9).
- FDI_INT connect to GND with 1K ohm.

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2.Recycled Resin and Coated Wire should be procured from Green Partners.

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PROJECT : GD5

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	SNB/IVB 1/4	1A
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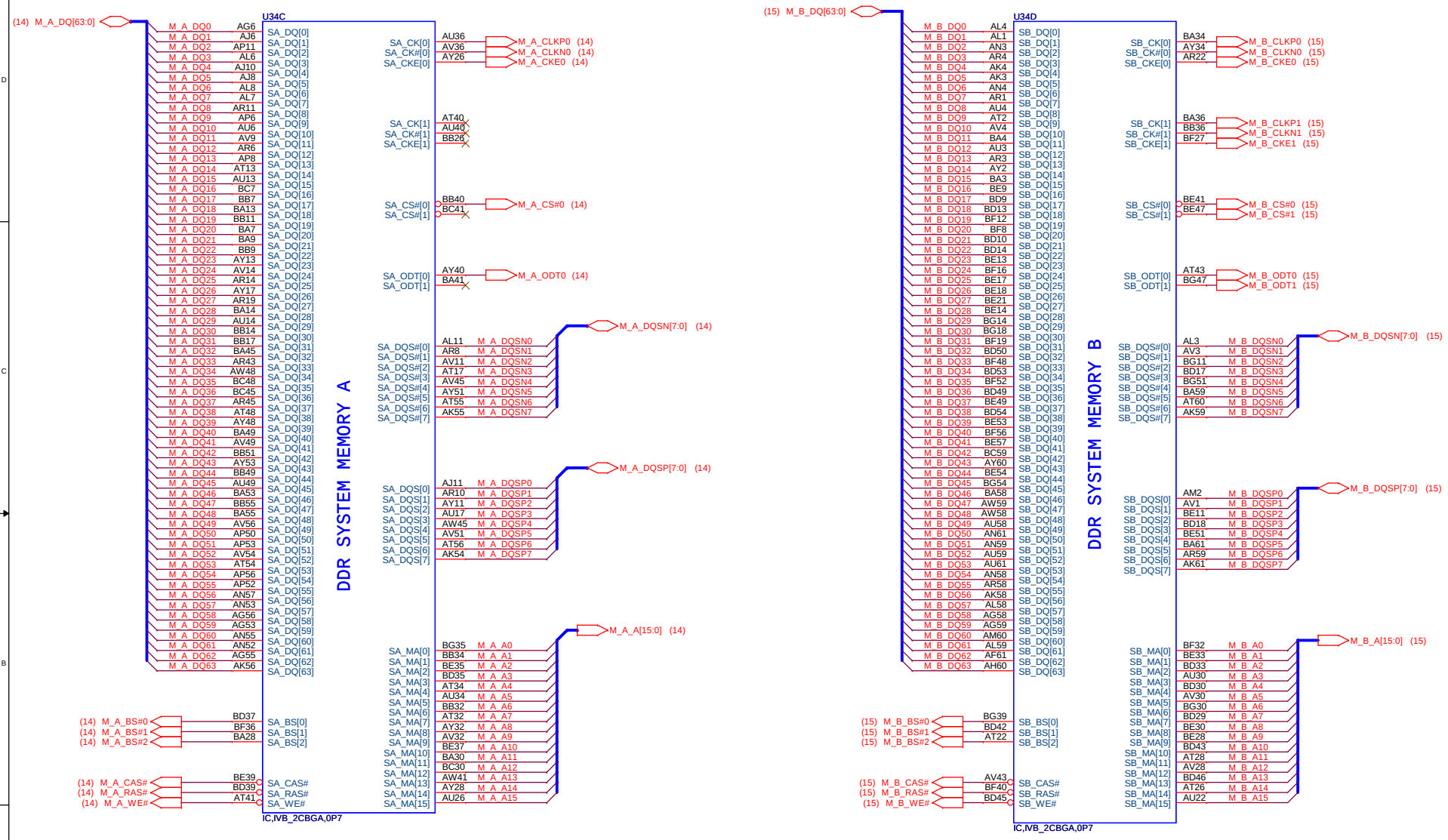
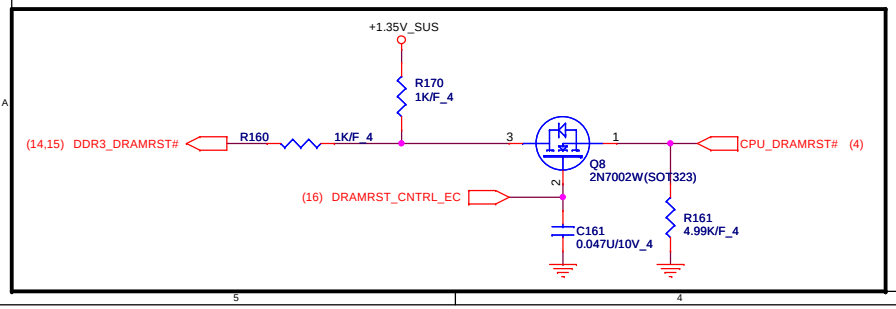
04

Ivy Bridge Processor (DDR3)





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	SNB/IVB 2/4	1A
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Ivy Bridge Processor (POWER)

Ivy Bridge Processor (GRAPHIC POWER)

POWER

CPU Core Power
(33A)

Intel request
2.2uF x16
22uF x12
330uF x4 9mohm

Intel request
10uF x10
1uF x26
(7.1A)

U34F

CORE SUPPLY
PEG IO AND DDR IO
QUIET RAILS
SVID
SENSE LINES

IC,IVB_2CBGA,0P7

POWER

U34G

Intel request
22uF x12
1uF x15
330uF x2
PDC: 10ux8
(25A)

Intel request
10uF x5
1uF x5
(4.5A)

(0.93A)

(4.75A)

Intel request
10uF x8
1uF x10

(0.6A)

VREF
GRAPHICS DDR3 - 1.5V RAILS
SENSE LINES
QUIET RAILS
1.8V RAIL
SA RAIL
VCCSA VID lines

IC,IVB_2CBGA,0P7

Layout note: need routing together and ALERT need between CLK and DATA

SVID CLK
Close to VR
R218 54.9/F_4
H_CPU_SVIDCLK R189 0_4 VR_SVID_CLK (26)
(50 ohm)

Place PU resistor close to CPU
SVID DATA
Close to VR
R184 130/F_4
R347 130/F_4
H_CPU_SVIDDAT R186 0_4 VR_SVID_DATA (26)
(50 ohm)

Place PU resistor close to CPU
SVID ALERT
R195 75/F_4
H_CPU_SVIDALRT# R192 43_4 R197 0_4 VR_SVID_ALERT# (26)
(50 ohm)

HR/CR: HR(VID1 only) CR(VID0&VID1)

VCCIO_SEL:
Sandy is High = 1.05V
Ivy is Low = 1.0V

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	SNB/IVB 3/4	1A

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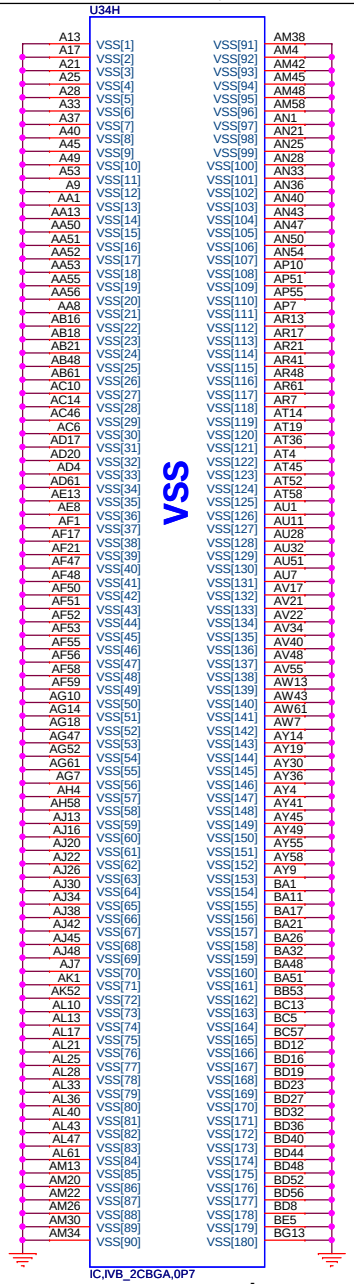
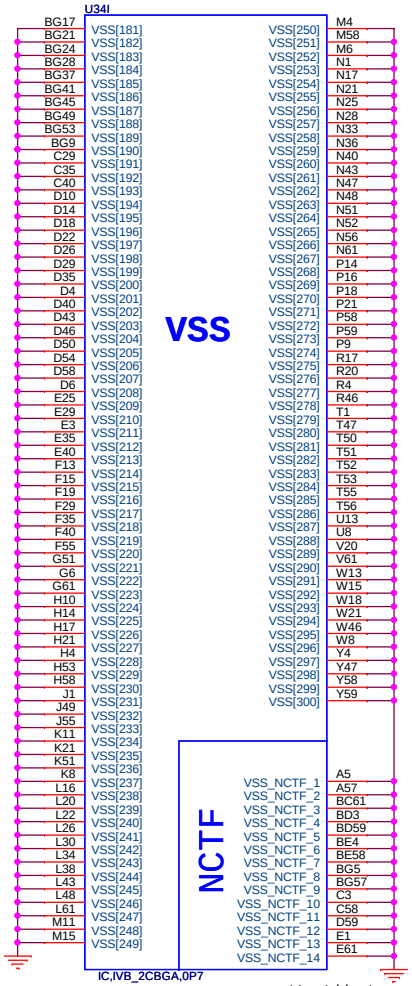
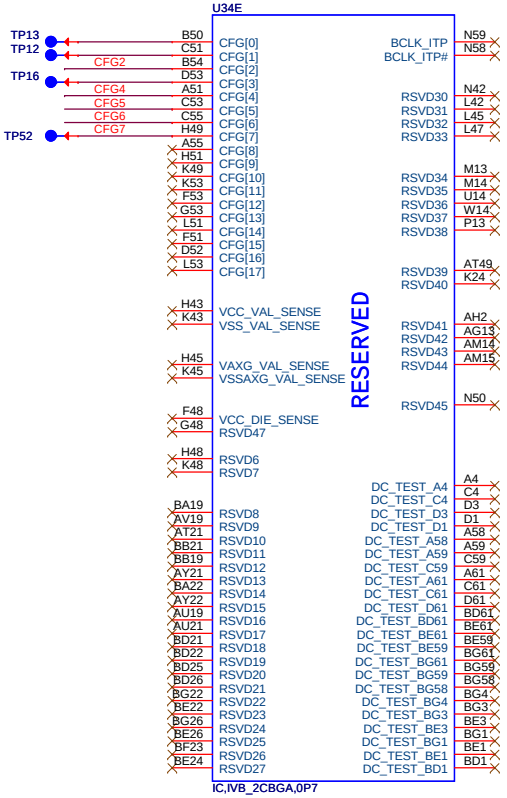
Ivy Bridge Processor (GND)



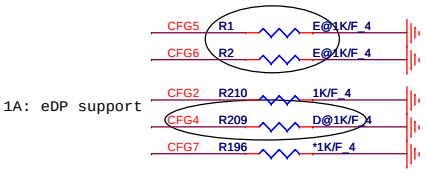


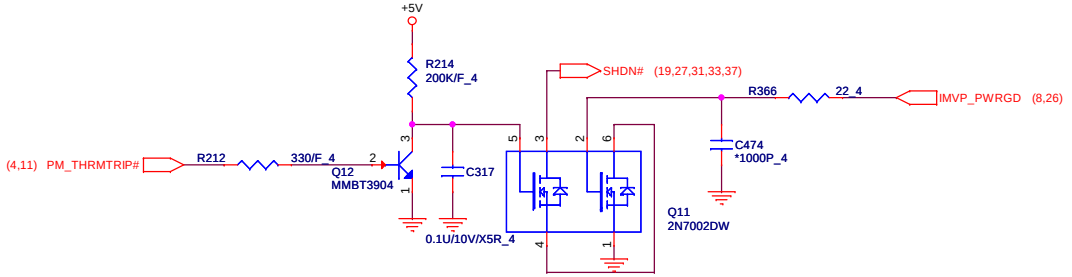
Ivy Bridge Processor (RESERVED, CFG)



1A: Add straps for Graphic

1A: eDP support





The CFG signals have a default value of '1' if not terminated on the board.

Processor Strapping

	1	0
CFG2 (PEG Static Lane Reversal)	Normal Operation	**Lane Reversed**
CFG4 (DP Presence Strap)	**Disable; No physical DP attached to eDP**	Enable; An ext DP device is connected to eDP
CFG7 (PEG Defer Training)	**PEG train immediately following xxRESETB de assertion**	PEG wait for BIOS training

CFG[6:5] (PCIE Port Bifurcation Straps)

11: (Default) x16 - X16 PEG interface
10: PEG x8 x8 bifurcation enableddisabled
01: Reserved - (Device 1 function 1 disabled ; function 2 enabled)
00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

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PROJECT : GD5

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	SNB/IVB 4/4	1A

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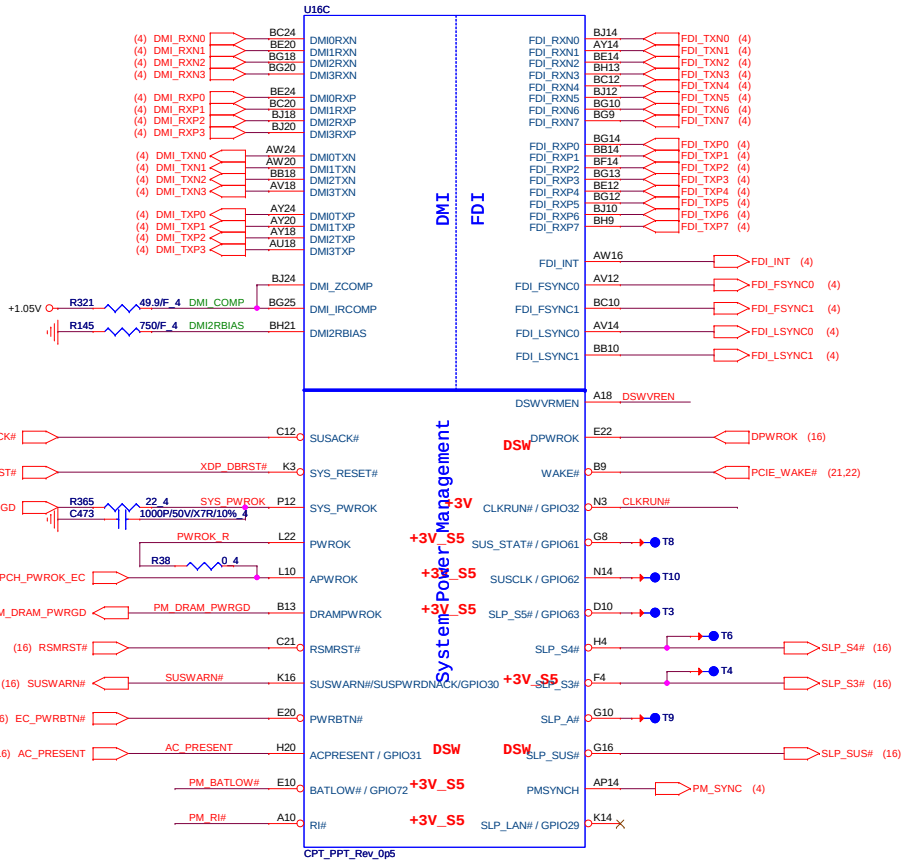
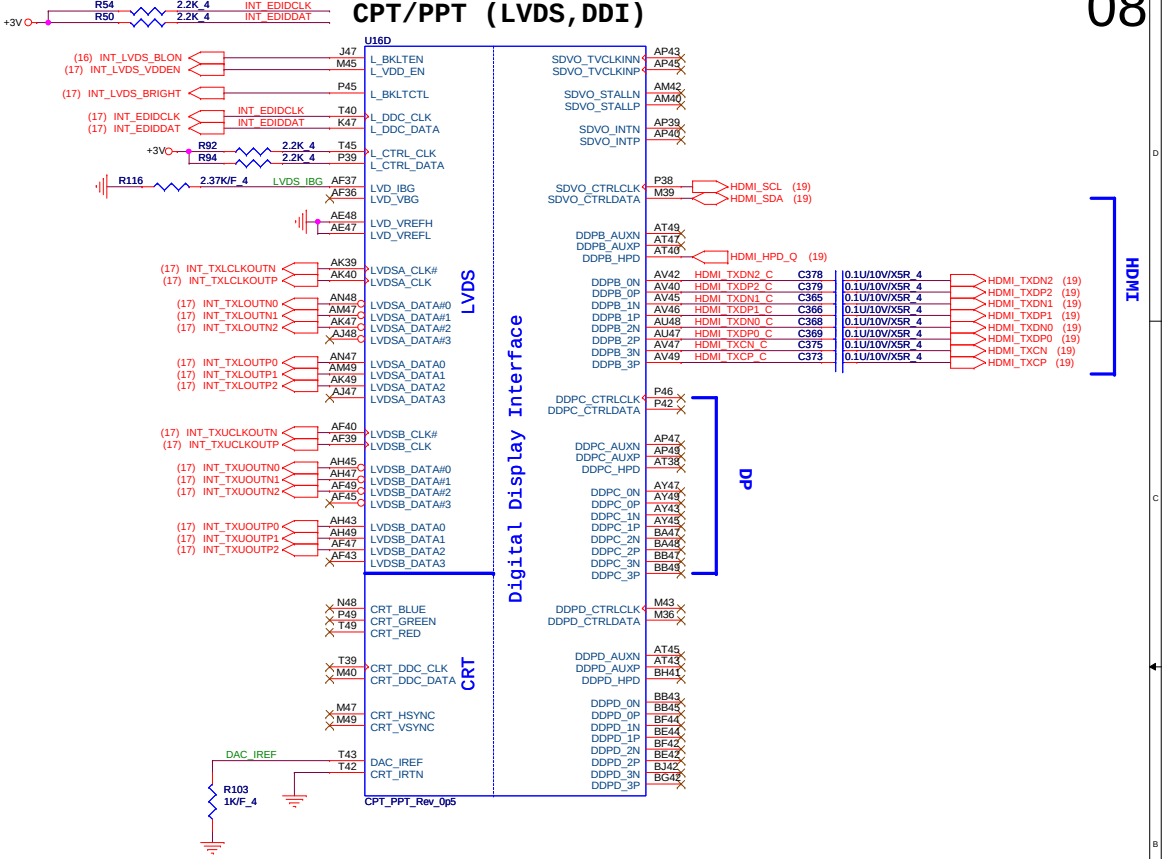
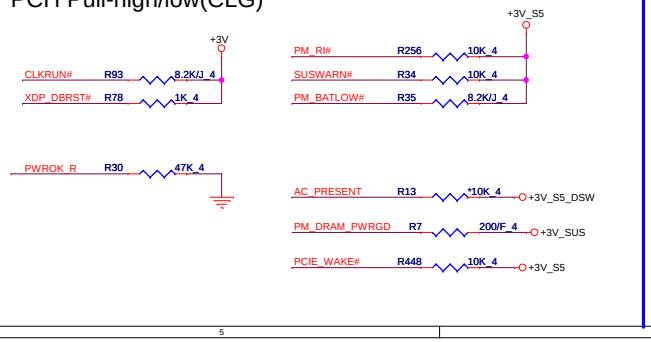
CPT/PPT (DMI,FDI,PM)

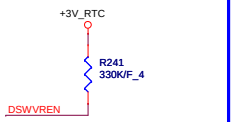


CPT/PPT (LVDS,DDI)



PCH Pull-high/low(CLG)





On Die DSW VR Enable
High = Enable (Default)
Low = Disable

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PROJECT : GD5

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	CPT/PTT 1/6	1A
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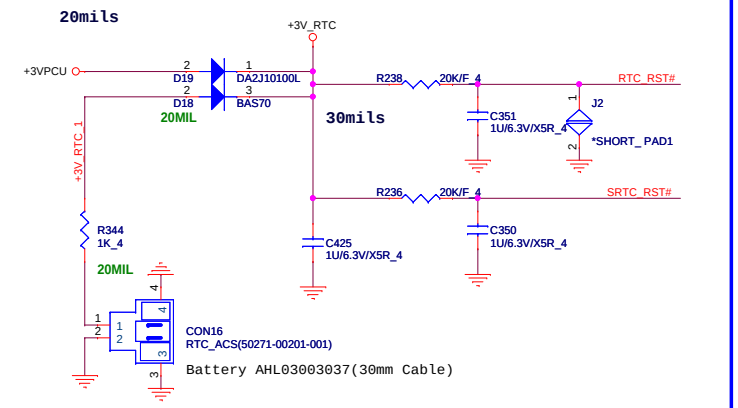
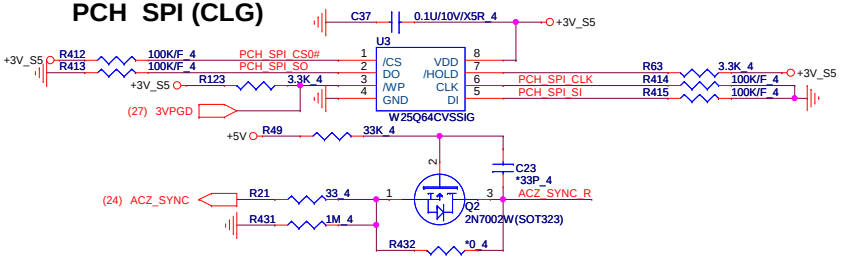
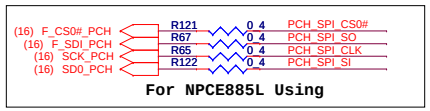
RTC Circuitry(RTC)



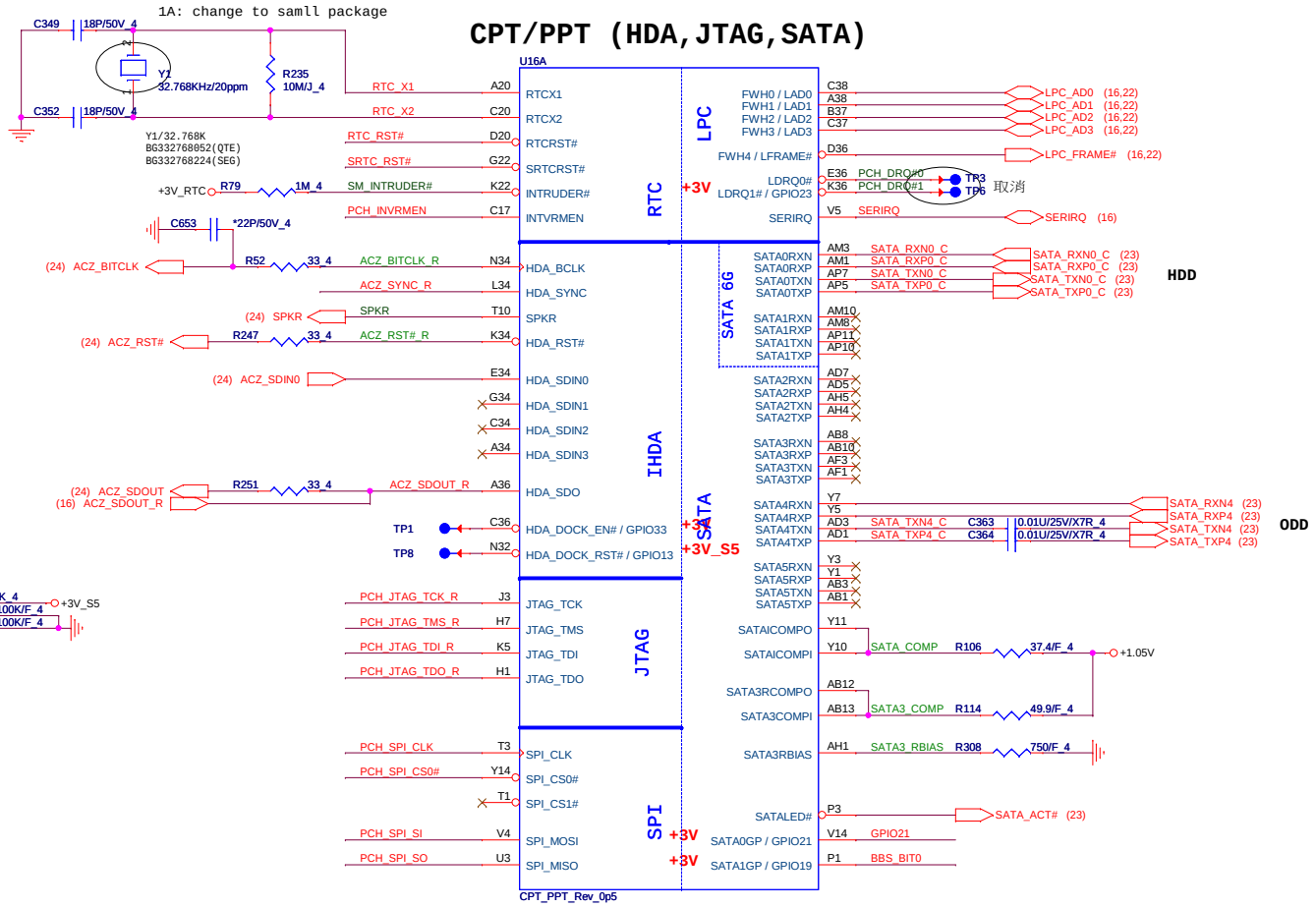
MX25L3205DM2I-12G: AKE39FP0Z00
W25X32VSSIG: AKE39ZP0N00

PCH SPI (CLG)





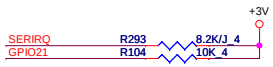
CPT/PPT (HDA,JTAG,SATA)



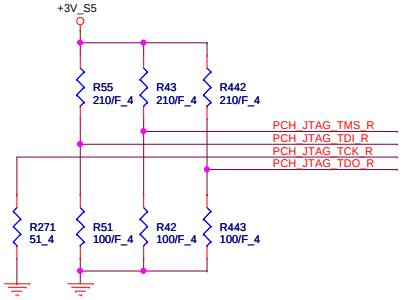
PCH Strap Table

Pin Name	Strap description	Sampled	Configuration	Note
SPKR	No reboot mode setting	PWROK	**0 = Default (weak pull-down 20K)** 1 = Setting to No-Reboot mode	SPKR
PCI_GNT3# / GPIO55	Top-Block Swap Override	PWROK	0 = "top-block swap" mode **1 = Default (weak pull-up 20K)**	TP53 PCI_GNT3# (10)
INTVRMEN	Integrated 1.05V VRM enable	ALWAYS	**Should be always pull-up**	+3V_RTC R242 330K/F_4 PCH_INVRMEN
GNT1# / GPIO51	Boot BIOS Selection 1 [bit-1]	PWROK	GNT1# / GNT0# / Boot Location: 1 / 1 / **SPI** *; 0 / 0 / LPC	**Default weak pull-up on GNT0/1#** **[Need external pull-down for LPC BIOS]**
GPIO19	Boot BIOS Selection 0 [bit-0]	PWROK		TP47 BBS_BIT1 (10) TP43 BBS_BIT0
HDA_SDO	Flash Descriptor Security	PWROK	0 = Default (weak pull-down 20K) 1 = Enabled	**ACZ_SDOUT_R**
DF_TVS	DMI/FDI Termination voltage	PWROK	0 = Set to Vss for Ivy Bridge 1 = Set to Vcc for Sandy Bridge (weak pull-down 20K)	R140 2.2K_4 +1.8V R320 1K_4 NV_CLE (11) H_SNB_IVB# (4)
GPIO28	On-die PLL Voltage Regulator	RSMRST#	0 = Disable 1 = Enable (Default)	TP44 PLL_ODVR_EN (11)
HDA_SYNC	On-Die PLL VR Voltage Select	RSMRST	0 = Support by 1.8V (weak pull-down) 1 = Support by 1.5V	+3V_S5 R29 1K_4 ACZ_SYNC_R

HR/CR: PROCSEL#



PCH JTAG Debug (CLG)



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

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	CPT/PPT 2/6	1A
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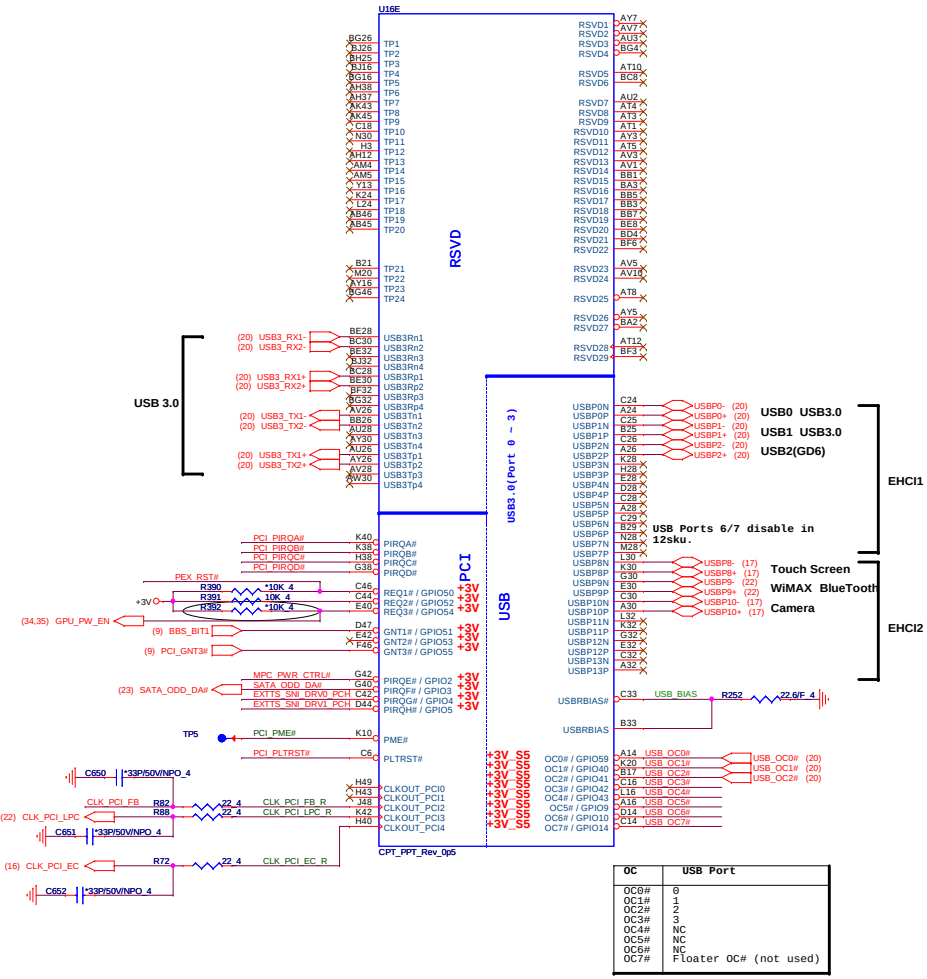
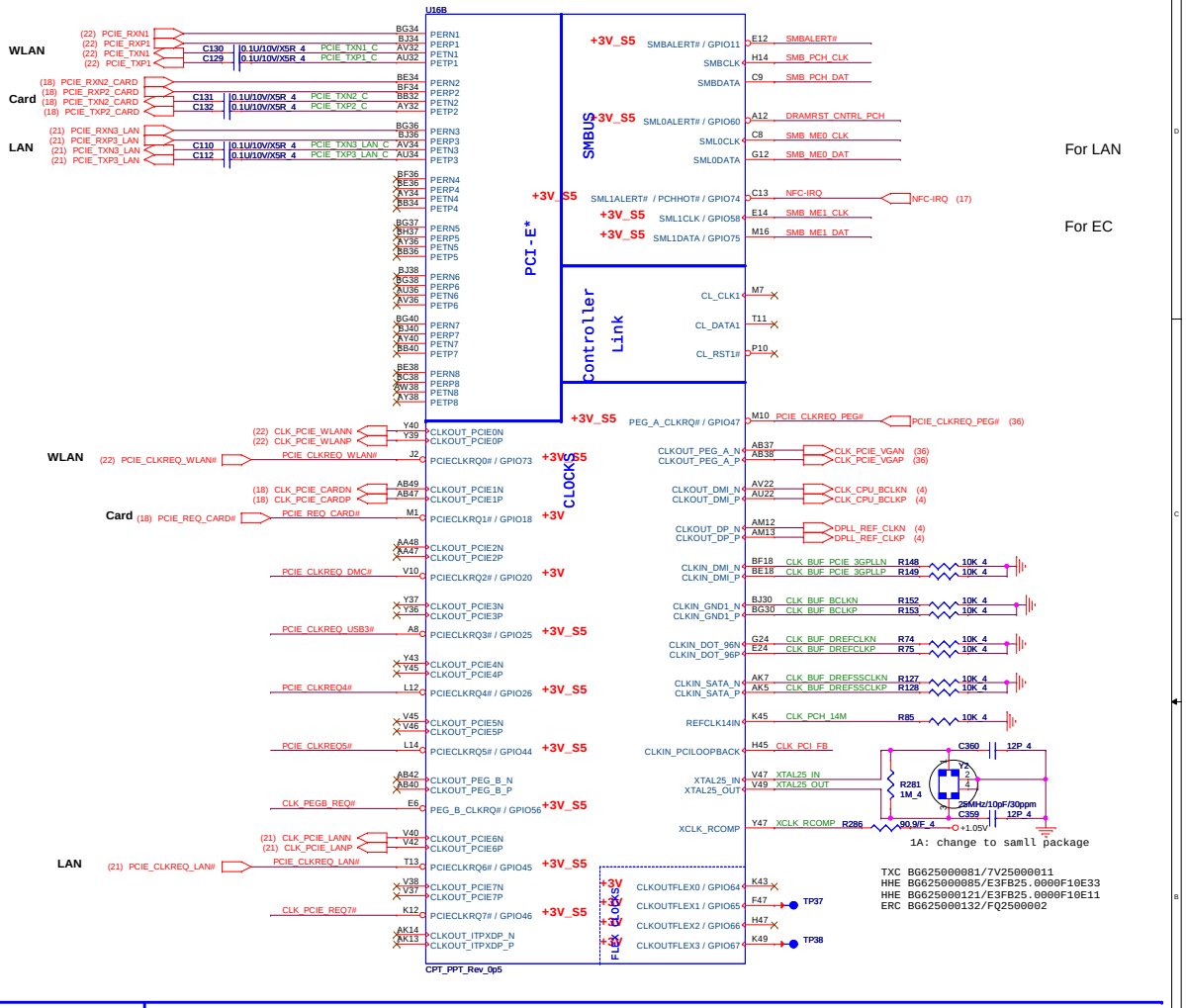
CPT/PPT (PCI,USB,NVRAM)



OC	USB Port
OC0#	0
OC1#	1
OC2#	2
OC3#	3
OC4#	NC
OC5#	NC
OC6#	NC
OC7#	Floater OC# (not used)

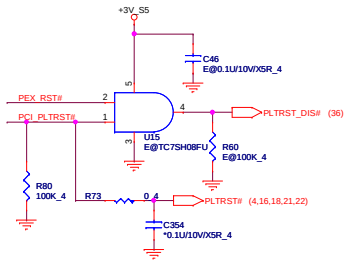
CPT/PPT (PCI-E,SMBUS,CLK)



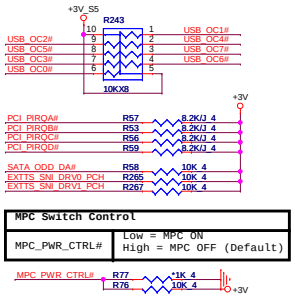
1A: change to samll package

TXC BG625000081/7V25000011
HHE BG625000085/E3FB25.0000F10E33
HHE BG625000121/E3FB25.0000F10E11
ERC BG625000132/FQ2500002

PLTRST#(CLG)



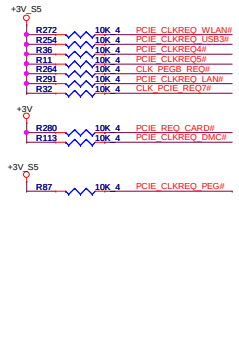
PCI/USBOC# Pull-up(CLG)



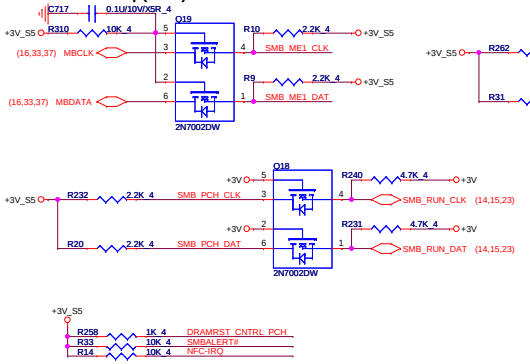
MPC Switch Control	
MPC_PWR_CTRL#	Low = MPC ON High = MPC OFF (Default)

MPC_PWR_CTRL# R77 *1K 4, R76 10K 4

CLK_REQ/Strap Pin(CLG)



SMBus/Pull-up(CLG)



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PROJECT : GD5

Size	Document Number	Rev
	CPT/PPT 3/6	1A

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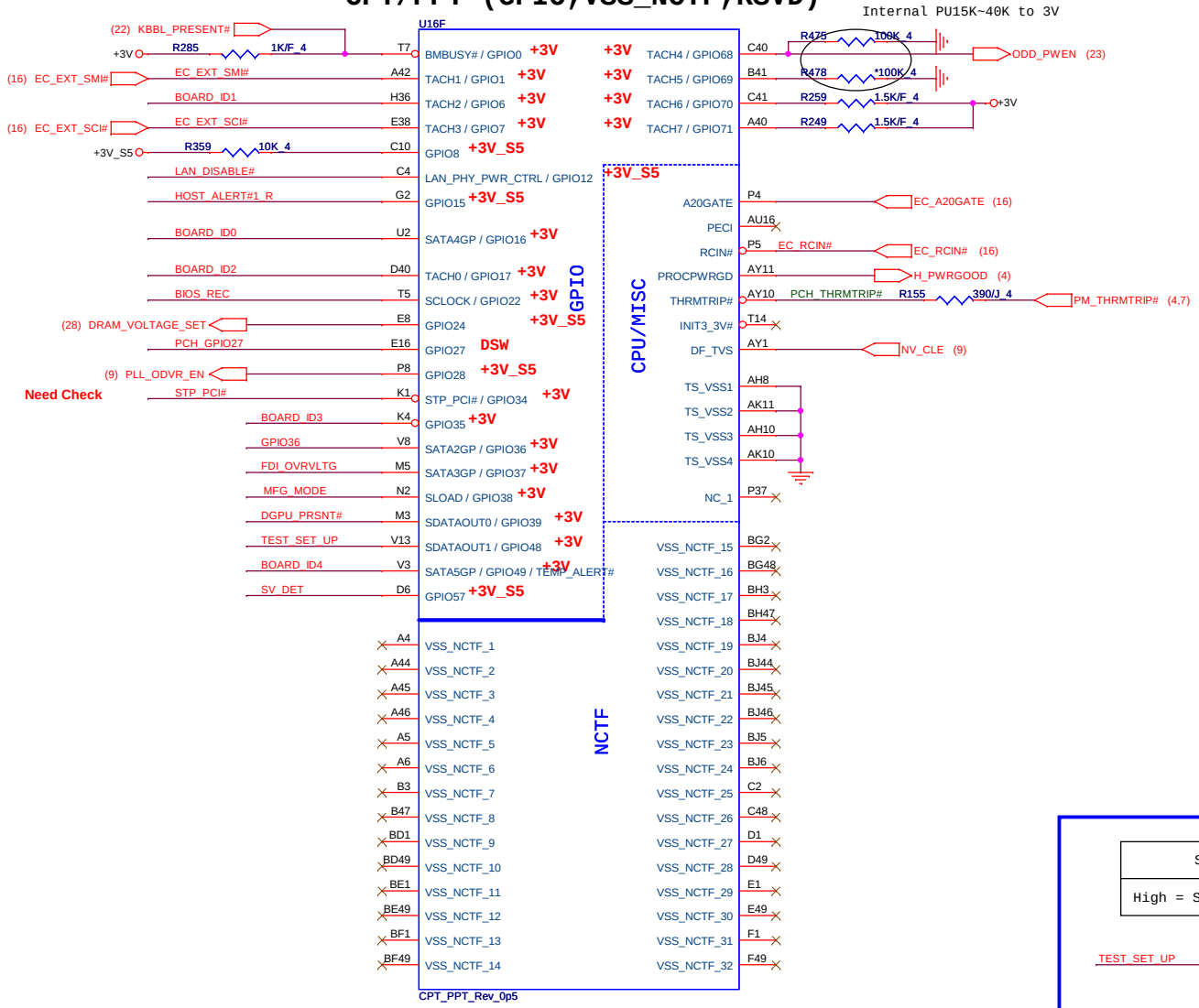
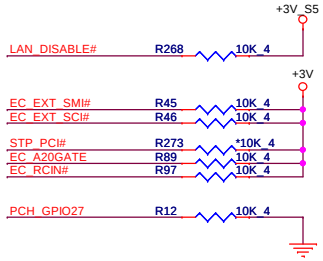
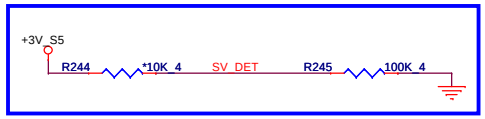
CPT/PPT (GPIO,VSS_NCTF,RSVD)

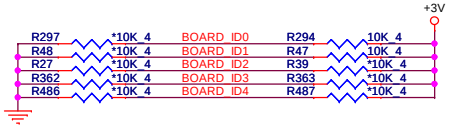




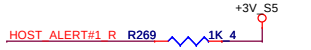


	0	1
Board ID0	CaspiCRA1/CRB1 HK8/9	SuperiorCRA1/CRB1 GD5/6
Board ID1	14"	15"

	Board ID2	Board ID3	Board ID4
SAM 2G	0	0	1
SAM 4G	0	1	0
HYN 2G	0	1	1
HYN 4G	1	0	0
ELP 2G	1	0	1
ELP 4G	1	1	0

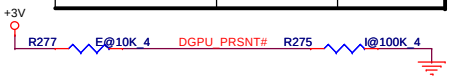


SV_SET_UP
High = Strong (Default)

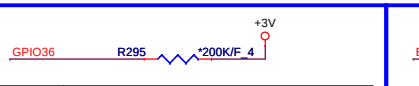


Intel ME Crypto Transport Layer Security (TLS) cipher suite
Low = Disable (Default)
High = Enable

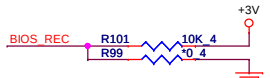
PCBA SKU	Discrete	UMA
R277(Pull High)	Stuff	No Stuff
R275(Pull Low)	No Stuff	Stuff



FDI TERMINATION VOLTAGE OVERRIDE	LOW - Tx, Rx terminated to same voltage



DMI TERMINATION VOLTAGE OVERRIDE	Low = Tx, Rx terminated to same voltage (DC Coupling Mode) (DEFAULT)



BIOS RECOVERY	High = Disable (Default) Low = Enable

MFG-TEST

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	CPT/PPT 4/6	1A
Date: Thursday, October 25, 2012	Sheet 11 of 41	

CPT/PPT (POWER)

POWER

VccCORE =1.3 A(60mils)

1A: CRT Disable

1mA(8mils)

1mA(8mils)

60mA(10mils)

1B: C114 change to 0603 at DVT stage.

(VccIO =2.925A (140mils))

VCCVRM: 1.8V (Destop)
1.5V (Mobile)

VCCDMI = 42mA(10mils)

VCCCLKDMI = 20mA(8mils)

VCCPNAND = 190 mA(15mils)

VCCSPI = 20mA(8mils)

VccASW =1.01 A(60mils)

65mA(10mils)

8mA(8mils)

VCCDIFFCLKN= 55mA(10mils)

VCCSSC= 95mA(10mils)

1mA(8mils)

VCCRTC<1mA(8mils)

VCCSUS3_3 = 119mA(15mils)

VCC5REFSUS=1mA

V5REF= 1mA

VCCSUS3_3 = 119mA(15mils)

VCCPCORE = 28mA(10mils)

VCCVRM = 114mA(15mils)

VCCME = 1.01A(60mils)

VCCSUSHDA= 10mA(8mils)

DEEP STANDBY

1A: Modify FP

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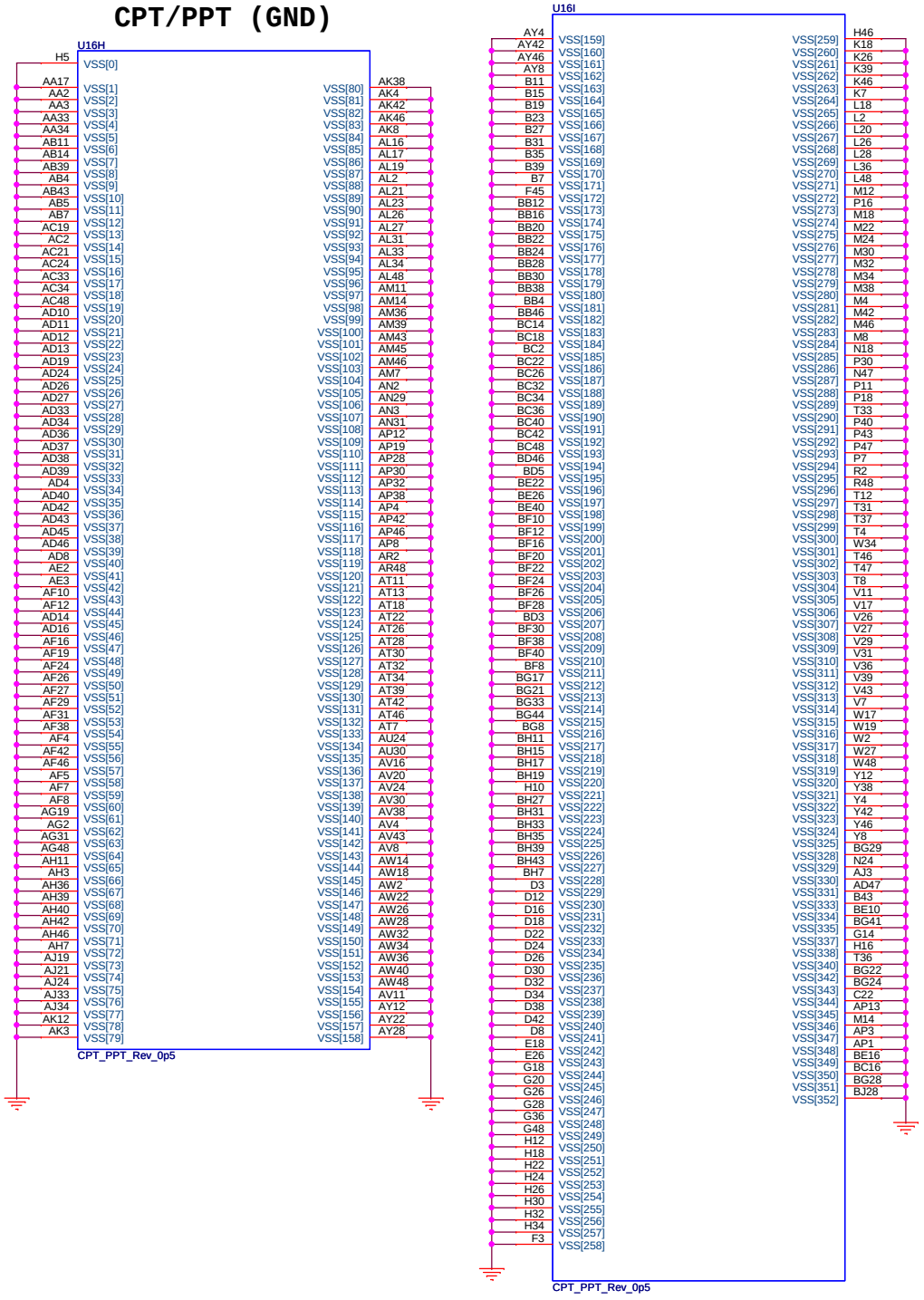
Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	CPT/PPT 5/6	1B

Date: Thursday, October 25, 2012 Sheet 12 of 41

CPT/PPT (GND)



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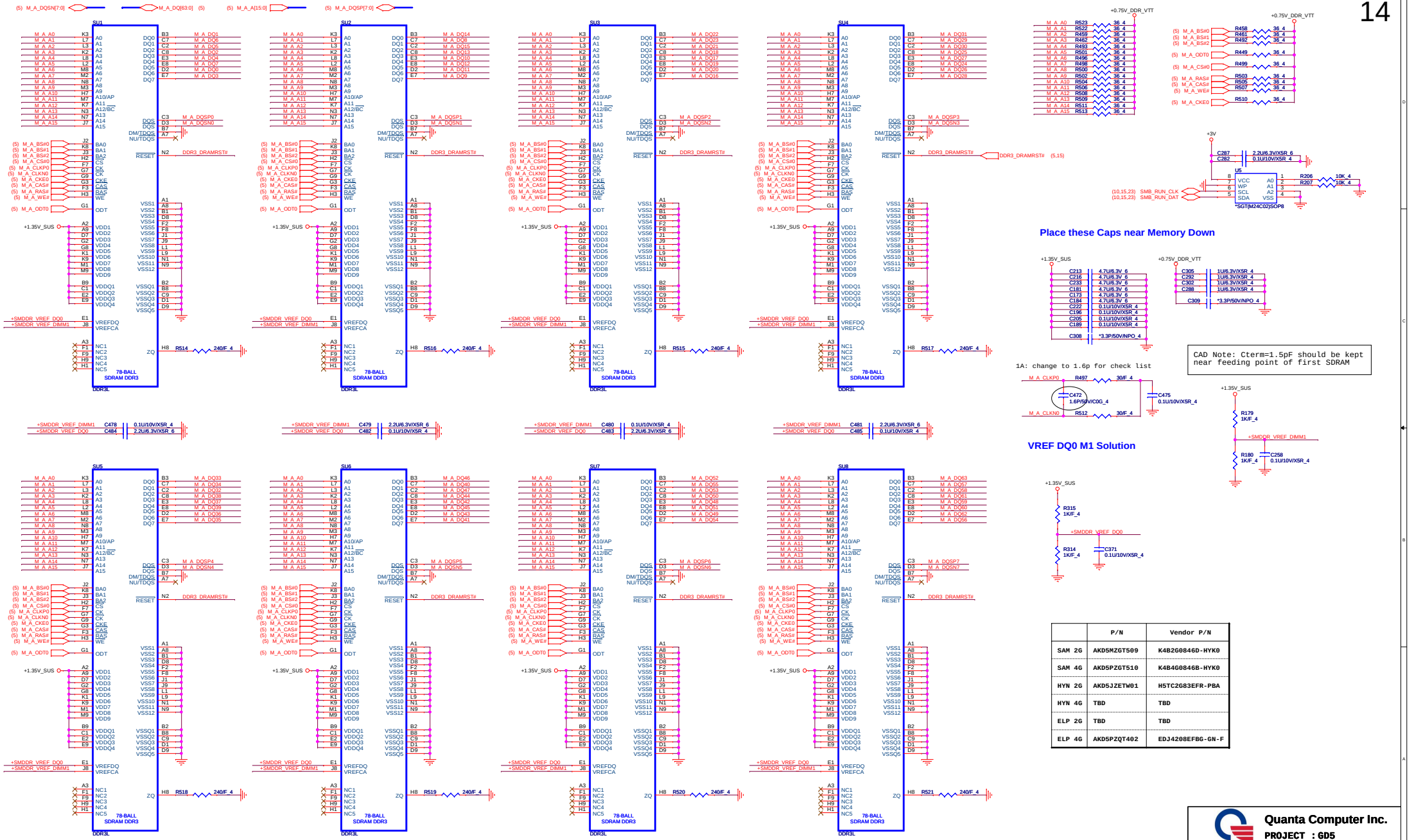
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	CPT/PPT 6/6	1A
Date: Thursday, October 25, 2012	Sheet 13 of 41	



	P/N	Vendor P/N
SAM 2G	AKD5MZGT509	K4B2G0846D-HYK0
SAM 4G	AKD5PZGT510	K4B4G0846B-HYK0
HYN 2G	AKD5JZETW01	H5TC2G83EFR-PBA
HYN 4G	TBD	TBD
ELP 2G	TBD	TBD
ELP 4G	AKD5PZQT402	EDJ4208EFBG-GN-F

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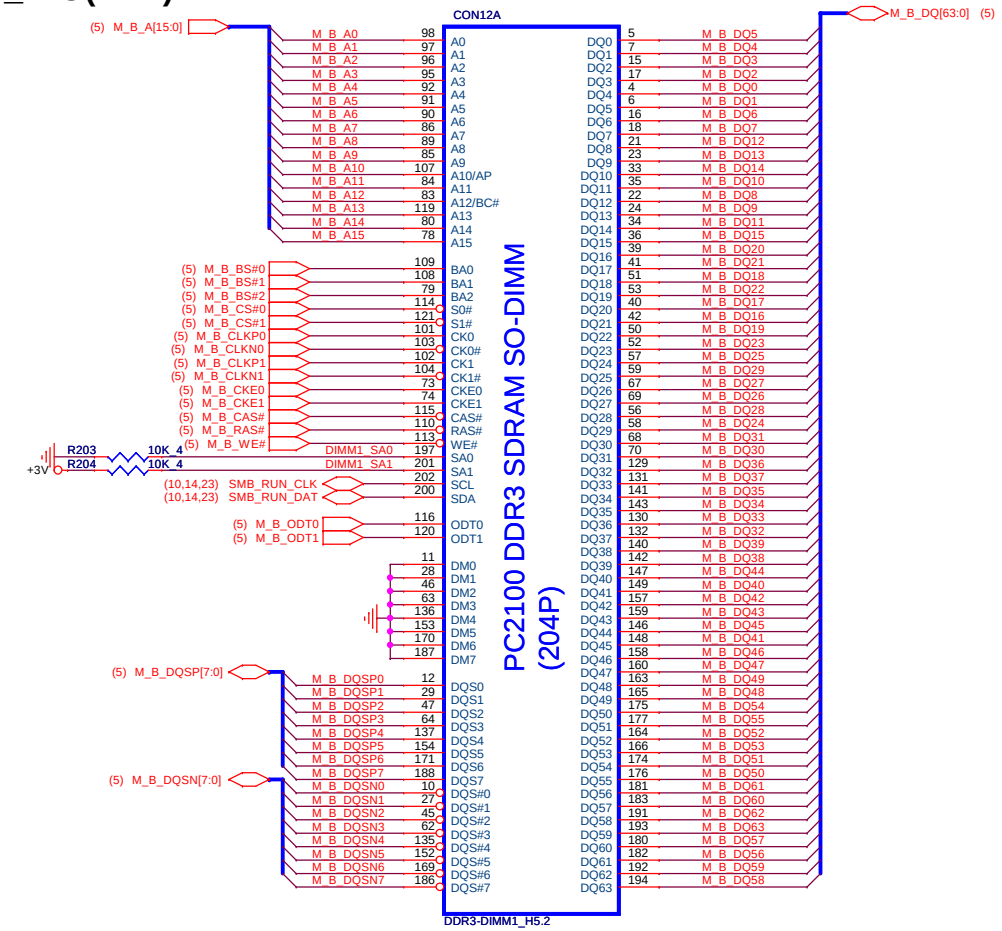
Quanta Computer Inc.
PROJECT : GD5

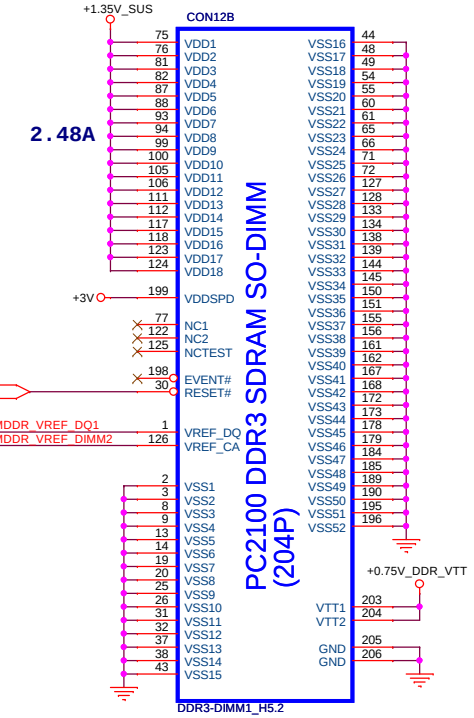
Size	Document Number	Rev
	DDR3L MEMORY DOWN	1A

Date: Thursday, October 25, 2012 Sheet 14 of 41

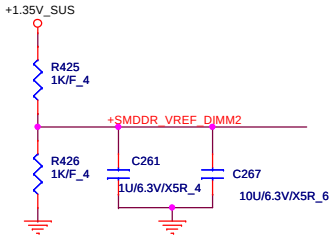
DDR_RVS(DDR)



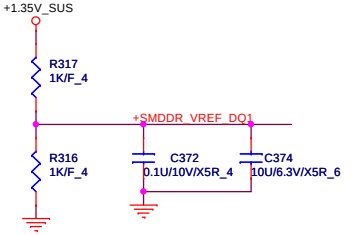
RUV Type



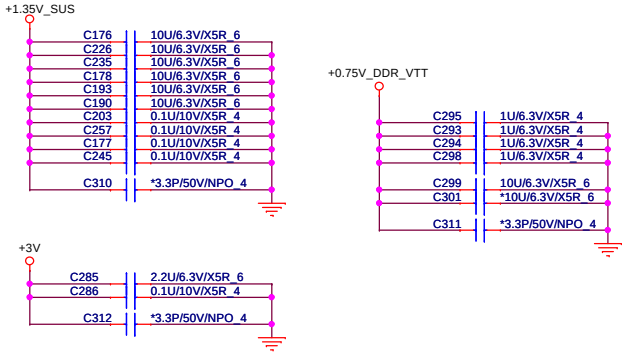
RUV Type



VREF DQ1 M1 Solution



Place these Caps near So-Dimm1.



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Quanta Computer Inc.
PROJECT : GD5

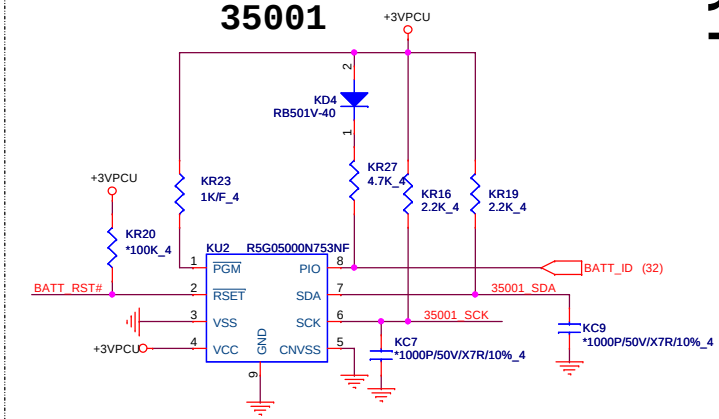
Size	Document Number	Rev
	DDRIII SO-DIMM-1	1A
Date: Thursday, October 25, 2012	Sheet 15 of 41	

**** Straping Pin,Can not pull low.**
Note the input leakage current to the strap pins must be less than 10uA.

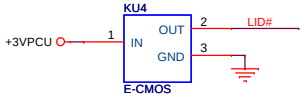
Since ECSCI is OD, no need for a back-drive protection diode on this signal. But note there is internal PU in chipset at default

KC16, KC17 can't mount(ME high limitation)

35001



Magnetic Lid Switch



E-CMOS :AL002618001/EC2618NLB1GR
PROLIFIC : AL003661003/PT3661-BB
BCD : AL009249000/AH9249NTR-G1

1A: Add MODEL ID

HK8/9 HIGH KR29 MOUNT
GD5/6 LOW KR30 MOUNT

DRAM_VOLTAGE_SET
Low: 1.5V
High : 1.35V

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Quanta Computer Inc. PROJECT : GD5		
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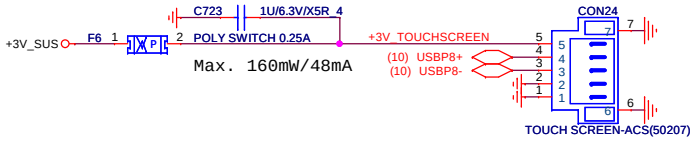
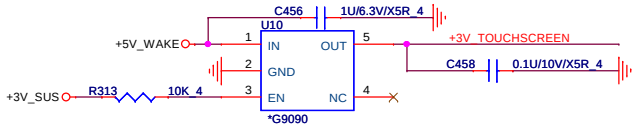
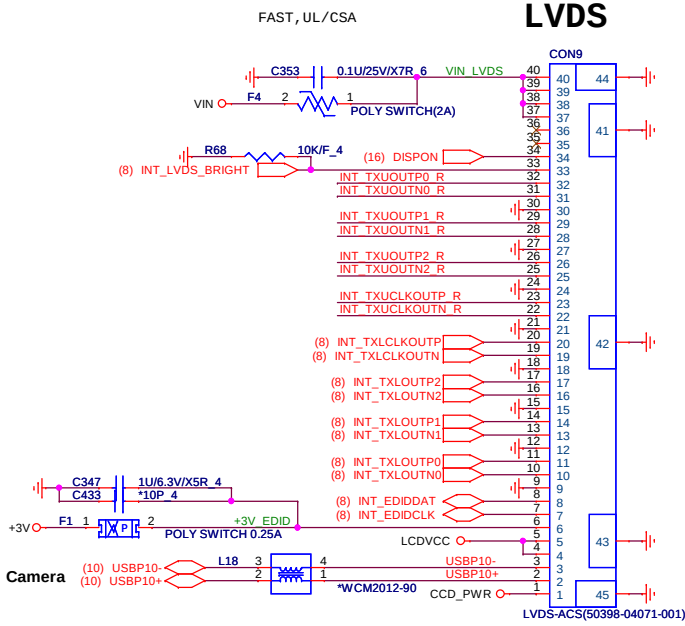
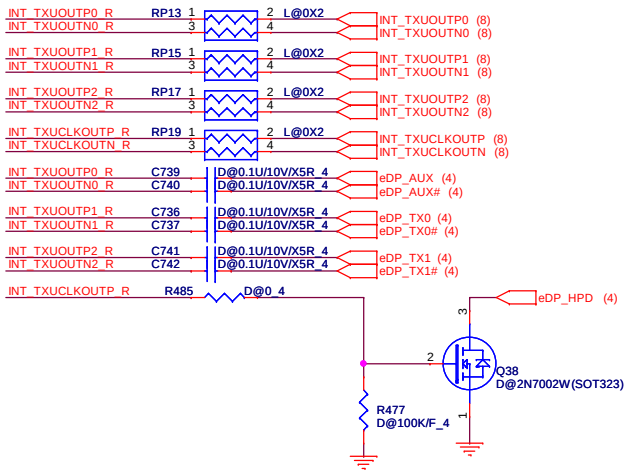
Touch Screen



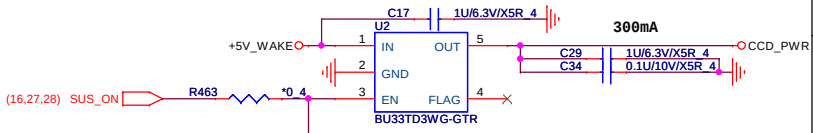


LVDS



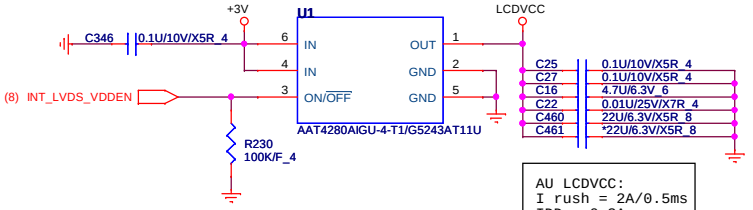


USB Camera Power



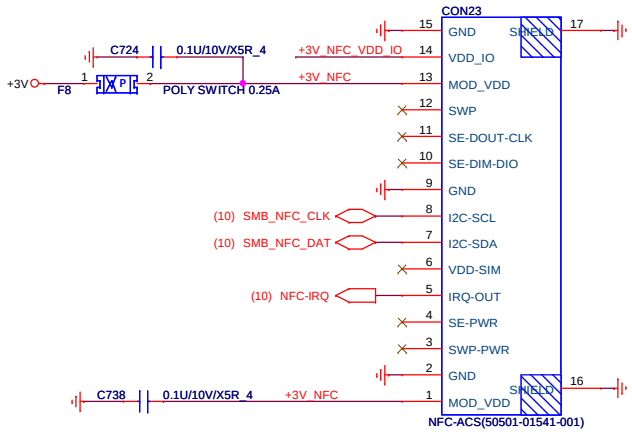
Camera HD specification
Voltage: Max. 3.6V
Current : Max. 200mA
OCP: 200mA ~ 300mA

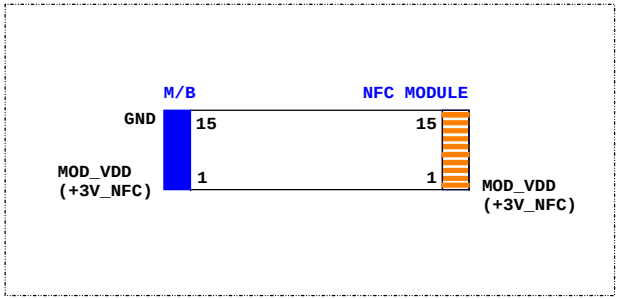
NB LVDS enable

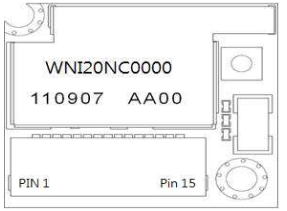


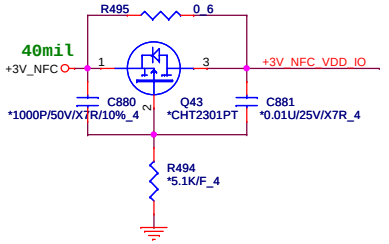
AU LCDVCC:
I rush = 2A/0.5ms
IDD = 0.3A

NFC









1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	NFC/TOUCH SCREEN/eDP	1A
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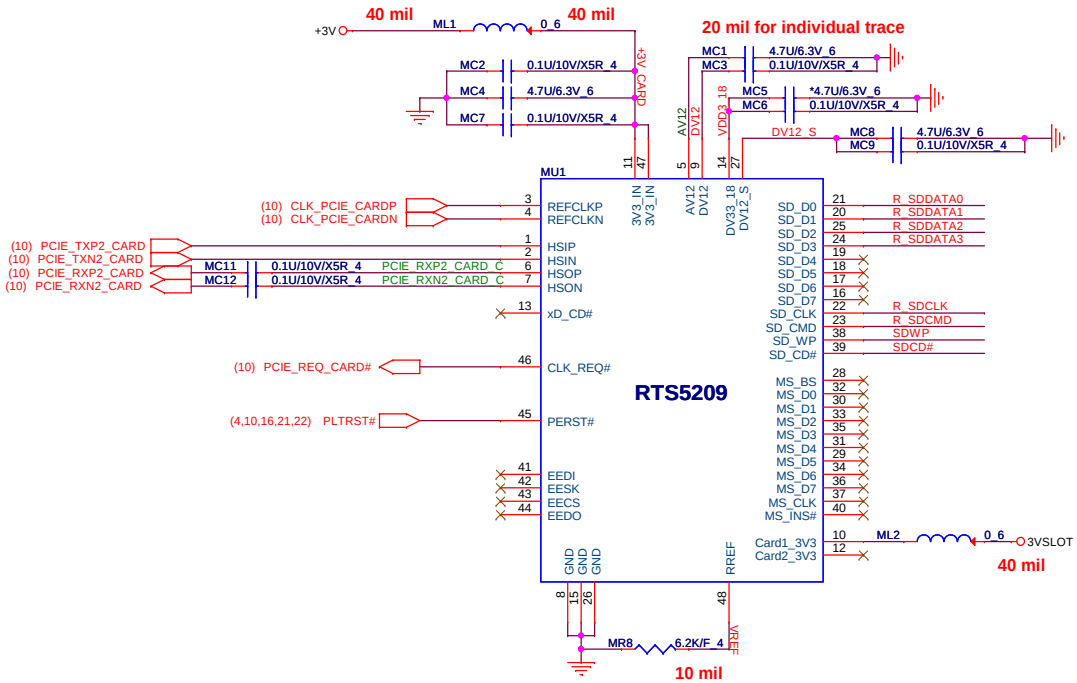

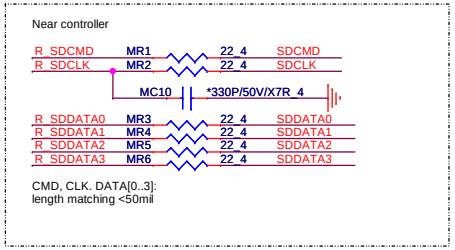

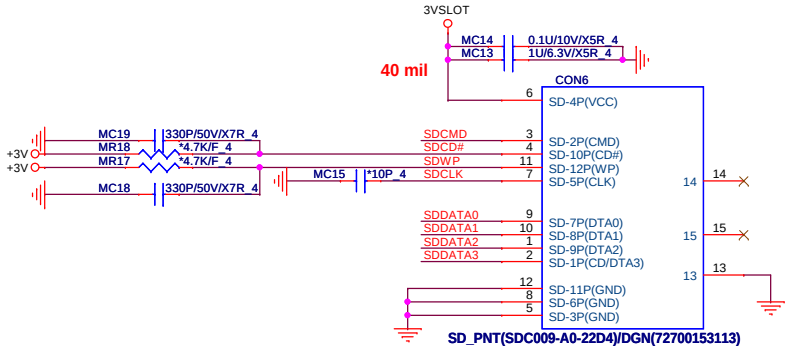

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Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	CARD	1A

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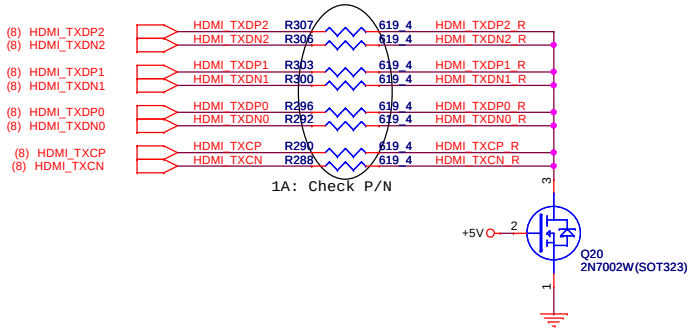
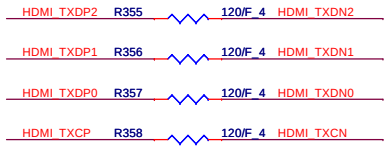
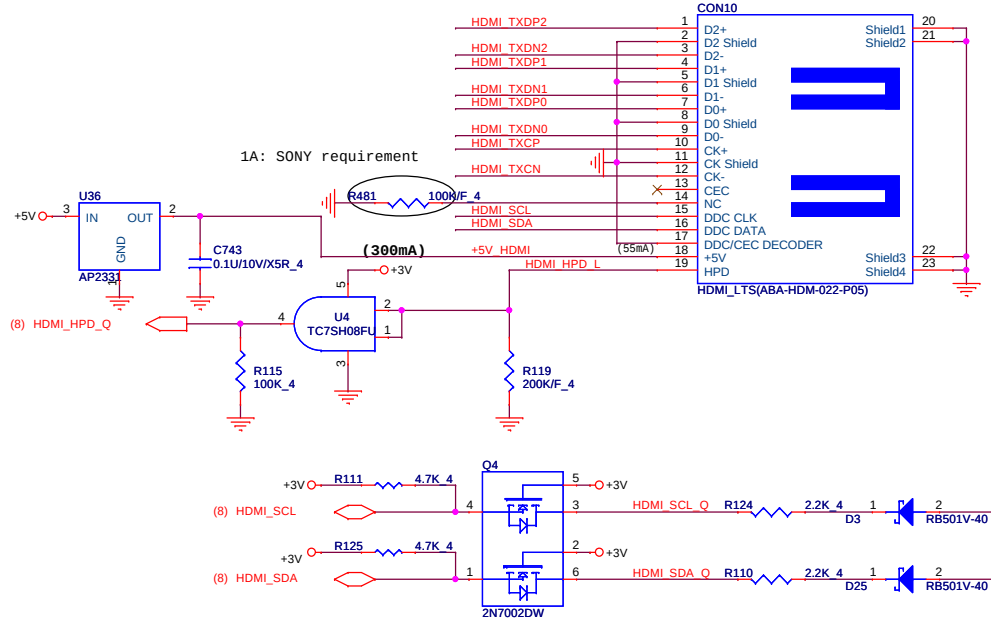
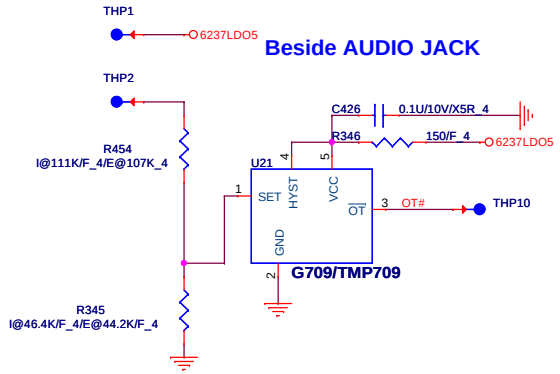
HDMI

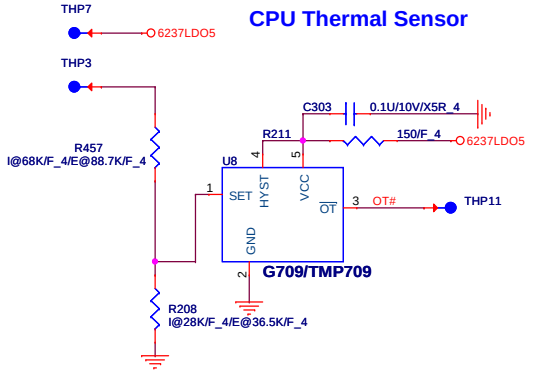


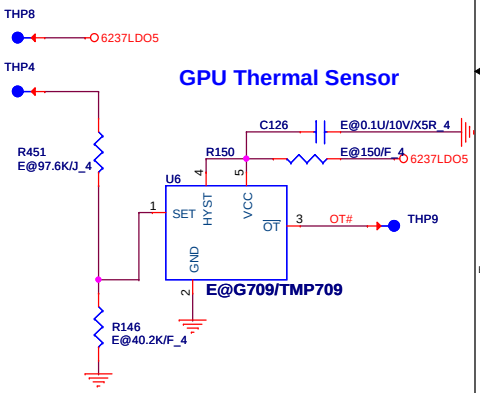




H/W Thermal Protect

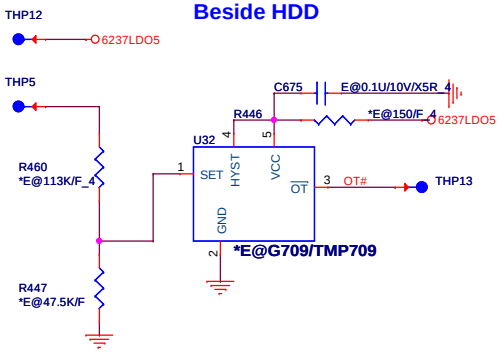


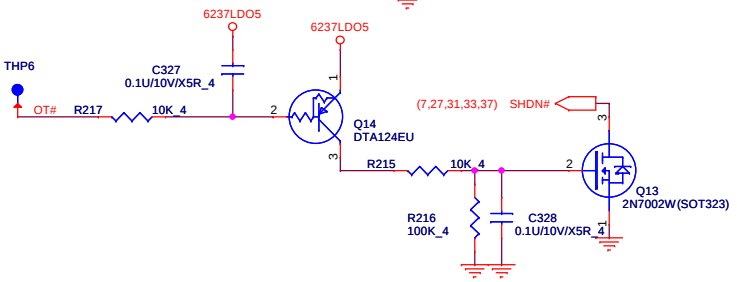




RSET(kΩ)= 0.0012T2 – 0.9308T + 96.147

95	18.5K
100	15K
107	10.3K
110	8.2K





DIS SKU

Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	70	R208=36.87K	36.5K	71	70
Near GFX sensor temp	65	R146=40.72K	40.2K	66.3	65.1
Near AUDIO sensor temp	60	R345=44.62K	44.2K	61.2	60

UMA SKU

Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	81	R208=28.63K	28K	82.3	81.4
Near AUDIO sensor temp	58	R345=46.2K	46.4K	58.4	57.1

1.Level 1 Environment-related Substances Should Never be Used.
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Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	HDMI/Thermal IC	1A

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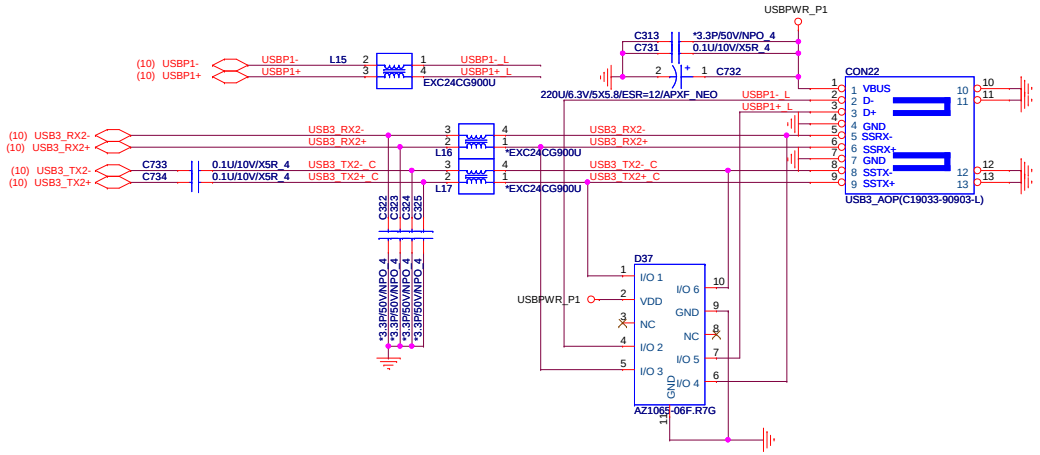
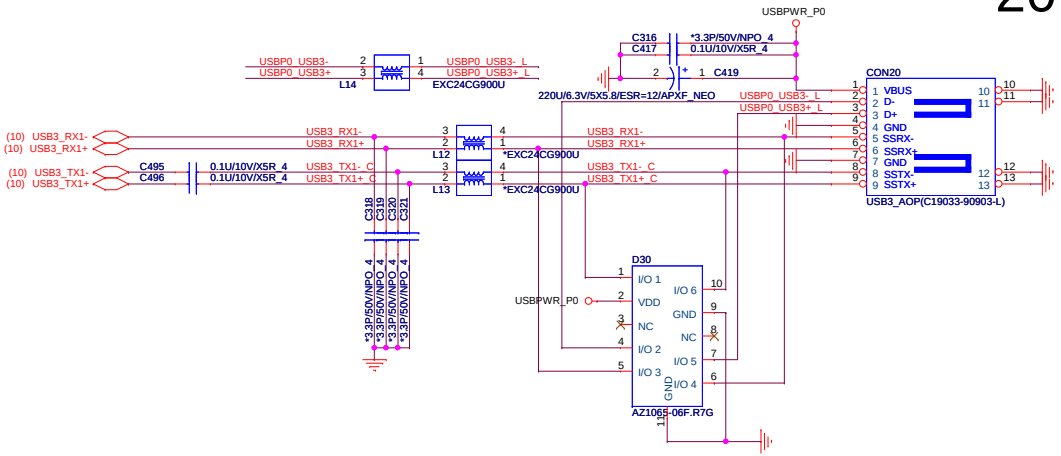
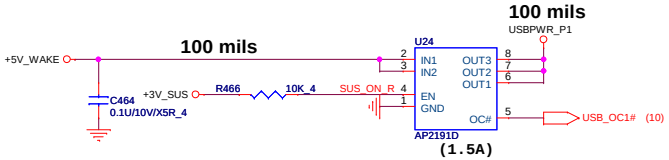
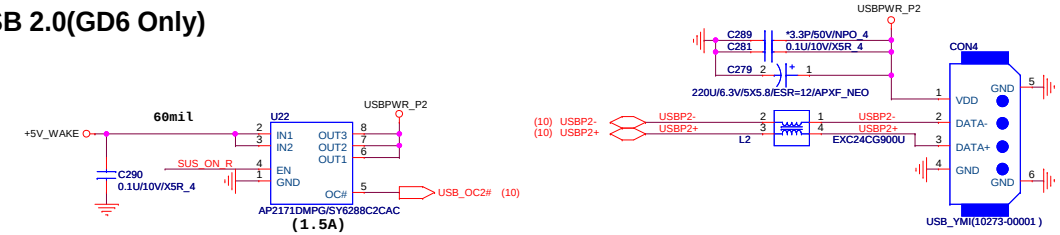


USB 3.0 PORT0

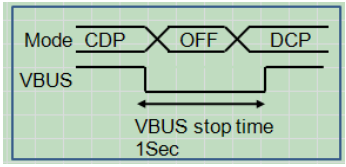




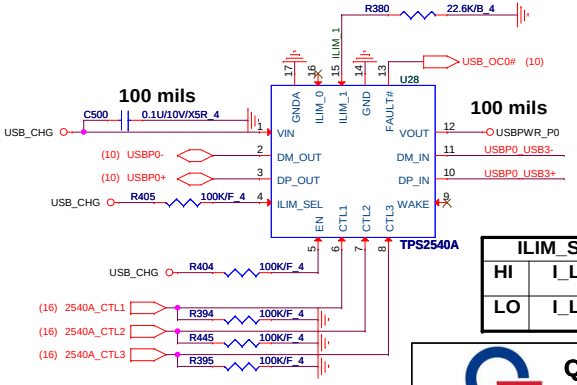
USB 2.0(GD6 Only)



SDP : Standard Downstream Port
CDP : Charging downstream port
DCP : Dedicated Charging Port
Enable/Disable : setting by BIOS



USB Charger



ILIM_SEL	(I LIMIT(A)= 48000/R)	
HI	I_LIM_1	
LO	I_LIM_0	48000/22.6K=2.123A

CTL_1	CTL_2	CTL_3	TPS 2540A/2543 Truth Table
0	0	0	OUT discharge, power switch OFF
0	X	1	DCP,Auto-detect(S3/S4/S5, 1.5A)
X	1	0	SDP, USB2.0 mode(S0, 0.5A)
1	0	0	DCP, BC SPEC1.2 only(S3/Deep standby/S4/S5, 1.5A)
1	0	1	DCP, Divider mode only(S3/S4/S5, 1.5A)
1	1	1	CDP (S0, 1.5A)

System State	USB Battery Charging Setting			
	Disable	C(1 2 3)	Enable	C(1 2 3)
S0	SDP	(X 1 0)	CDP	(1 1 1)
S3	SDP	(X 1 0)	DCP BC	(1 0 0)
DS3	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S4	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S5	Charger OFF	(0 0 0)	DCP BC	(1 0 0)

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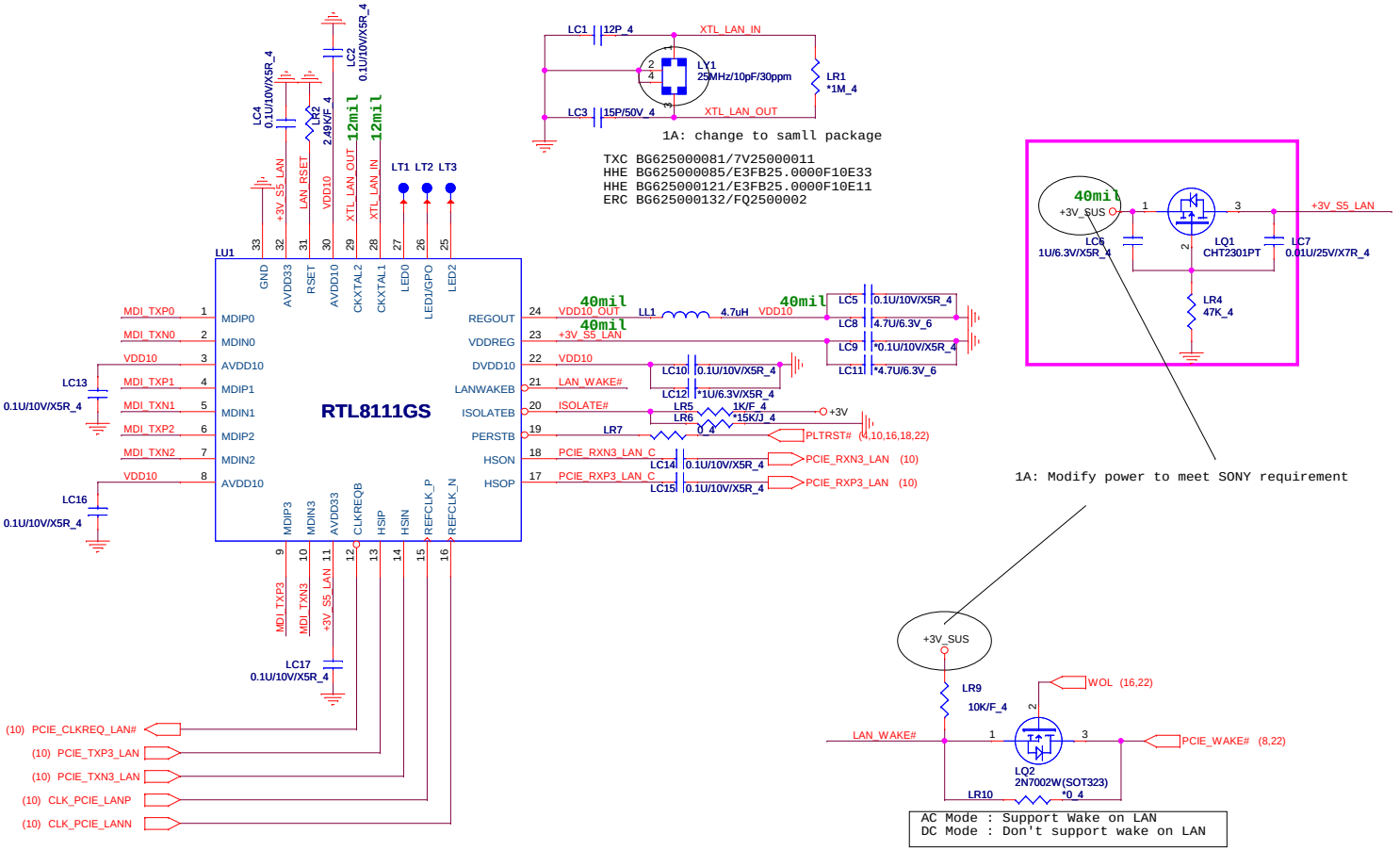
Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	USB/USB Charger	1A
Date: Thursday, October 25, 2012	Sheet 20 of 41	

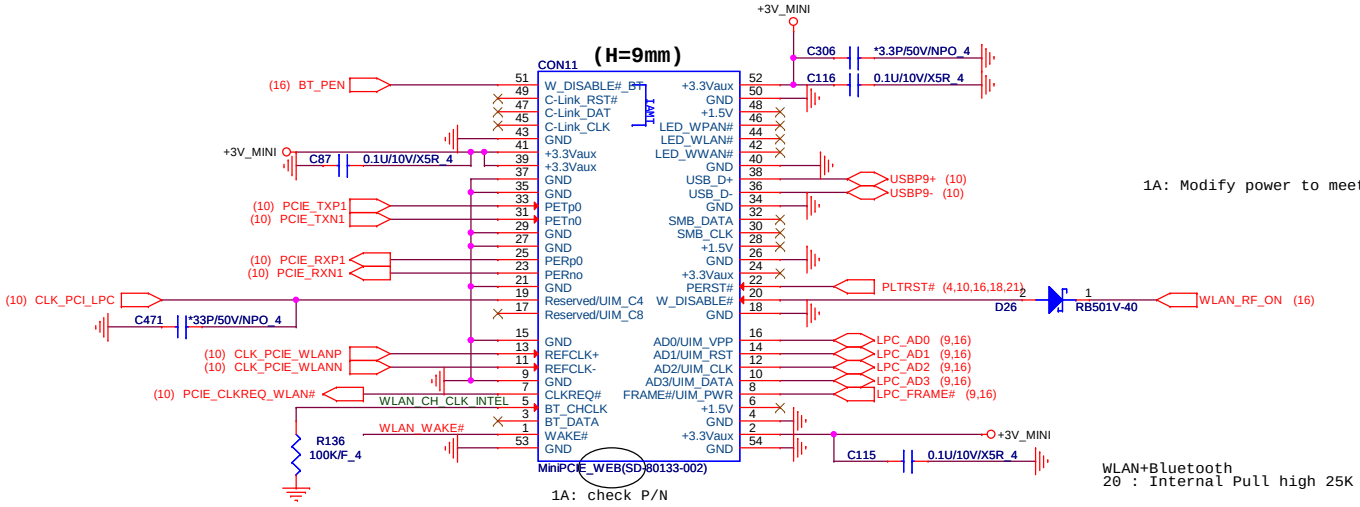


1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

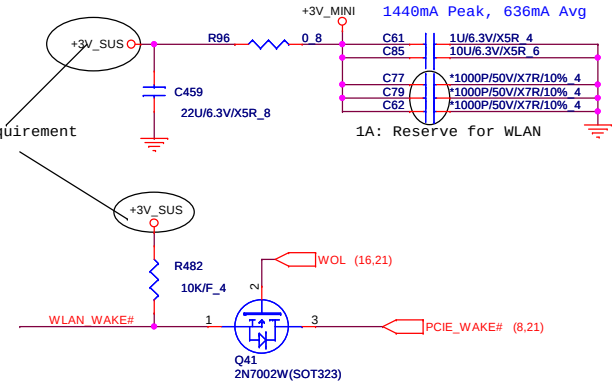
Size	Document Number	Rev
	Giga LAN RTL8111GS	1A
Date: Thursday, October 25, 2012	Sheet 21 of 41	

WLAN/WIMAX/WIDI



1A: check P/N

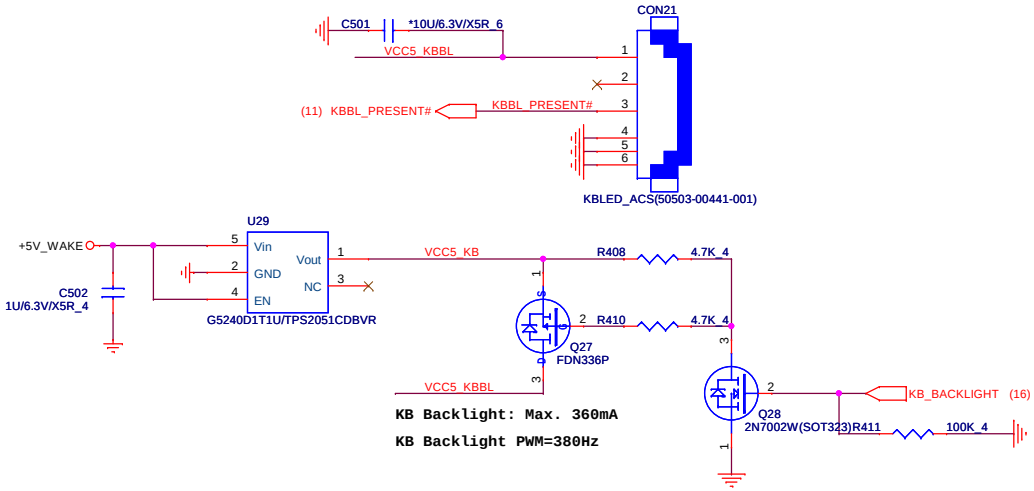
1A: Modify power to meet SONY requirement



WLAN+Bluetooth
20 : Internal Pull high 25K ~ 58K

AC Mode : Support Wake on WLAN
DC Mode : Don't support wake on WLAN

KB BACKLIGHT



KB Backlight: Max. 360mA

KB Backlight PWM=380Hz

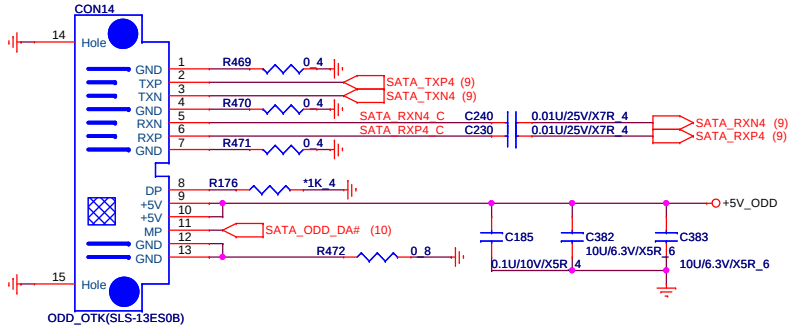
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

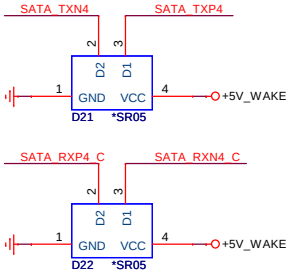
Quanta Computer Inc.
PROJECT : GD5

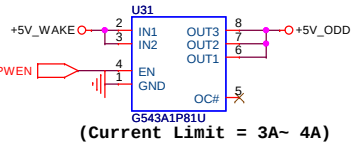
Size	Document Number	Rev
	WLAN/KB BL	1A

Date: Thursday, October 25, 2012 Sheet 22 of 41

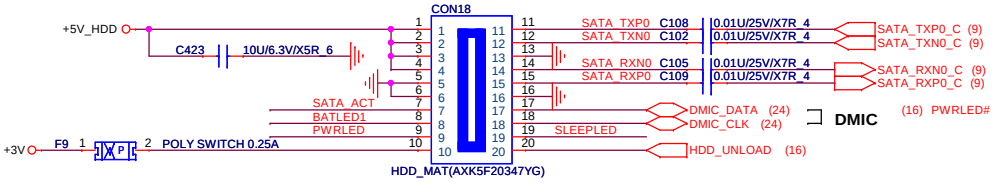
ODD CONNECTOR



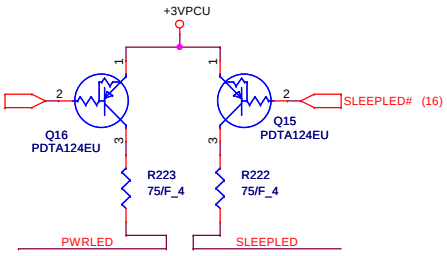


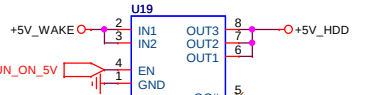


HDD BOARD CONNECTOR

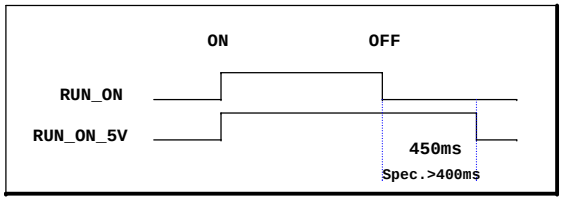


Power/Sleep LED

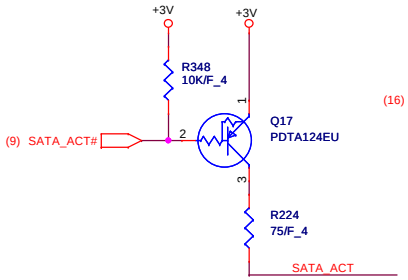




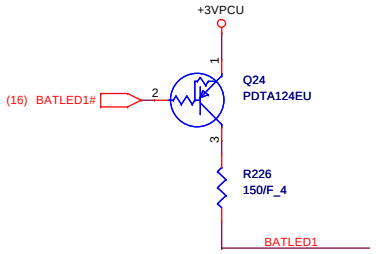
(2.5"HDD VCC5 Max=1.5A, AVG=0.6A)
(2.5"SSD VCC5 typ=0.8A,Max=1.54A)

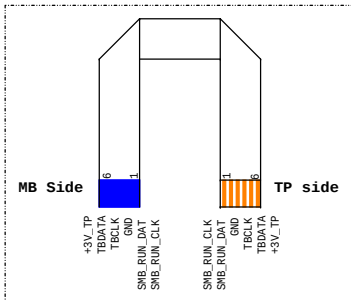


SATA LED

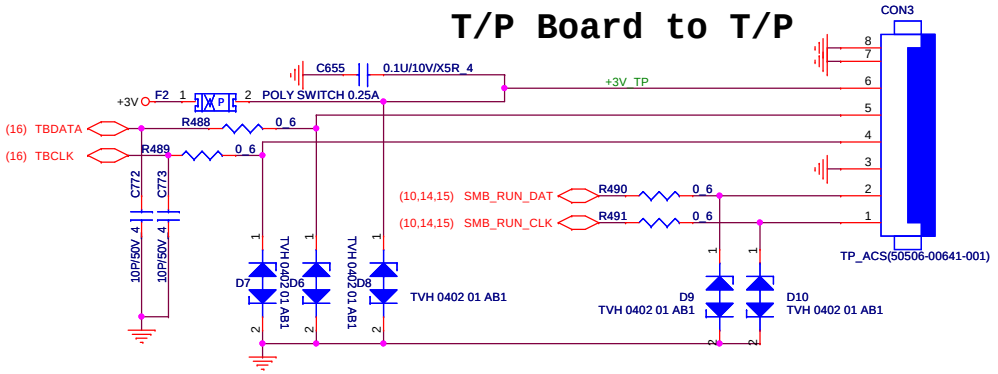


BATTERY LED

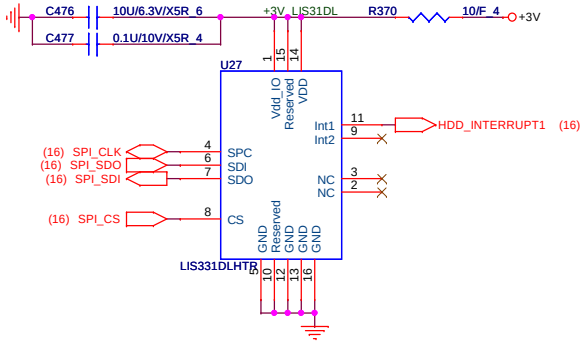




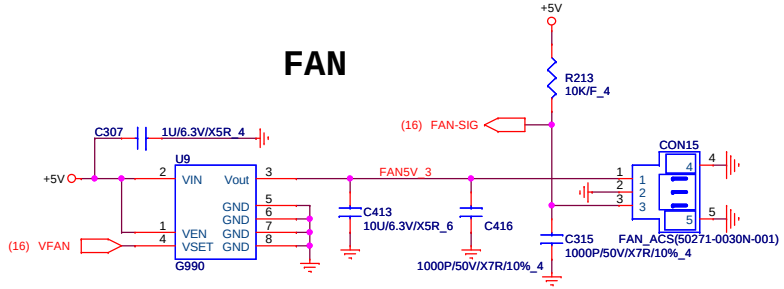
T/P Board to T/P



HDD PROTECT



FAN



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	HDD/ODD/TP/FAN	1A

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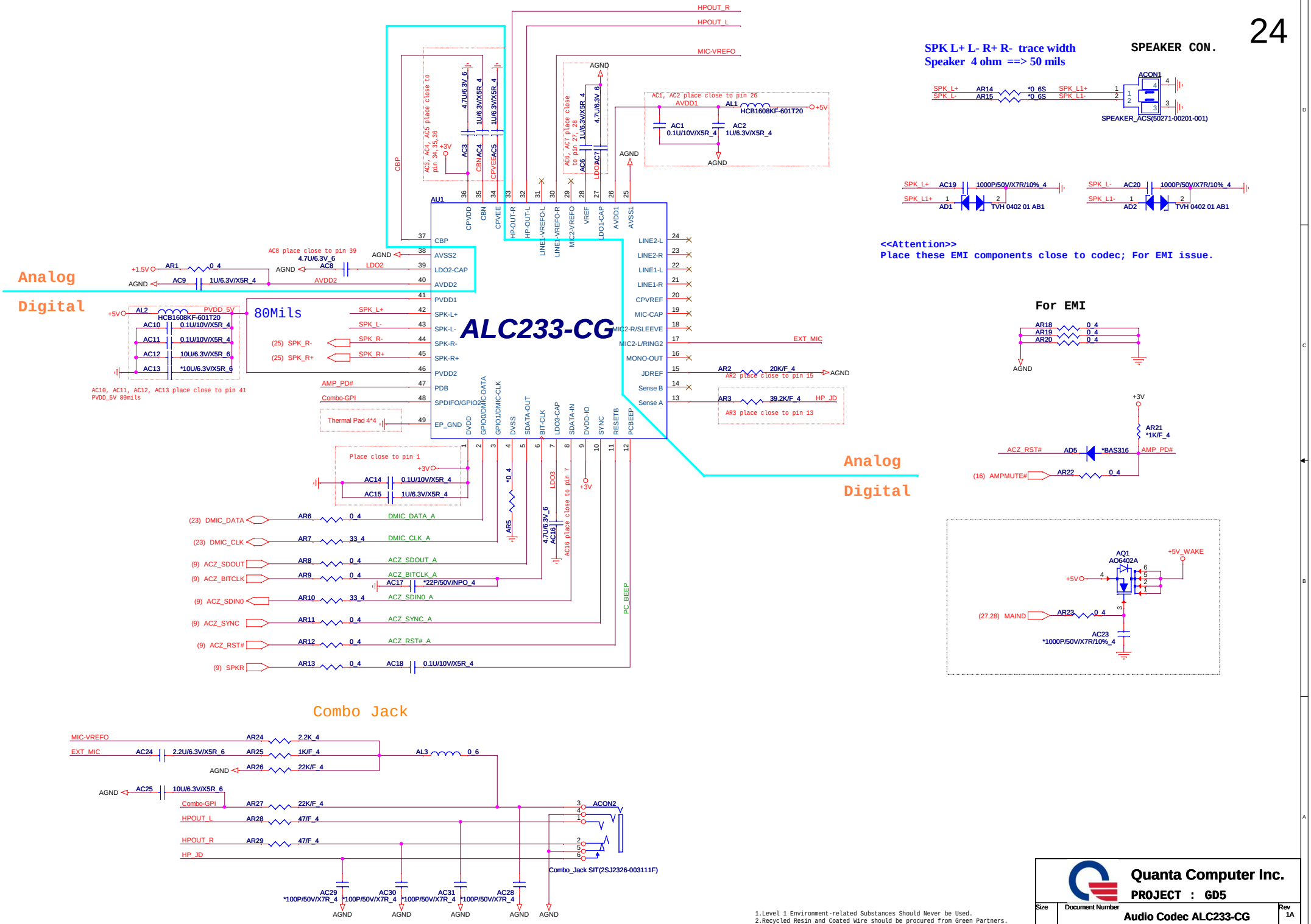

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	Audio Codec ALC233-CG	1A
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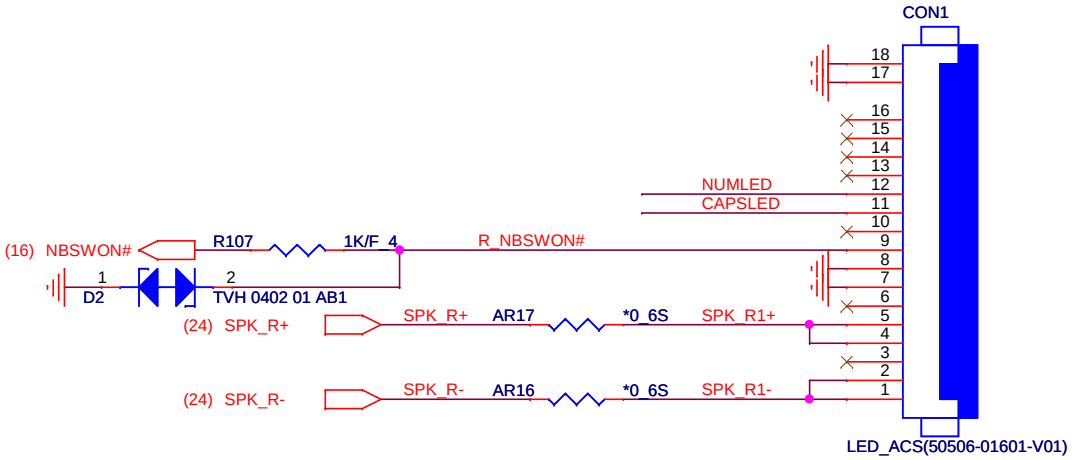
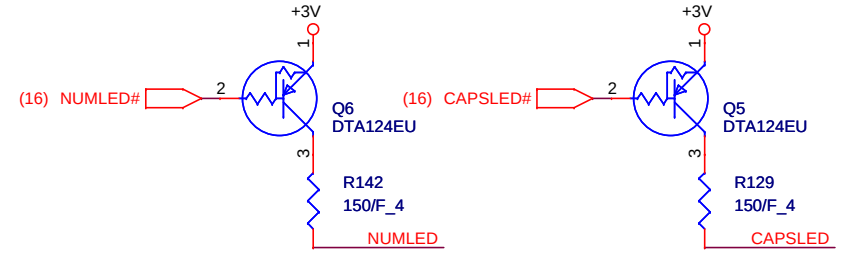
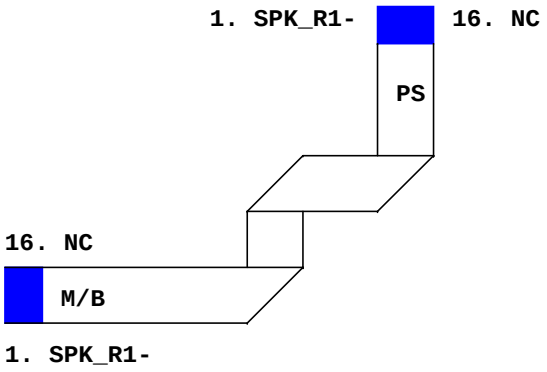
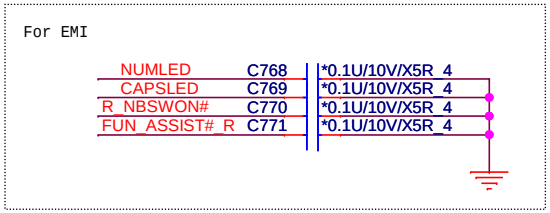
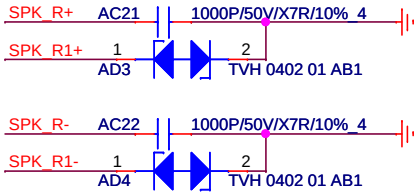
Power SW Board Connector











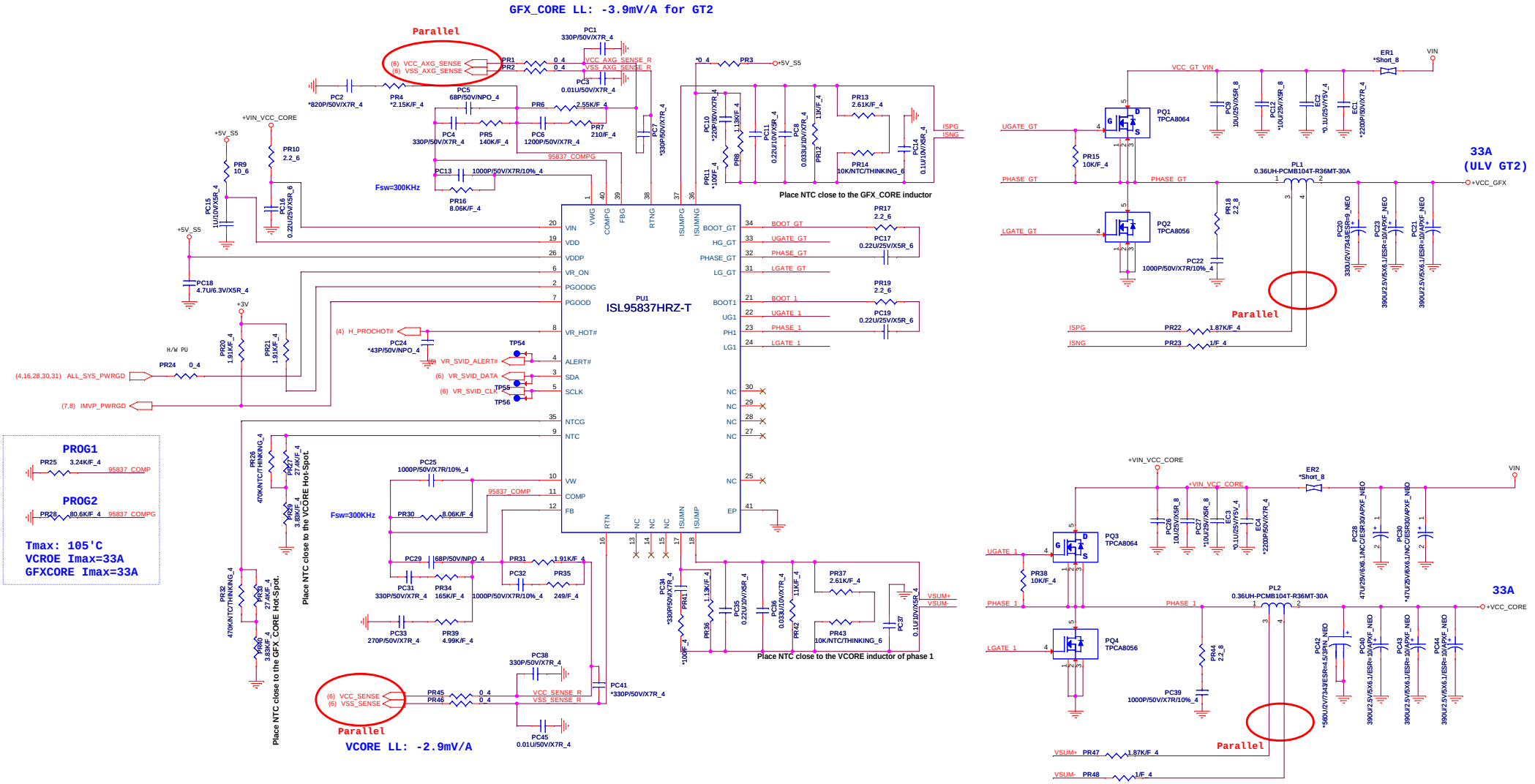
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.

PROJECT :GD5

Size	Document Number	Rev
	LED/PS	1A

Date: Thursday, October 25, 2012 Sheet 25 of 41



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	+VCC_CORE (ISL95837)	ES1

Date: Thursday, October 25, 2012 Sheet 26 of 41

3.3V & 5V

Fsw setting=400KHz

Fsw setting=500KHz

0.6A

7.5A

5A

2.0A

1.2A

1.5A

10mil

+3V_SUS for DS3

For EuP Lot 6 (2nd stage)

Quanta Computer Inc.
PROJECT : GD5

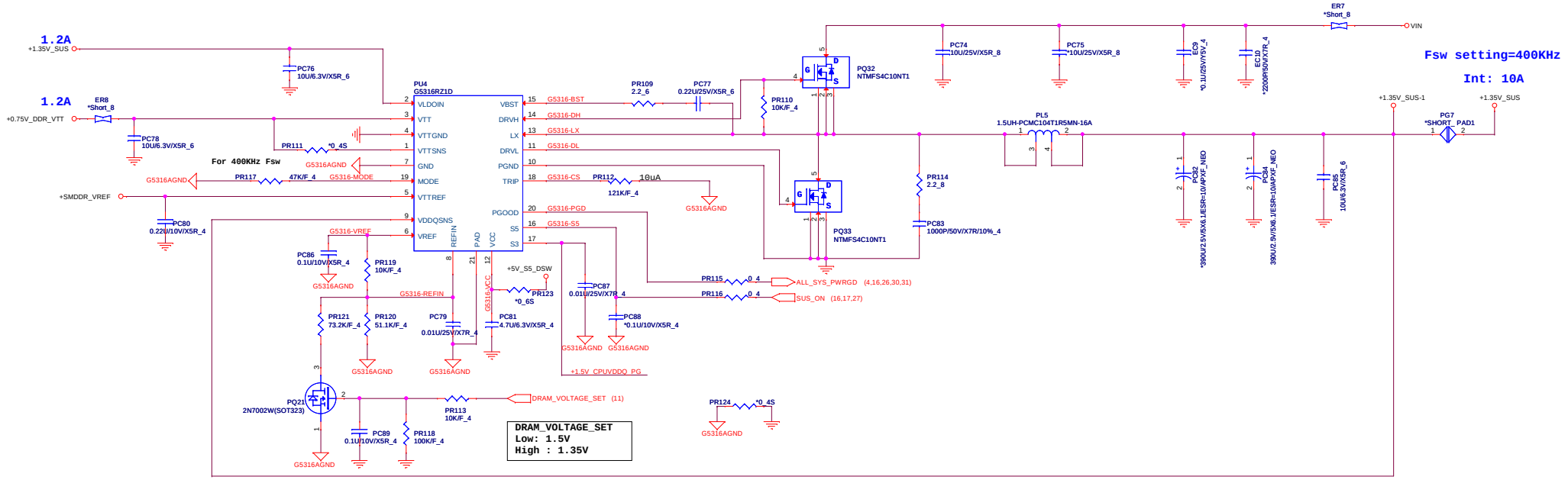
3VPCU&5VPCU(TPS51427A)

Size | Document Number | Rev 1A

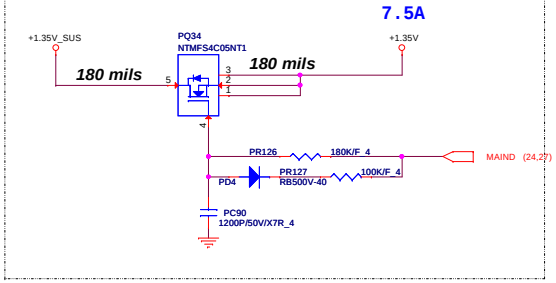
Date: Thursday, October 25, 2012 | Sheet 27 of 41

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

1.35VSUS & VTT_MEM

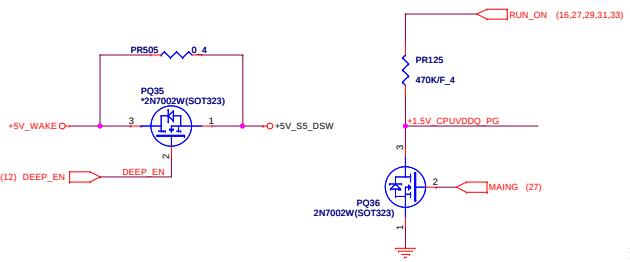


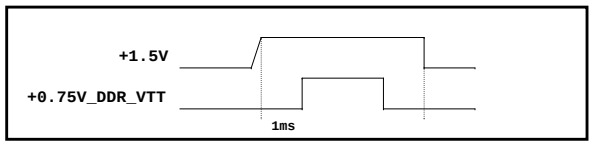
+1.35V



MODE	Resistor on Mode	Fsw	Discharge Mode
3	200Kohm	400KHz	Tracking discharge
2	100Kohm	300KHz	
1	68Kohm	300KHz	**Non-tracking discharge**
0	**47Kohm**	**400KHz**	

STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	On	On	On
S3	0	1	On	On	Off/High Z
S4/S5	0	0	Off	Off	Off





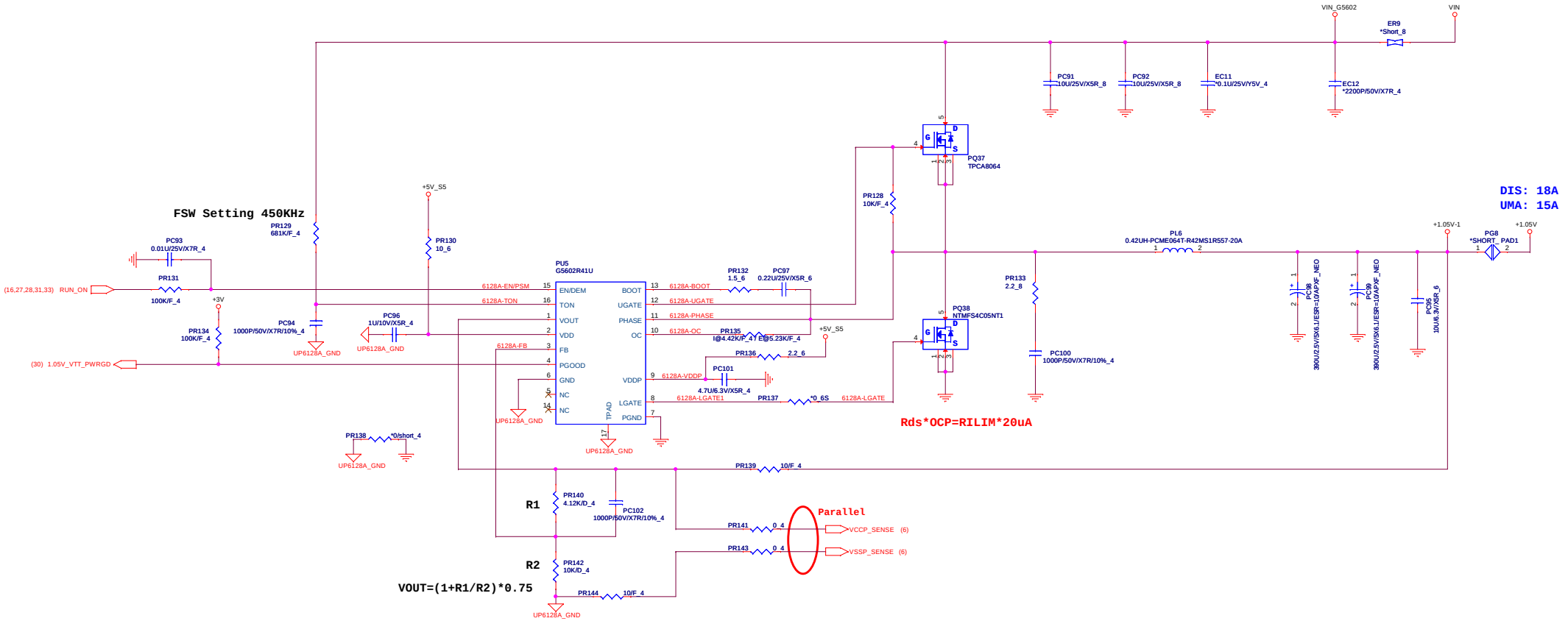
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	1.5VSUS/VTT_MEM	1A

Date: Thursday, October 25, 2012 Sheet 28 of 41

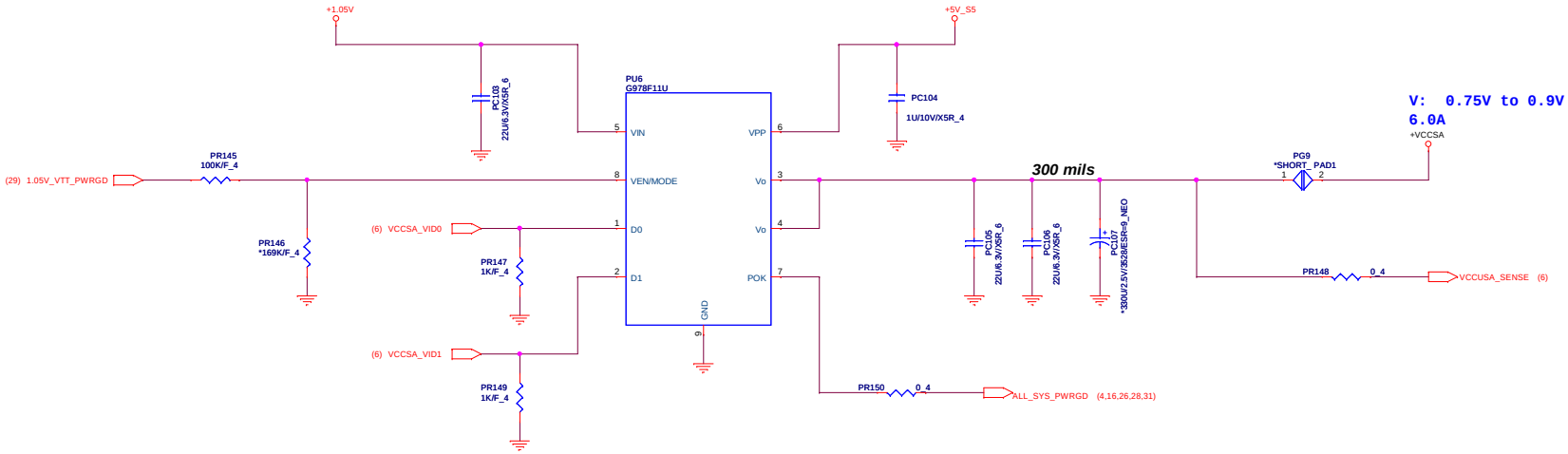
+1.05V / 15.0A



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.		
PROJECT : GD5		
Size	Document Number	Rev
	+1.05V (UP1522RQDD)-15A	1A
Date: Thursday, October 25, 2012	Sheet 29 of 41	

+VCCSA

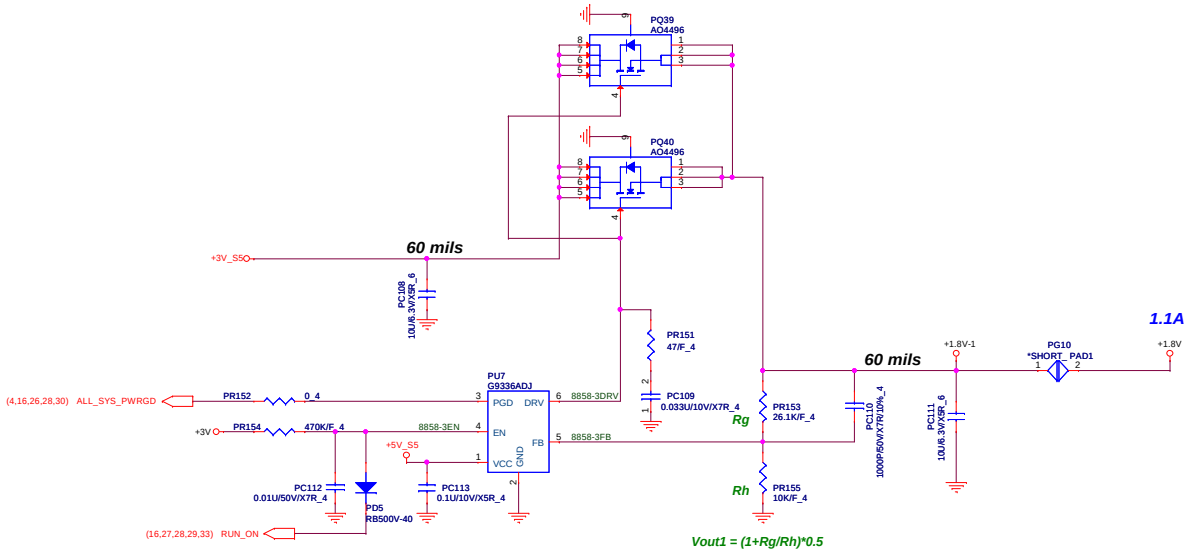


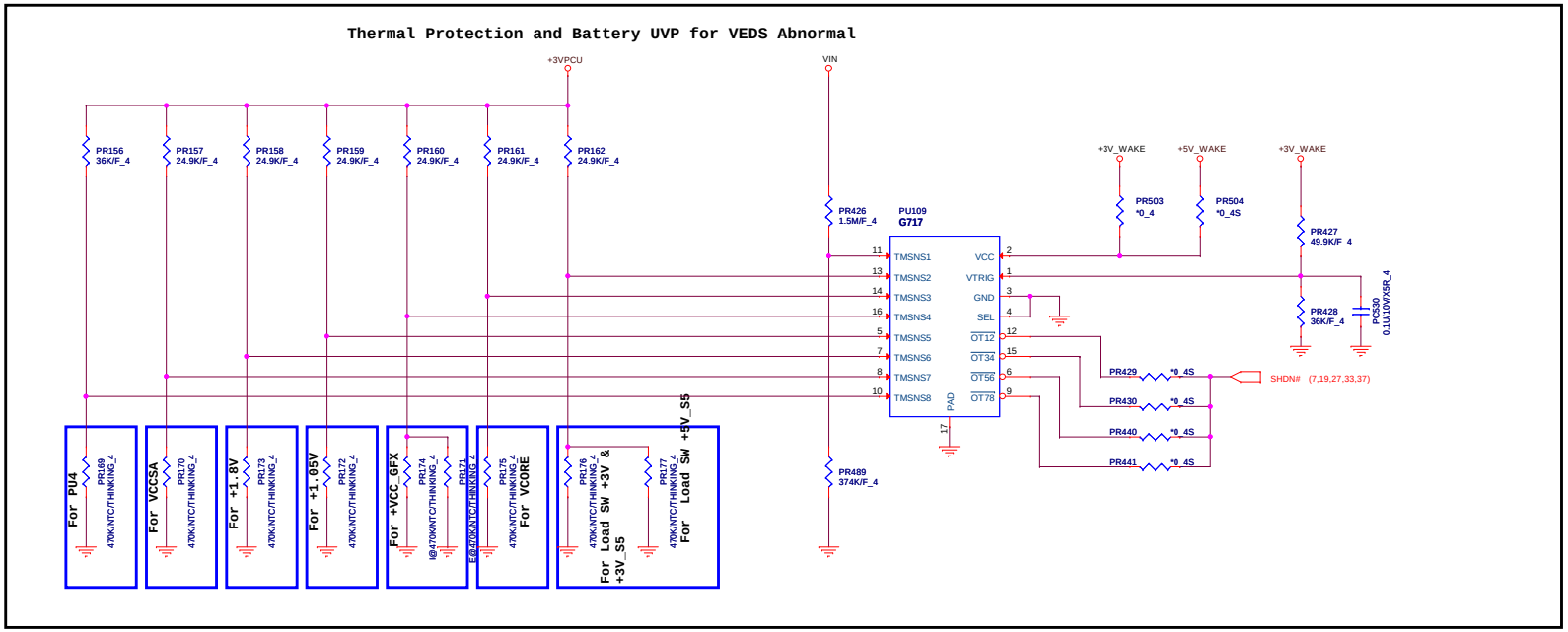
For Chief River ULV

VCCSA_VID0	VCCSA_VID1	+VCCSA
0	0	0.9V
0	1	0.85V
1	0	0.775V
1	1	0.75V

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

VCC1.8

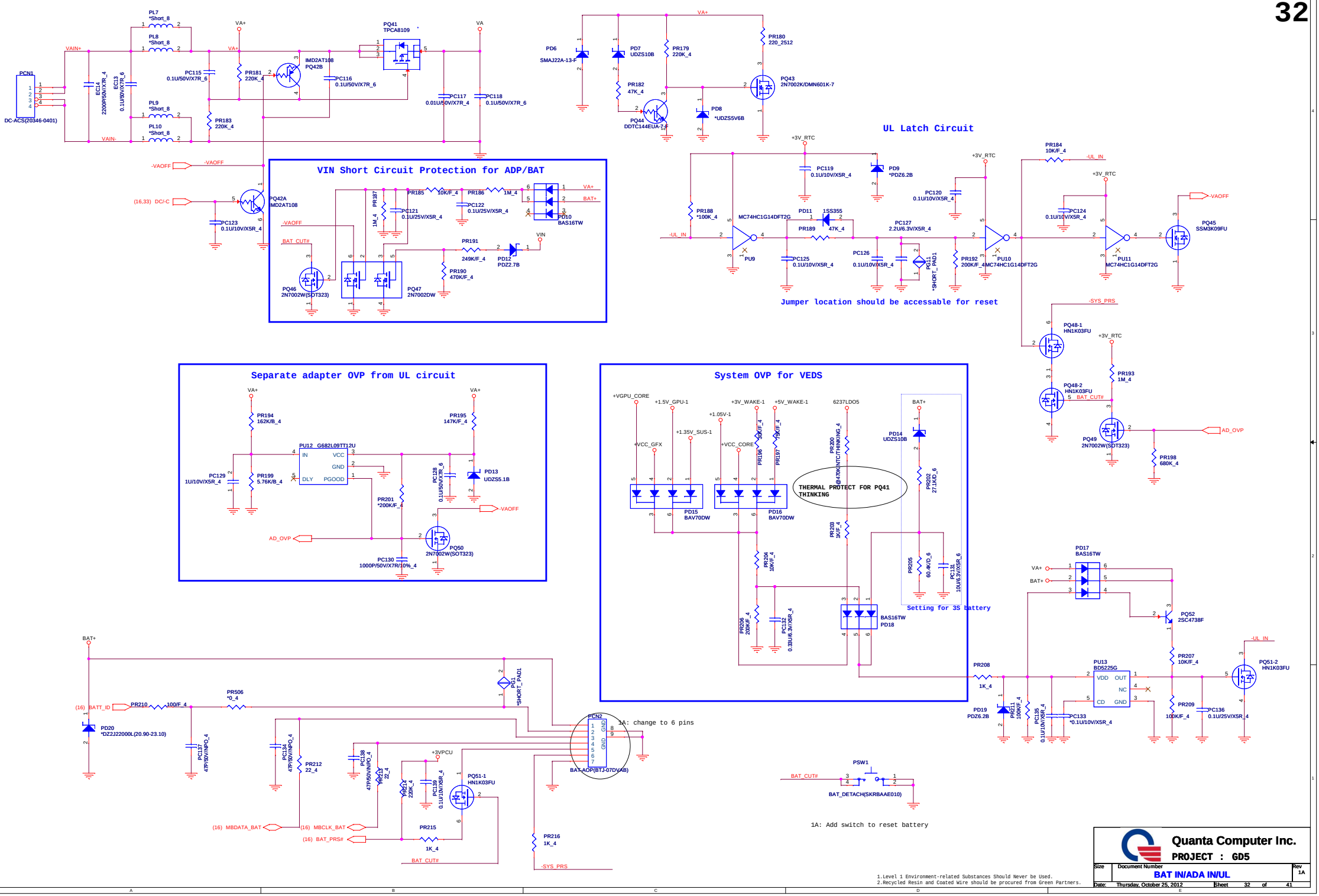


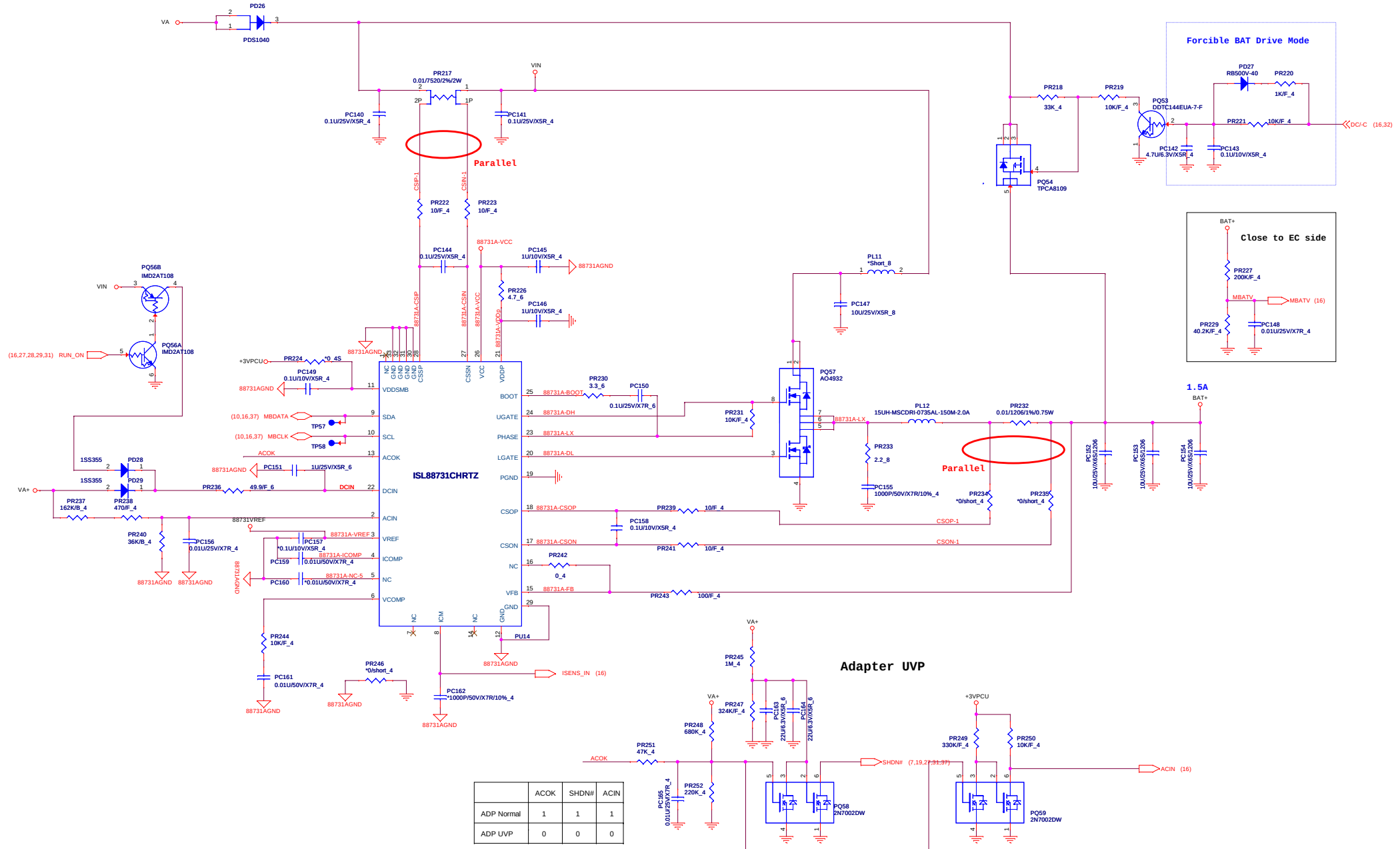


1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	VCC1.8	1A
Date: Thursday, October 25, 2012	Sheet 31 of 41	


VIN Short Circuit Protection for ADP/BAT
UL Latch Circuit
Jumper location should be accessable for reset
Separate adapter OVP from UL circuit
System OVP for VEDS
THERMAL PROTECT FOR PQ41 THINKING
Setting for 3S battery
1A: change to 6 pins
1A: Add switch to reset battery
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.
Quanta Computer Inc.
PROJECT : GD5
BAT IN/ADA IN/UL
Size
Document Number
Rev
1A
Date: Thursday, October 25, 2012
Sheet 32 of 41



	ACOK	SHDN#	ACIN
ADP Normal	1	1	1
ADP UVP	0	0	0

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	CHARGER(ISL88731C)	1A

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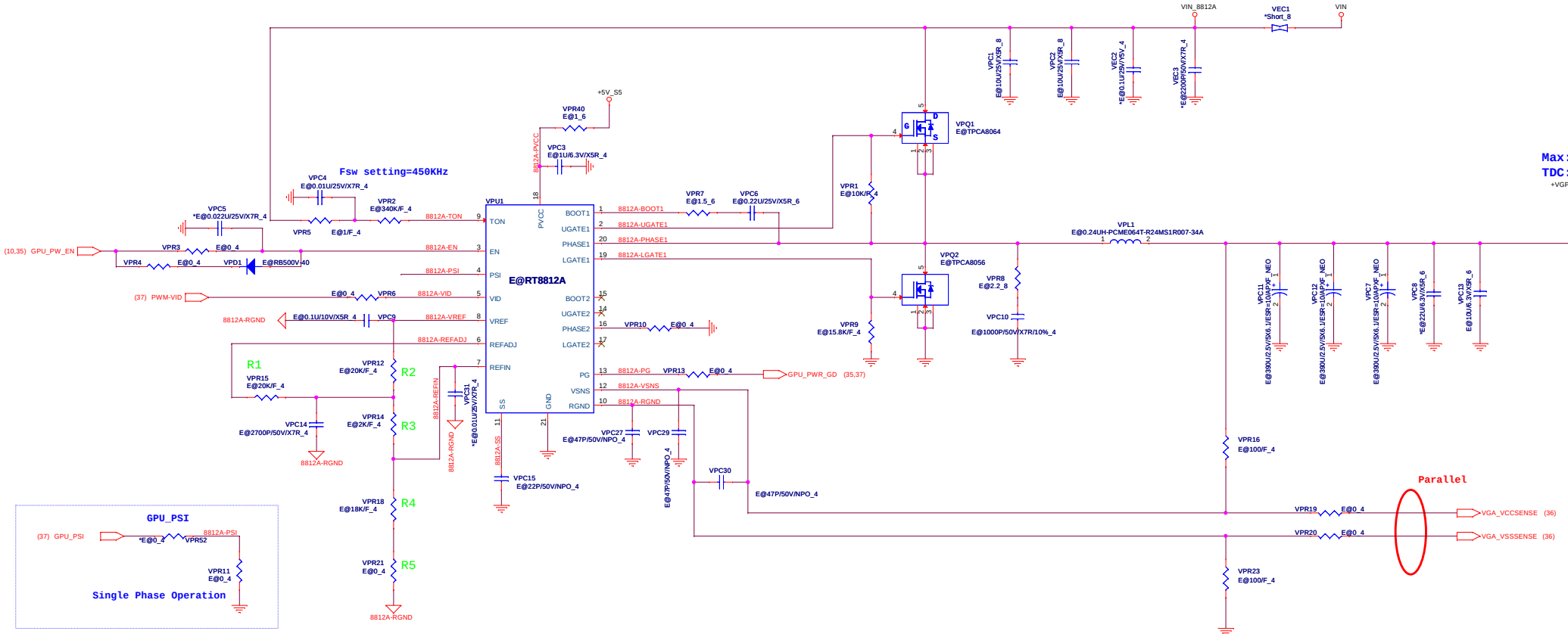
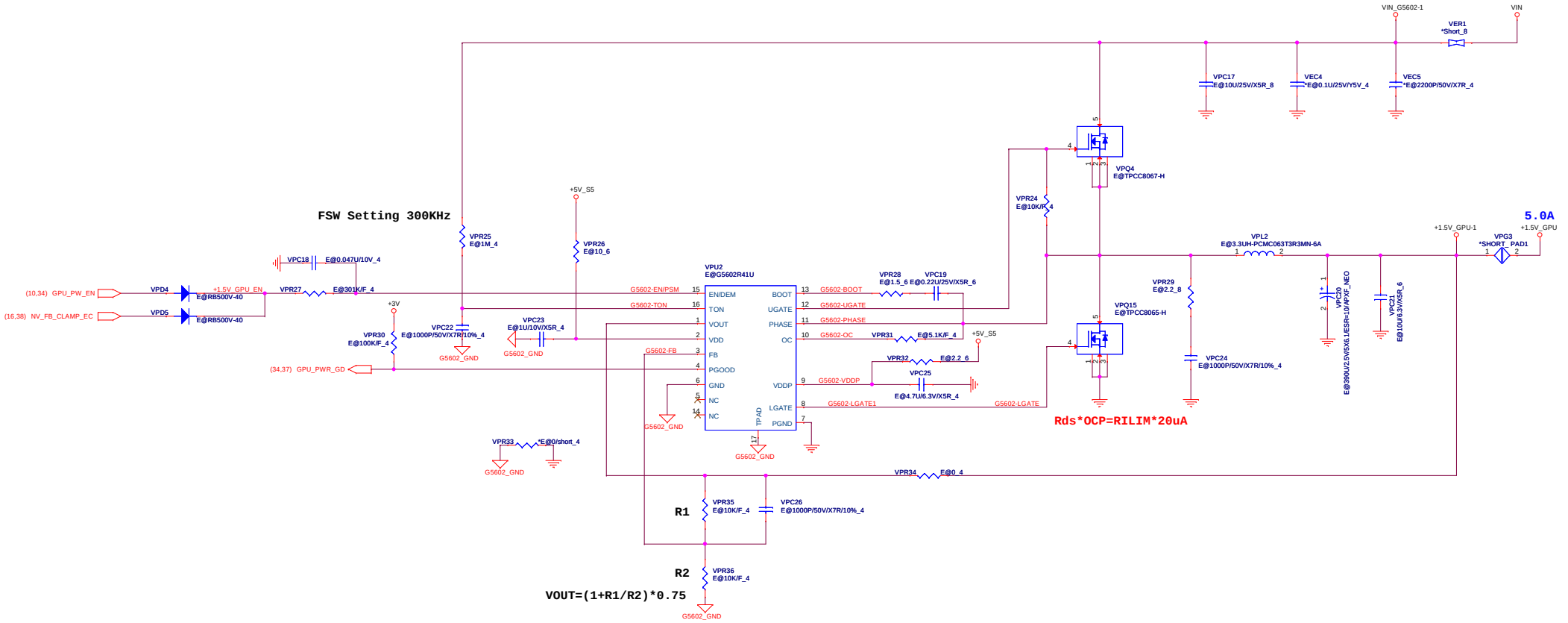
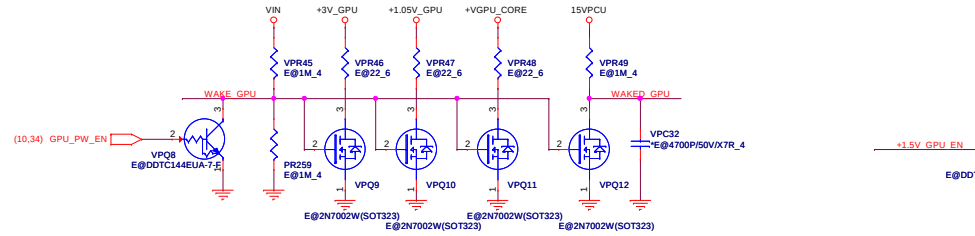
VGA-CORE

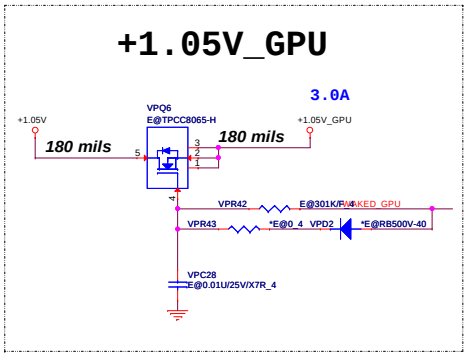


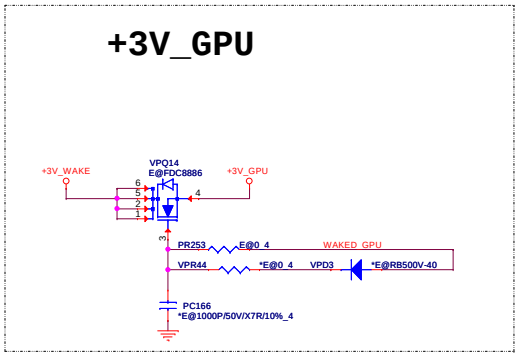
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

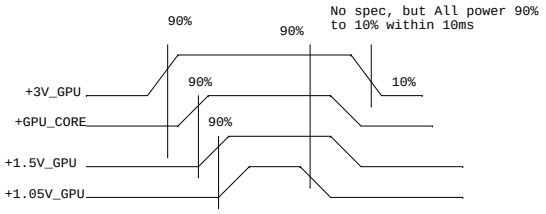
Quanta Computer Inc.		
PROJECT : GD5		
Size	Document Number	Rev
	VGA_CORE (RT8812A)	1A
Date: Thursday, October 25, 2012	Sheet 34 of 41	







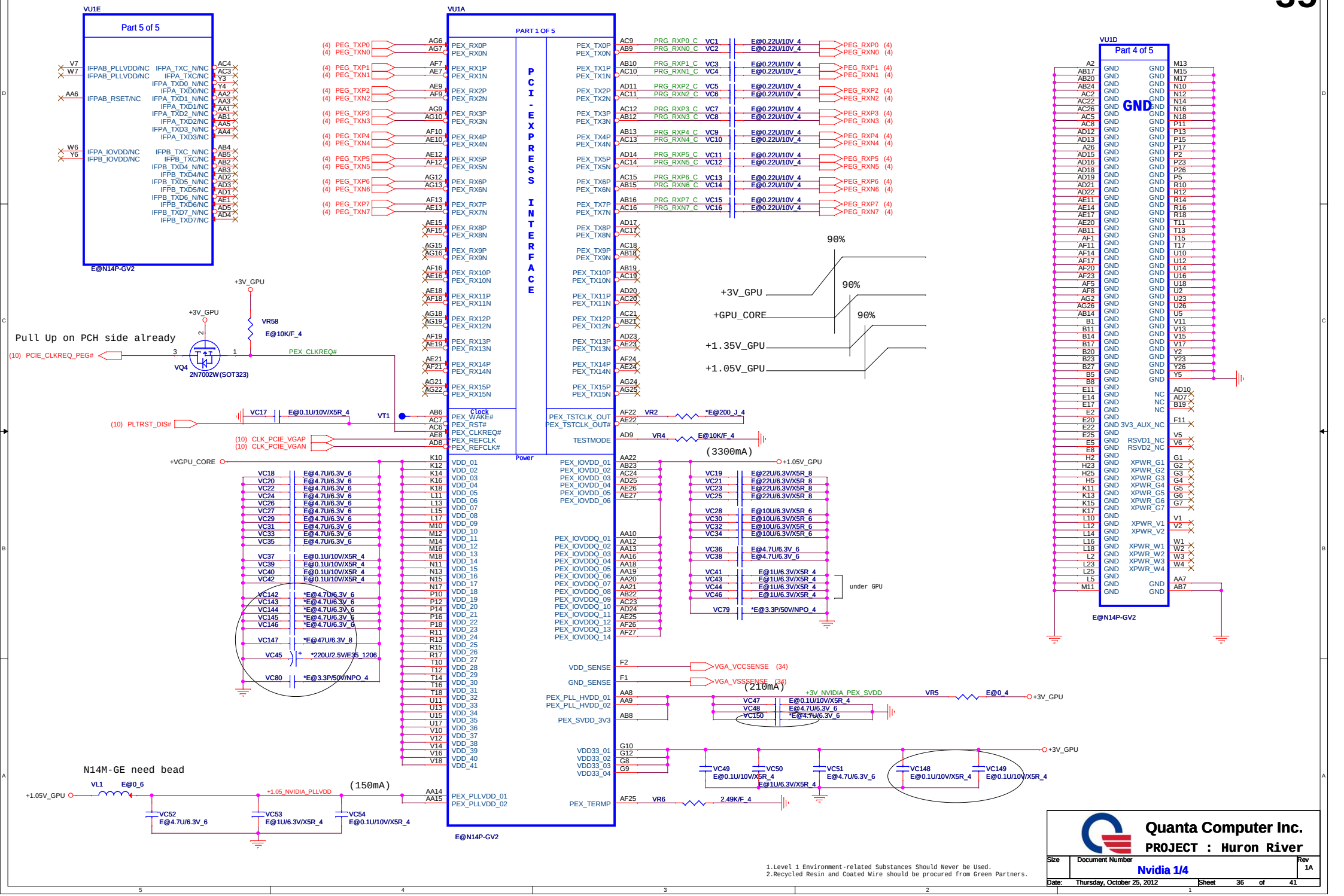


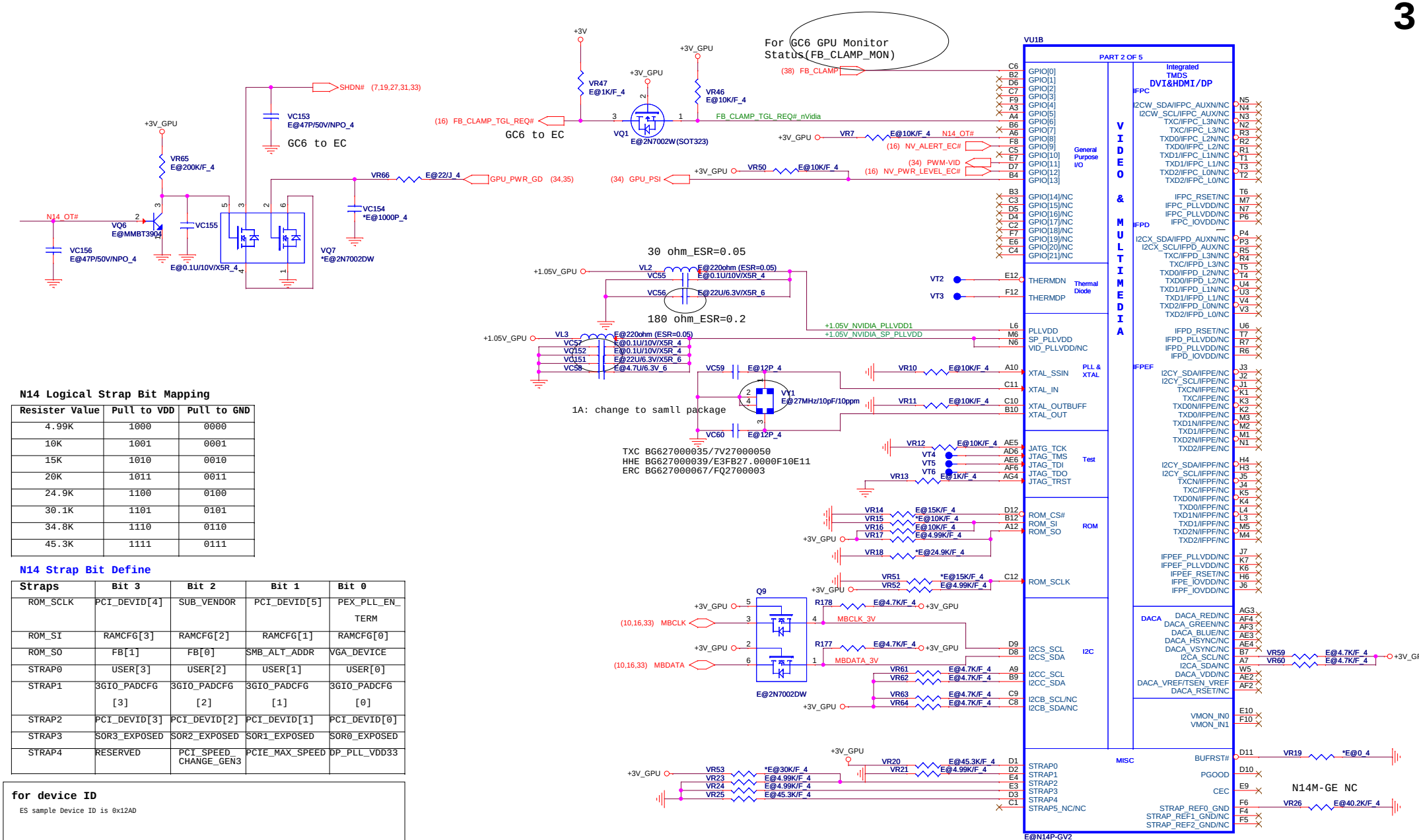


1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	1.8_GPU / 1.0_GPU	1A
Date: Thursday, October 25, 2012	Sheet 35 of 41	


VU1E
Part 5 of 5
E@N14P-GV2
VU1A
PART 1 OF 5
PCI-EXPRESS INTERFACE
Clock
Power
E@N14P-GV2
VU1D
Part 4 of 5
GND
E@N14P-GV2
Pull Up on PCH side already
2N7002W(SOT323)
N14M-GE need bead
(3300mA)
(210mA)
(150mA)
under GPU
90%
+3V_GPU
+GPU_CORE
+1.35V_GPU
+1.05V_GPU
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.
Quanta Computer Inc.
PROJECT : Huron River
Nvidia 1/4
Size
Document Number
Rev
1A
Date: Thursday, October 25, 2012
Sheet 36 of 41



N14 Logical Strap Bit Mapping

Resister Value	Pull to VDD	Pull to GND
4.99K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
24.9K	1100	0100
30.1K	1101	0101
34.8K	1110	0110
45.3K	1111	0111

N14 Strap Bit Define

Straps	Bit 3	Bit 2	Bit 1	Bit 0
ROM_SCLK	PCI_DEVID[4]	SUB_VENDOR	PCI_DEVID[5]	PEX_PLL_EN_TERM
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
ROM_SO	FB[1]	FB[0]	SMB_ALT_ADDR	VGA_DEVICE
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]
STRAP1	3GIO_PADCFG [3]	3GIO_PADCFG [2]	3GIO_PADCFG [1]	3GIO_PADCFG [0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP3	SOR3_EXPOSED	SOR2_EXPOSED	SOR1_EXPOSED	SOR0_EXPOSED
STRAP4	RESERVED	PCI_SPEED_CHANGE_GEN3	PCIE_MAX_SPEED	DP_PLL_VDD33

for device ID

ES sample Device ID is 0x12AD

	VRAM Capacity	VRAM Vender	ID	VR35	P/N	P/N
N14M-GS/LP	128Mx16 DDR3	Samsung	0111	PD45.3K	K4W2G1646E-BC1A	
		Micron	0101	PD30.1K	MT41J128M16JT-093G:K	
	256Mx16 DDR3	Samsung	0011	PD20K	K4W4G1646B-HC11	
		Micron	0001	PD10K	MT41K256M16HA-107G:E	

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : Huron River

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	Nvidia 2/4	1A

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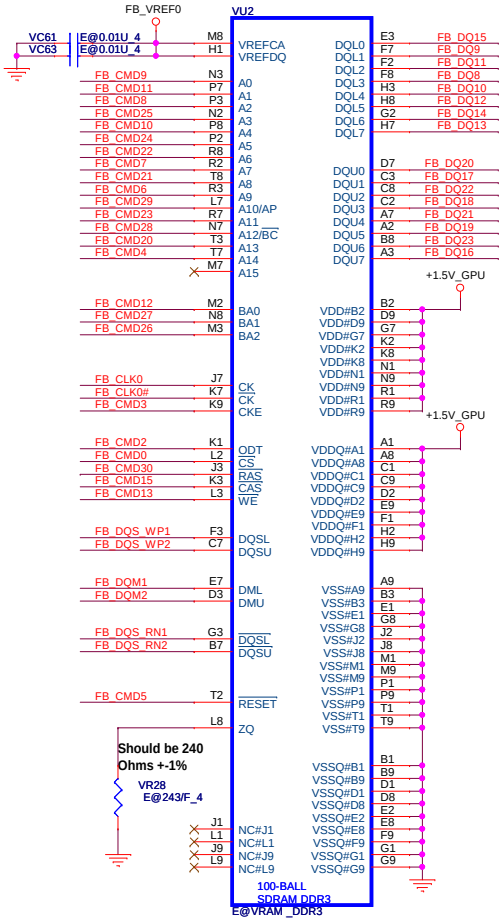
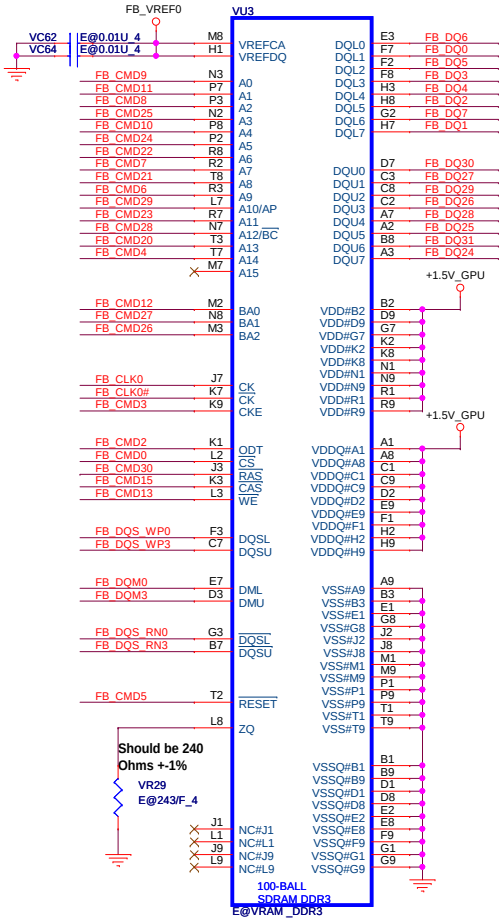
Down Side VRAM TOP/BOT

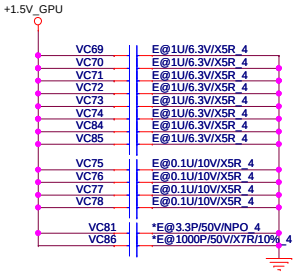
TOP



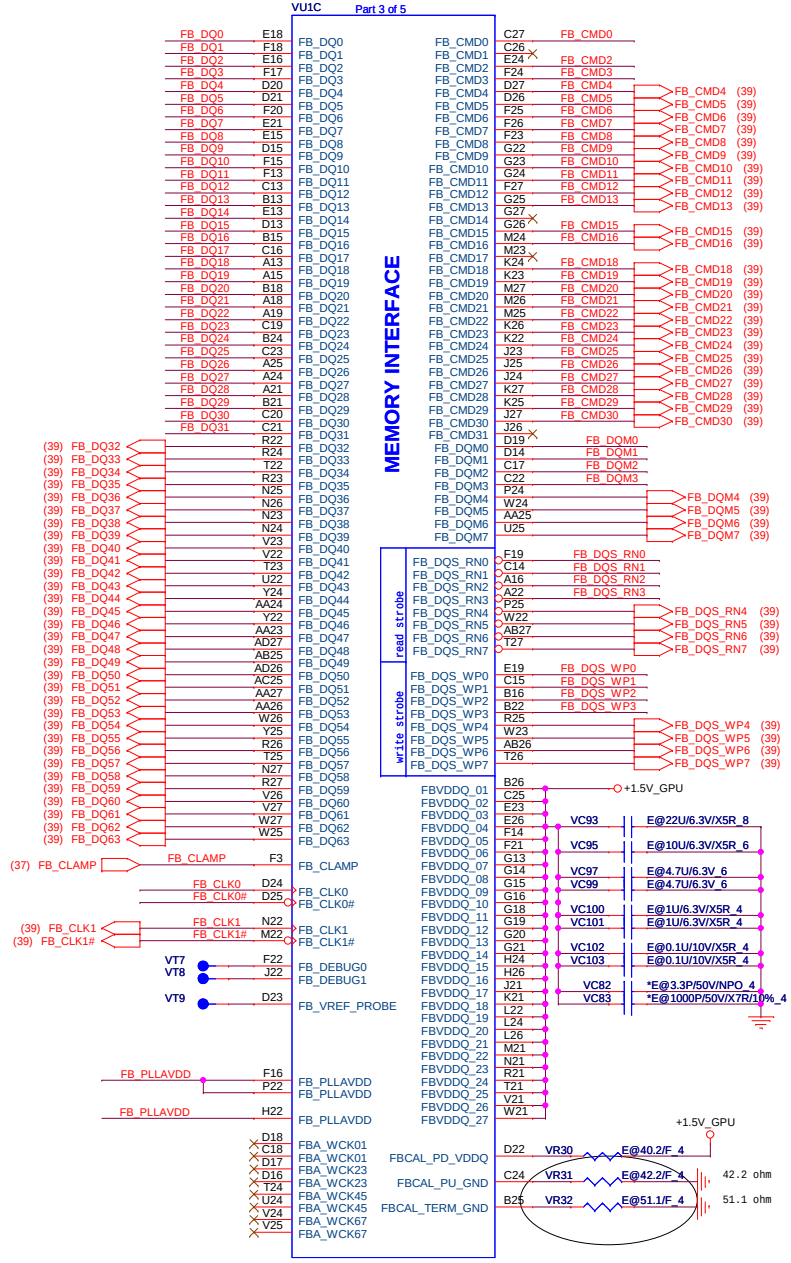
BOT



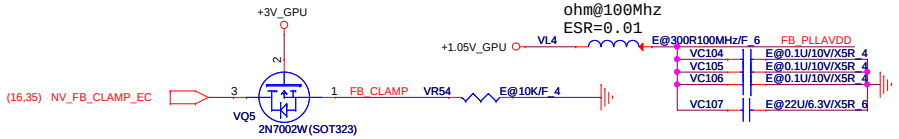
Place around VU2 and VU3

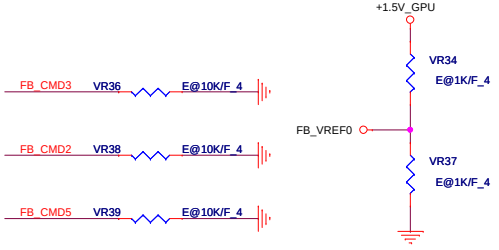


VU1C Part 3 of 5 — MEMORY INTERFACE



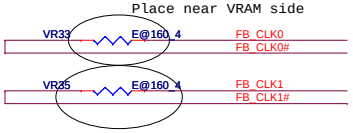
need find Bead 30 ohm@100Mhz ESR=0.01





1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Place near VRAM side



Quanta Computer Inc.
PROJECT : Huron River

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	VRAM 3/4	1A
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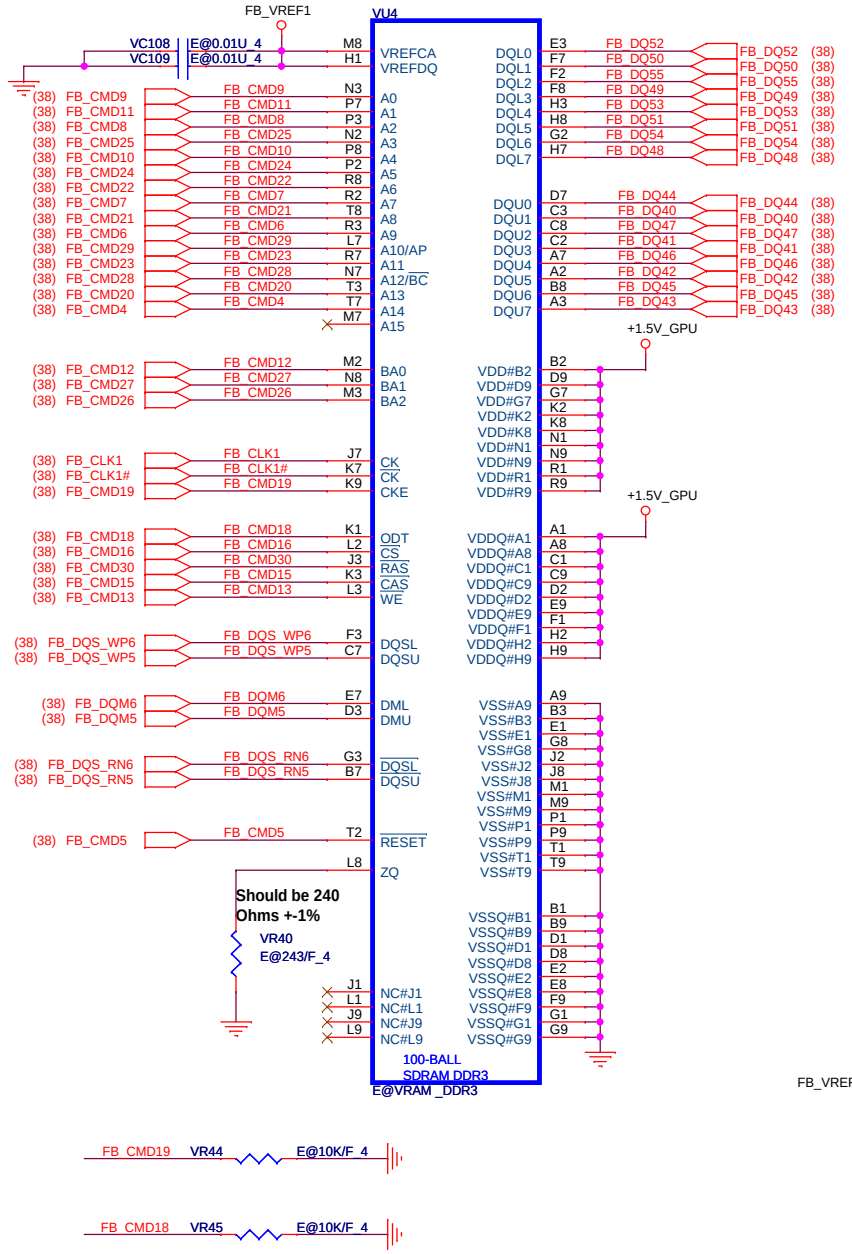
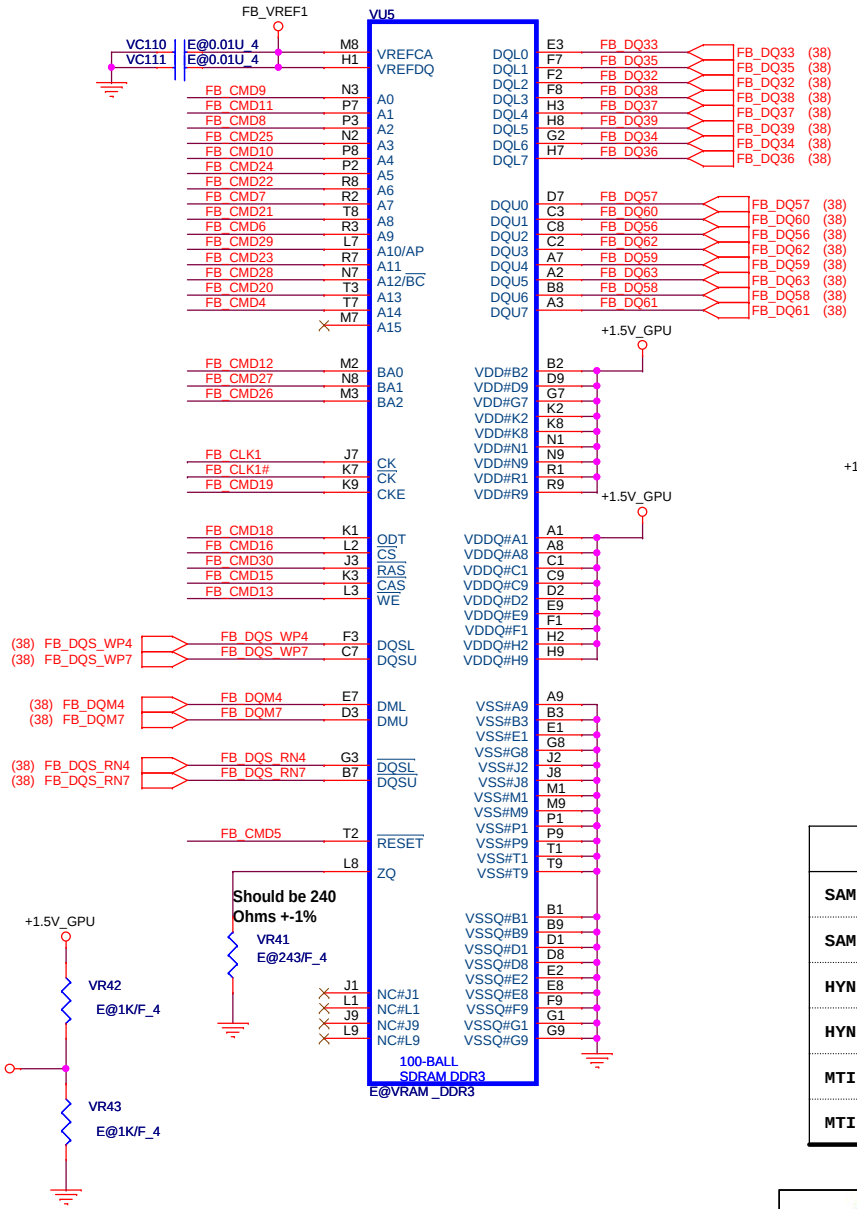
Up Side VRAM TOP/BOT

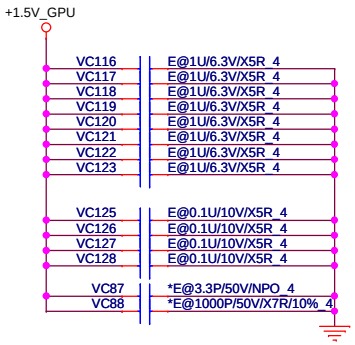
TOP



BOT



Place around VU4 and VU5



	P/N	Vendor P/N
SAM 2G	AKD5MGGT525	K4W2G1646E-BC11
SAM 4G	AKD5MGSTL14	K4W4G1646B-HC11
HYN 2G		
HYN 4G		
MTI 2G	AKD5MGWT525	MT41J128M16JT-107G:K
MTI 4G	AKD5PGSTL07	MT41K256M16HA-107G:E

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Quanta Computer Inc.
PROJECT : Huron River

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	VRAM 4/4	1A

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GRAPHIC NUT

H4 H-C217D118P2

H5 H-C217D118P2

PCH NUT

H6 H-C217D118P2

WALN NUT

H1 h-tc217bc182d142p2

H2 h-tc217bc182d142p2

HDD/B NUT

H7 h-c217d146p2

H3 *H-GD5-PAD6

H9 *HG-GD5-PAD1

H10 *HG-GD5-PAD5

H11 *H-R315I139D99P2

H12 *HG-GD5-PAD2

H13 *HG-GD5-PAD3

H14 *HG-GD5-PAD4

H16 *H-C315D99P2

H23 *SPAD-RE320X700NP

H24 *H-TC236BO99X110D99X110P2

H25 *H-C236D99X110P2

H27 *H-R315X315D99P2

H28 *H-R315X315D99P2

H29 *H-R315X315D99P2

H30 *H-R315X315D99P2

H31 *H-C236D99P2

H32 *H-C276D276N

H33 *H-C276D276N

H34 *PAD-C177

H35 *PAD-C177

H36 *SPAD-RE31X181NP

H37 *SPAD-RE31X181NP

KEY BOARD Connector

CON2

(16) MY15 1
(16) MY14 2
(16) MY13 3
(16) MY12 4
(16) MY11 5
(16) MX7 6
(16) MY10 7
(16) MX6 8
(16) MX5 9
(16) MY9 10
(16) MX4 11
(16) MY8 12
(16) MX7 13
(16) MY7 14
(16) MY6 15
(16) MY5 16
(16) MY4 17
(16) MY3 18
(16) MX3 19
(16) MY2 20
(16) MY1 21
(16) MY0 22
(16) MX2 23
(16) MX1 24
(16) MX0 25
(16) MY17 26
27
28

KB_ACS(50503-02601-001)

MY15	C744	*220P_4
MY14	C745	*220P_4
MY13	C746	*220P_4
MY12	C747	*220P_4
MY11	C748	*220P_4
MX7	C749	*220P_4
MY10	C750	*220P_4
MX6	C751	*220P_4
MX5	C752	*220P_4
MY9	C753	*220P_4
MX4	C754	*220P_4
MY8	C755	*220P_4
MY7	C756	*220P_4
MY6	C757	*220P_4
MY5	C758	*220P_4
MY4	C759	*220P_4
MY3	C760	*220P_4
MX3	C761	*220P_4
MY2	C762	*220P_4
MY1	C763	*220P_4
MY0	C764	*220P_4
MX2	C765	*220P_4
MX1	C766	*220P_4
MX0	C767	*220P_4

+3VPCU

MX0	R163	10K_4
MX1	R162	10K_4
MX2	R159	10K_4
MX3	R156	10K_4
MX4	R154	10K_4
MX5	R151	10K_4
MX6	R147	10K_4
MX7	R143	10K_4

+VCC_GFX	C900	1000P/50V/X7R/10%_4 +VCC_CORE
VIN	C901	1000P/50V/X7R/10%_4 +1.05V
VIN	C902	1000P/50V/X7R/10%_4 +1.05V
VIN	C903	1000P/50V/X7R/10%_4 +1.05V
VIN	C904	1000P/50V/X7R/10%_4 +1.05V
VIN	C905	1000P/50V/X7R/10%_4 +1.05V
VIN	C906	1000P/50V/X7R/10%_4
+VCC_GFX	C907	1000P/50V/X7R/10%_4
+1.05V	C908	1000P/50V/X7R/10%_4 +VCC_CORE
VA	C909	1000P/50V/X7R/10%_4
VA	C910	1000P/50V/X7R/10%_4
VA	C911	1000P/50V/X7R/10%_4
BAT+	C921	1000P/50V/X7R/10%_4
BAT+	C922	*1000P/50V/X7R/10%_4
BAT+	C923	1000P/50V/X7R/10%_4
+1.05V	C924	1000P/50V/X7R/10%_4
+1.05V	C925	1000P/50V/X7R/10%_4
+5V	C942	1000P/50V/X7R/10%_4
+5V	C943	1000P/50V/X7R/10%_4
+5V	C944	1000P/50V/X7R/10%_4
+VGPU_CORE	C963	1000P/50V/X7R/10%_4

VIN	C912	1000P/50V/X7R/10%_4
VIN	C913	1000P/50V/X7R/10%_4
VIN	C914	1000P/50V/X7R/10%_4
VIN	C915	1000P/50V/X7R/10%_4
VIN	C916	*1000P/50V/X7R/10%_4
VIN	C917	1000P/50V/X7R/10%_4
VIN	C918	1000P/50V/X7R/10%_4
VIN	C919	1000P/50V/X7R/10%_4
VIN	C920	1000P/50V/X7R/10%_4
+VCC_GFX	C926	1000P/50V/X7R/10%_4
+VCC_GFX	C927	*1000P/50V/X7R/10%_4
+VCC_GFX	C928	1000P/50V/X7R/10%_4
+VCC_GFX	C929	*1000P/50V/X7R/10%_4
+1.05V_GPU	C930	1000P/50V/X7R/10%_4
+1.05V_GPU	C931	1000P/50V/X7R/10%_4
+VCC_CORE	C932	1000P/50V/X7R/10%_4
+VCC_CORE	C933	*1000P/50V/X7R/10%_4
+VCC_CORE	C934	1000P/50V/X7R/10%_4
+VCC_CORE	C935	*1000P/50V/X7R/10%_4
+VCC_CORE	C936	1000P/50V/X7R/10%_4
+VCC_CORE	C937	*1000P/50V/X7R/10%_4
+3V_S5	C947	1000P/50V/X7R/10%_4
+3V_S5	C948	1000P/50V/X7R/10%_4
+3V_S5	C949	1000P/50V/X7R/10%_4
+3V_S5	C950	1000P/50V/X7R/10%_4

+5V_WAKE	C938	1000P/50V/X7R/10%_4
+5V_WAKE	C939	1000P/50V/X7R/10%_4
+5V_WAKE	C940	1000P/50V/X7R/10%_4
+5V_WAKE	C941	1000P/50V/X7R/10%_4
+3V	C945	1000P/50V/X7R/10%_4
+3V	C946	1000P/50V/X7R/10%_4
+3V_SUS	C951	1000P/50V/X7R/10%_4
+3V_SUS	C952	1000P/50V/X7R/10%_4
+1.35V_SUS	C953	1000P/50V/X7R/10%_4
+1.35V_SUS	C954	1000P/50V/X7R/10%_4
+1.35V_SUS	C955	*1000P/50V/X7R/10%_4
+1.35V_SUS	C956	1000P/50V/X7R/10%_4
+1.35V_SUS	C957	*1000P/50V/X7R/10%_4
+1.35V_SUS	C958	1000P/50V/X7R/10%_4
+1.35V_SUS	C959	1000P/50V/X7R/10%_4
+1.35V_SUS	C960	1000P/50V/X7R/10%_4
+1.35V_SUS	C961	1000P/50V/X7R/10%_4
+1.35V_SUS	C962	1000P/50V/X7R/10%_4
BAT+	C966	1000P/50V/X7R/10%_4
BAT+	C967	1000P/50V/X7R/10%_4

VIN	C964	1000P/50V/X7R/10%_4 BAT+
VIN	C965	1000P/50V/X7R/10%_4 BAT+

	Quanta P/N	Vendor P/N	Foot Print
14"	DFFC24FS002	88483-2441-FN	88483-2441-fn-24p-rdv_ab
15"	DFFC26FR039	50503-02601-001	88513-2641-26p-l-smt

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	HOLE/EMI/KB	1A

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USB PORT Architecture	
PORT 0	USB3.0
PORT 1	USN2.0
PORT 2	USN2.0
PORT 3	USB2.0
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A
PORT 9	WiMax/BT
PORT 10	Camera
PORT 11	N/A
PORT 12	N/A
PORT 13	N/A

PCIE BUS	
PORT 1	WLAN Port
PORT 2	CARD READER
PORT 3	GLAN(RTL8111E)
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A

SATA BUS	
PORT 0	HDD
PORT 1	N/A
PORT 2	N/A
PORT 3	N/A
PORT 4	ODD
PORT 5	N/A

SM BUS	MBCLK/MBDATA	WRITE	READ	Function
ISL88731CHRTZ	0001 001X	0001 0010	0001 0011	Charger
AMD Thames	0100 0001	-	0100 0001	Graphice
LIS331DL	0011 101X	0011 1010	0011 1011	G Sensor

SM BUS	MBCLK_BAT/MBDATA_BAT	WRITE	READ	Function
VGP-BPS26	0001 011X	0001 0110	0001 0111	Battery

SM BUS	SMB_PCH_CLK/SMB_PCH_DAT	WRITE	READ	Function
DIMM Module0	1010 000X	1010 0000	1010 0001	DDRIII
DIMM Module 1	1010 010X	1010 0100	1010 0101	DDRIII
Synaptics	0010 110X	0010 1100	0010 1101	Click PAD

R363(High) R362(Low) / R294(High) R297(low)

	Board ID3	Board ID0
14"/HK6	0	0
15"/HK5	0	1
17"/HK7	1	0

Board ID1 (VRAM Vendor)	Samaung(1)	Hynix(0)
R47(High)	Stuff	No Stuff
R48(Low)	No Stuff	Stuff

Board ID2		
14" 4PCS	1G	512M
15" 8PCS	1G	2G
R39(High)	Stuff	No Stuff
R27(Low)	No Stuff	Stuff

PCBA SKU	Discrete	UMA
R277(Pull High)	Stuff	No Stuff
R275(Pull Low)	No Stuff	Stuff

	S0	S3	DS3	S4	S5 (Charger Enable)	S5 (Charger Disable)
RUN_ON	H	L	L	L	L	L
+3V	H	L	L	L	L	L
+5V	H	L	L	L	L	L
+0.75V_DDR_VTT	H	L	L	L	L	L
+1.05V	H	L	L	L	L	L
+0.85V	H	L	L	L	L	L
+1.5V	H	L	L	L	L	L
+1.8V	H	L	L	L	L	L
+1.8V_GPU	H	L	L	L	L	L
+1.0V_GPU	H	L	L	L	L	L
+VGPU_CORE	H	L	L	L	L	L
+VCC_GFX	H	L	L	L	L	L
+VCC_CORE	H	L	L	L	L	L
SUS_ON	H	H	H	L	L	L
+1.5V_SUS	H	H	H	L	L	L
S5_ON	H	H	L	H	L	L
+5V_S5	H	H	L	H	L	L
+3V_S5	H	H	L	H	L	L
EC_WAKE_ON	H	H	H	H	H	L
+3V_WAKE	H	H	H	H	H	L
+5V_WAKE	H	H	H	H	H	L
DEEP_EC_EN	H	H	H	H	L	L
+3V_S5_DSW	H	H	H	H	L	L
+3V_SUS	H	H	L	L	L	L