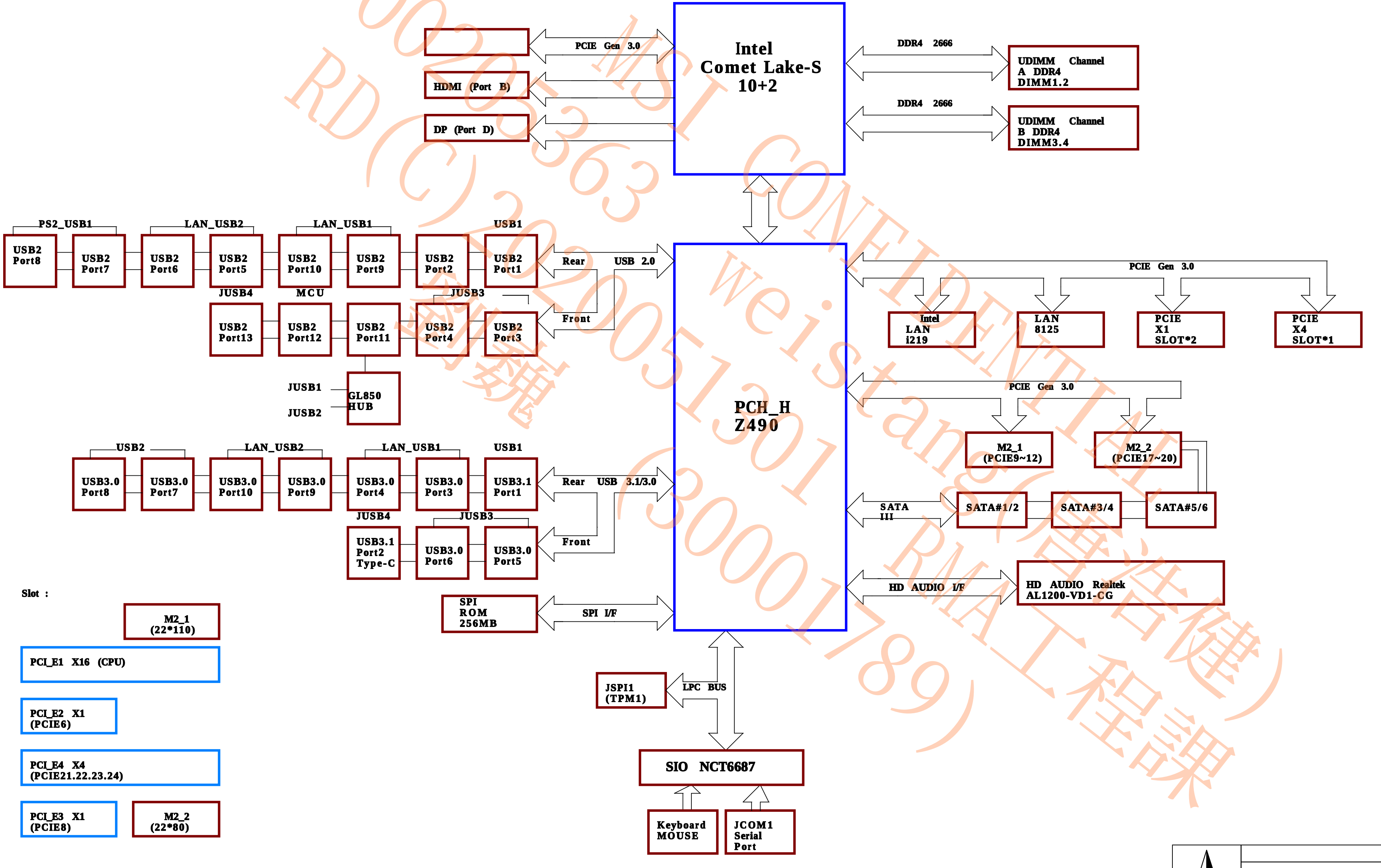
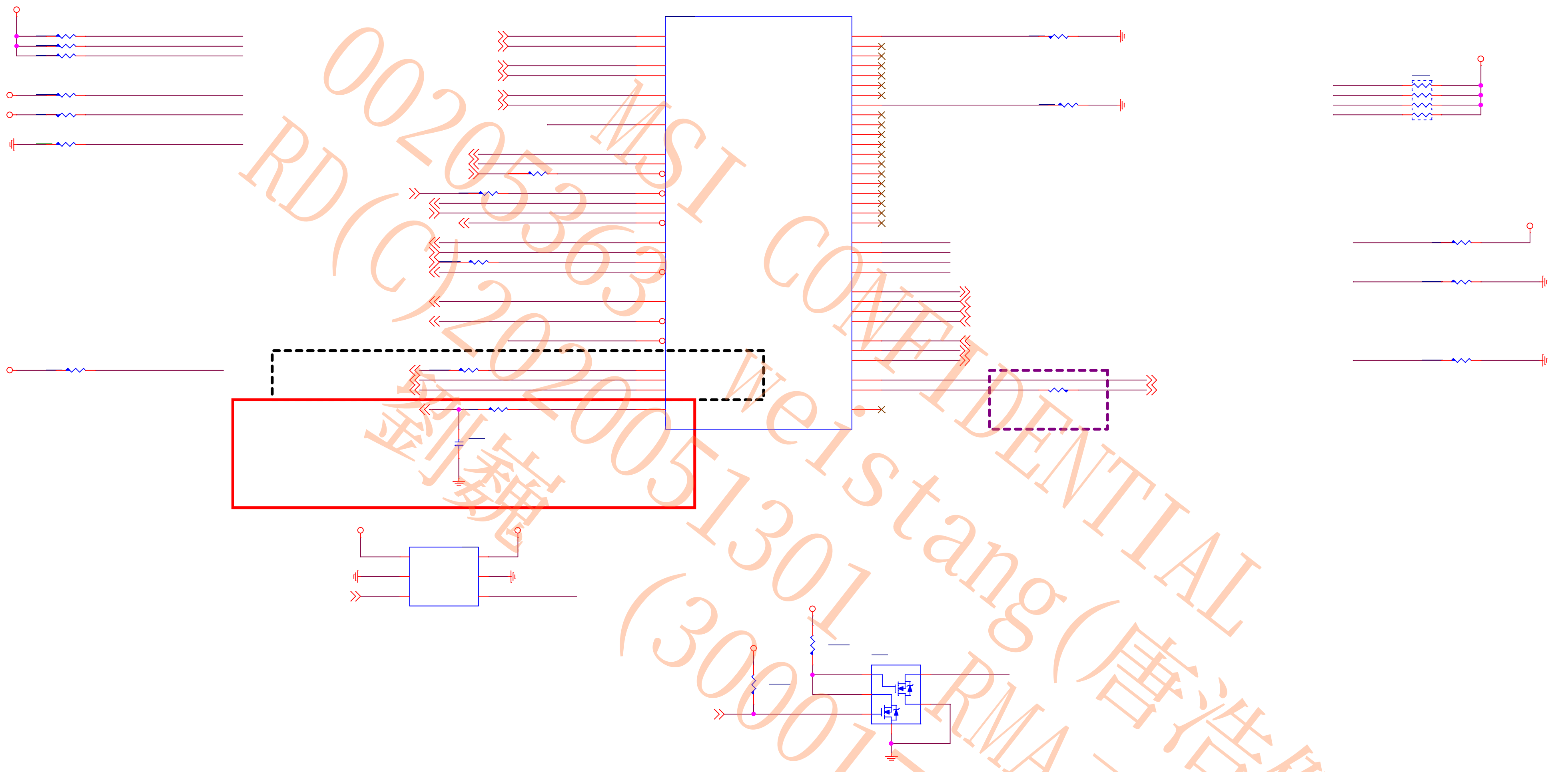
MS-7C80

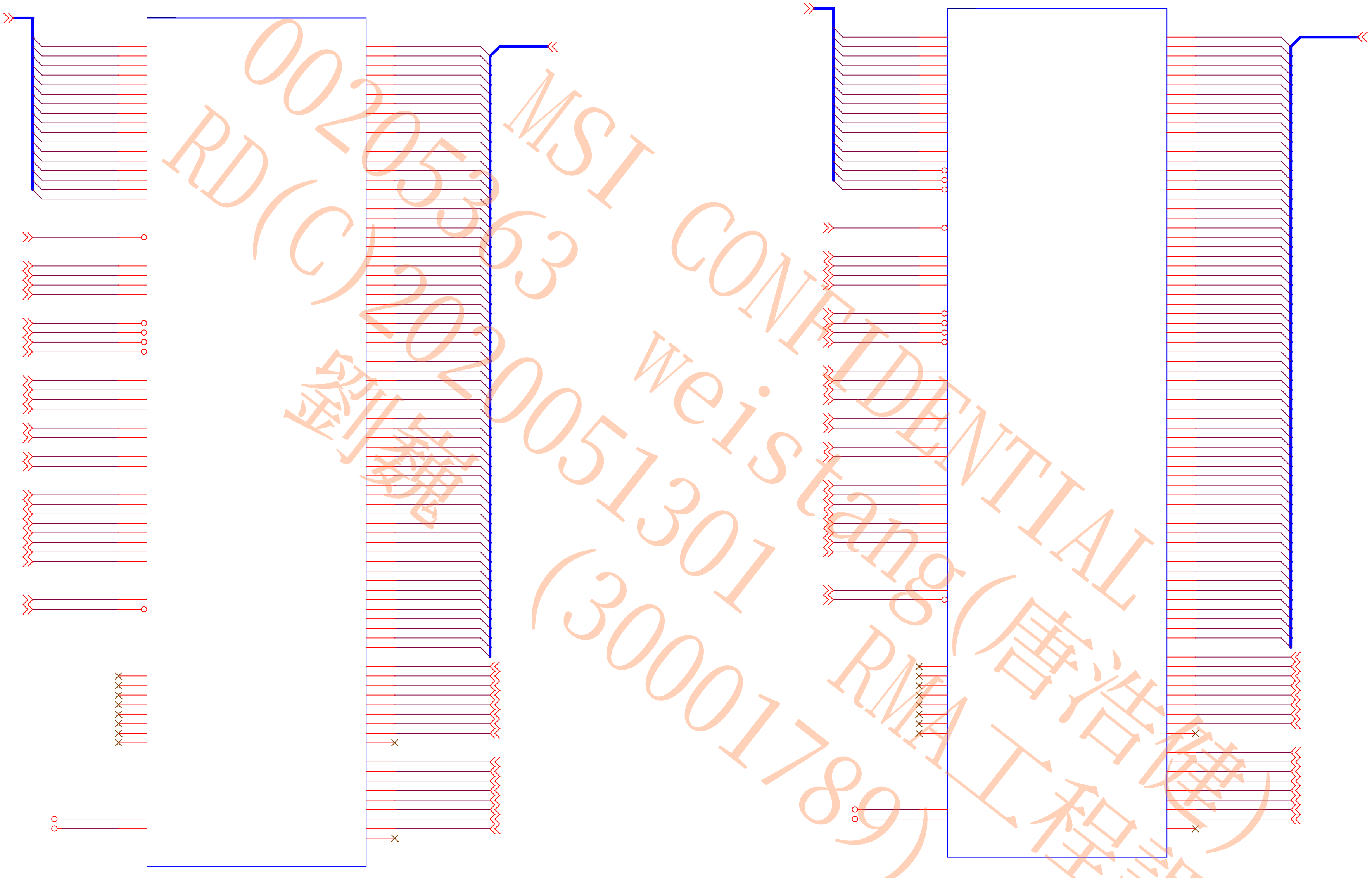
CML Platform

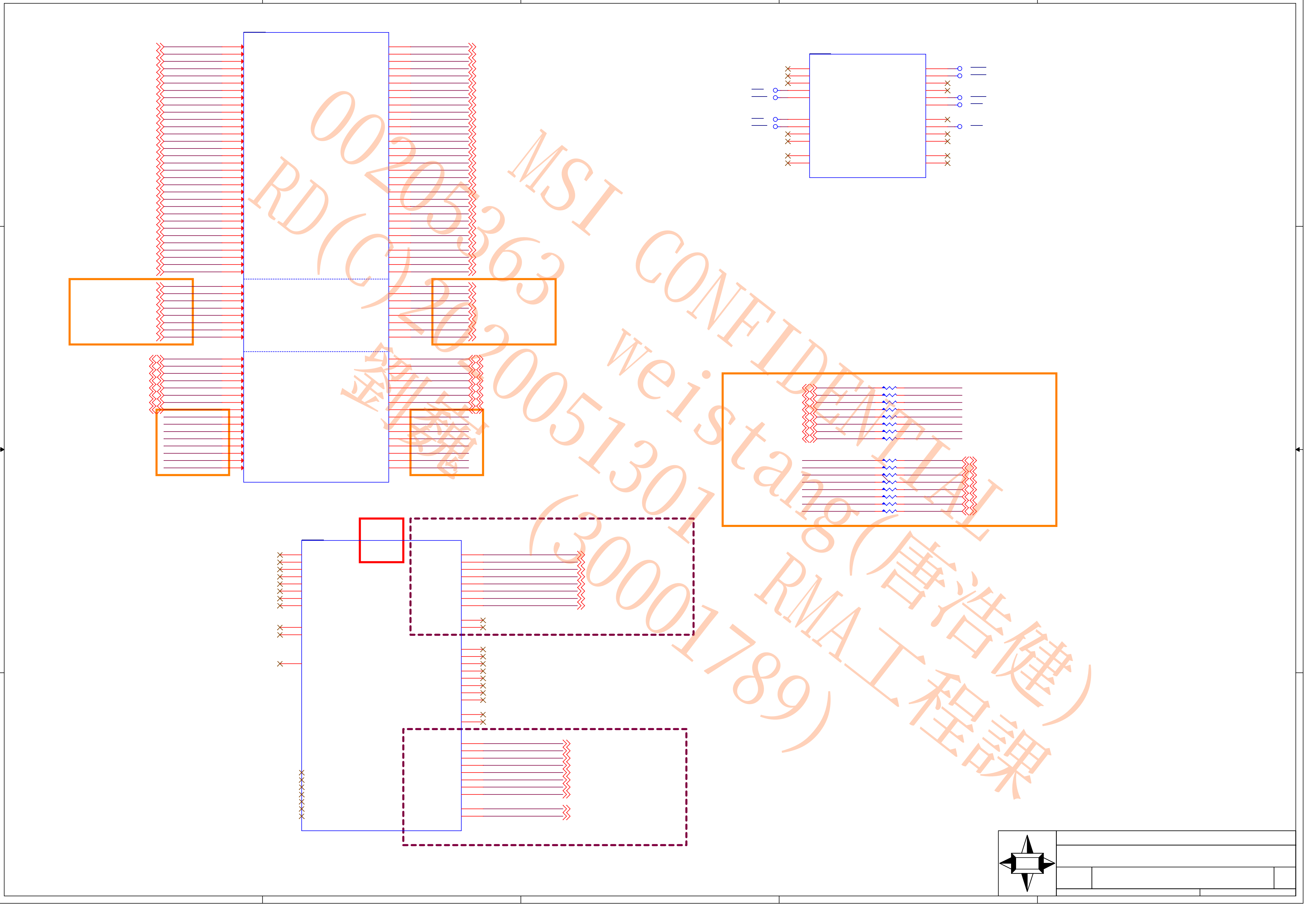
MS-7C80 Block Diagram

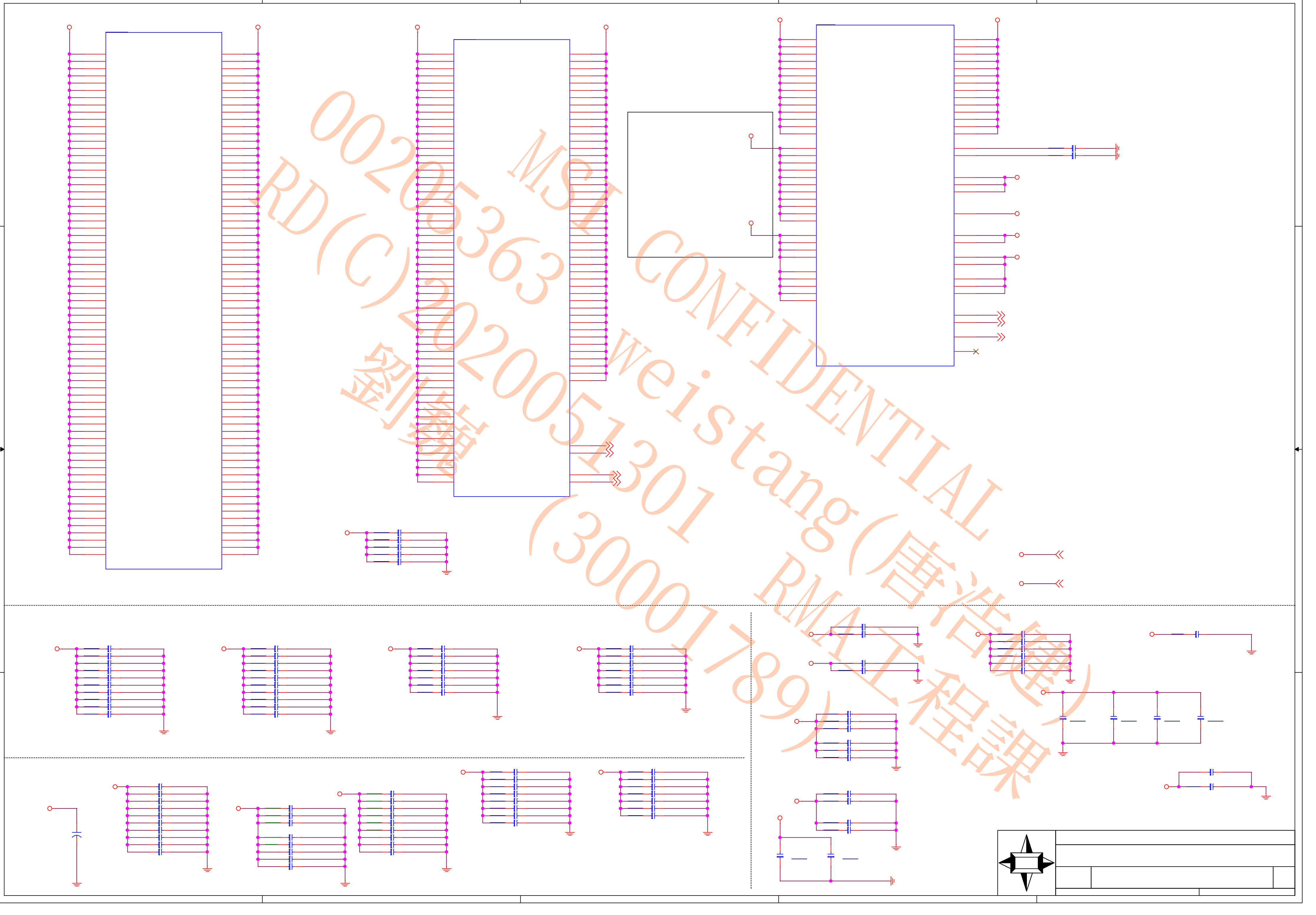


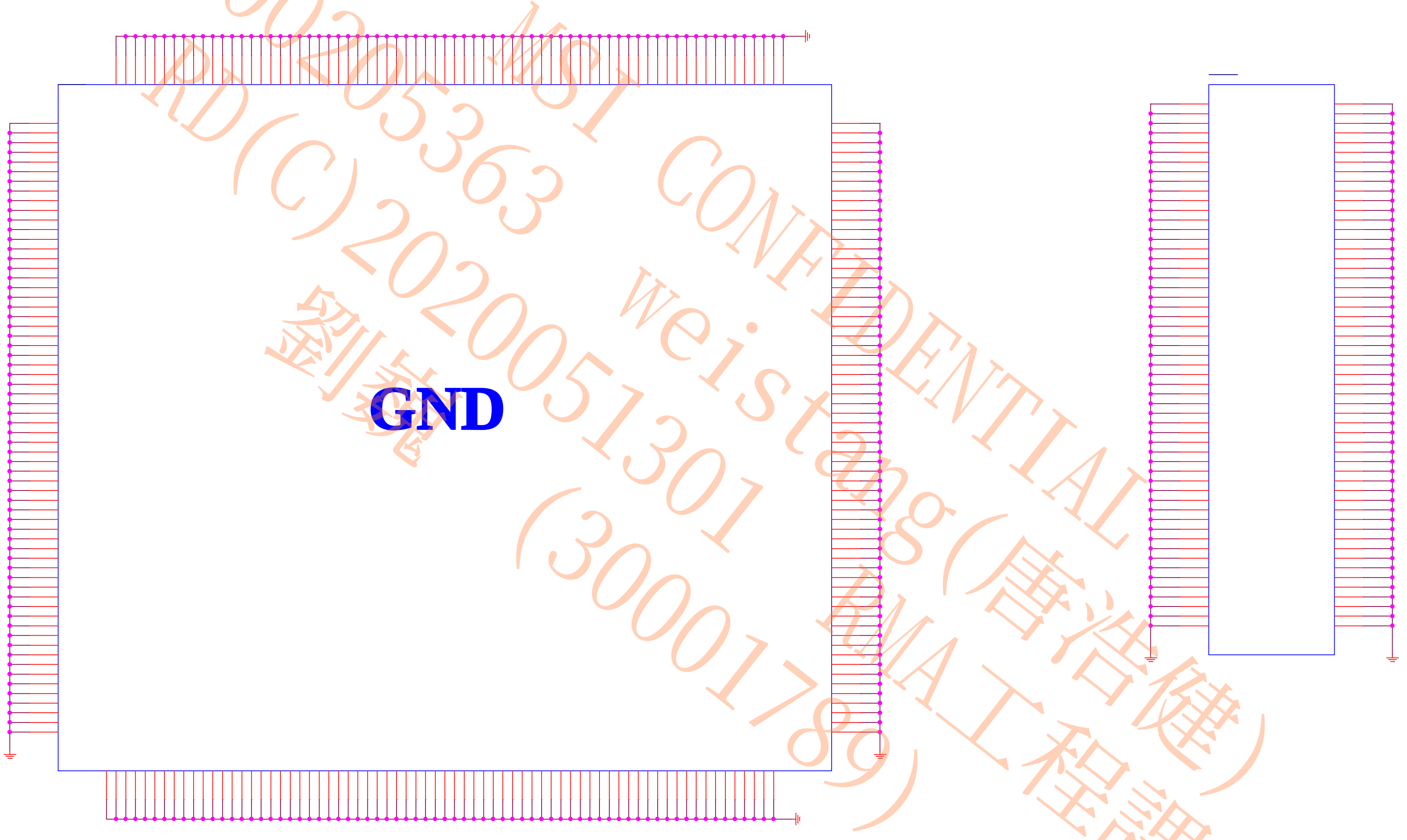


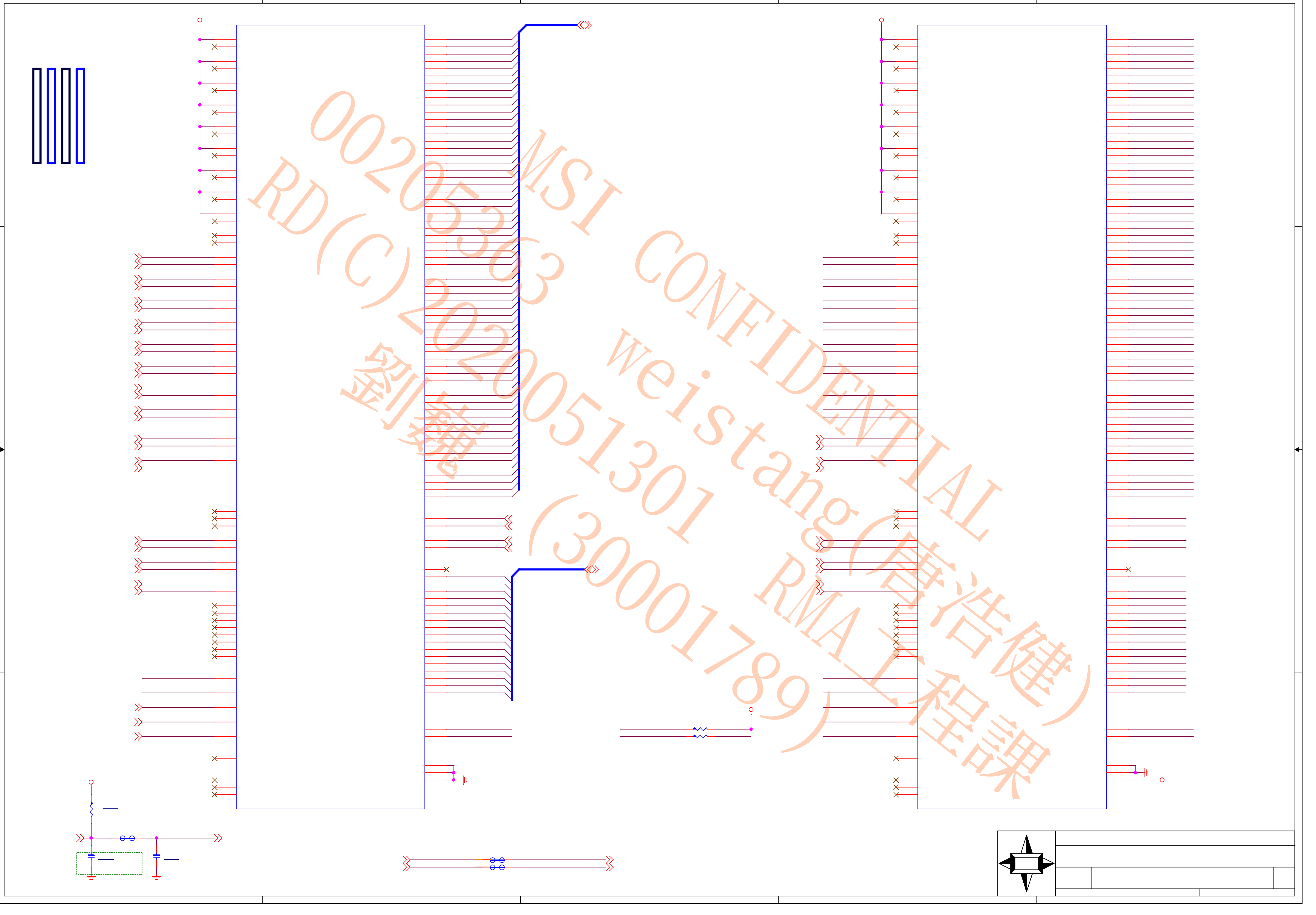
CFG Strap

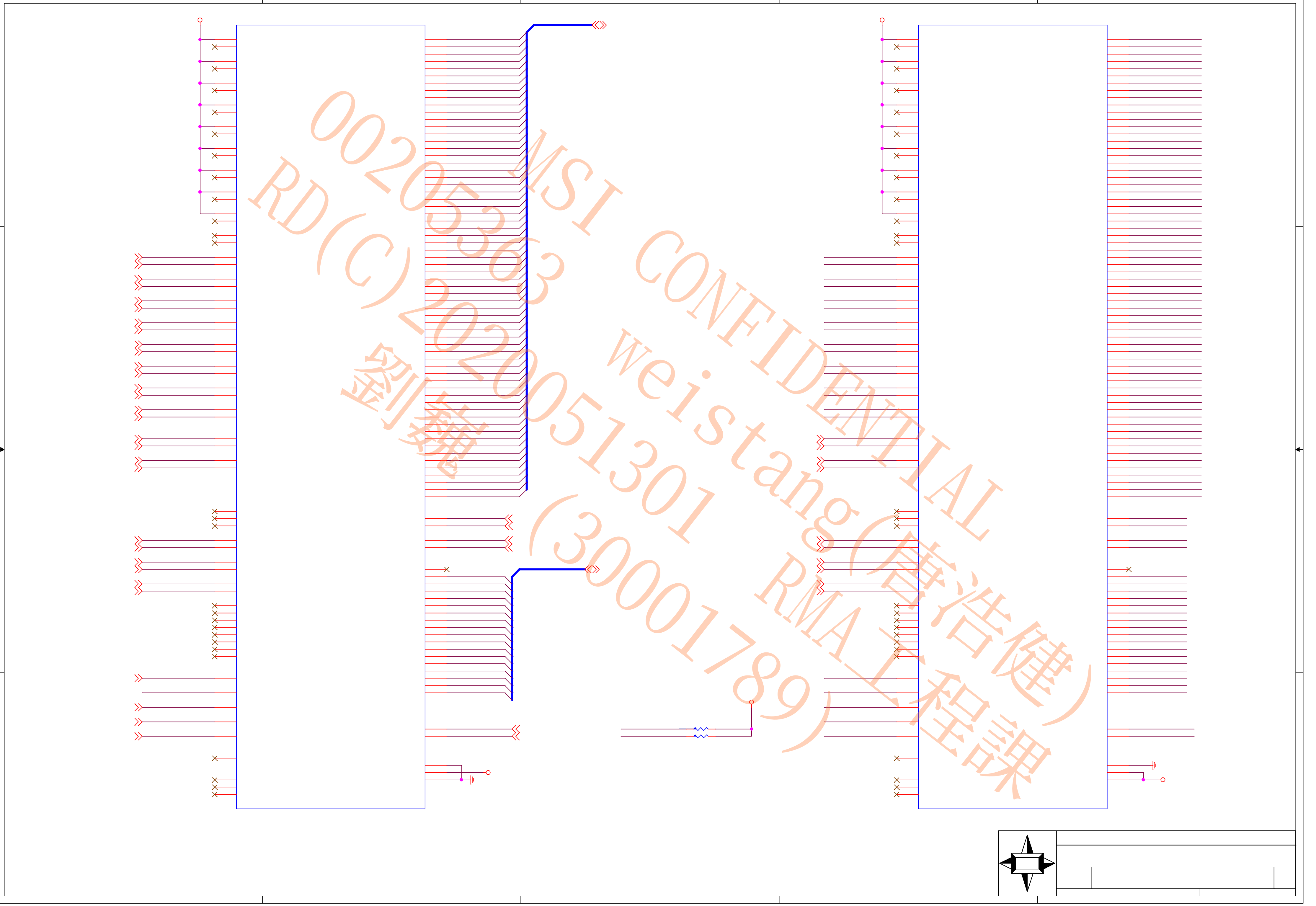


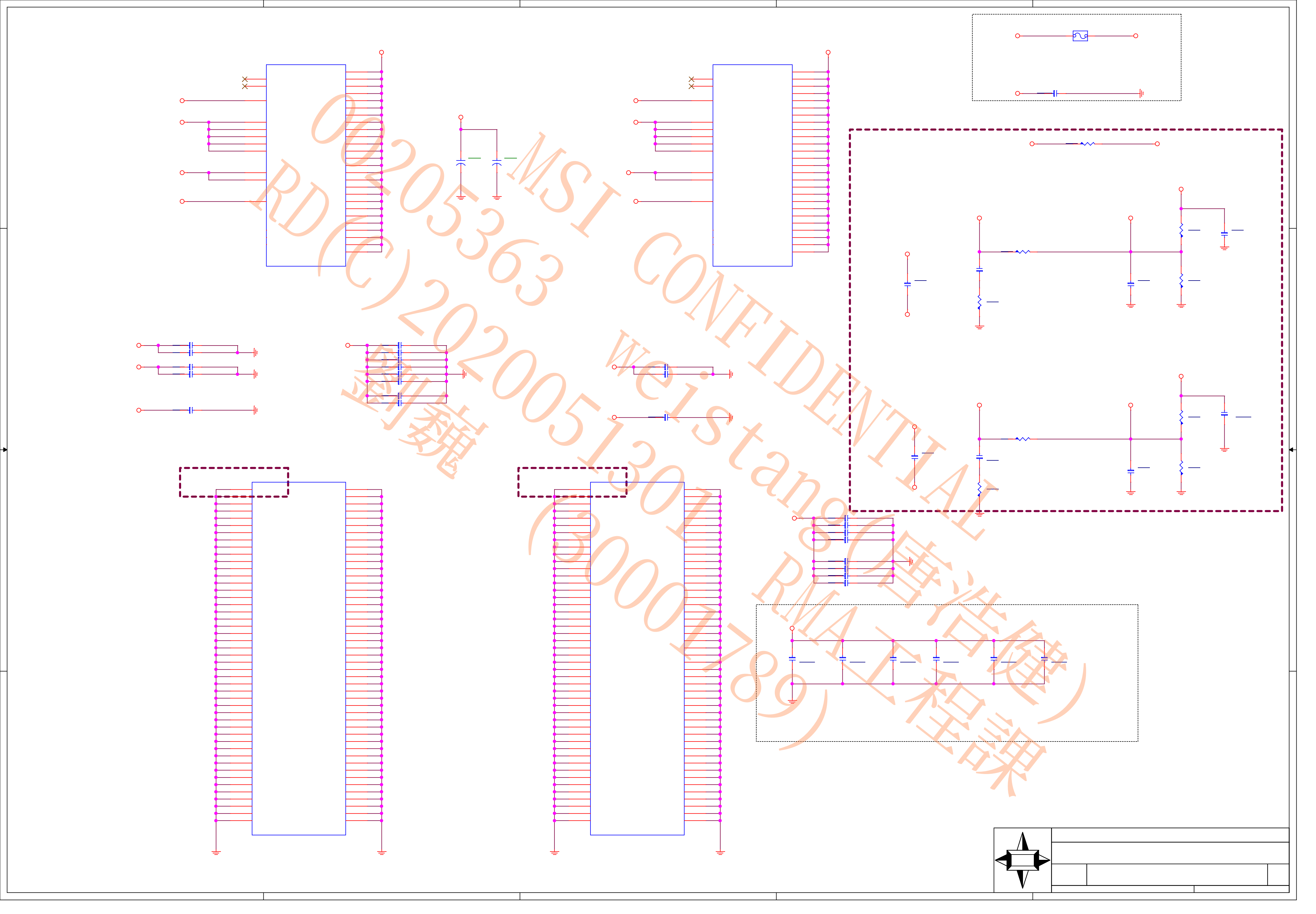


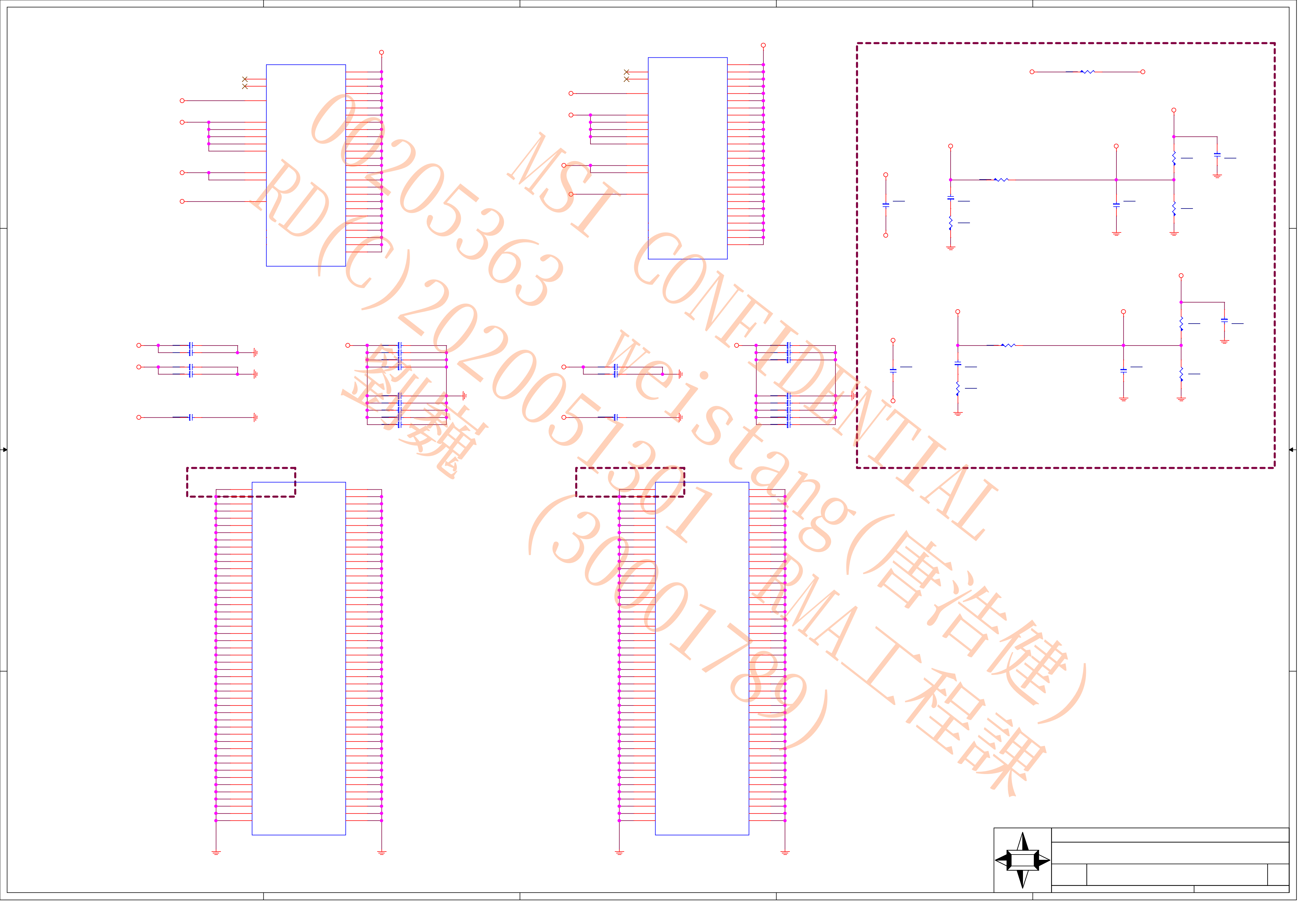


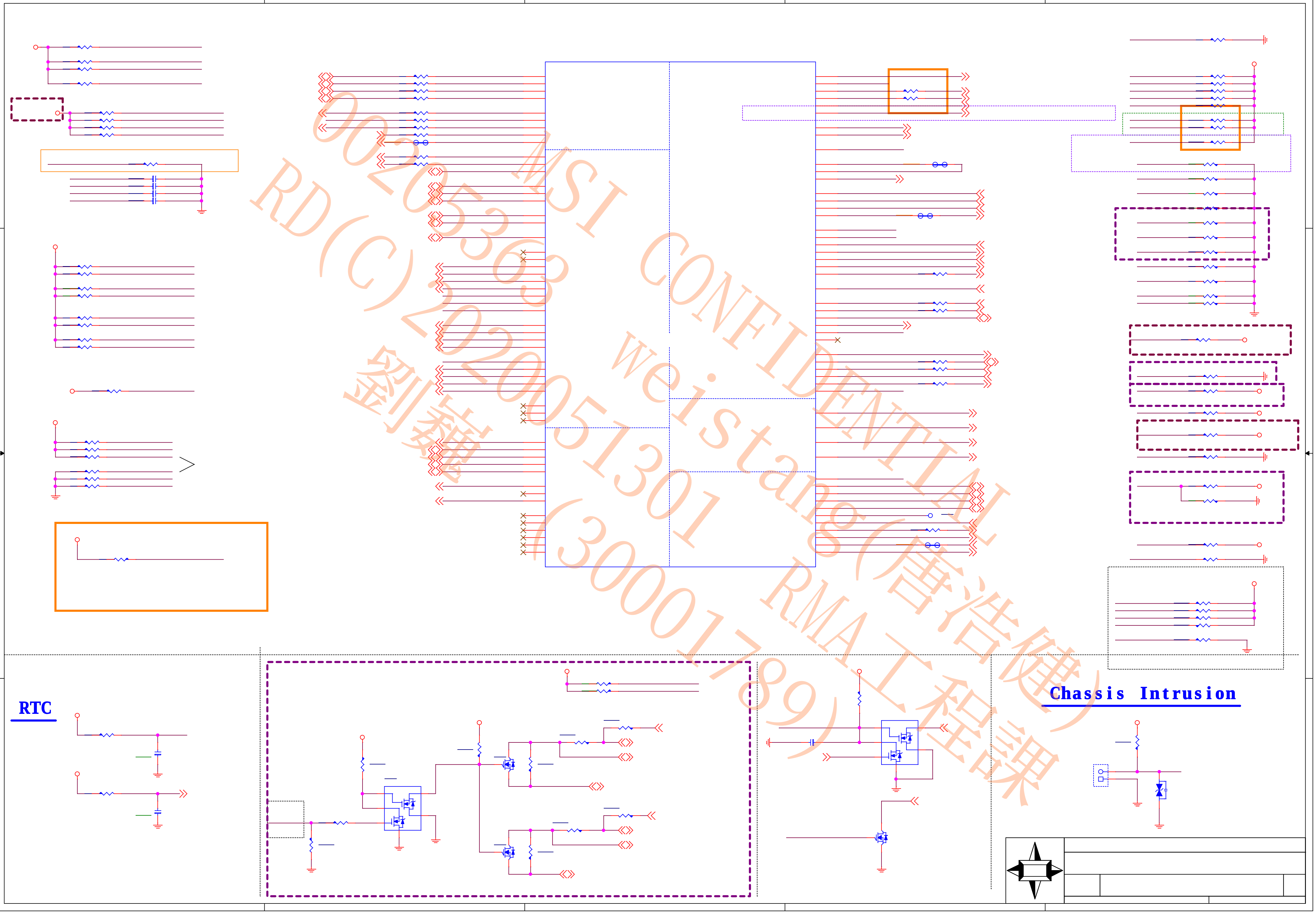






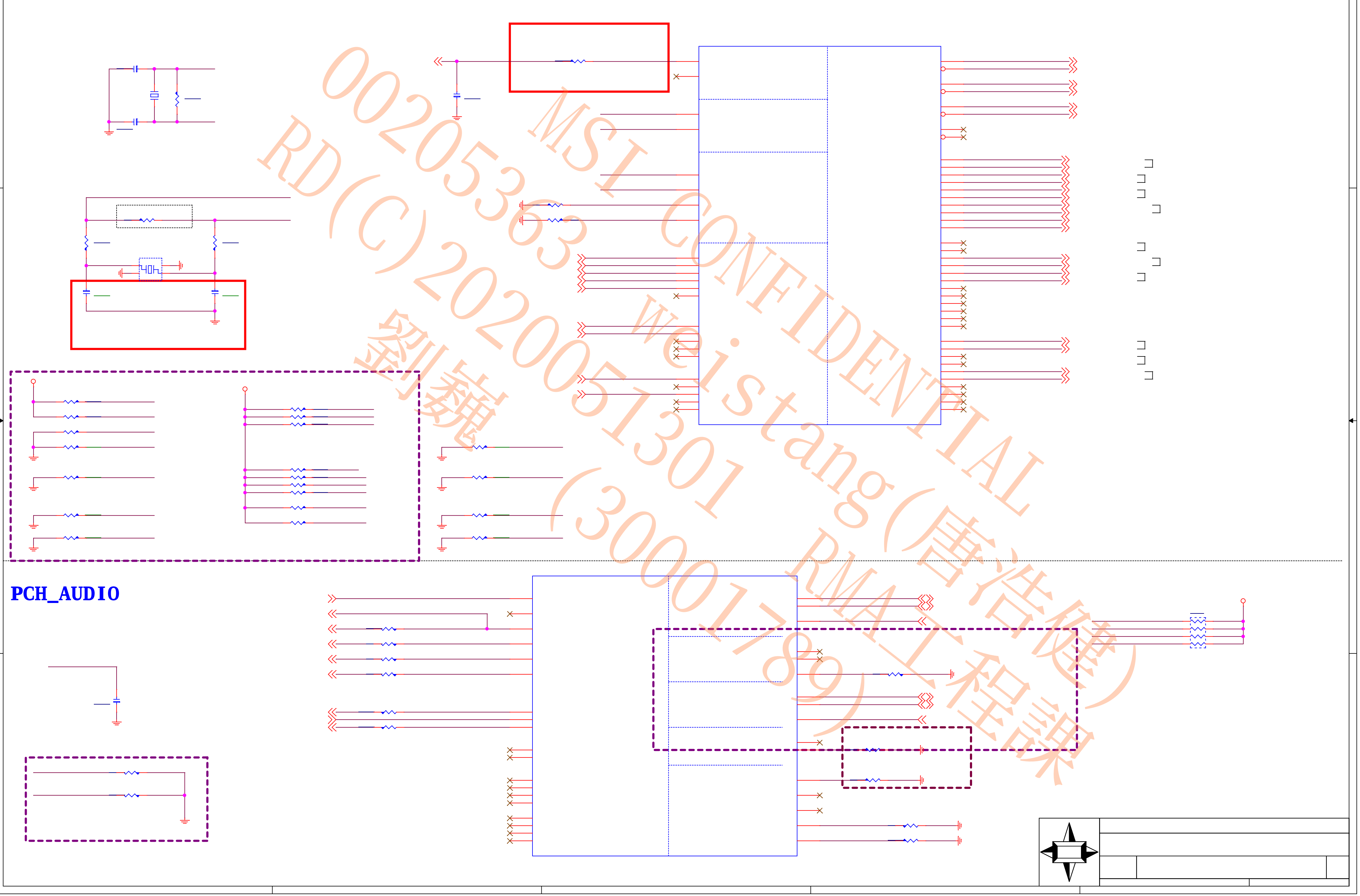


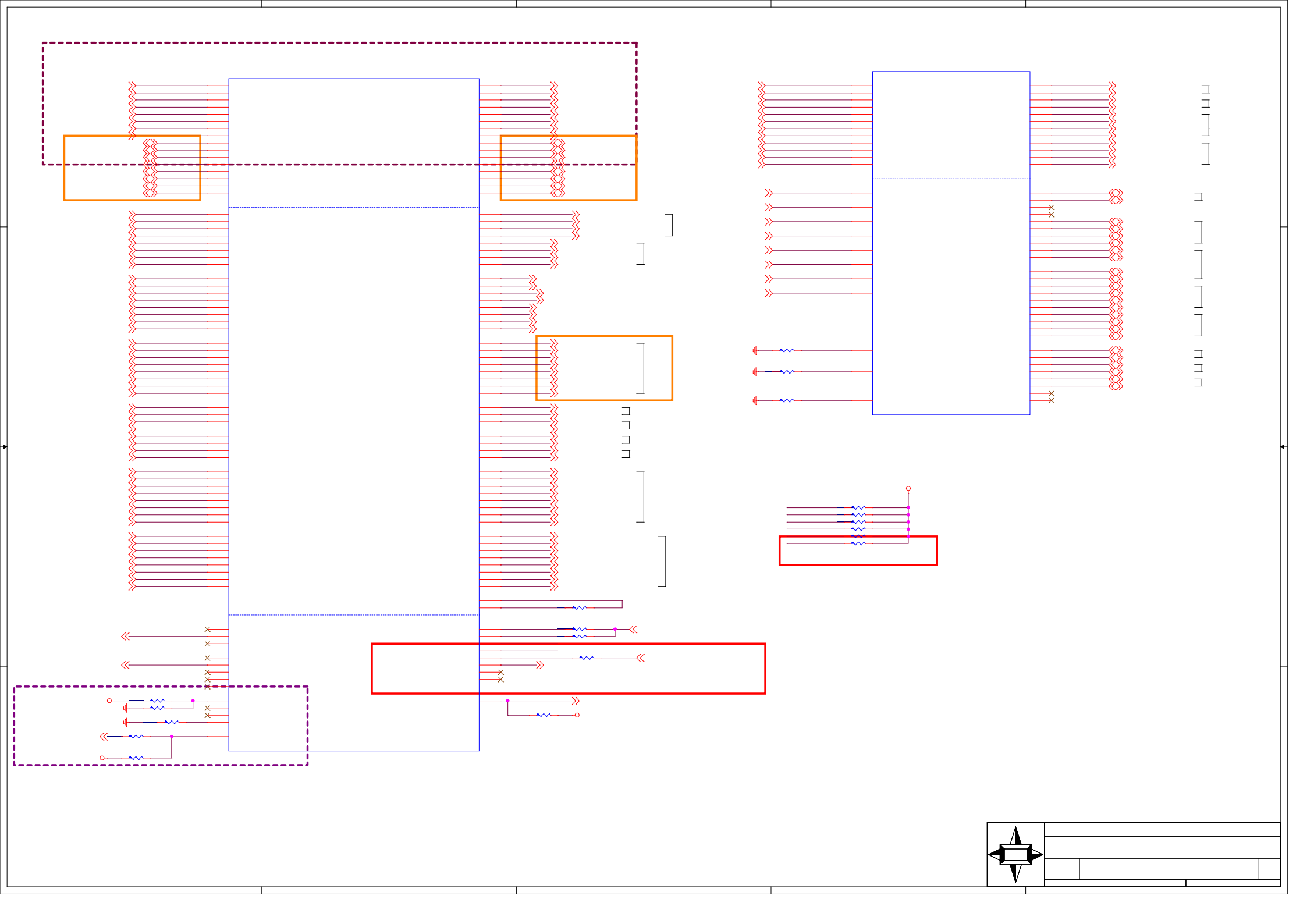


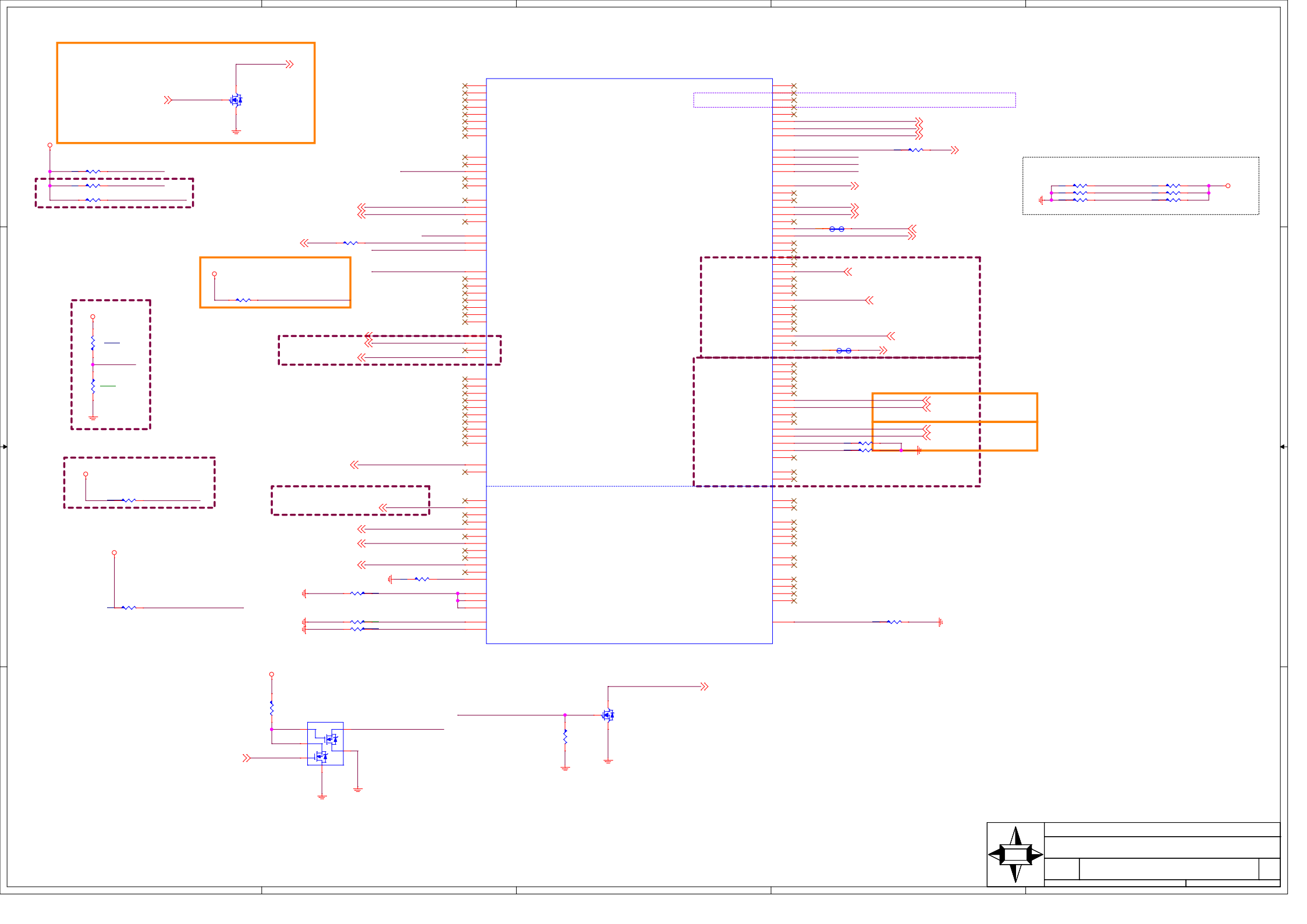

RTC
Chassis Intrusion

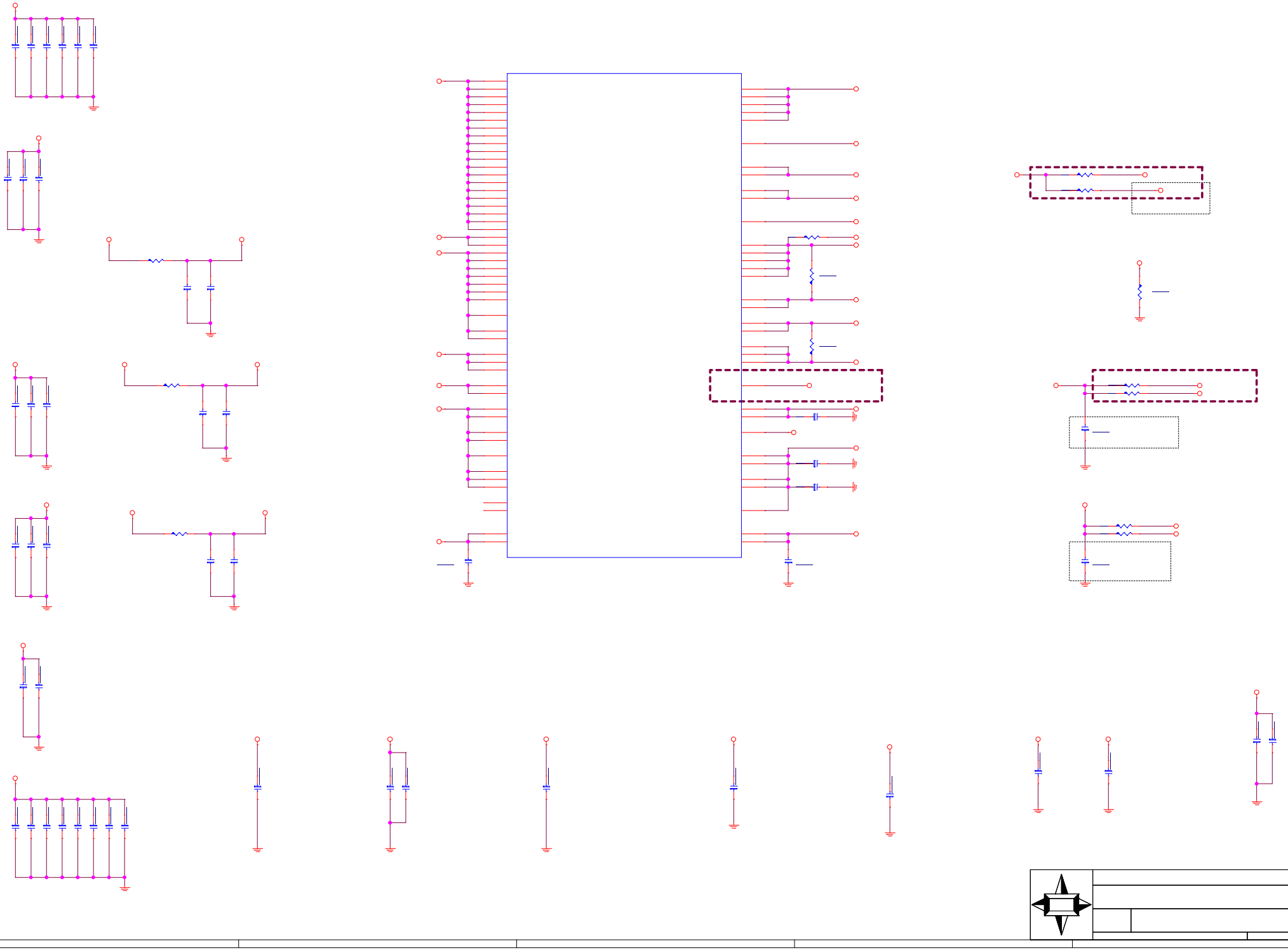
PCH_CLK

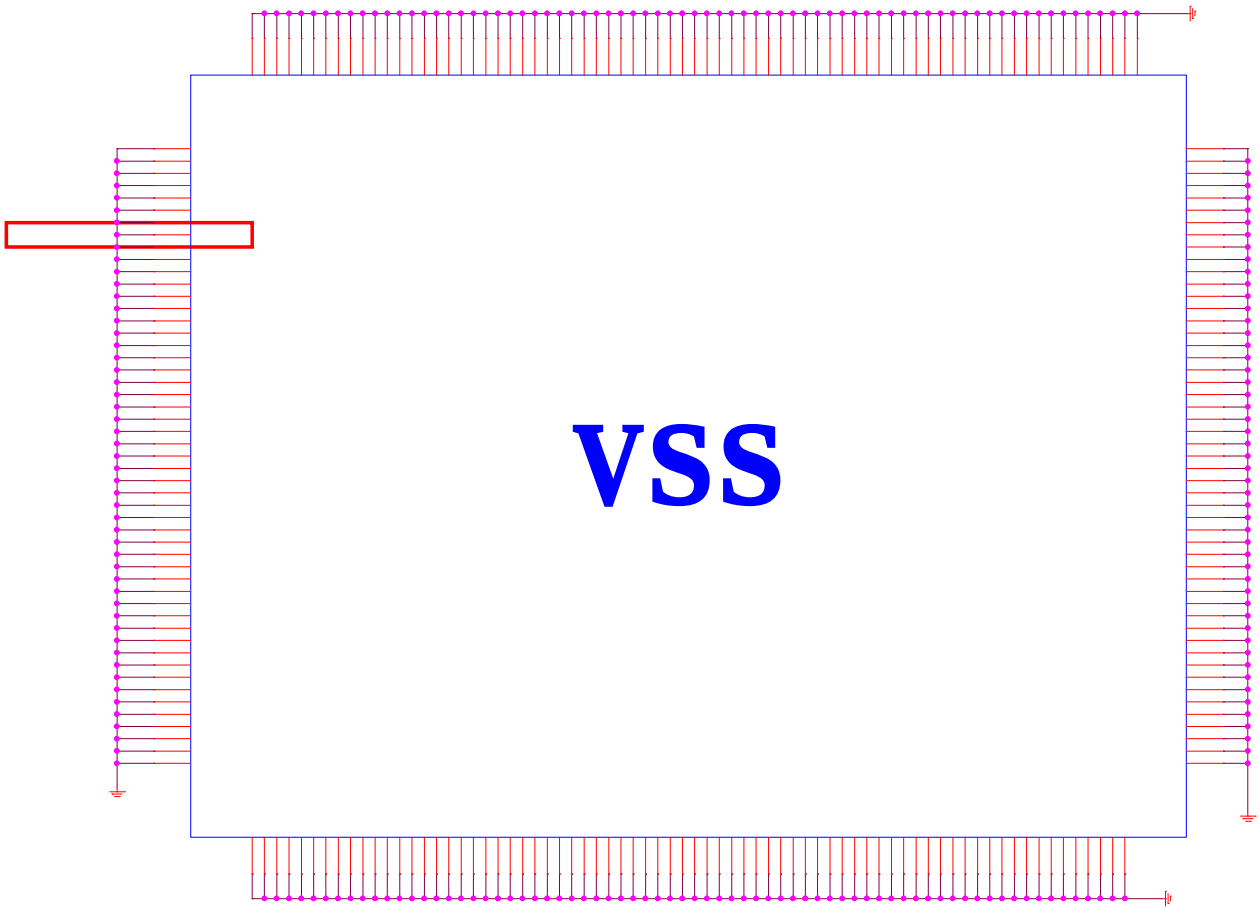
PCH_AUDIO

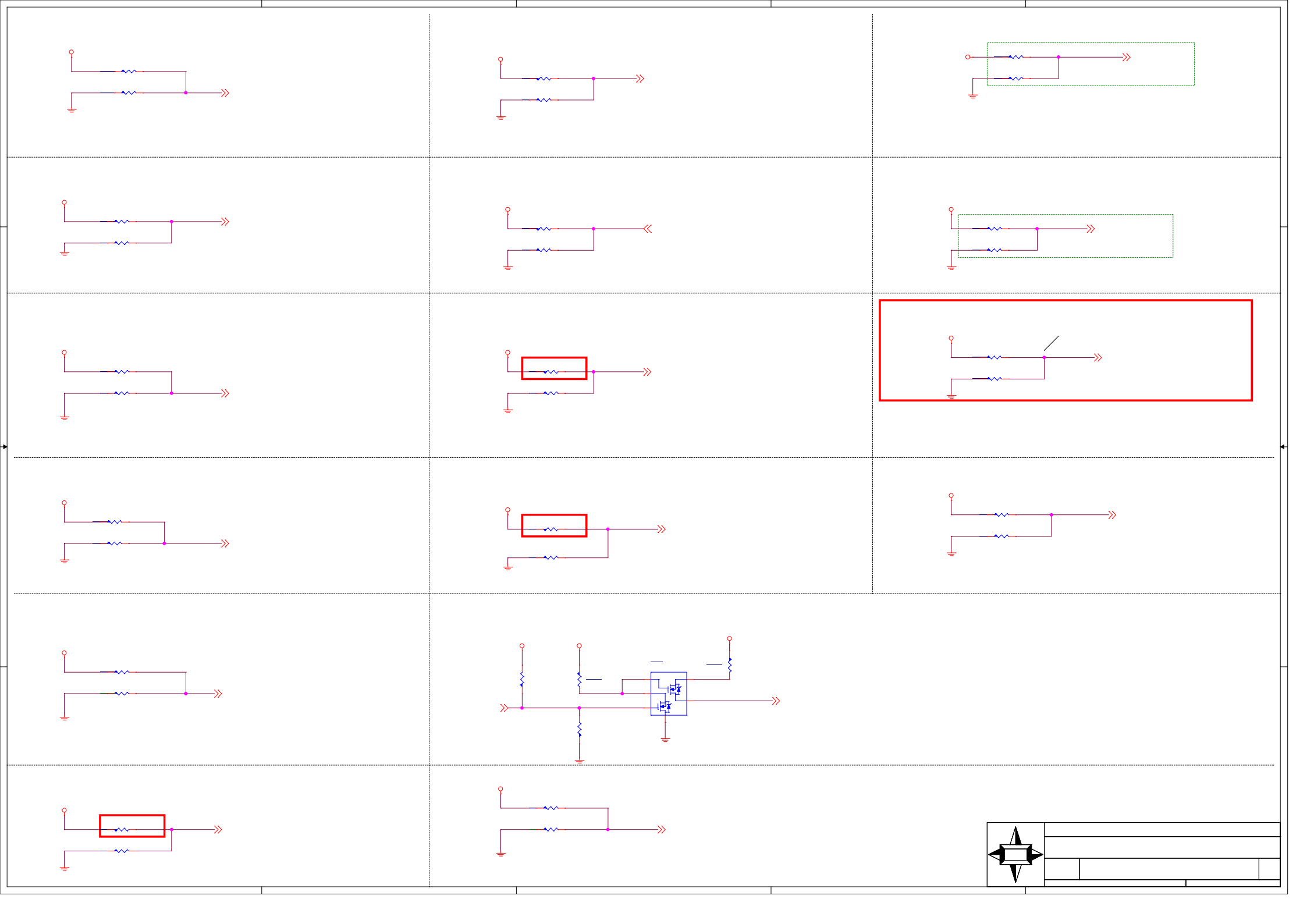


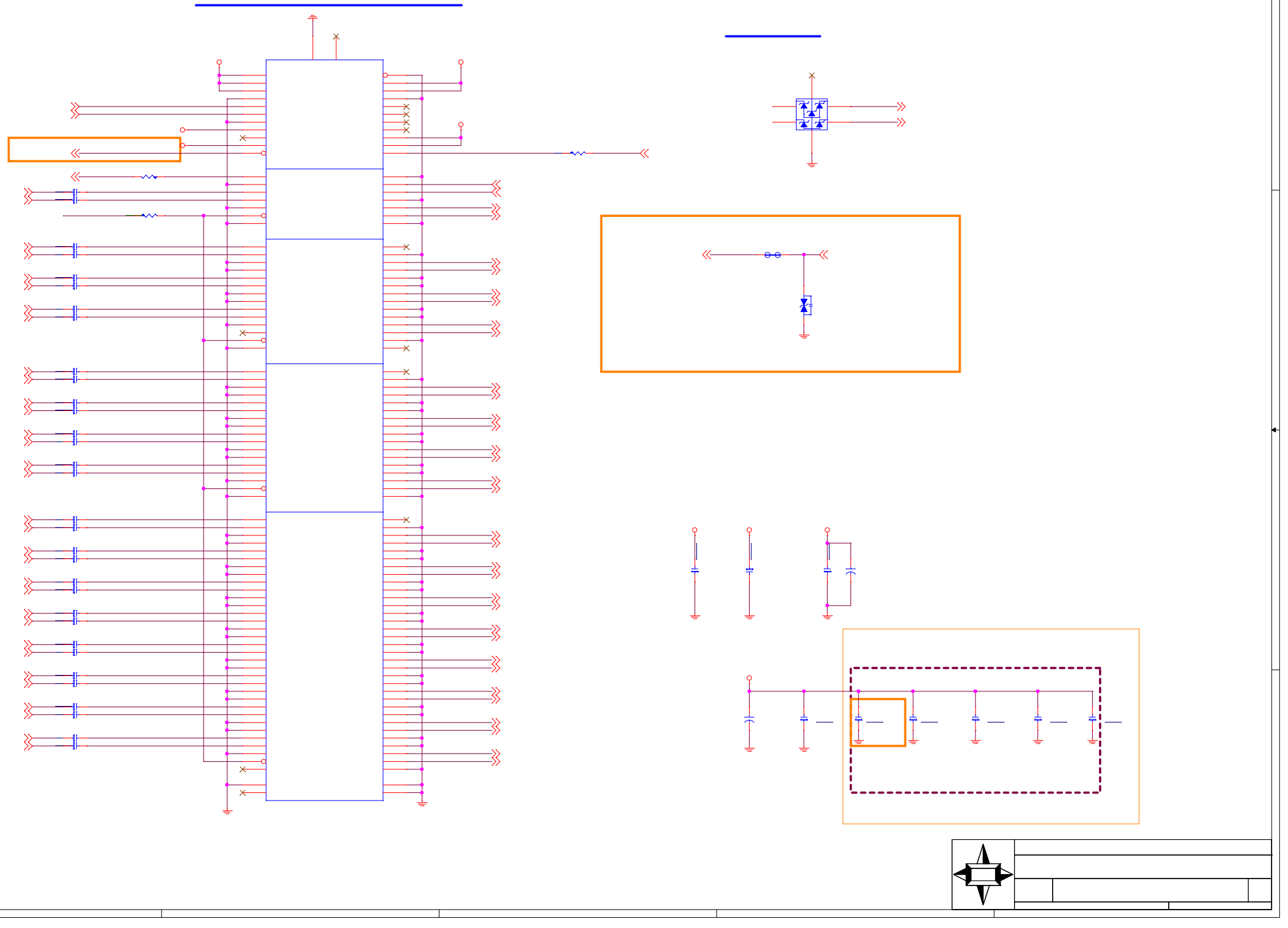


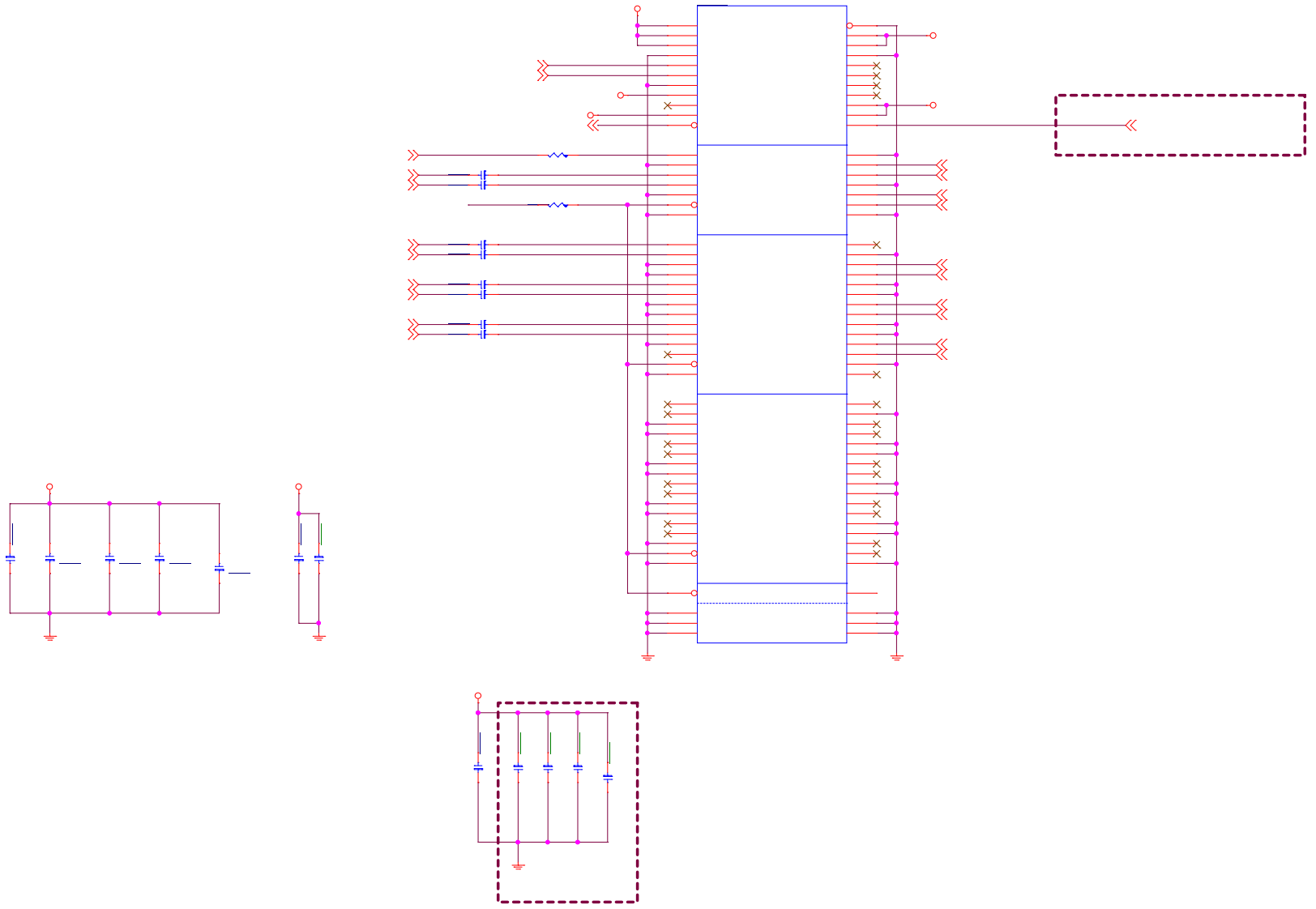


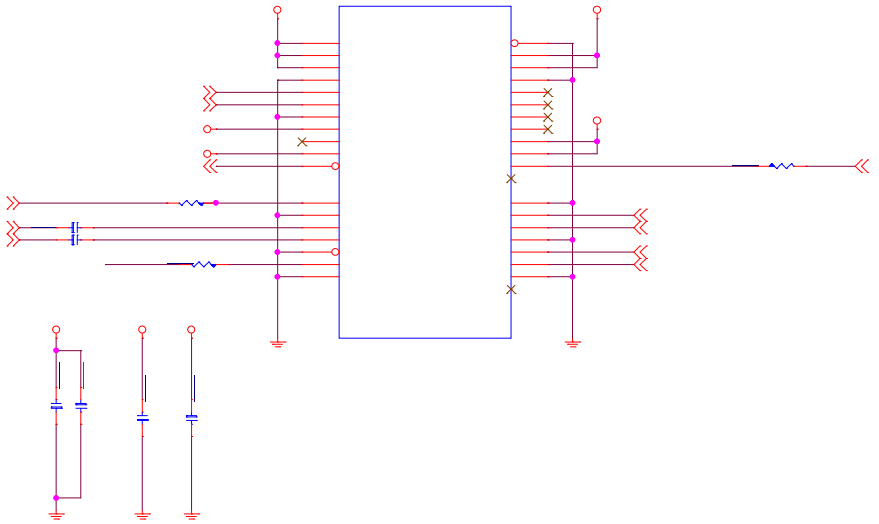


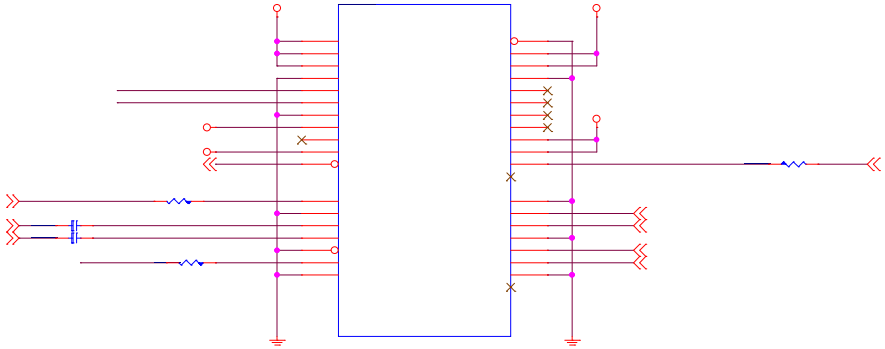

VSS

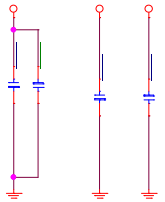


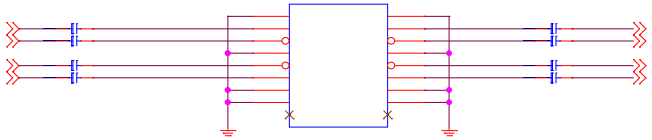


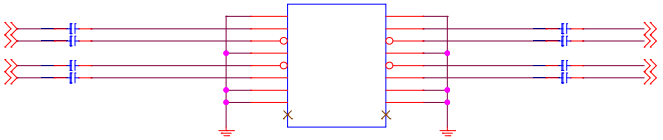


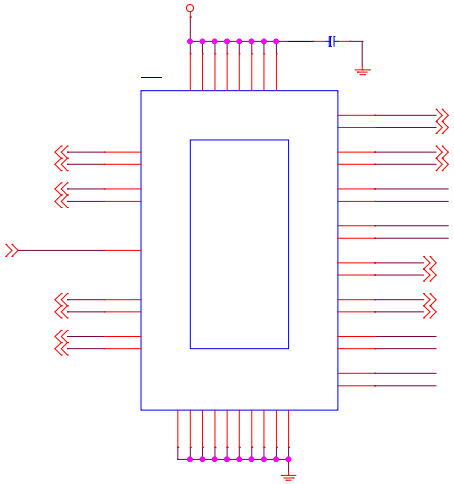


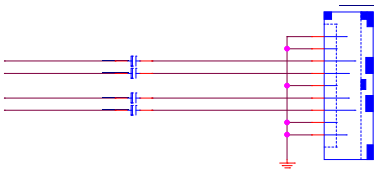


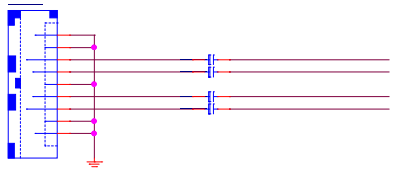


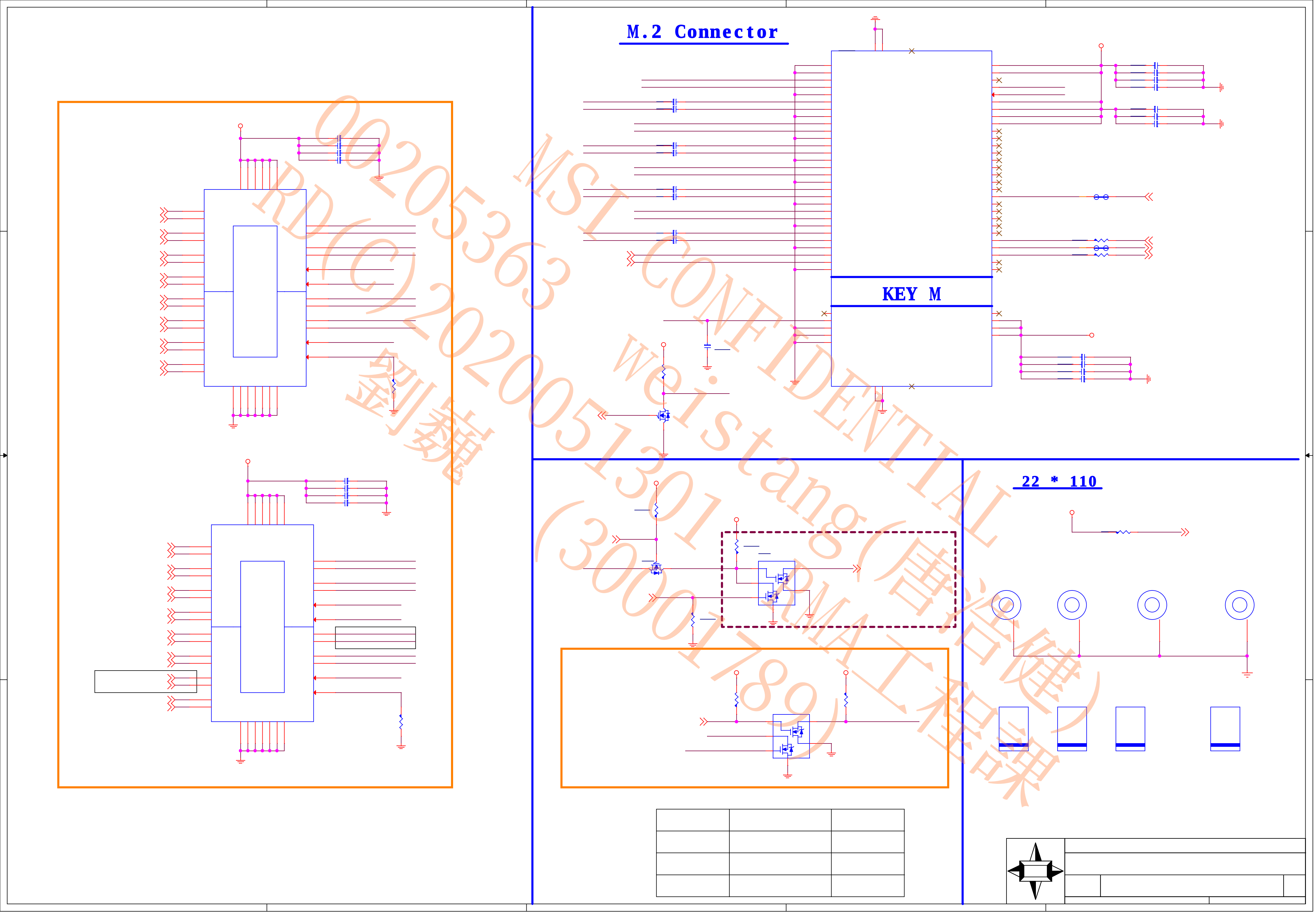


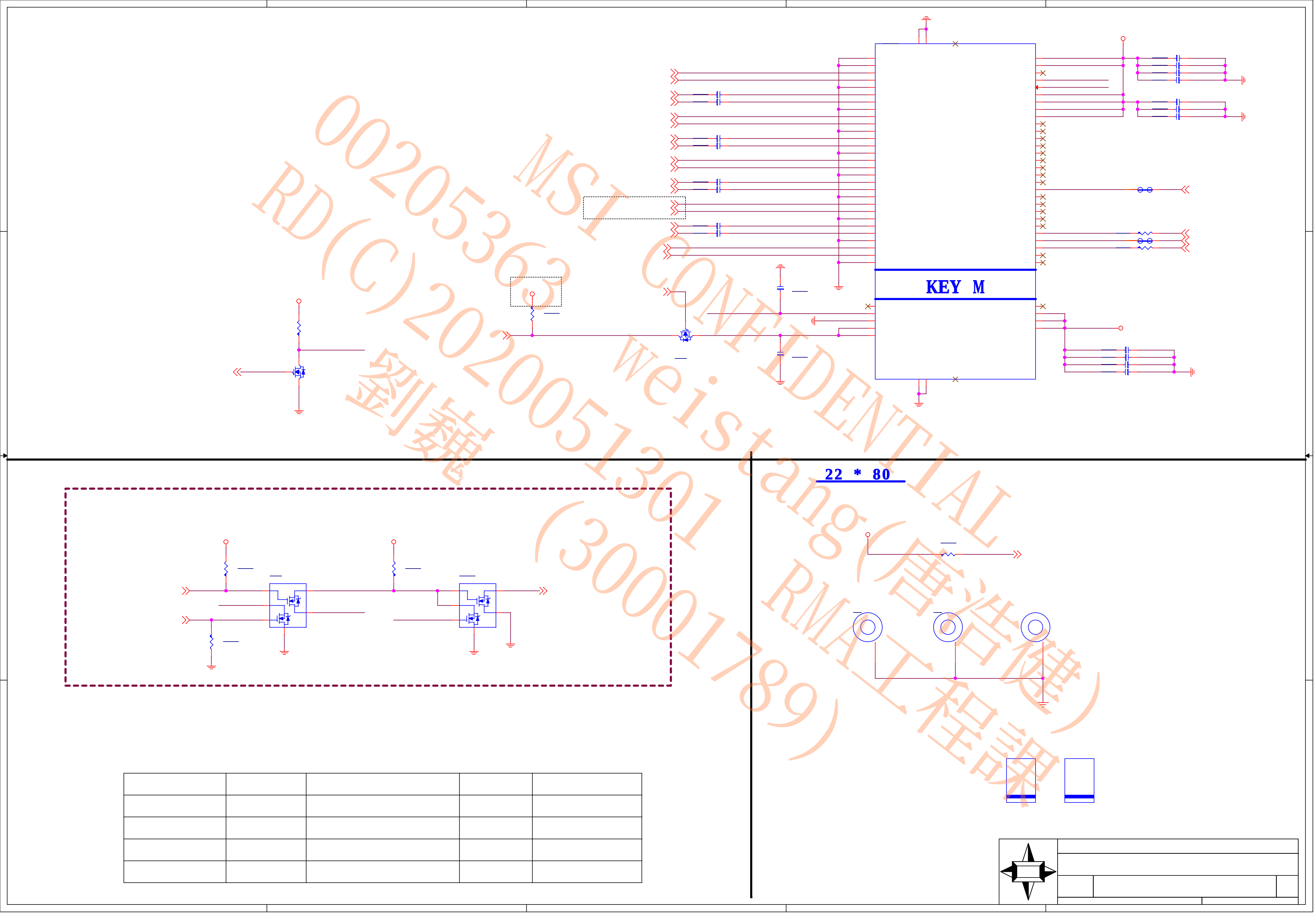


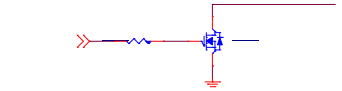


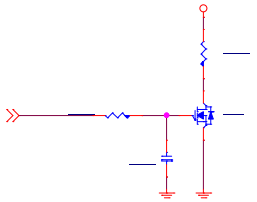


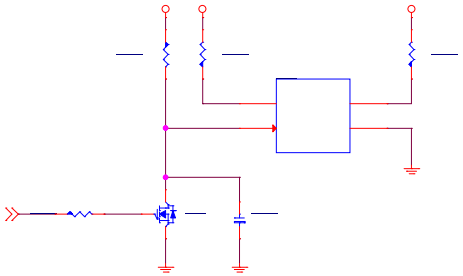


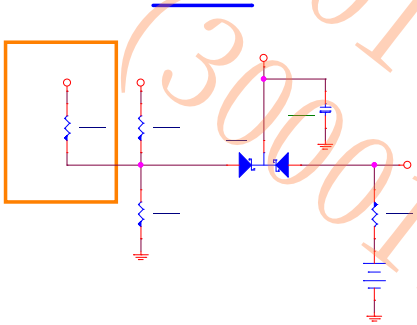


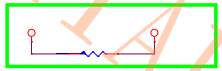




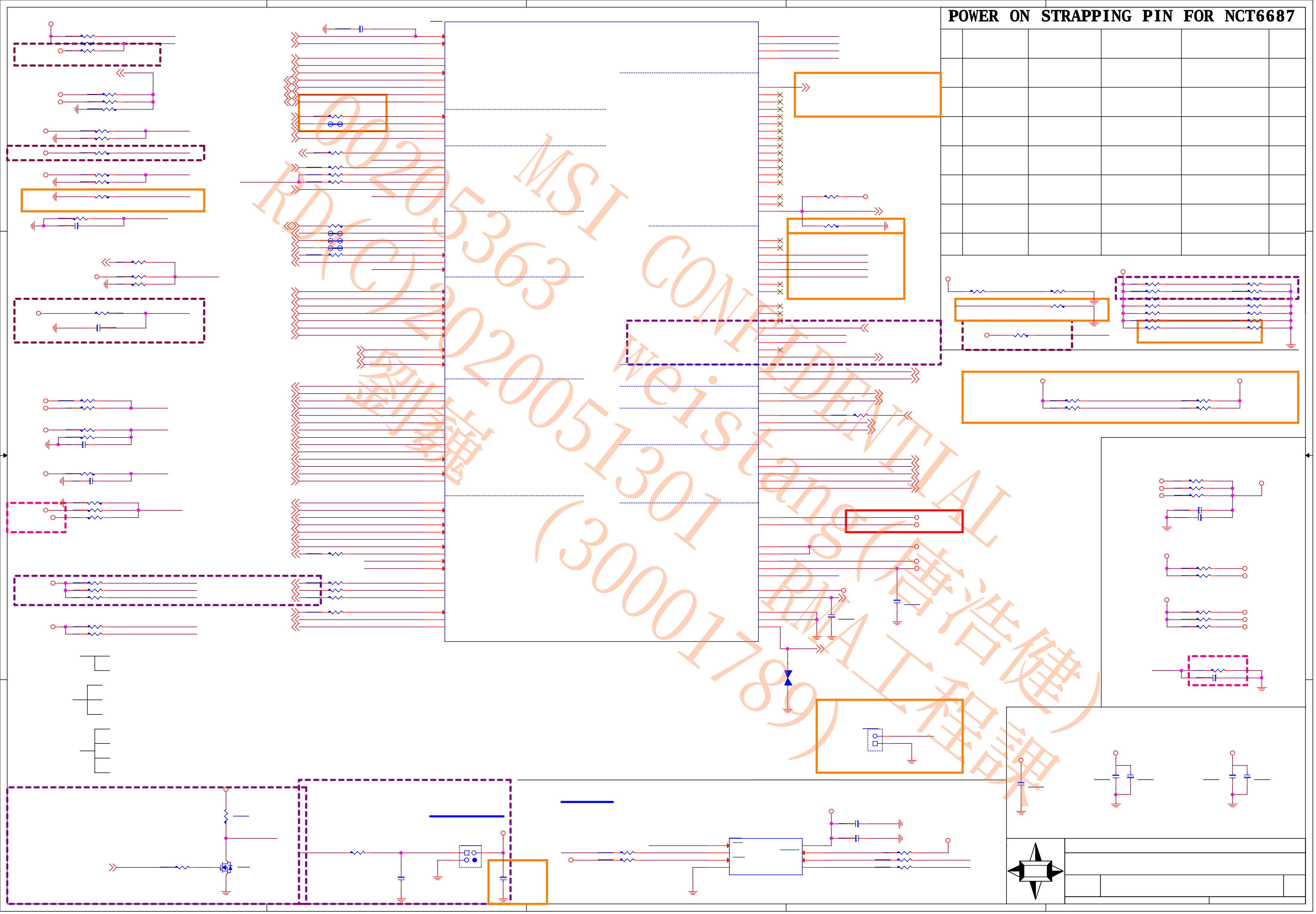


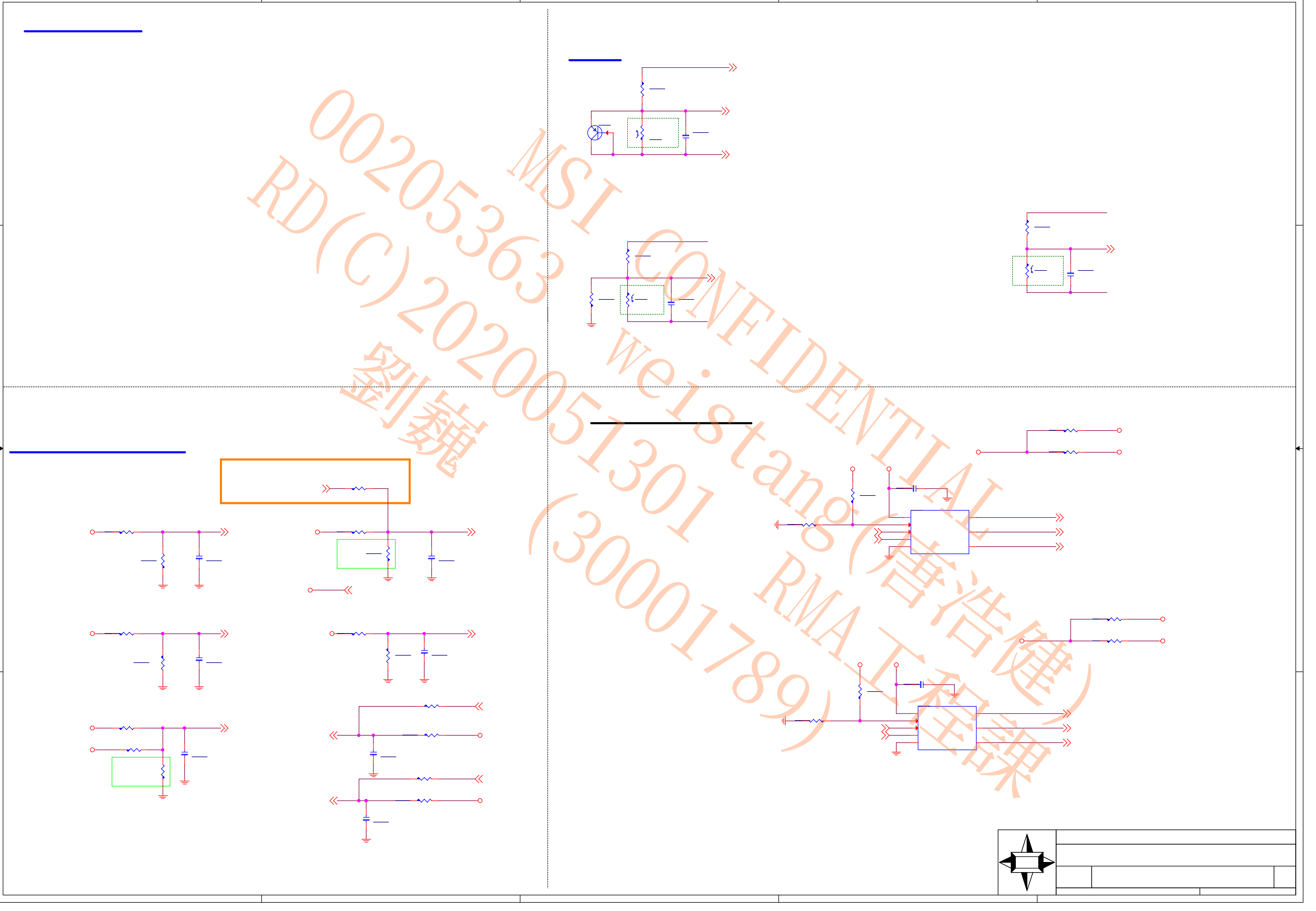






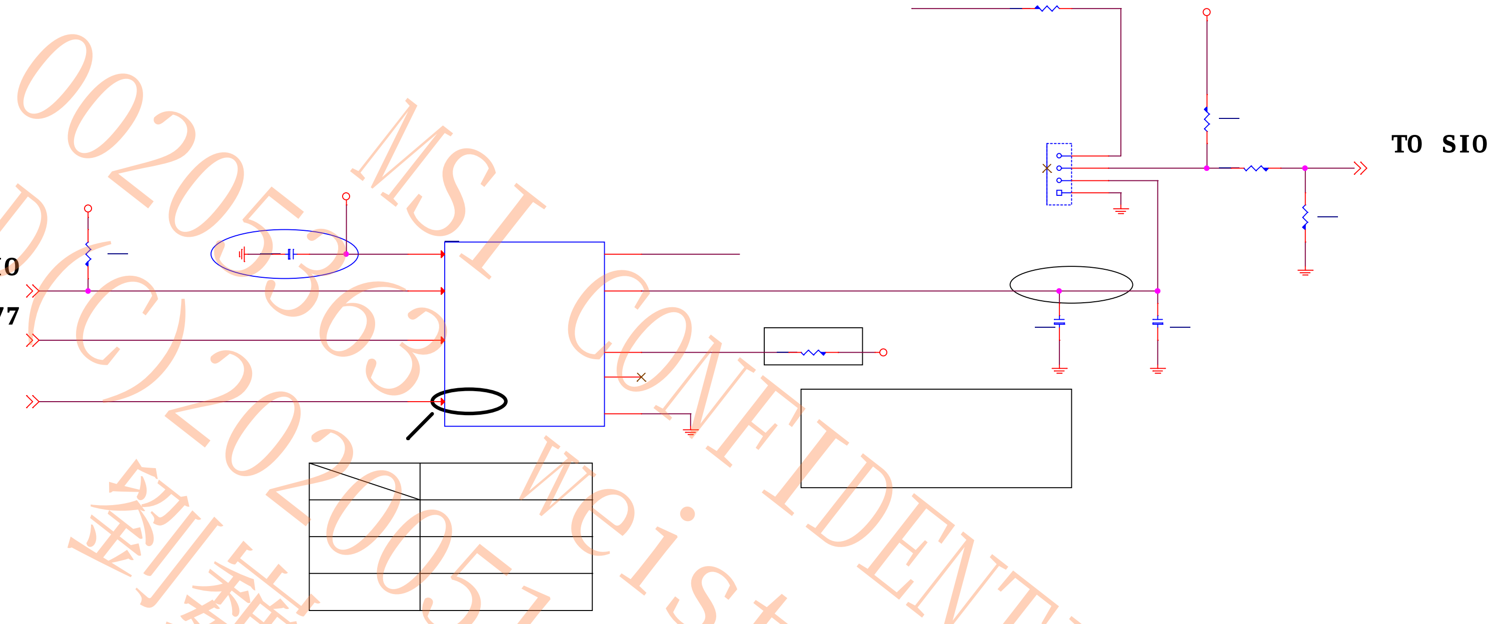
POWER ON STRAPPING PIN FOR NCT6687





TYPE M : 4 PIN CPU FAN USE NCT3961S USE PCH GPIO CONTROL FAN MODE

1.Mode GPIO BIOS can swtich PWM/DC MODE



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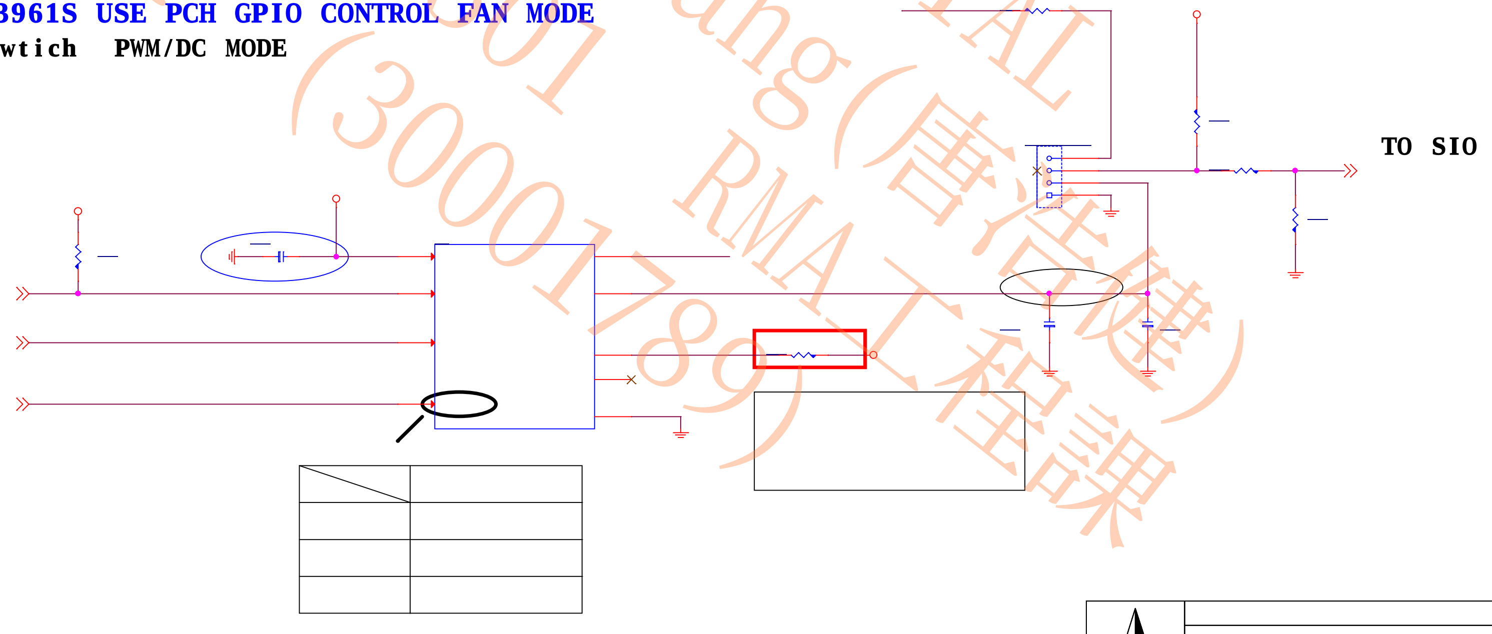

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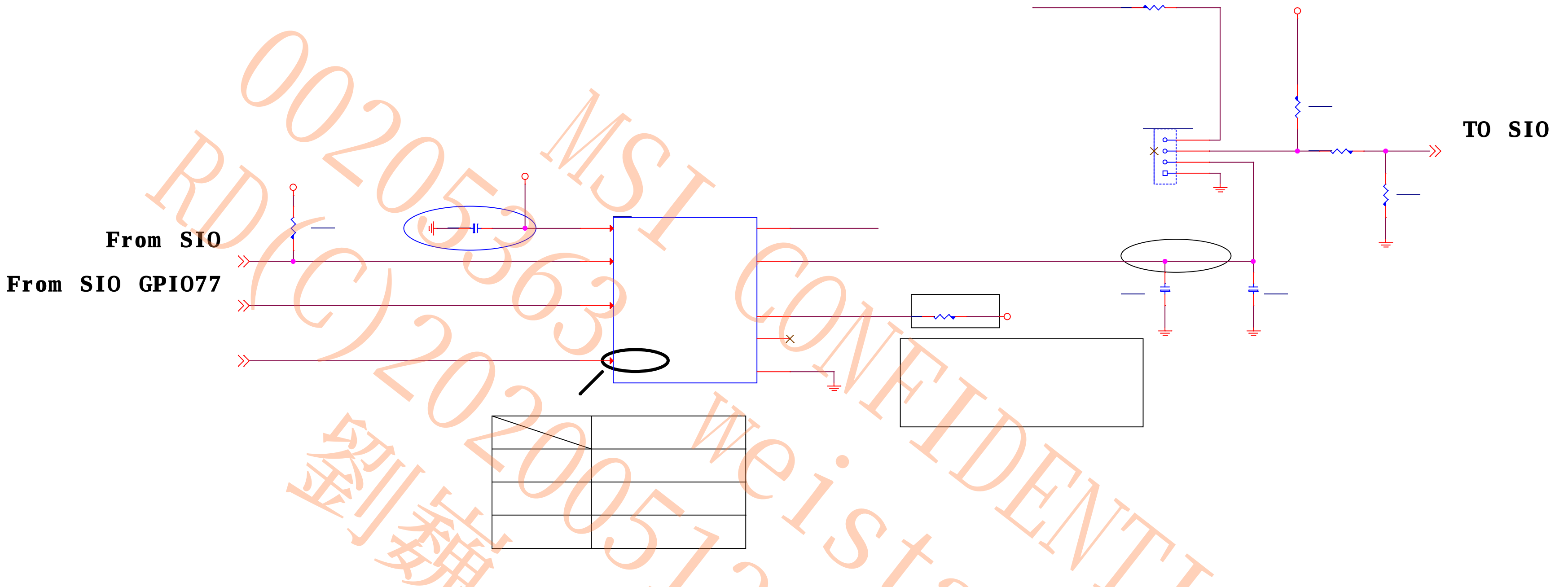

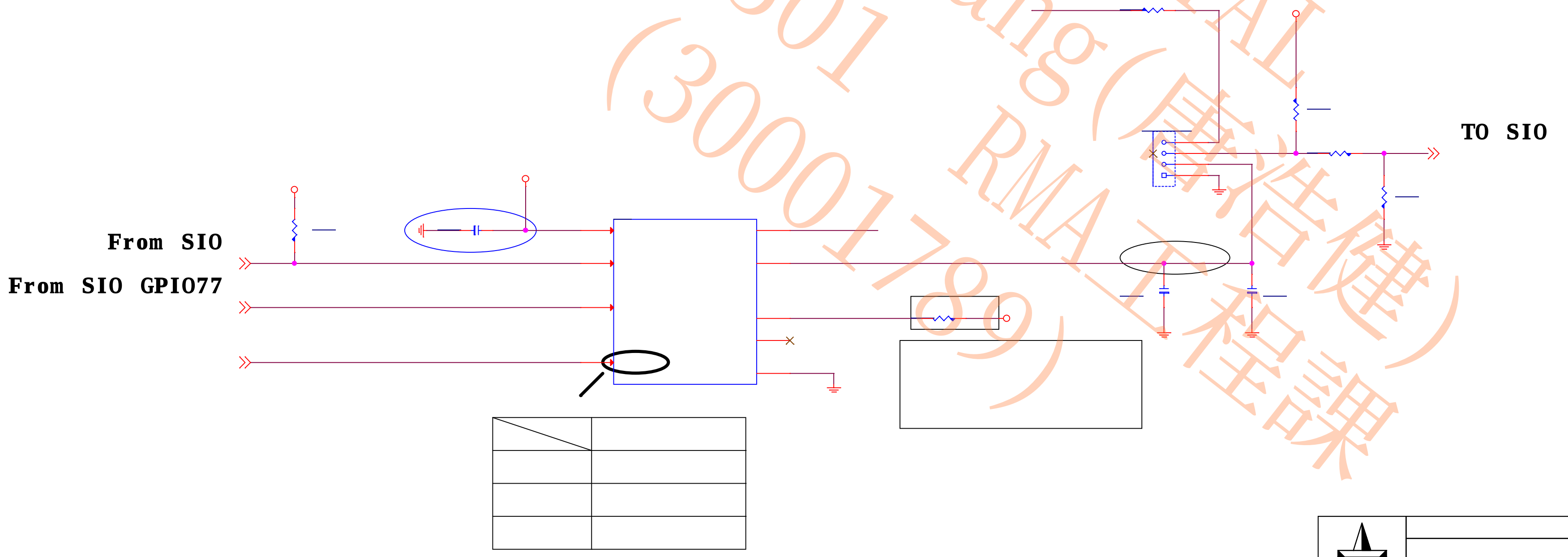

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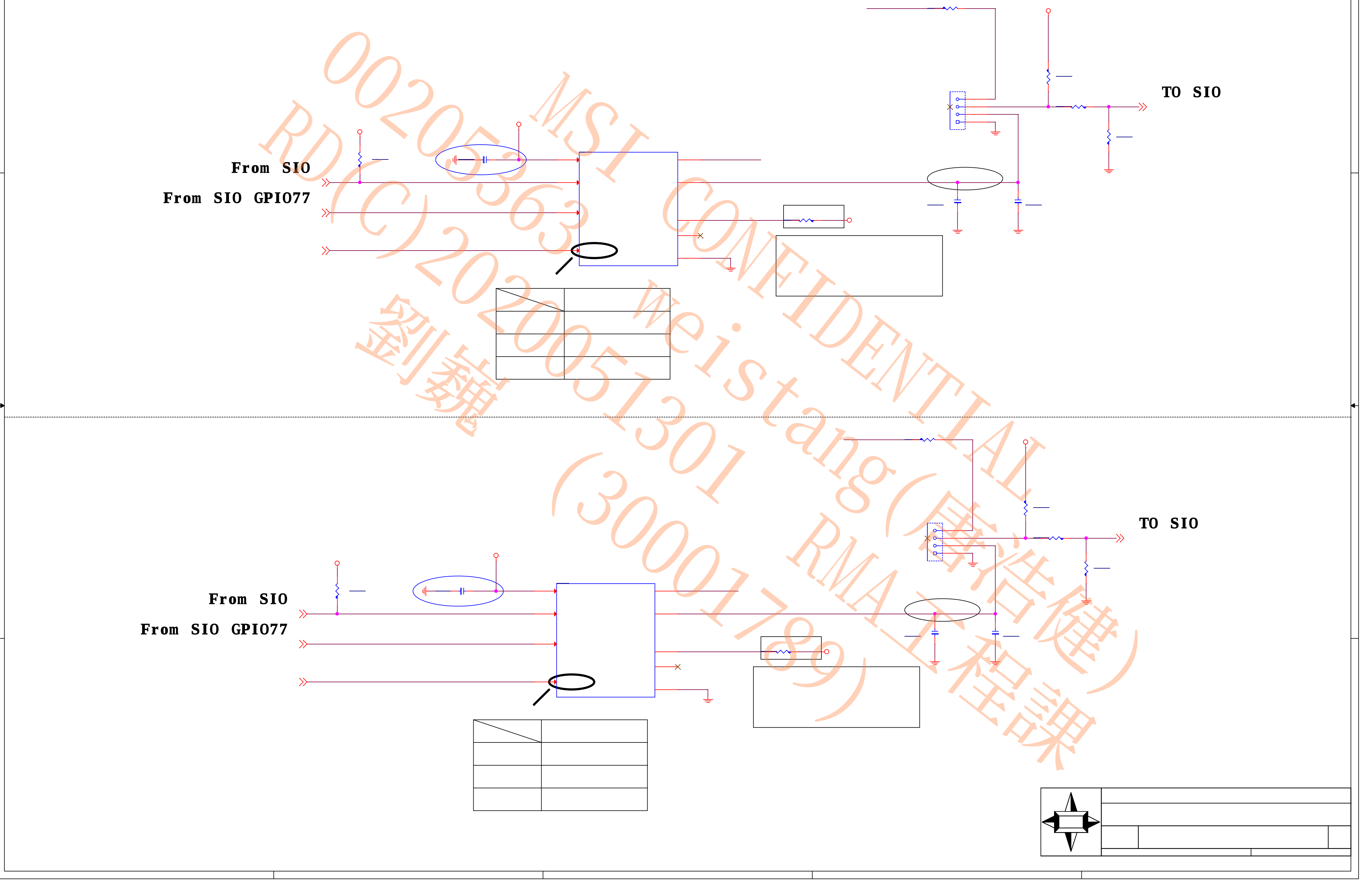

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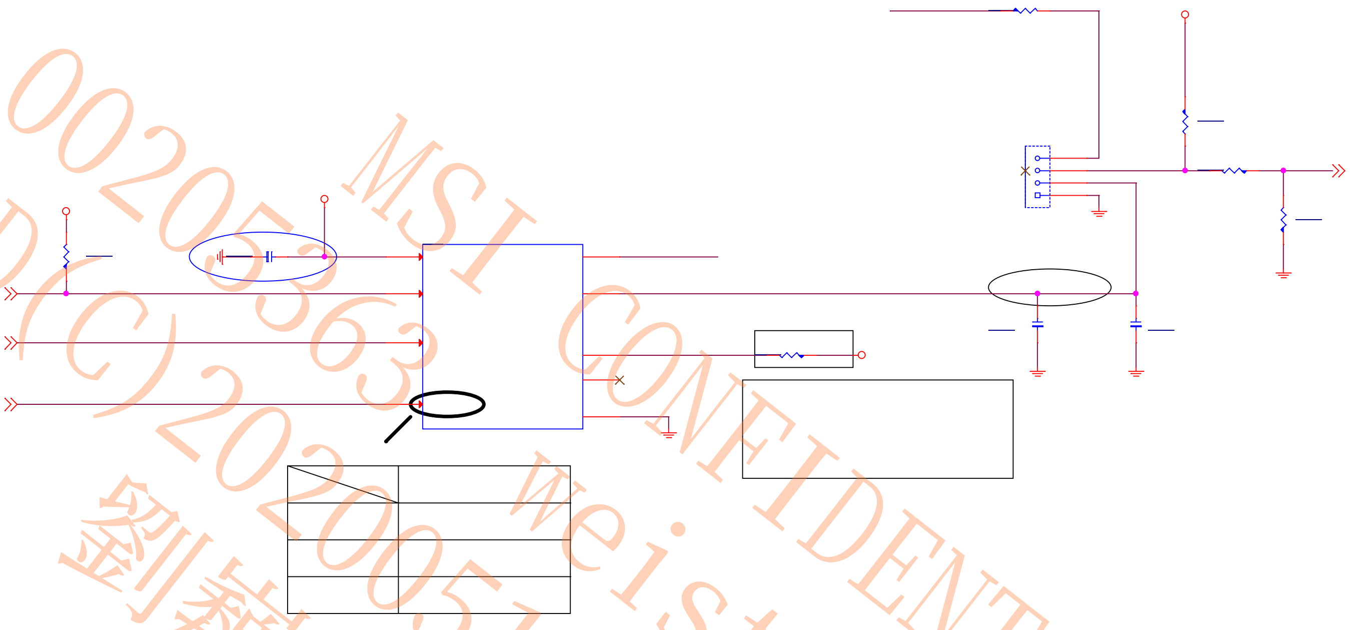
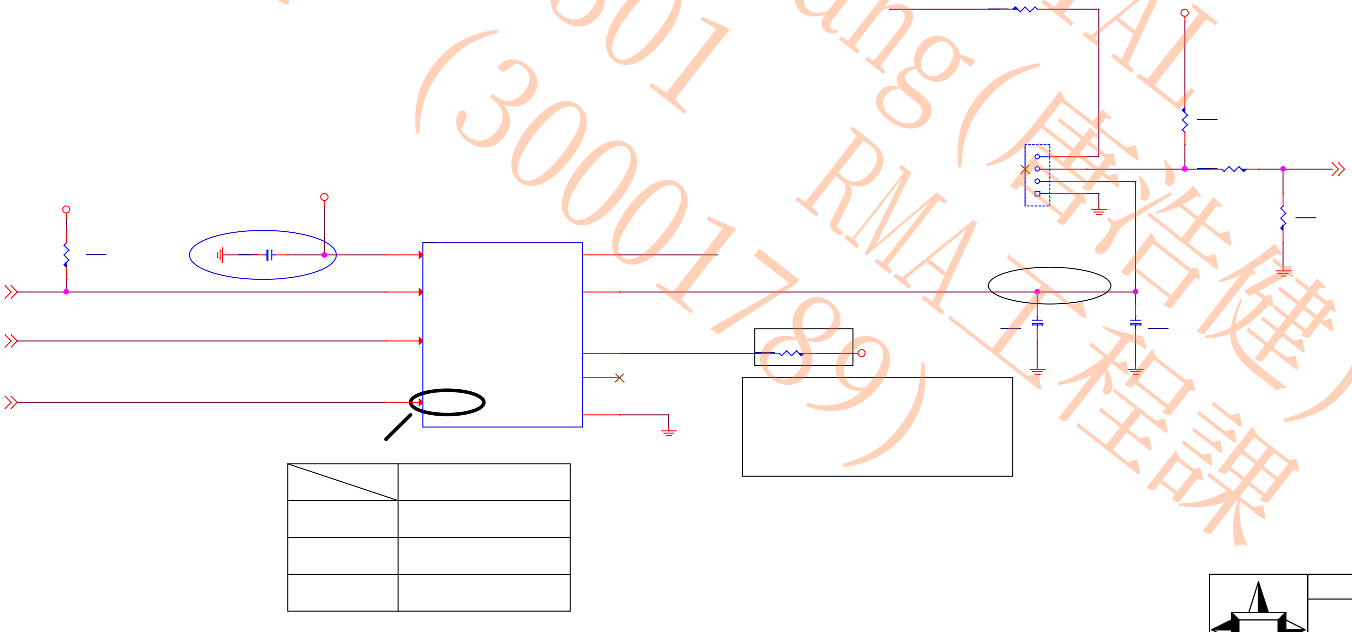
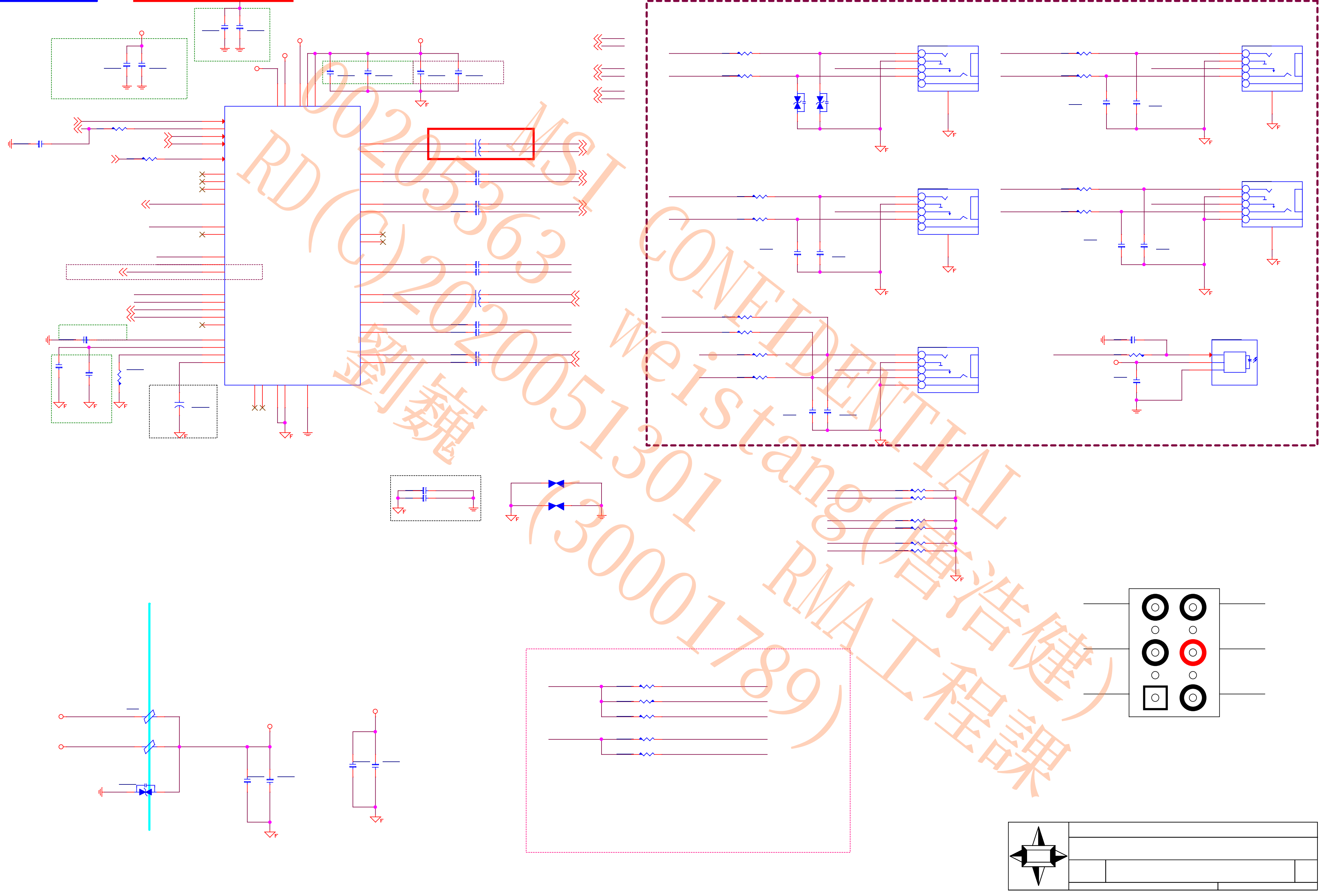
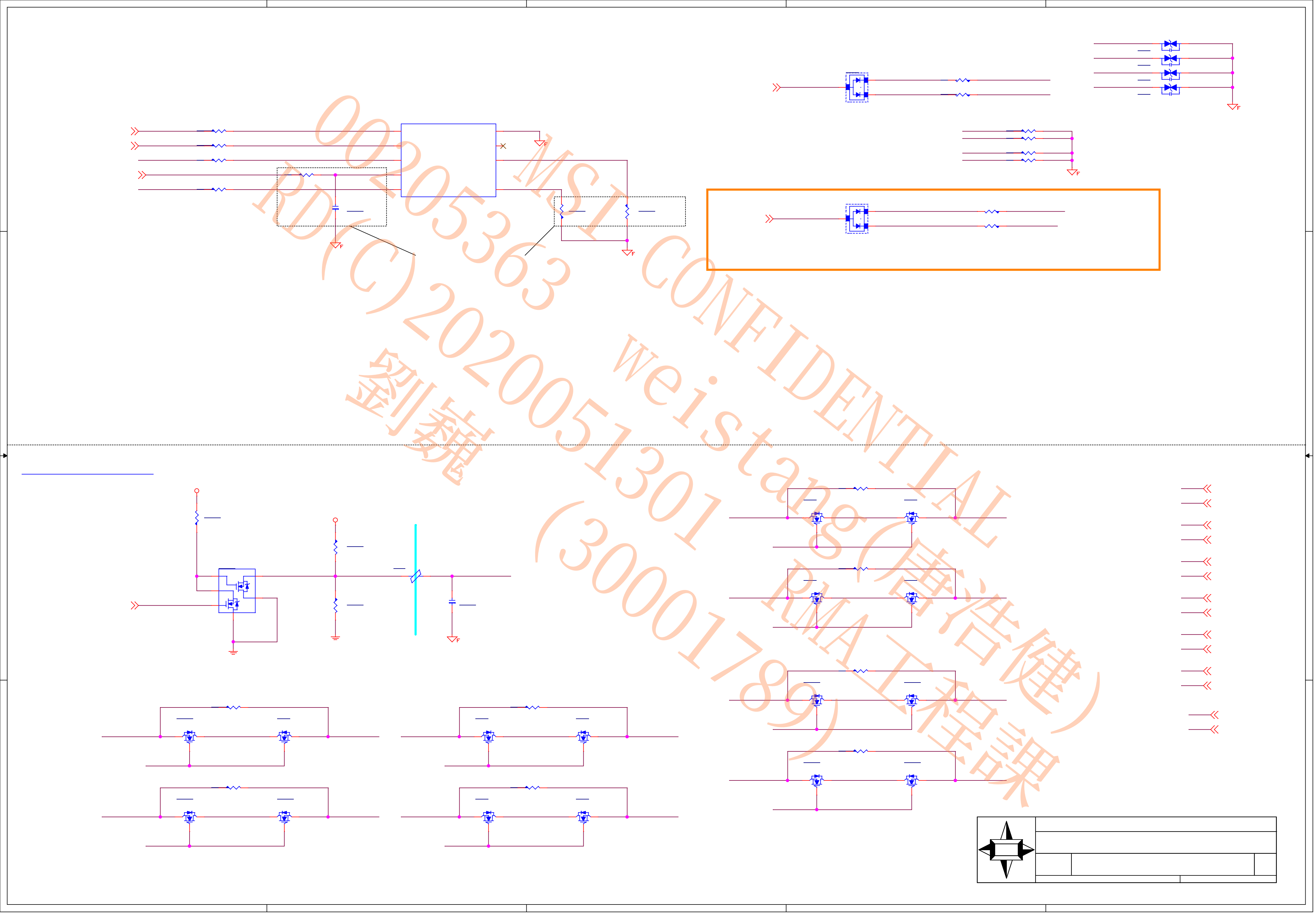
1.Mode GPIO BIOS can swtich PWM/DC MODE

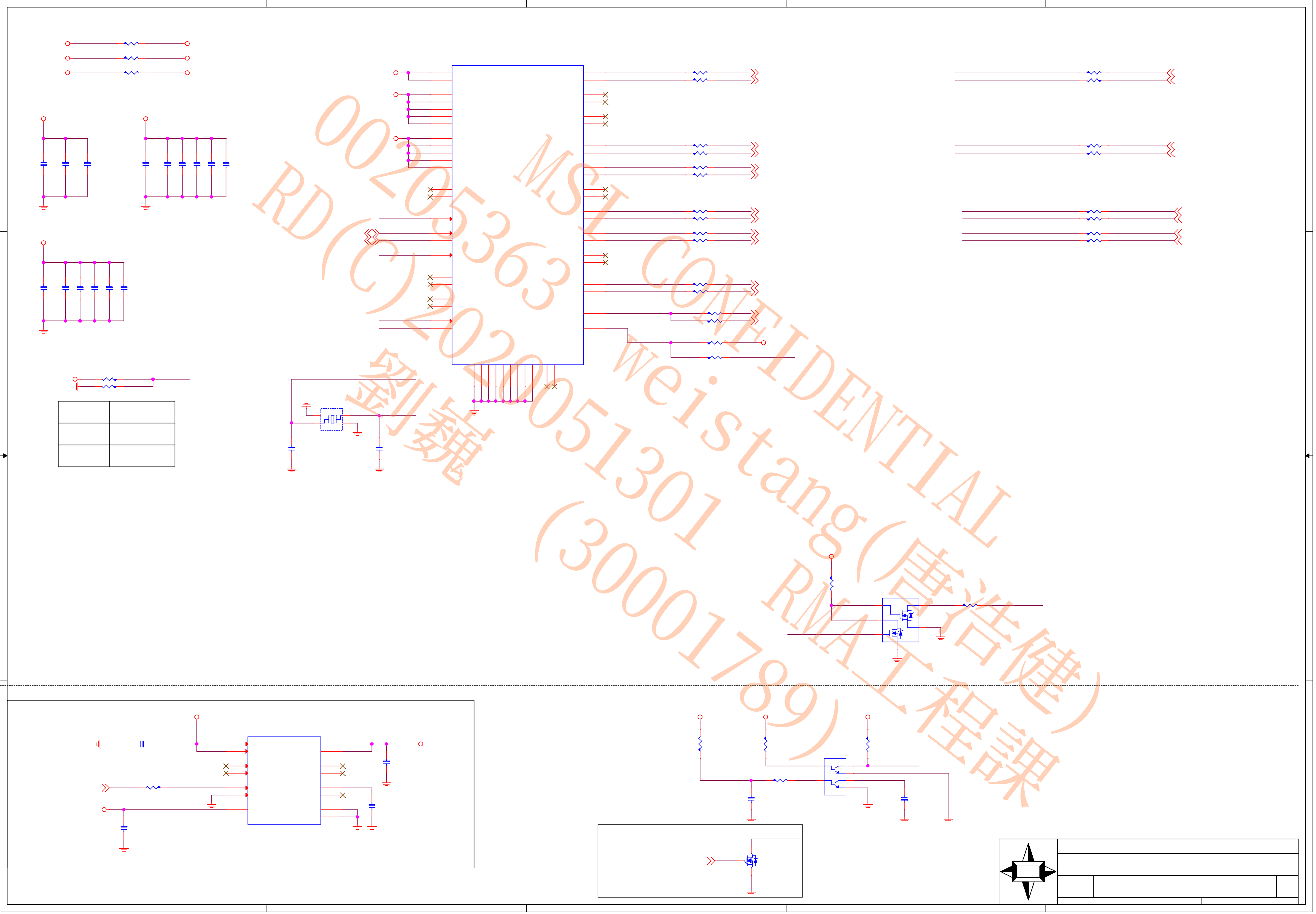




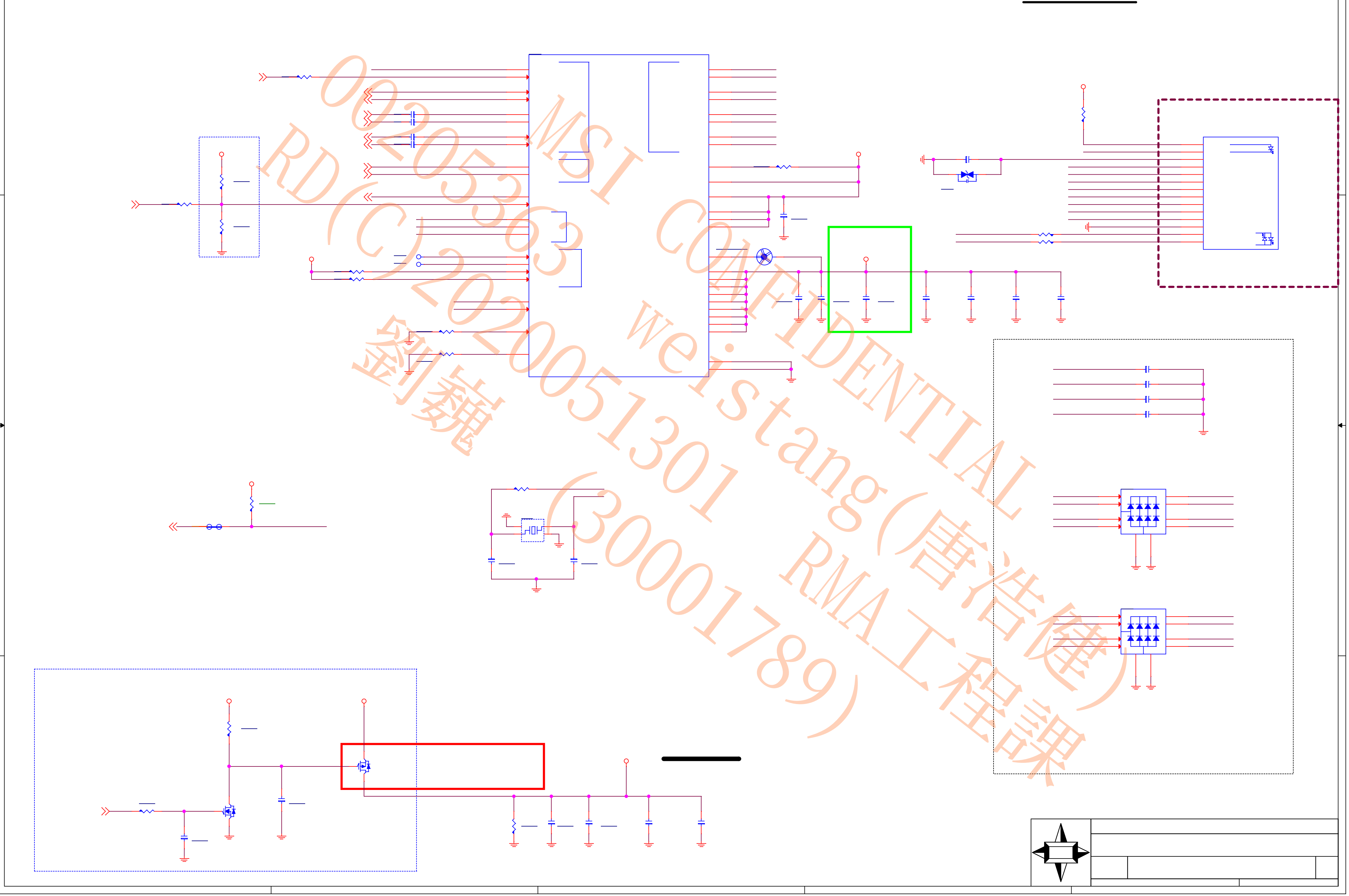
ALC1200



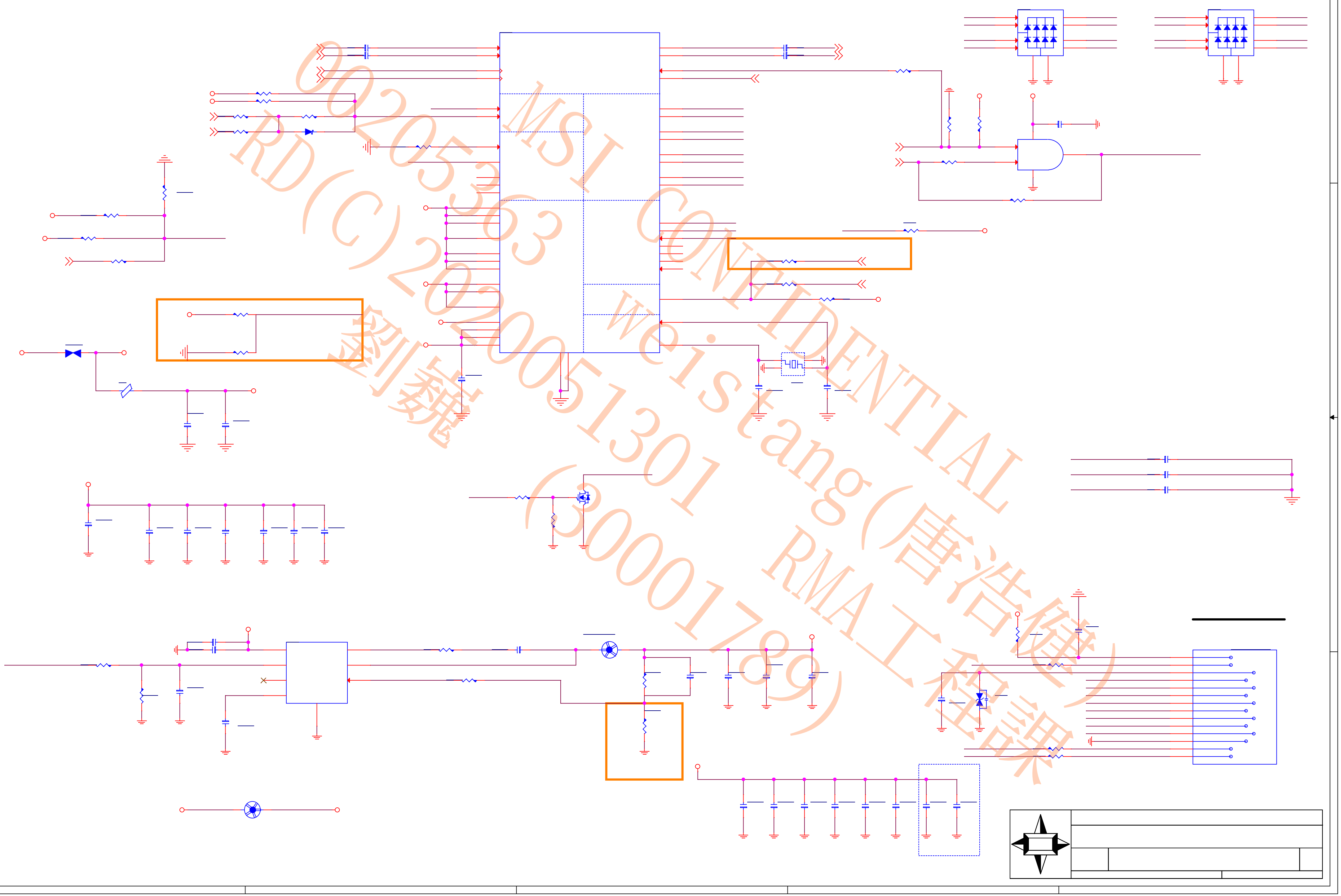




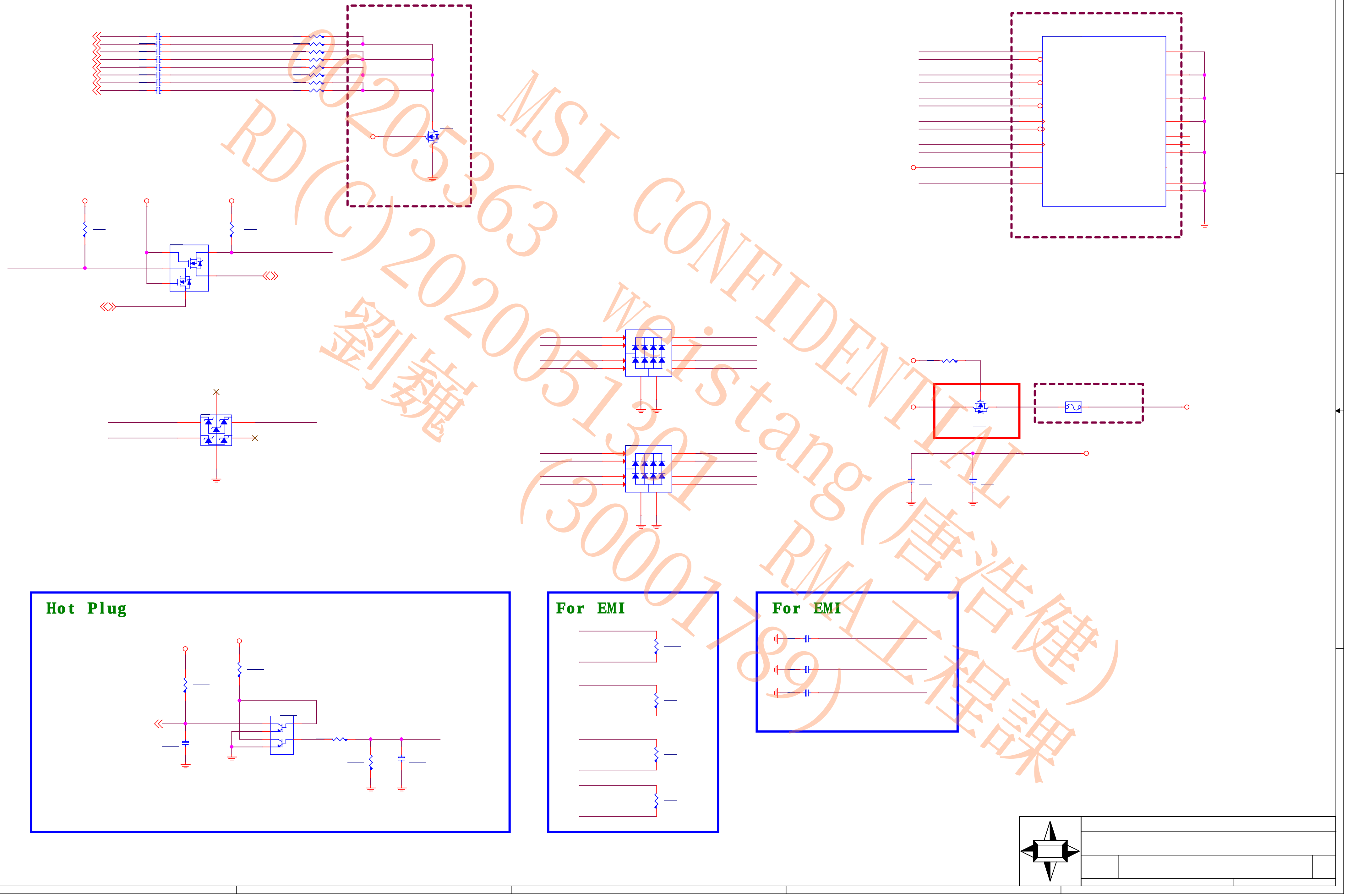
Intel Lan2- i219

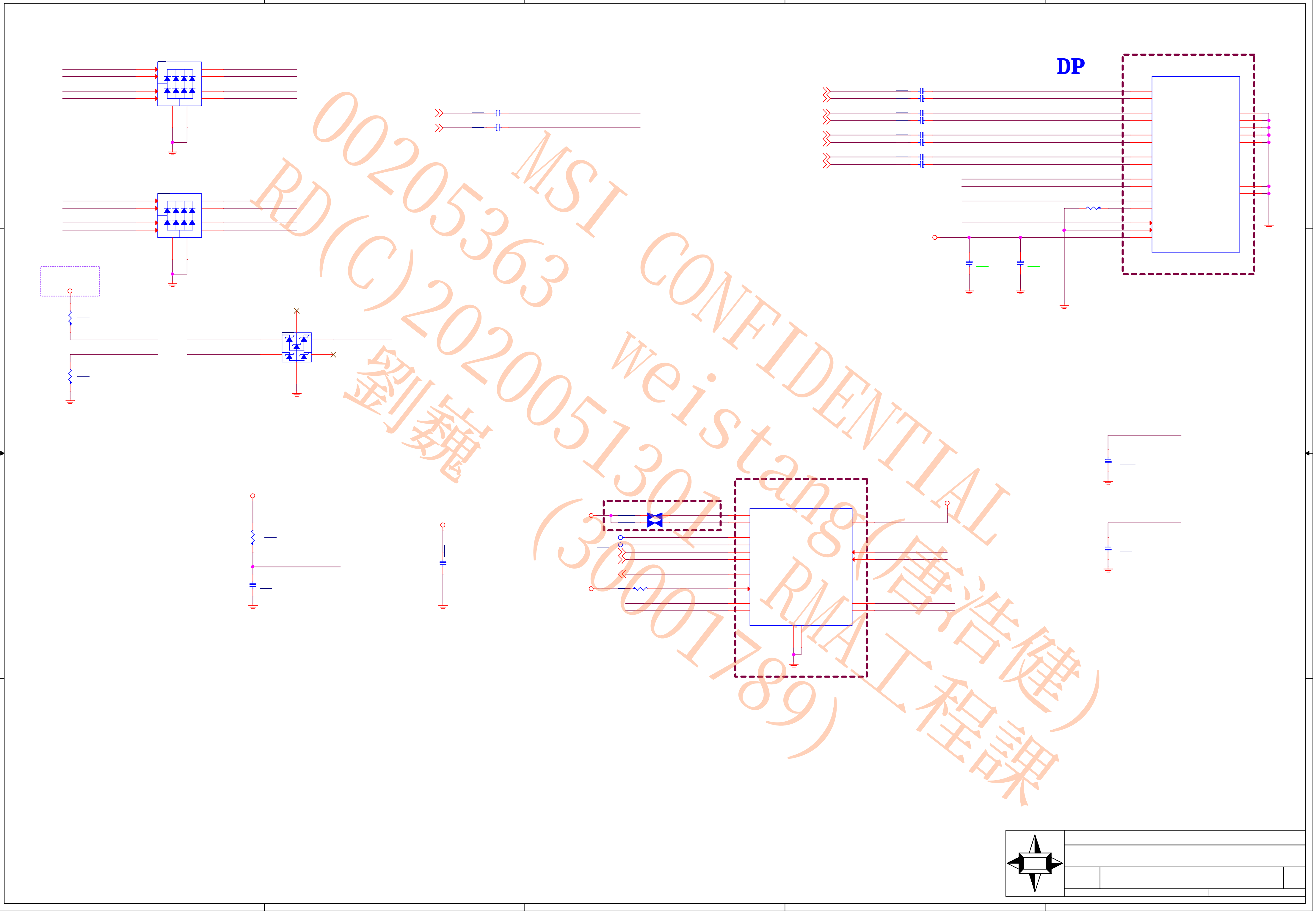


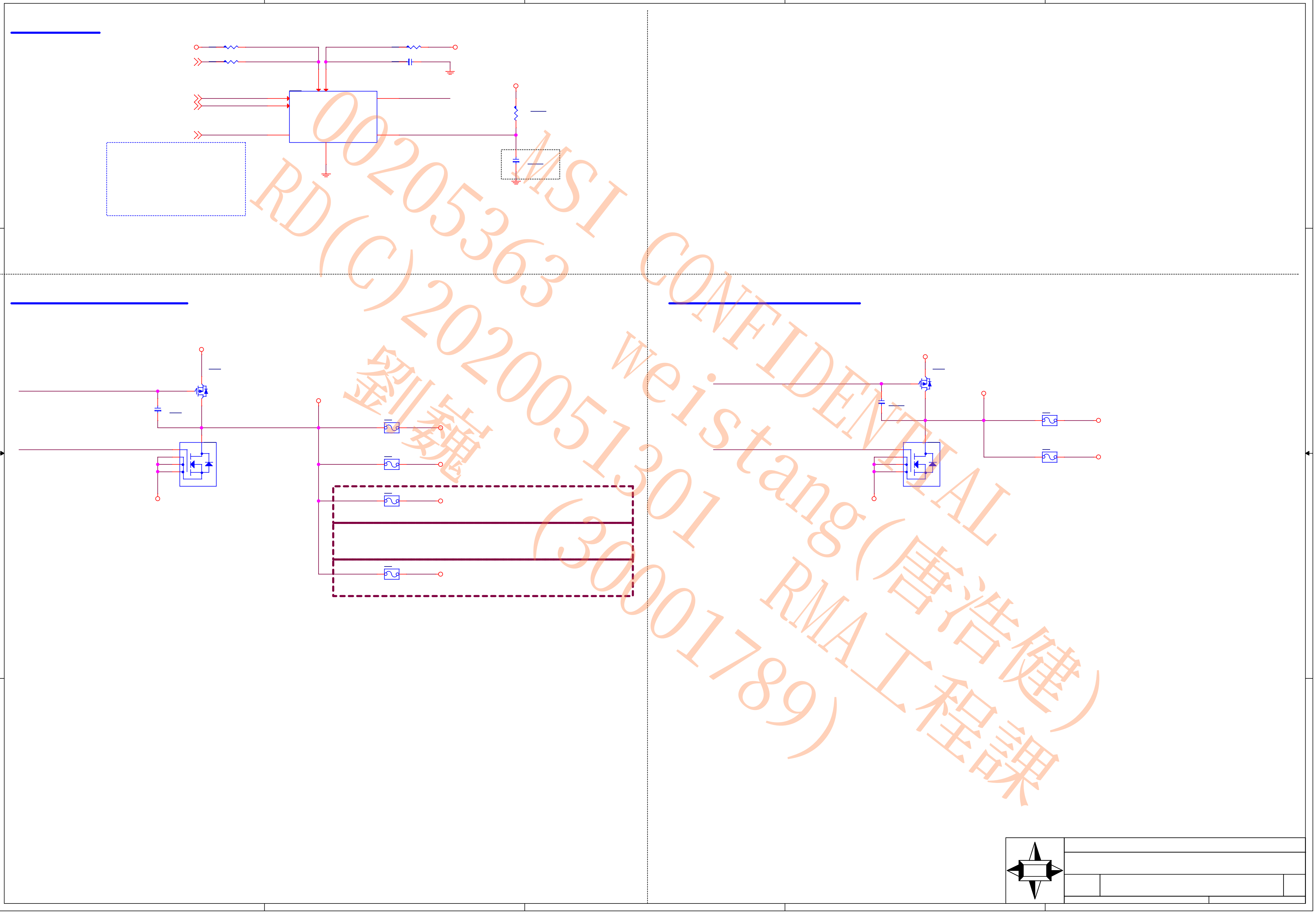
Realtek Lan1-RTL8125B



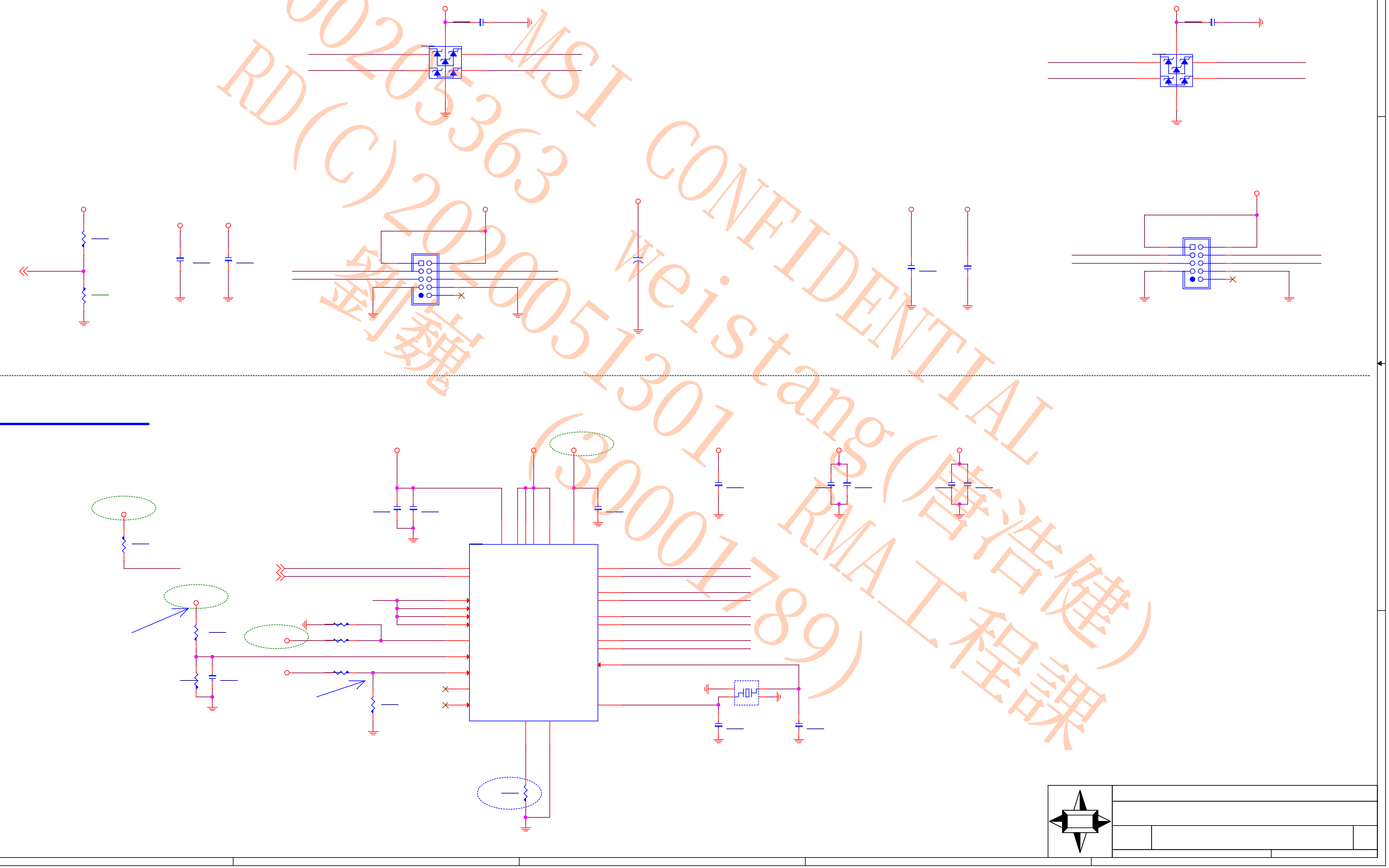
HDMI 1.4b



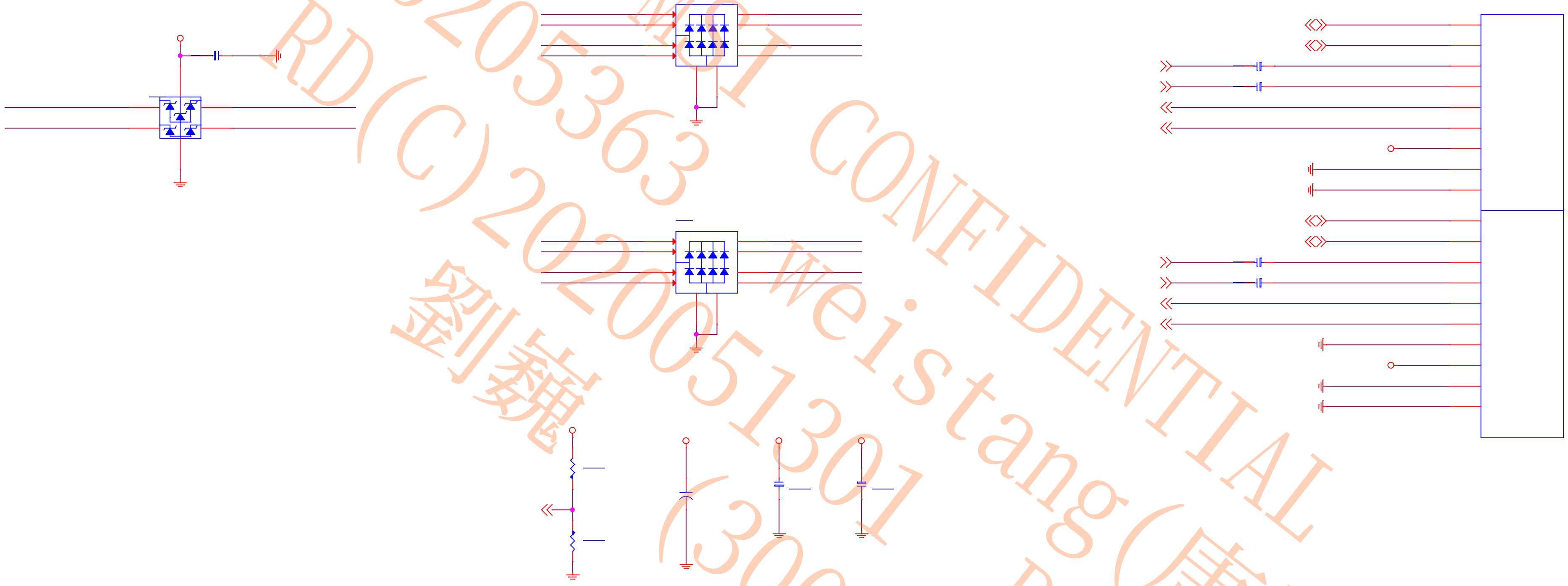




Front USB2.0



Front USB3.1 Gen1



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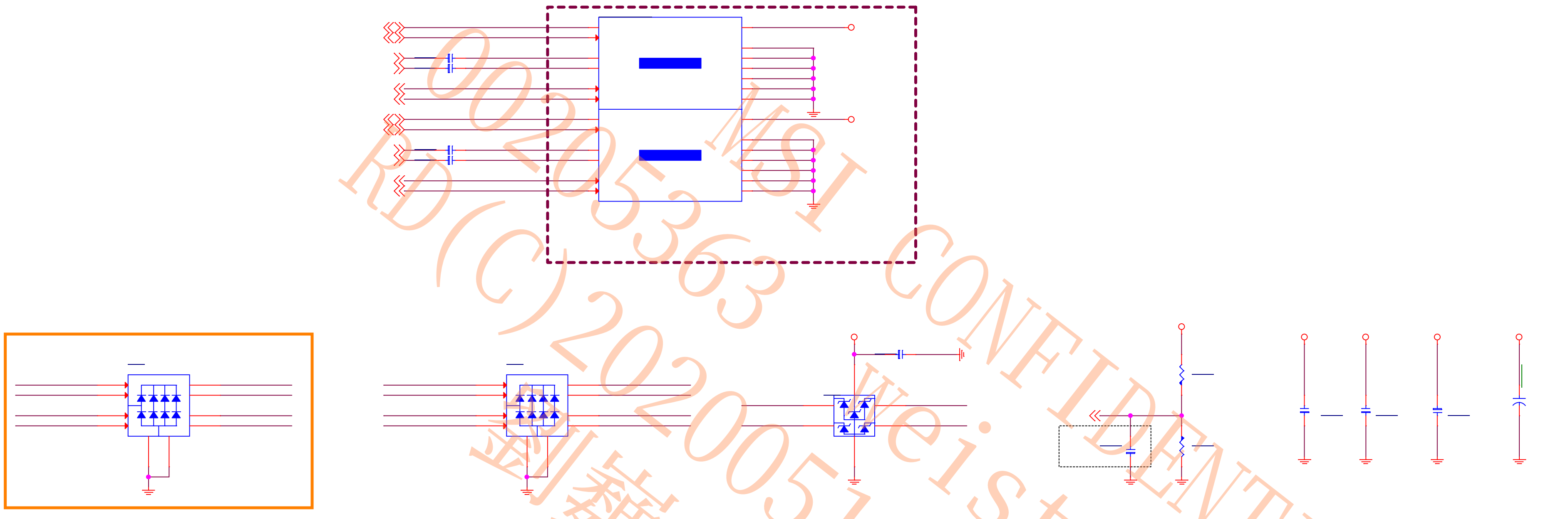
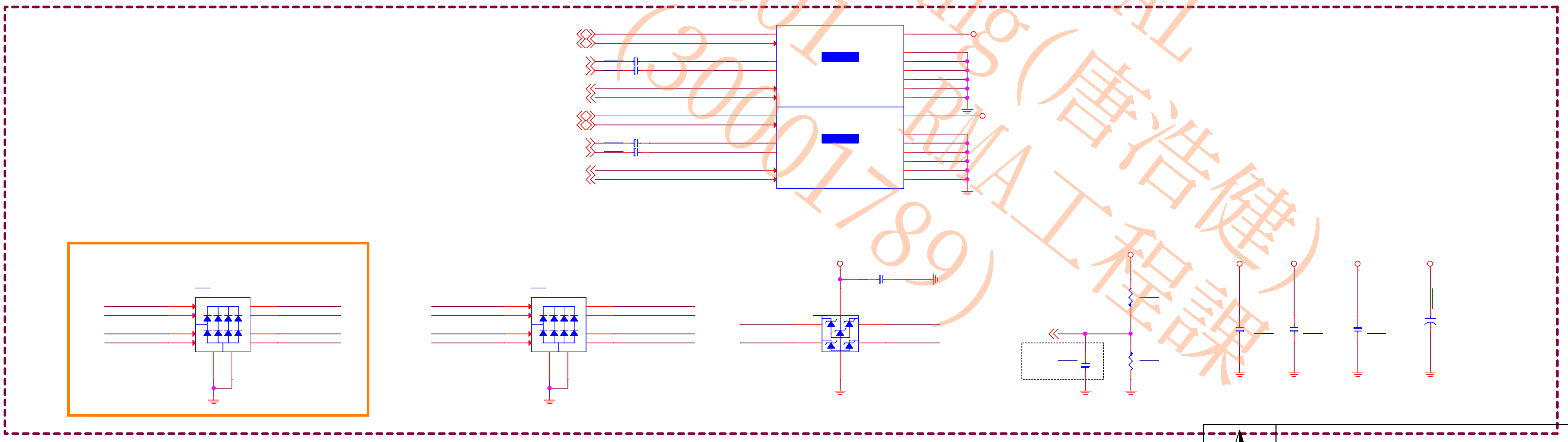
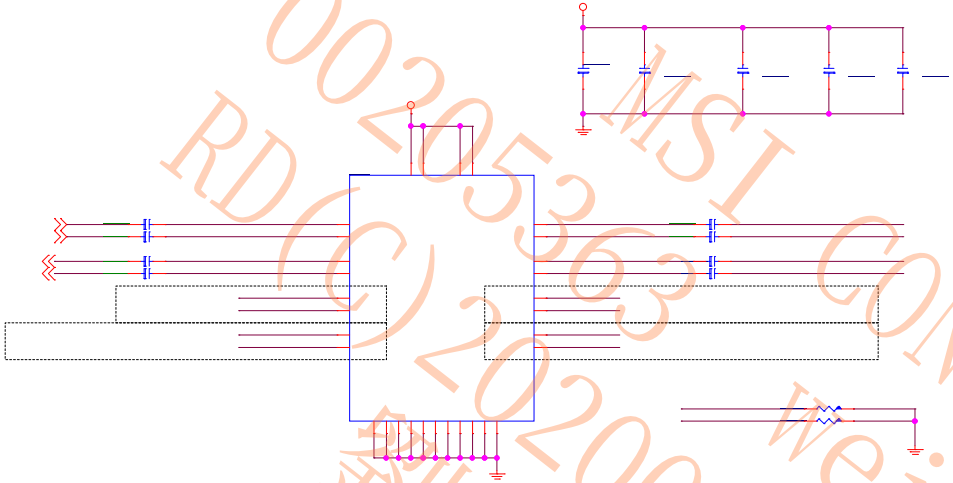
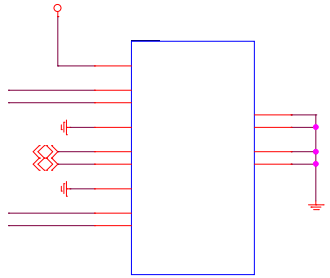
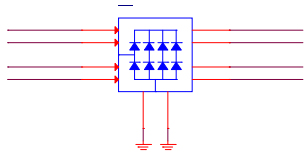


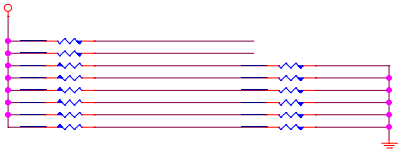
Rear LAN1 USB3.1 Gen1

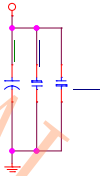


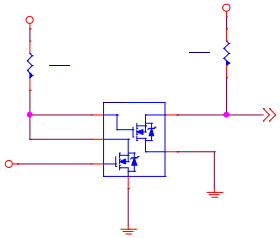






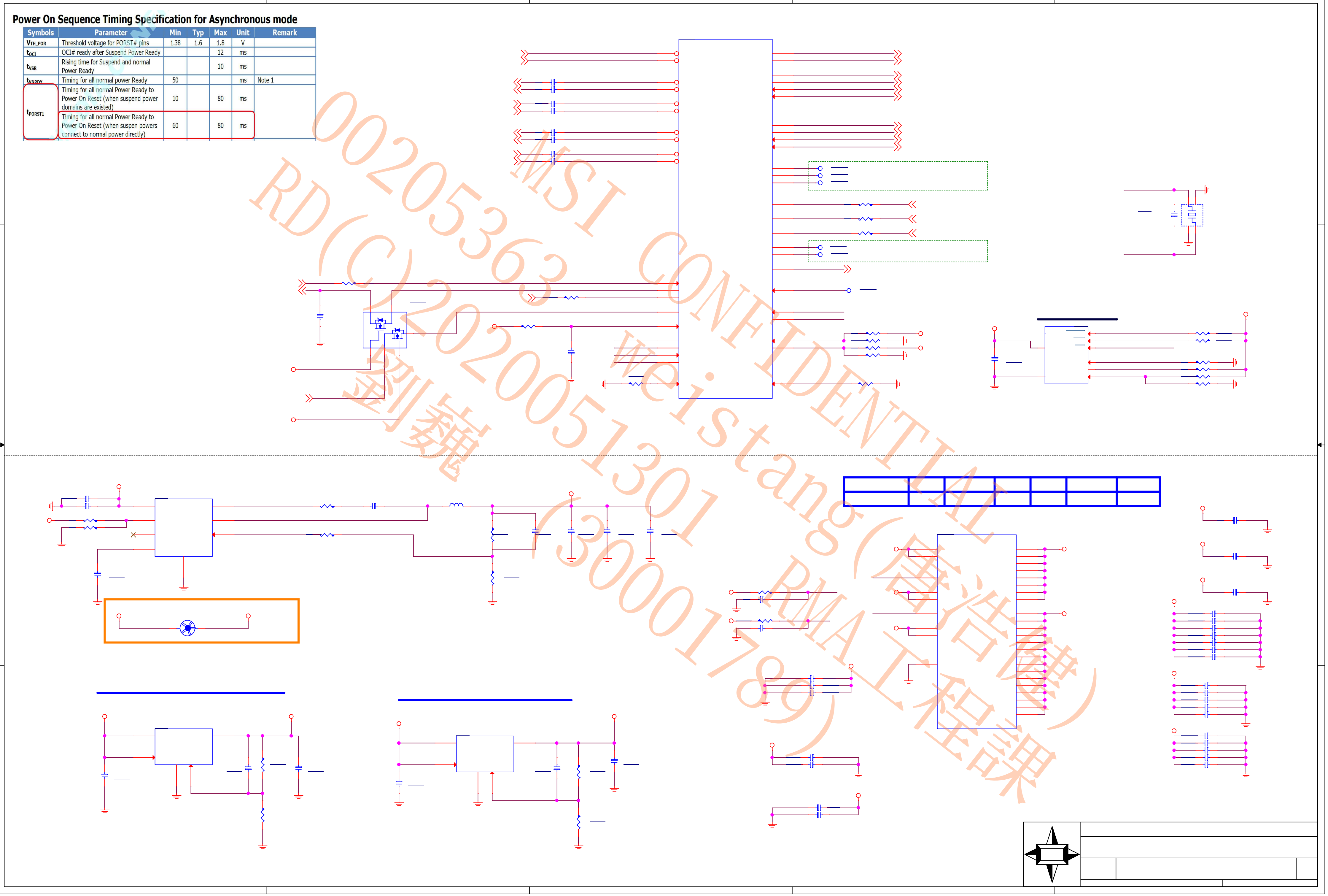


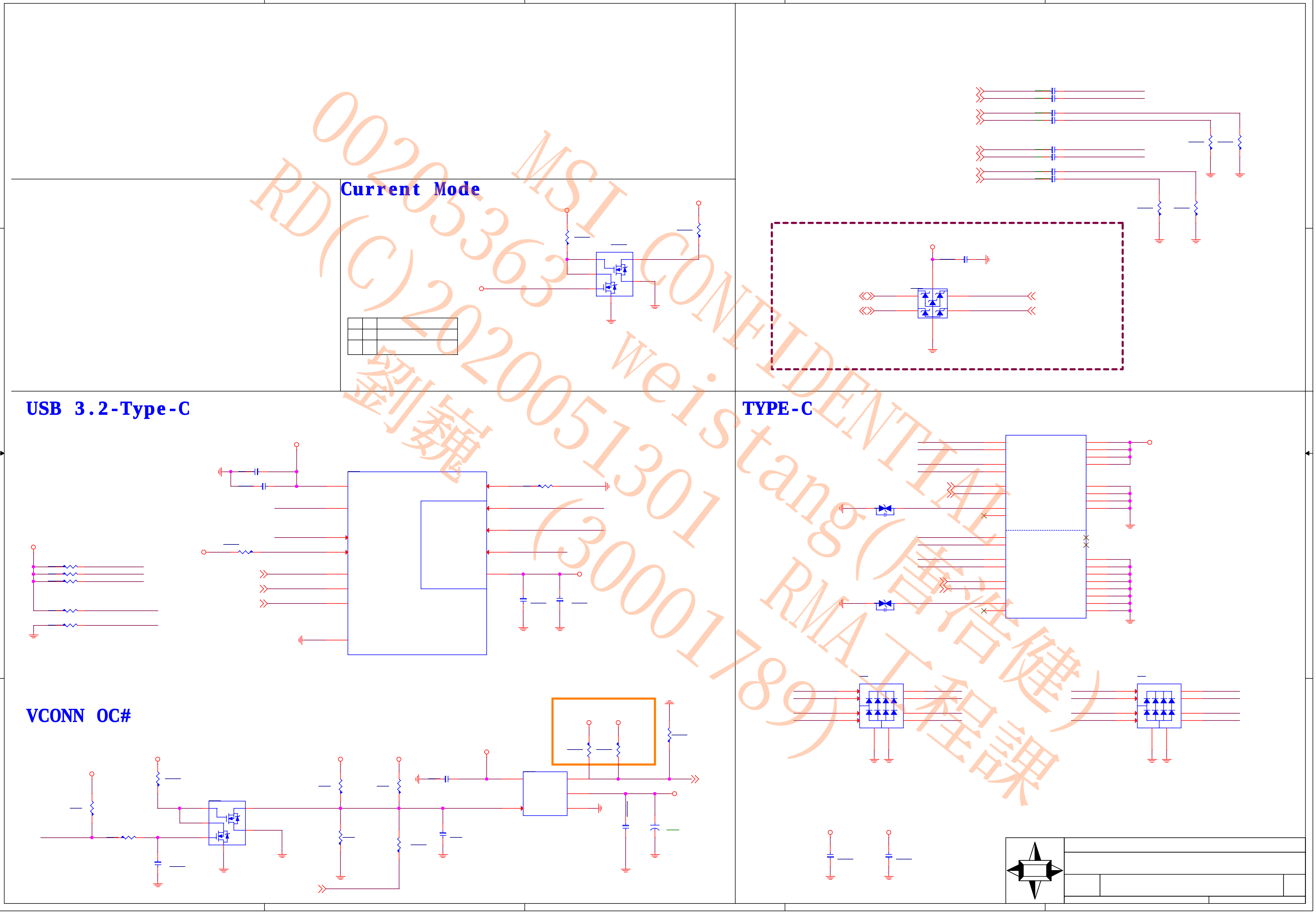


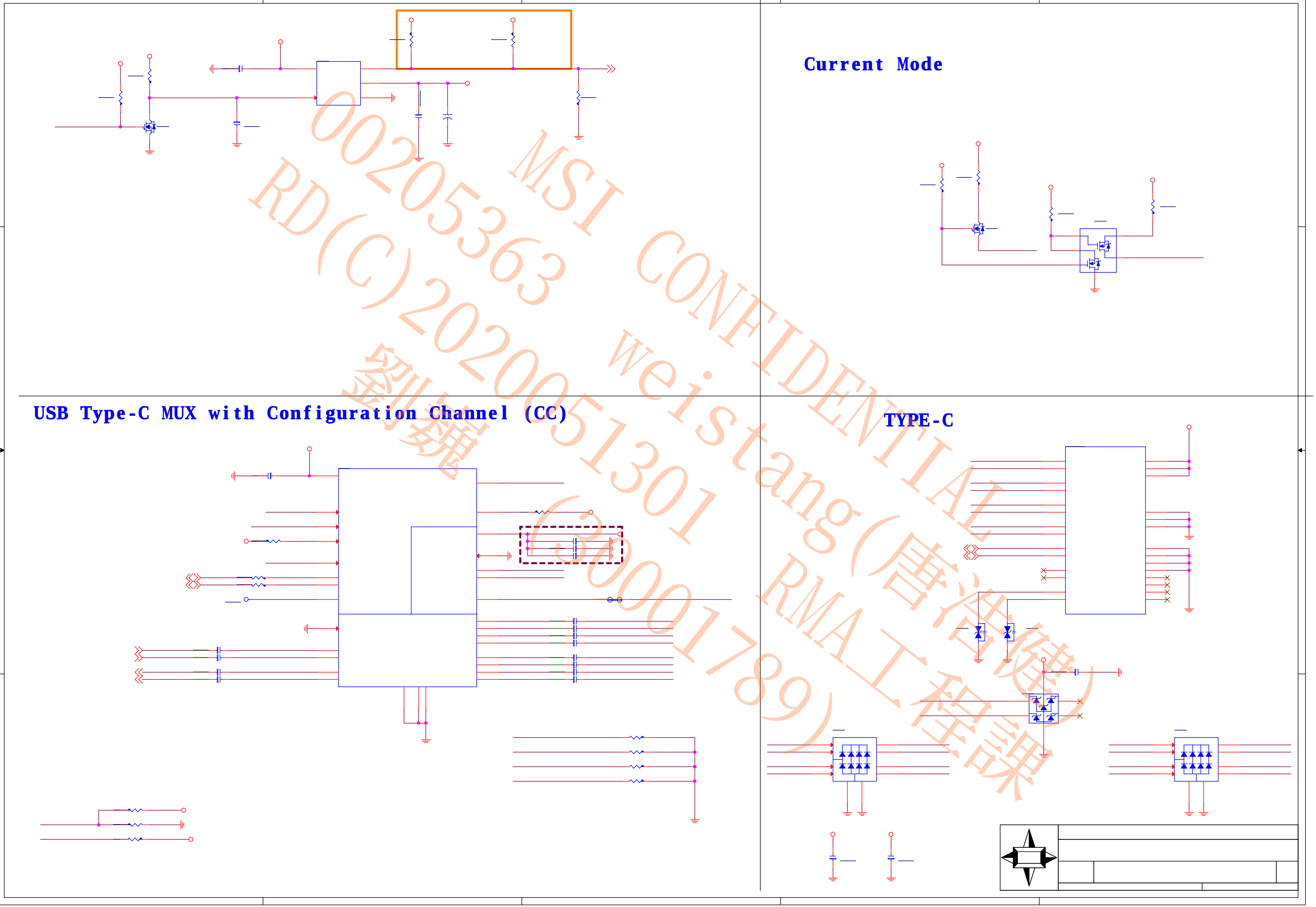


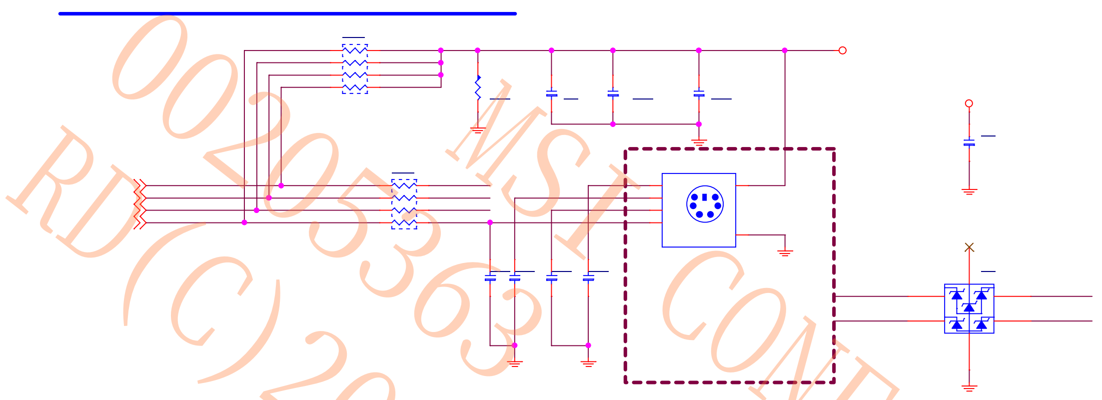
Power On Sequence Timing Specification for Asynchronous mode

Symbols	Parameter	Min	Typ	Max	Unit	Remark
V_{TH_POR}	Threshold voltage for PORST# pins	1.38	1.6	1.8	V	
t_{OCI}	OCI# ready after Suspend Power Ready			12	ms	
t_{VSR}	Rising time for Suspend and normal Power Ready			10	ms	
t_{VNRDY}	Timing for all normal power Ready	50			ms	Note 1
t_{PORST1}	Timing for all normal Power Ready to Power On Reset (when suspend power domains are existed)	10		80	ms	
	Timing for all normal Power Ready to Power On Reset (when suspen powers connect to normal power directly)	60		80	ms	

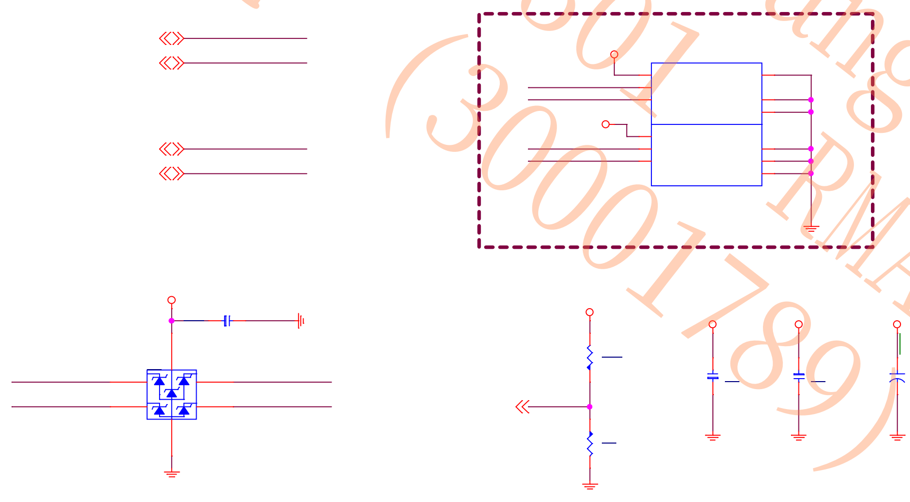




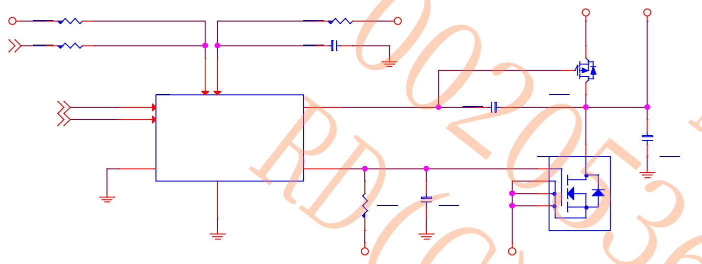




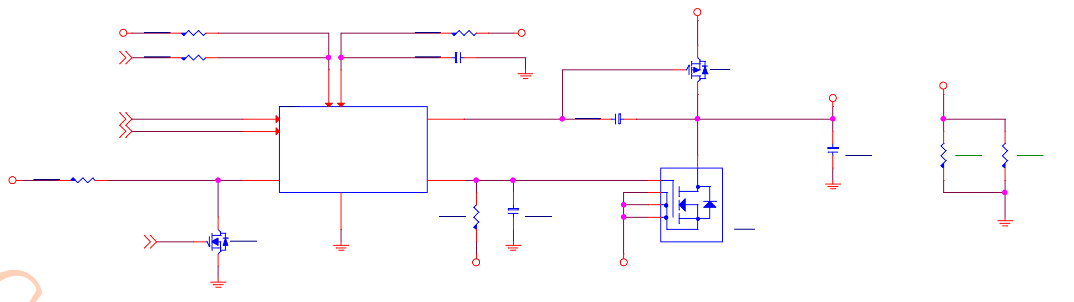
PS2 USB2.0



5VDIMM FOR DDR



5VDUAL



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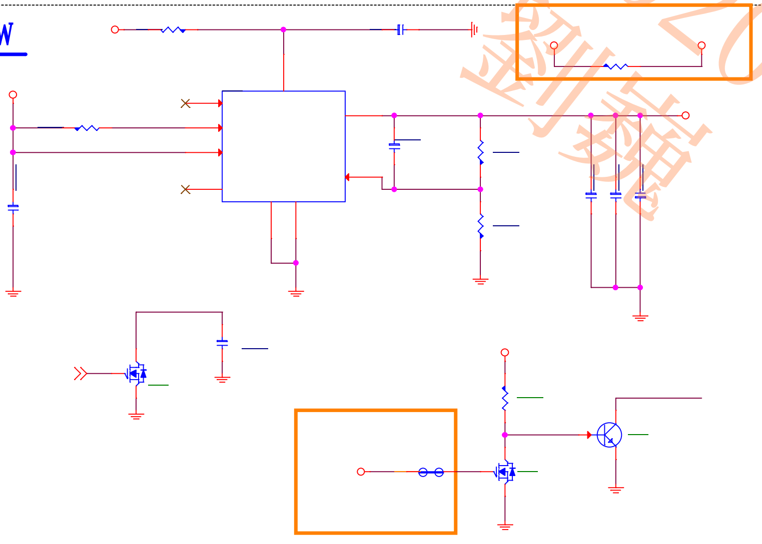

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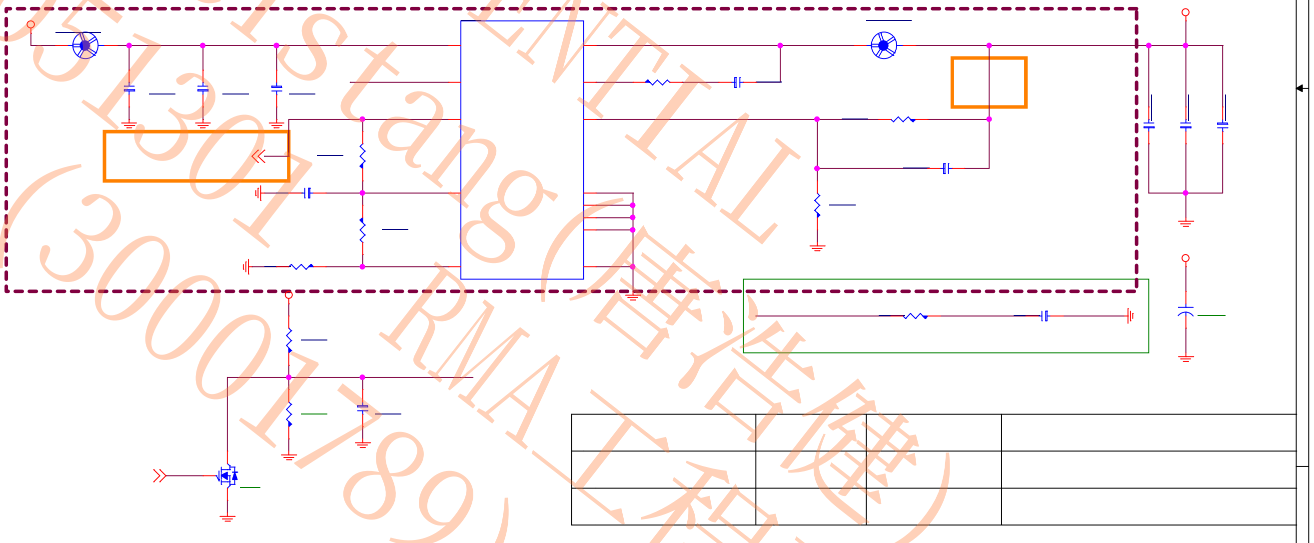

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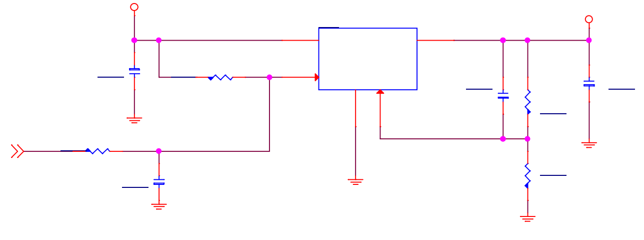
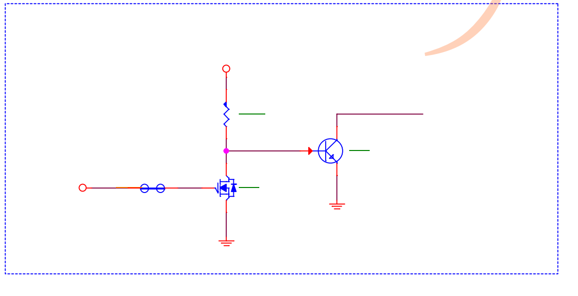
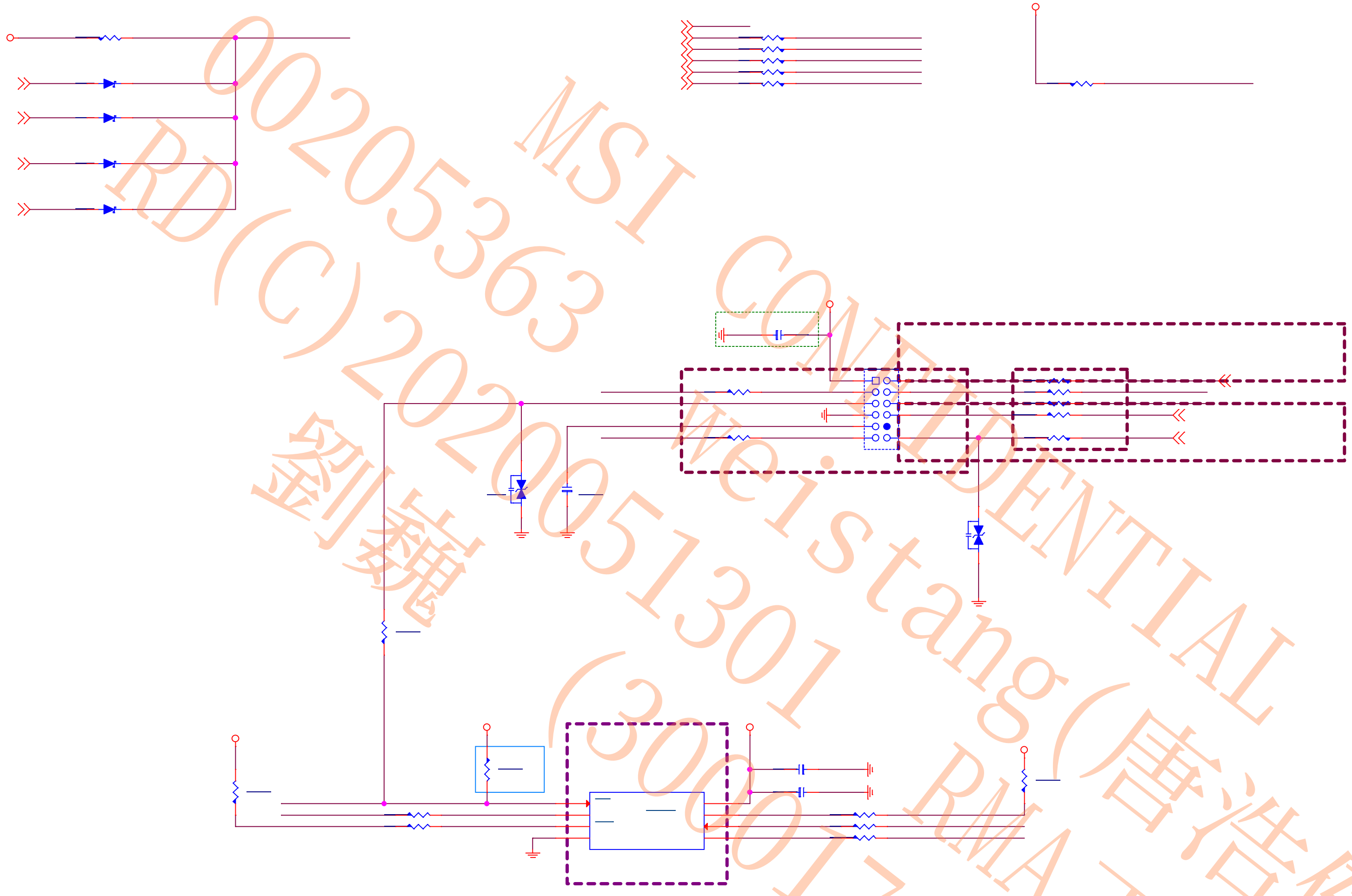
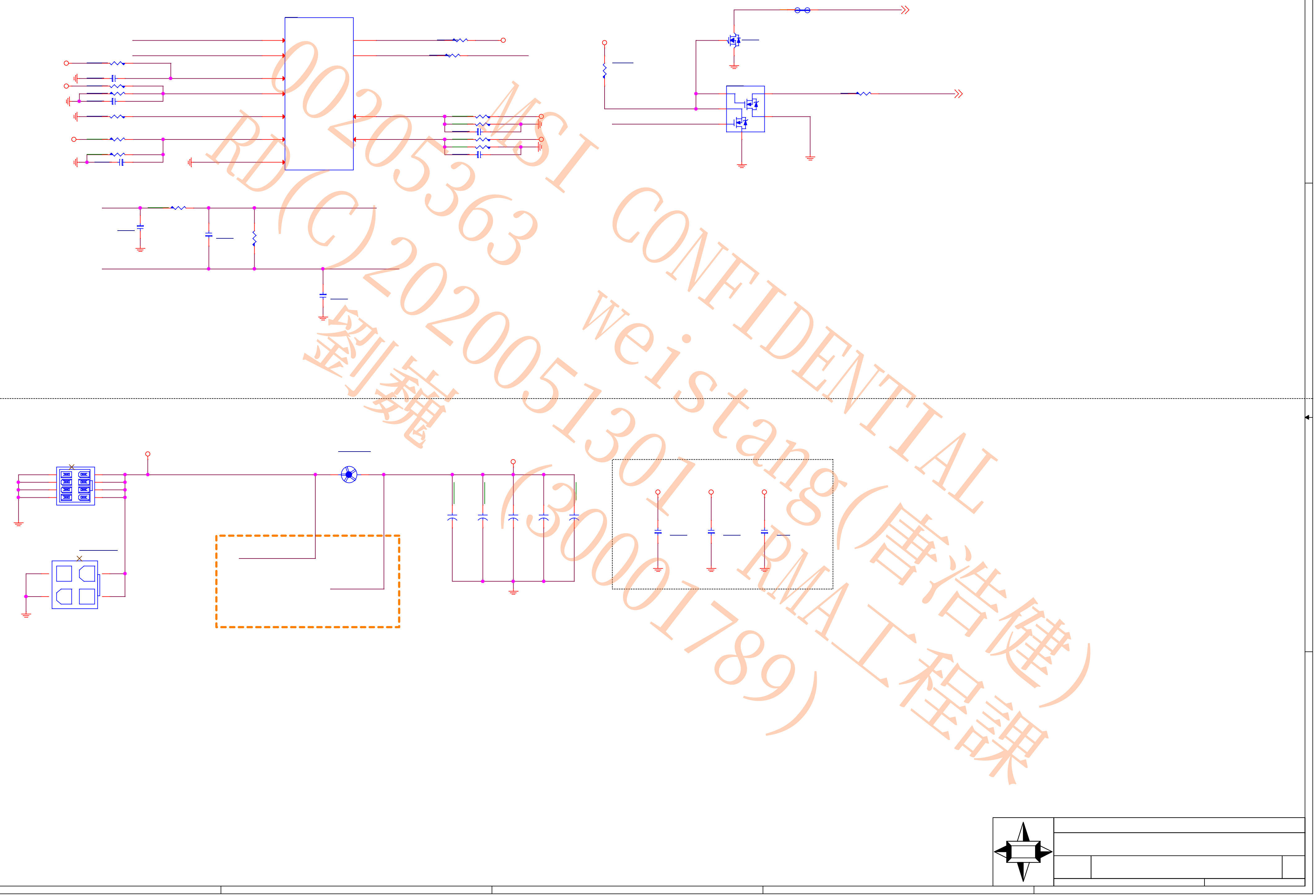
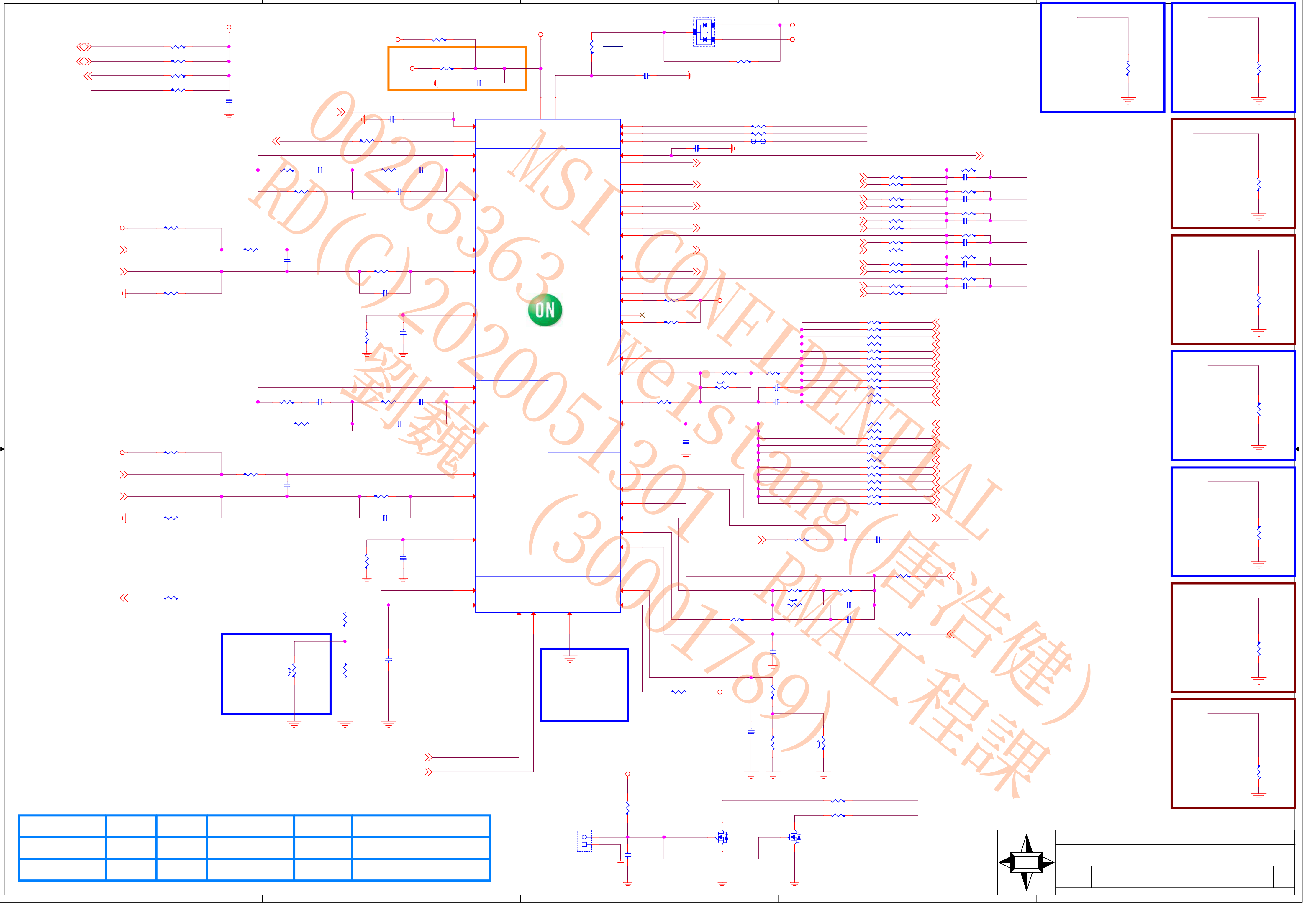


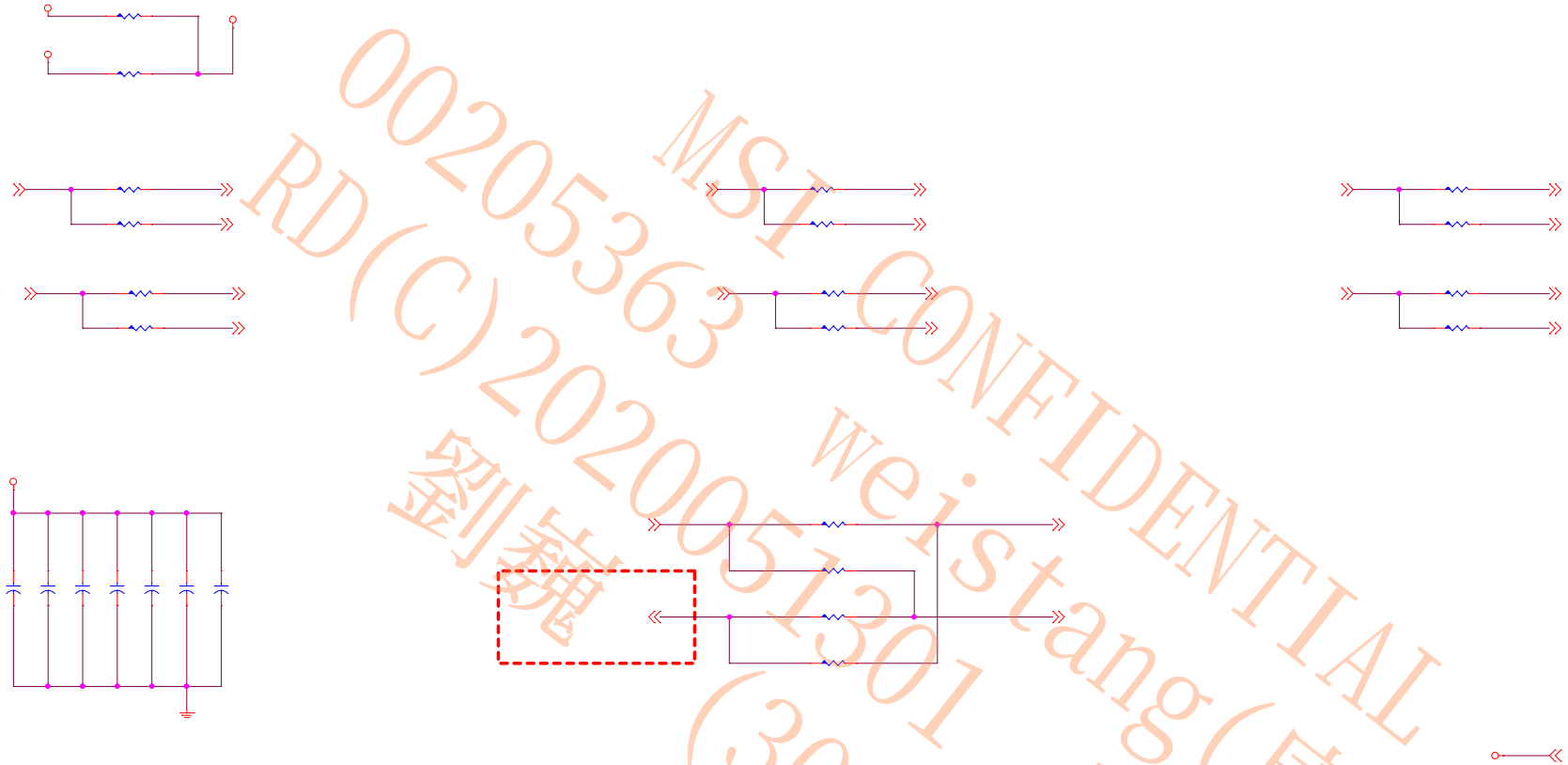


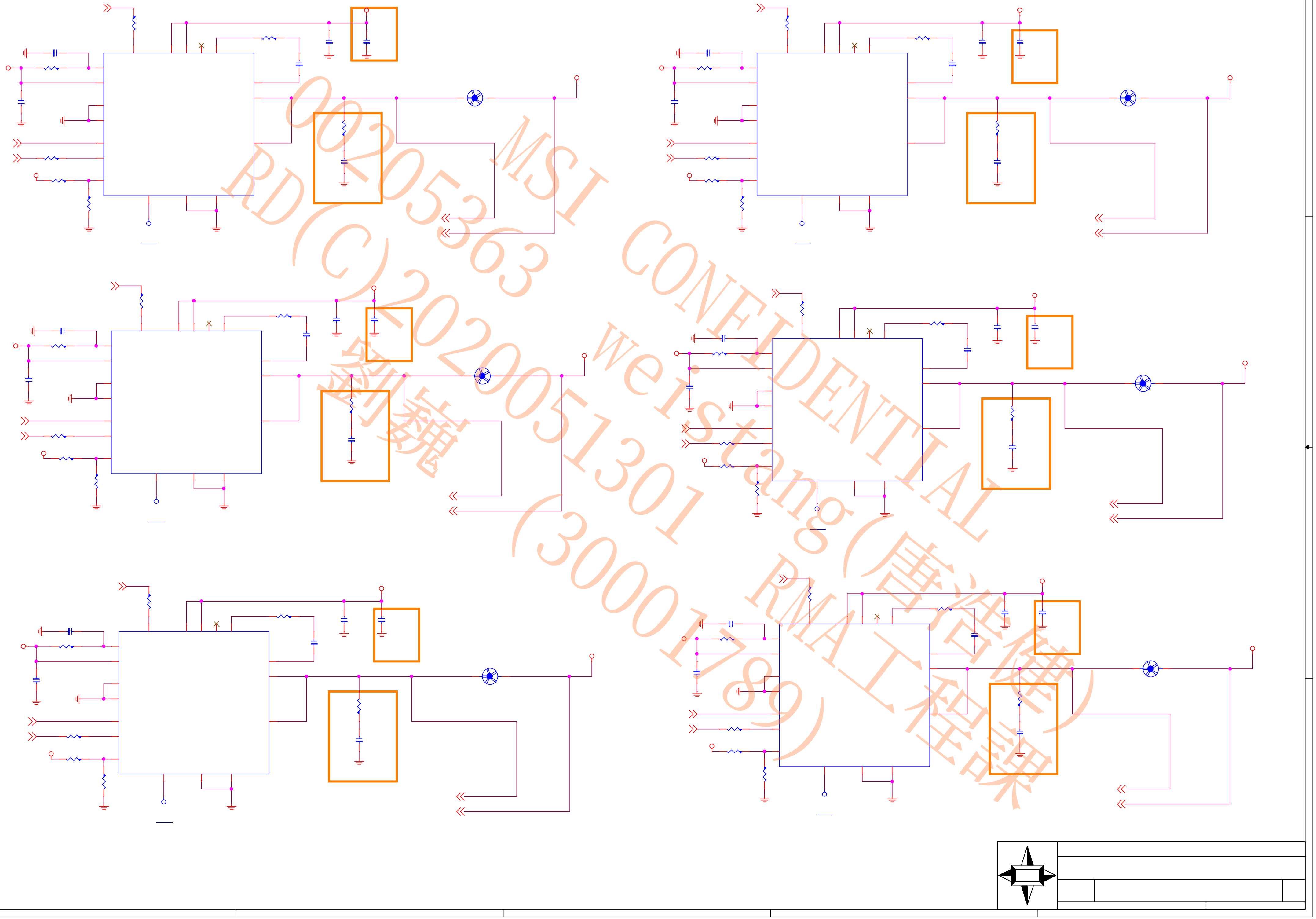


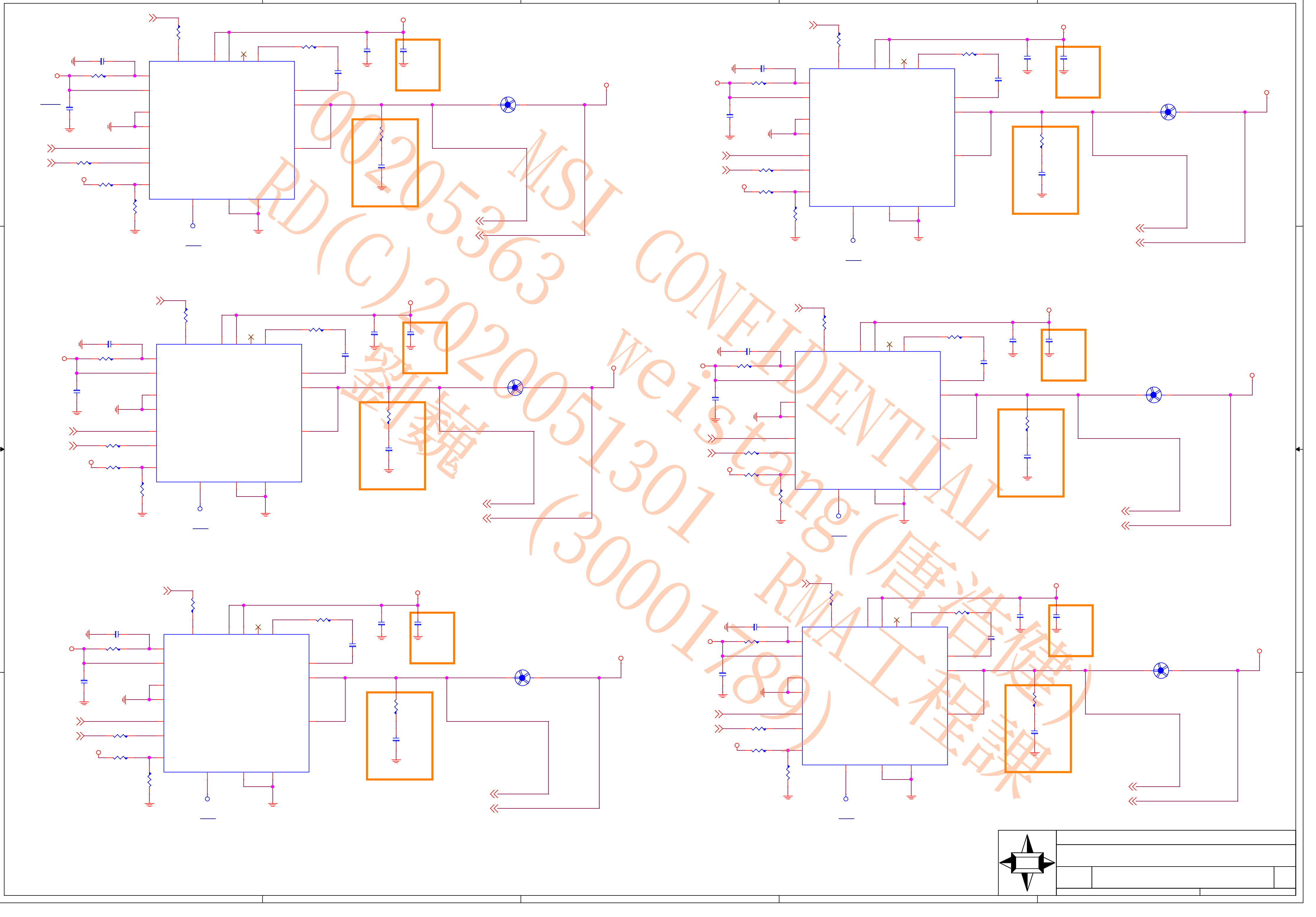
OCP: 45A For 10 core 245A Support

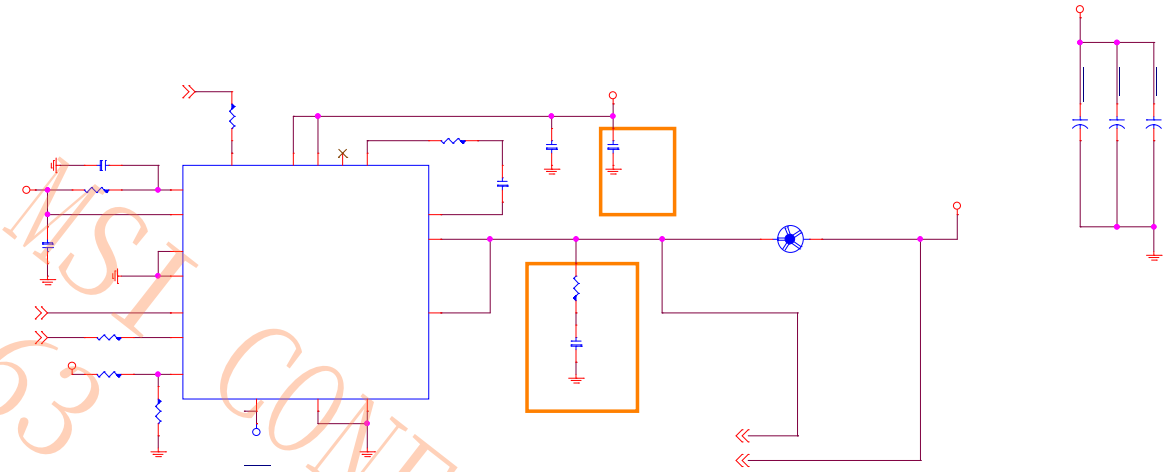


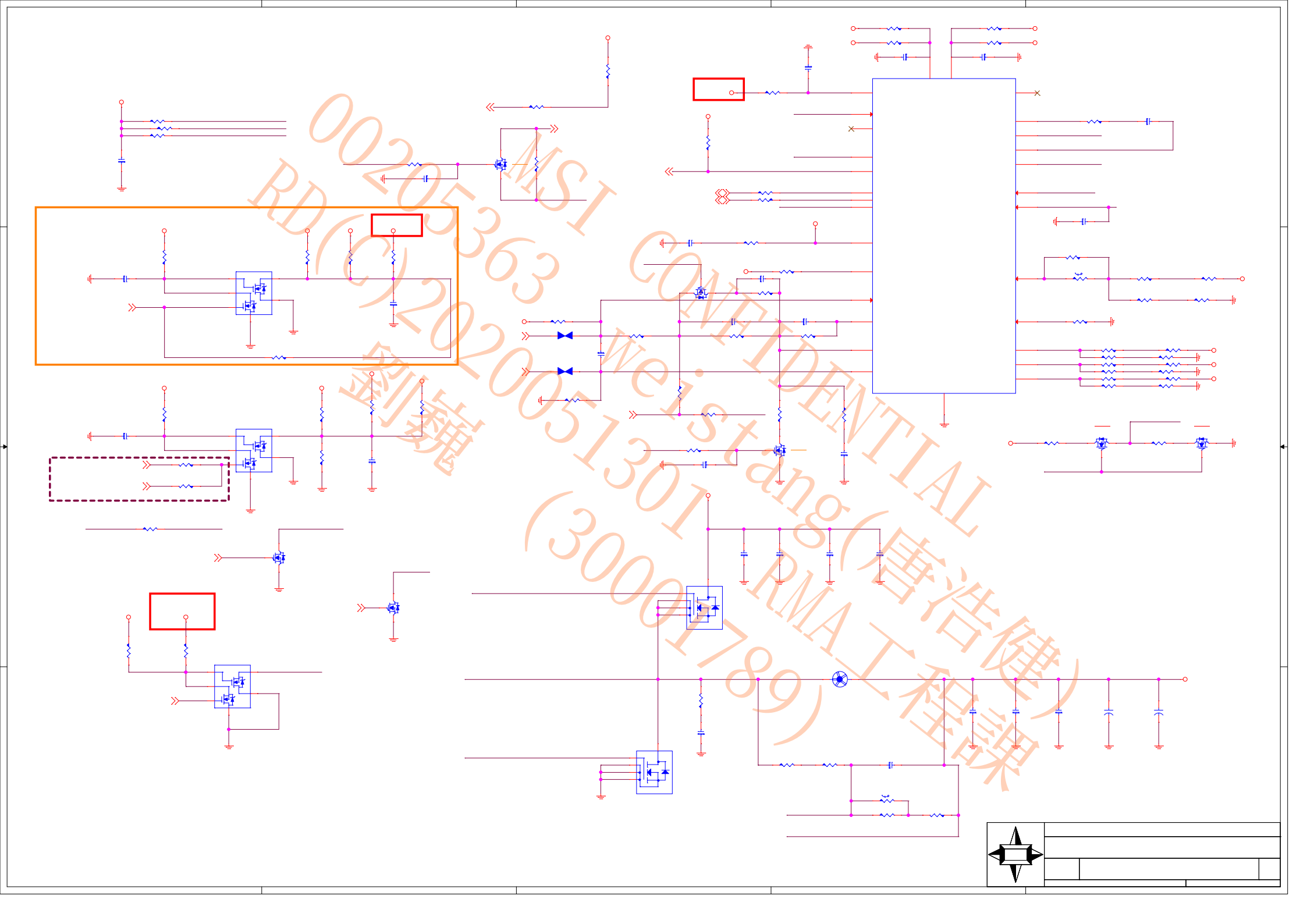








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Weistang(唐浩健)
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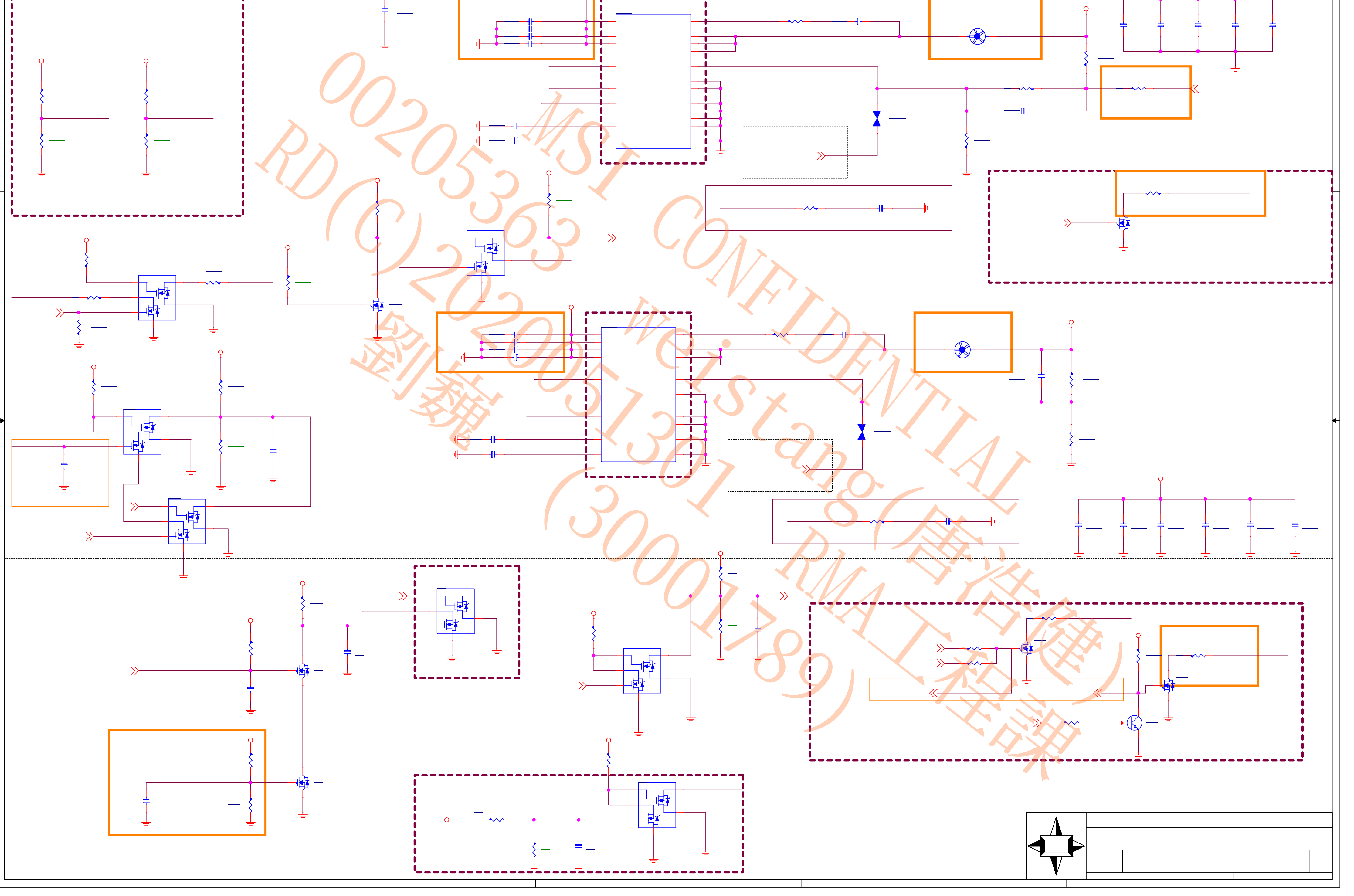
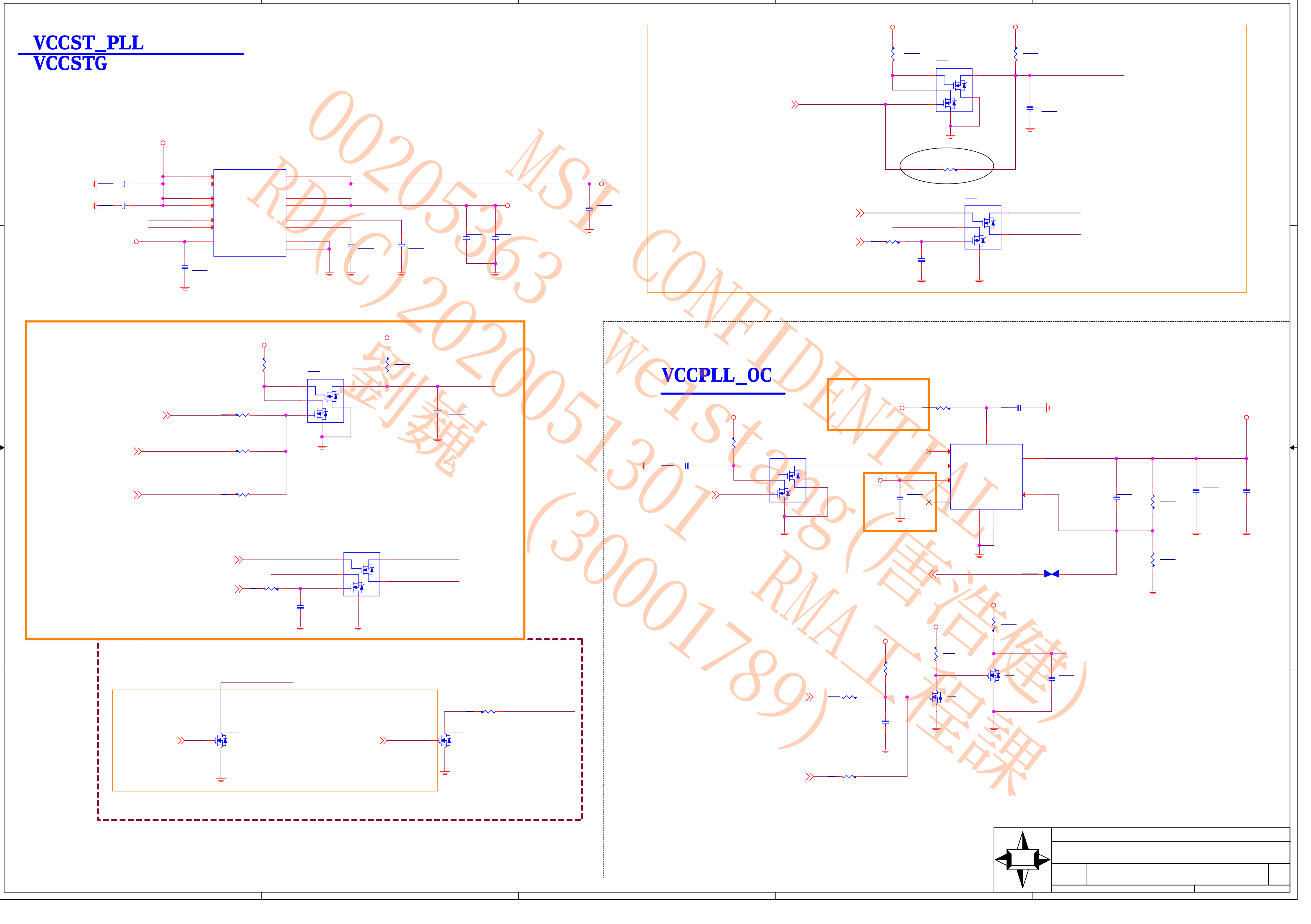
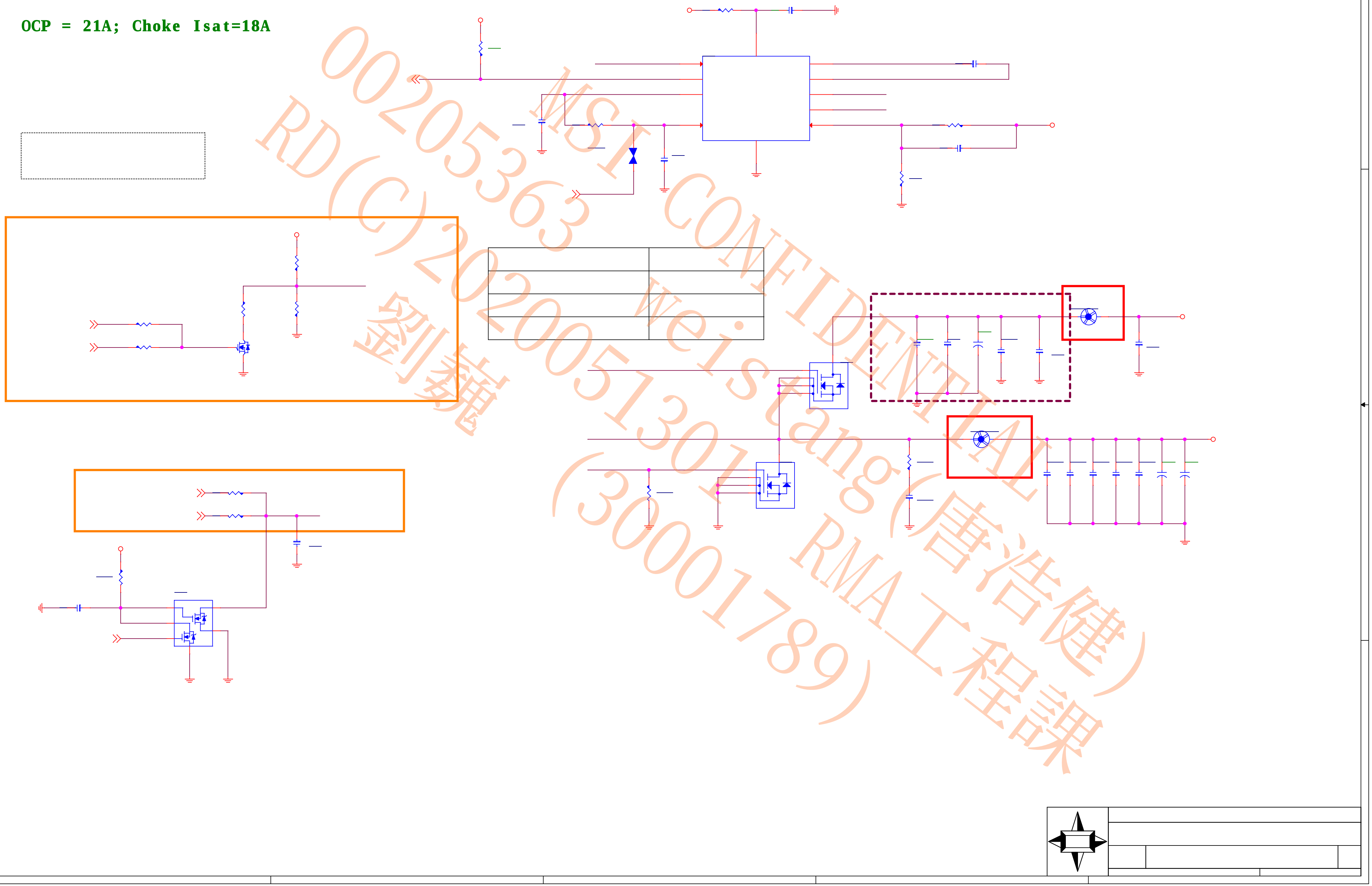
VCCIO_0 Power





DDR4 Power:1.2V,13.48A(17.08A)

OCP = 21A; Choke Isat=18A



VPP2.5V Power:2.5V,6A(for dimm LED)

IC OCP:7.6A(6.6A~8.6A)