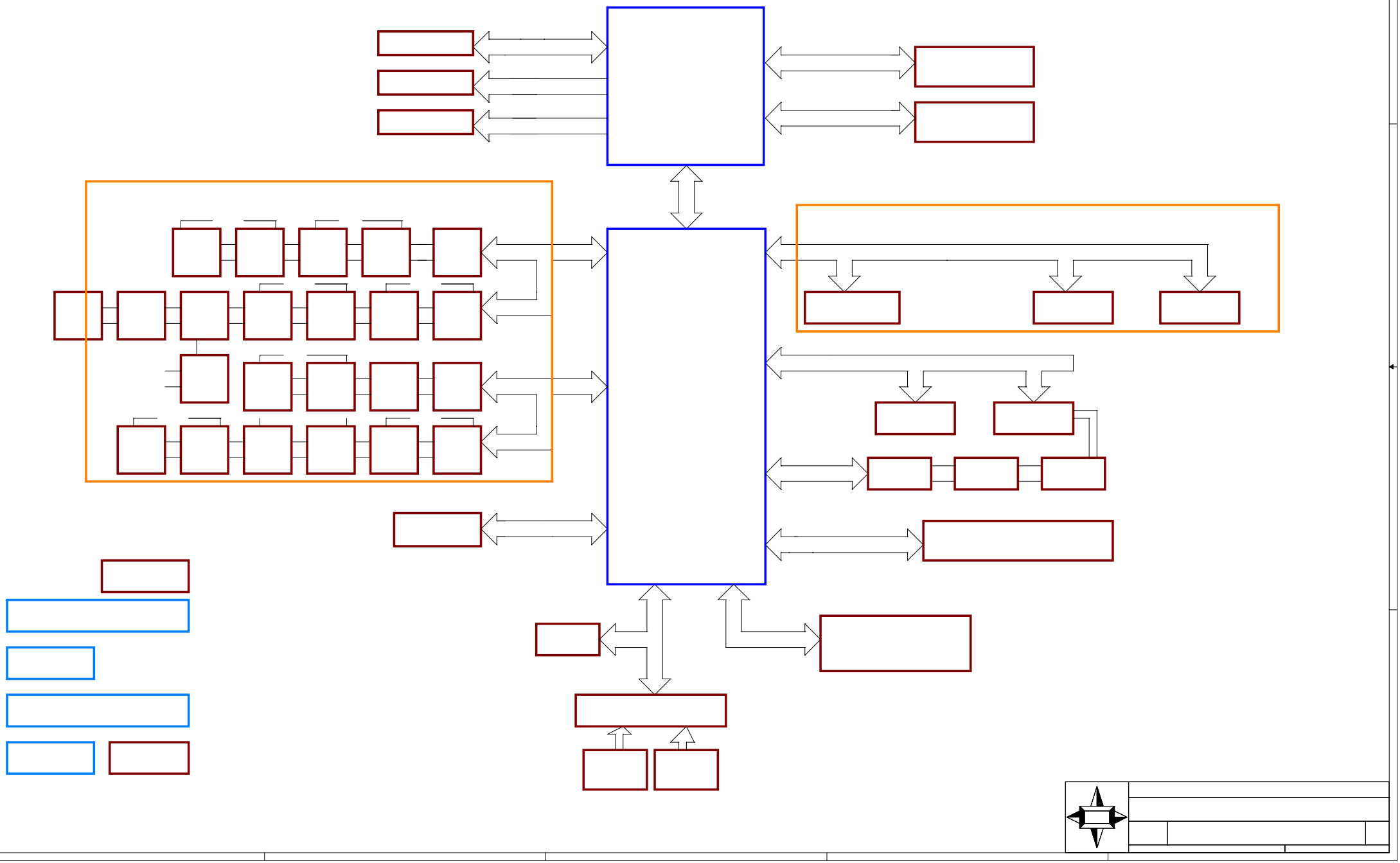
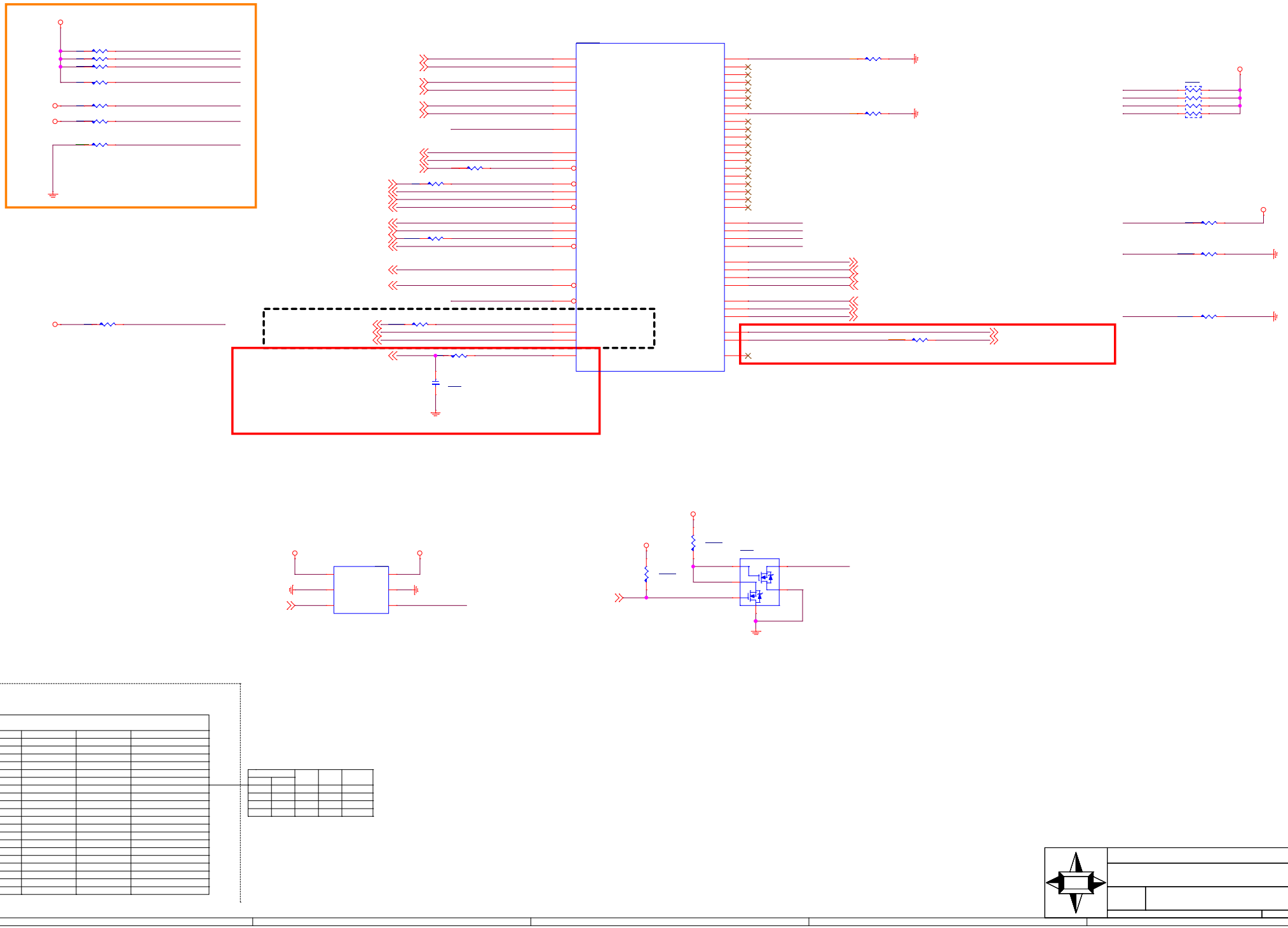
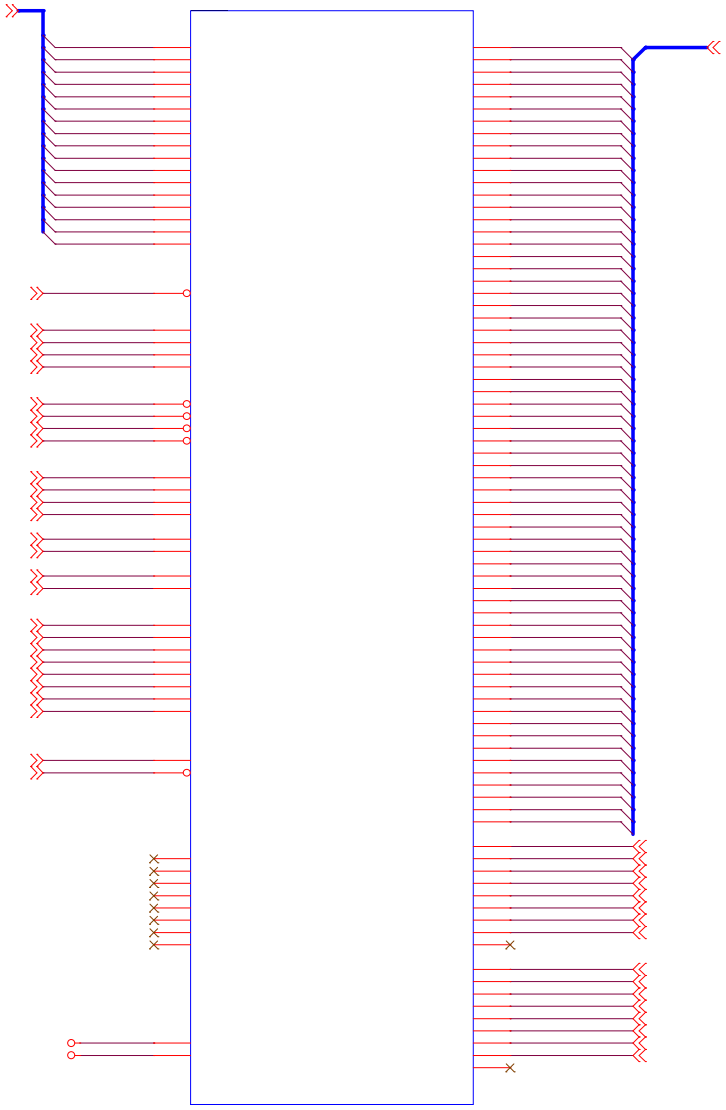
MS-7C79

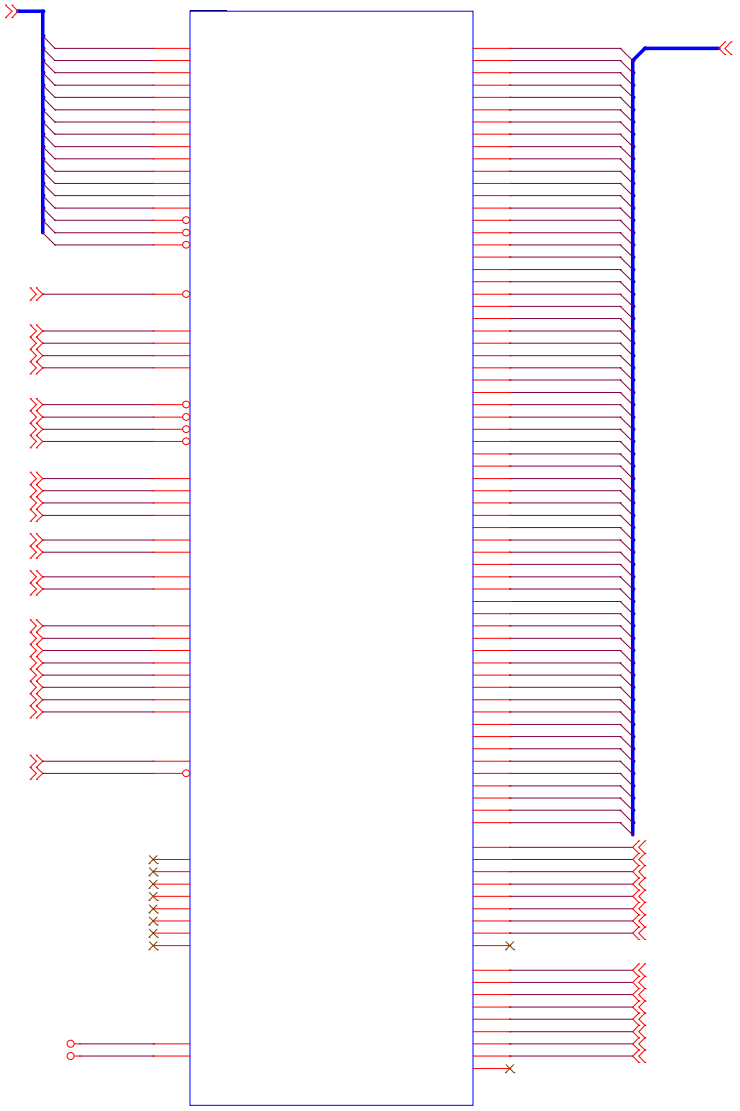
CML Platform

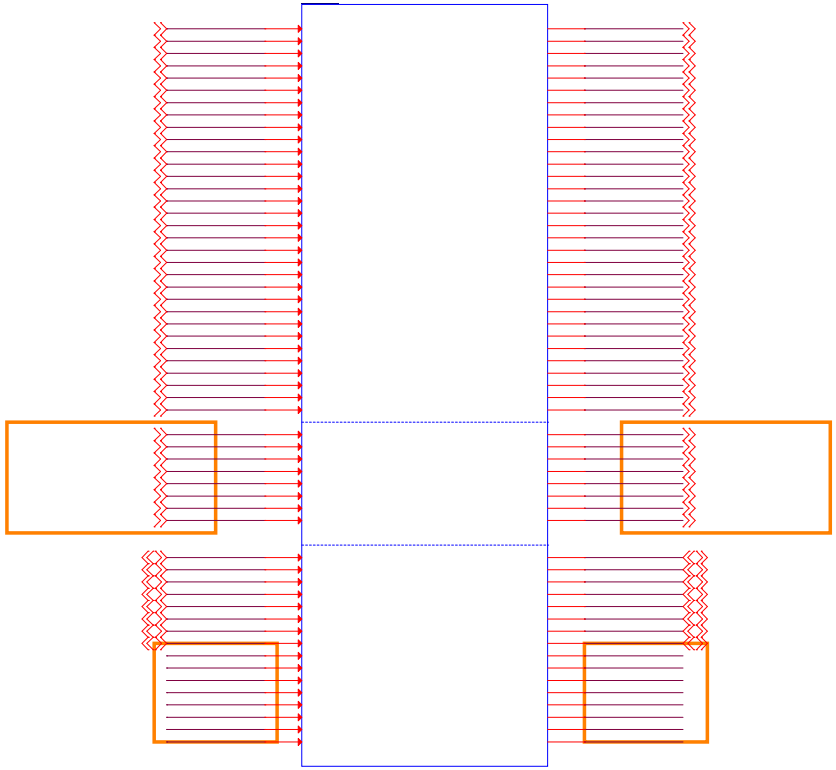
MS-7C79 Block Diagram

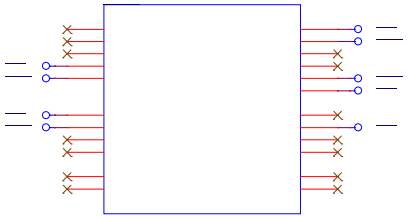


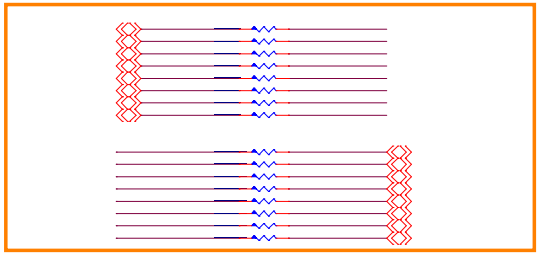


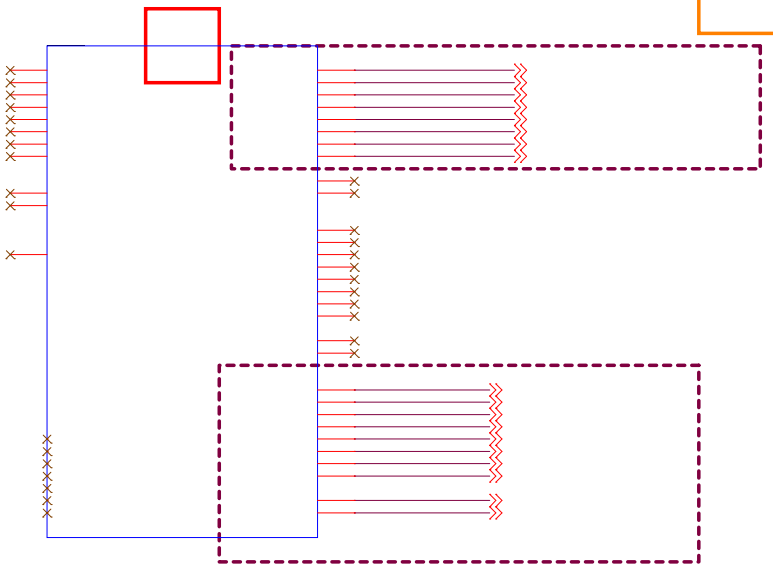


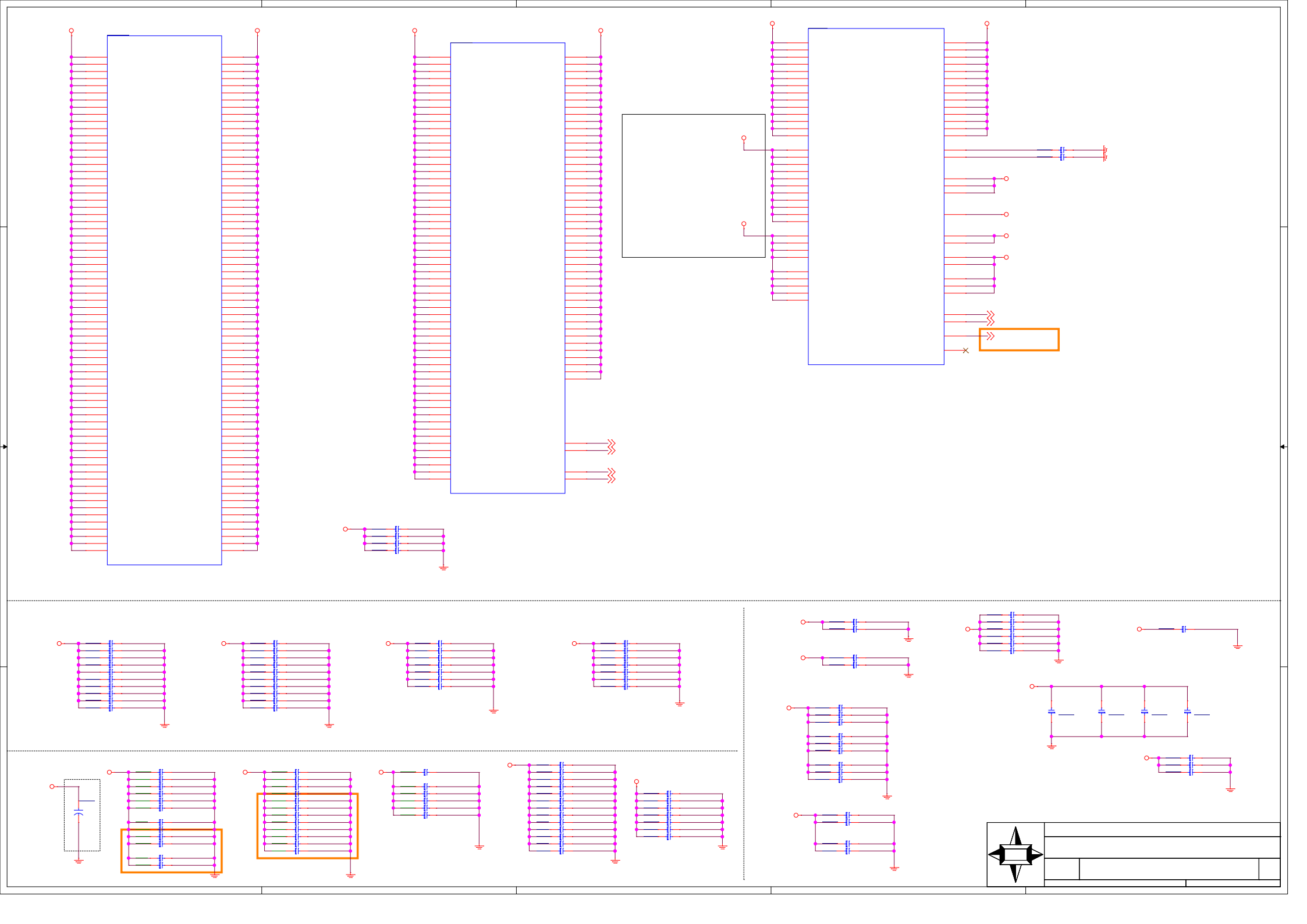


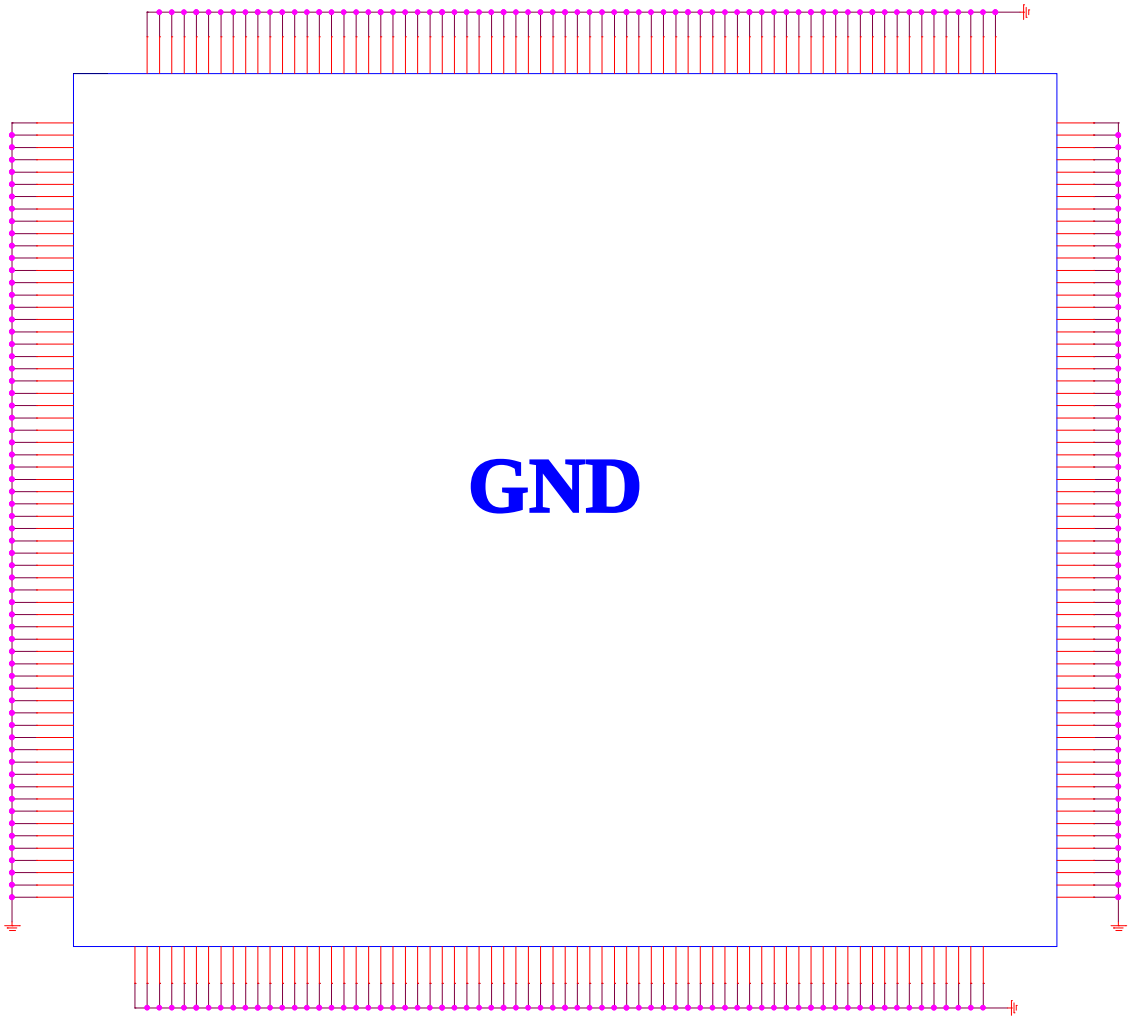








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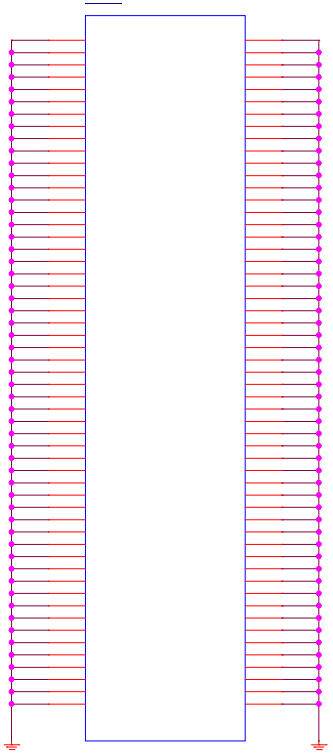

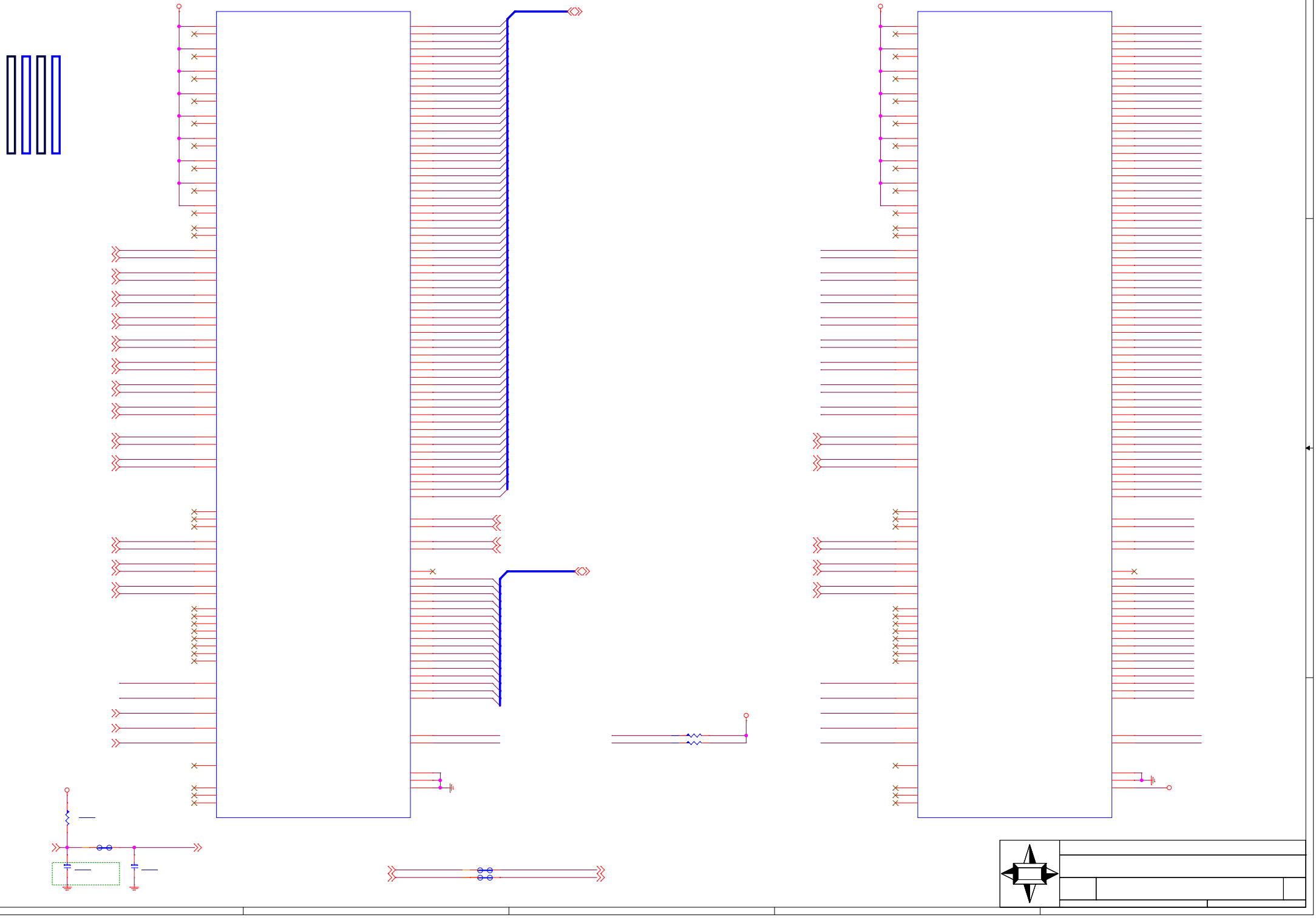

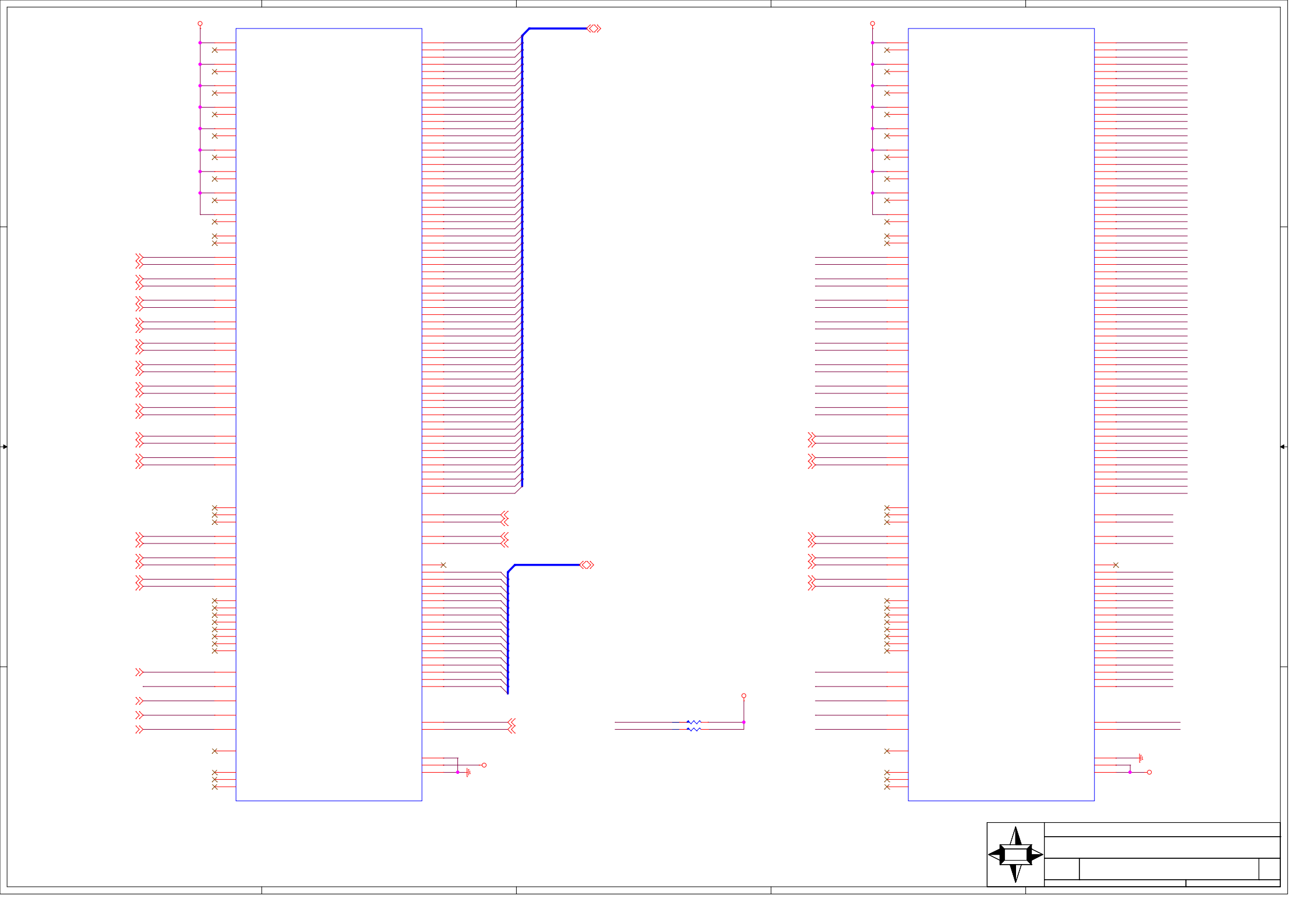

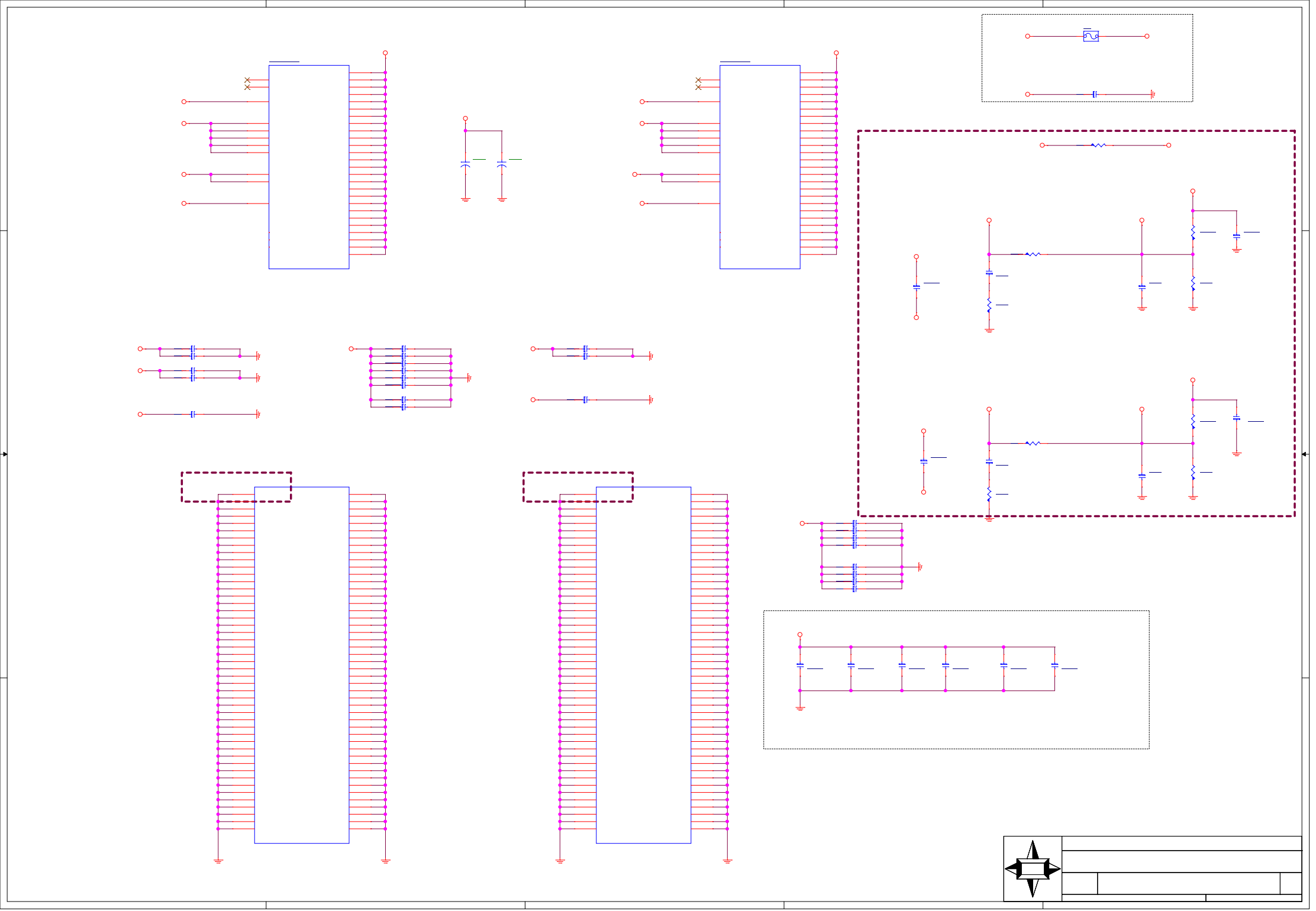

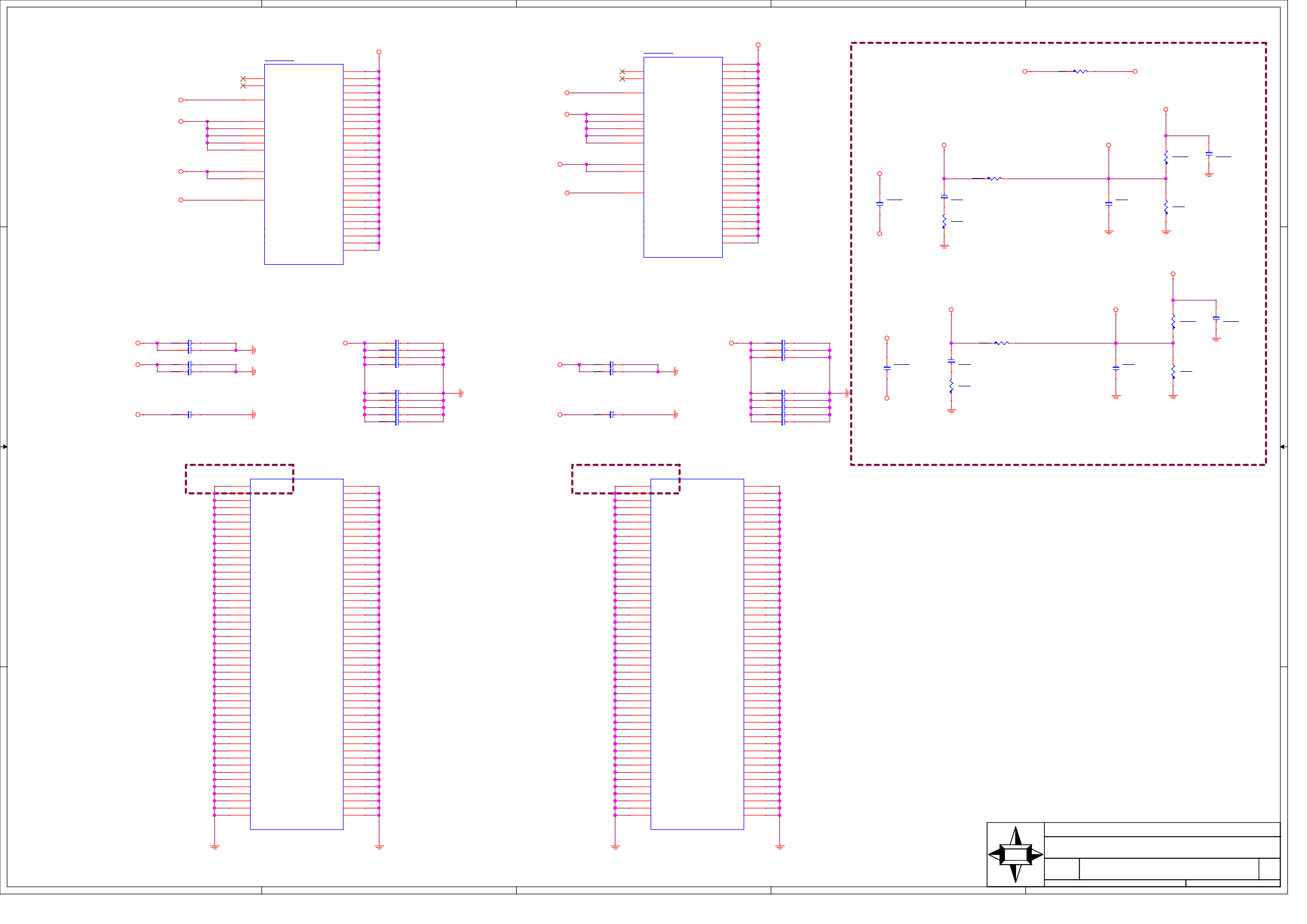

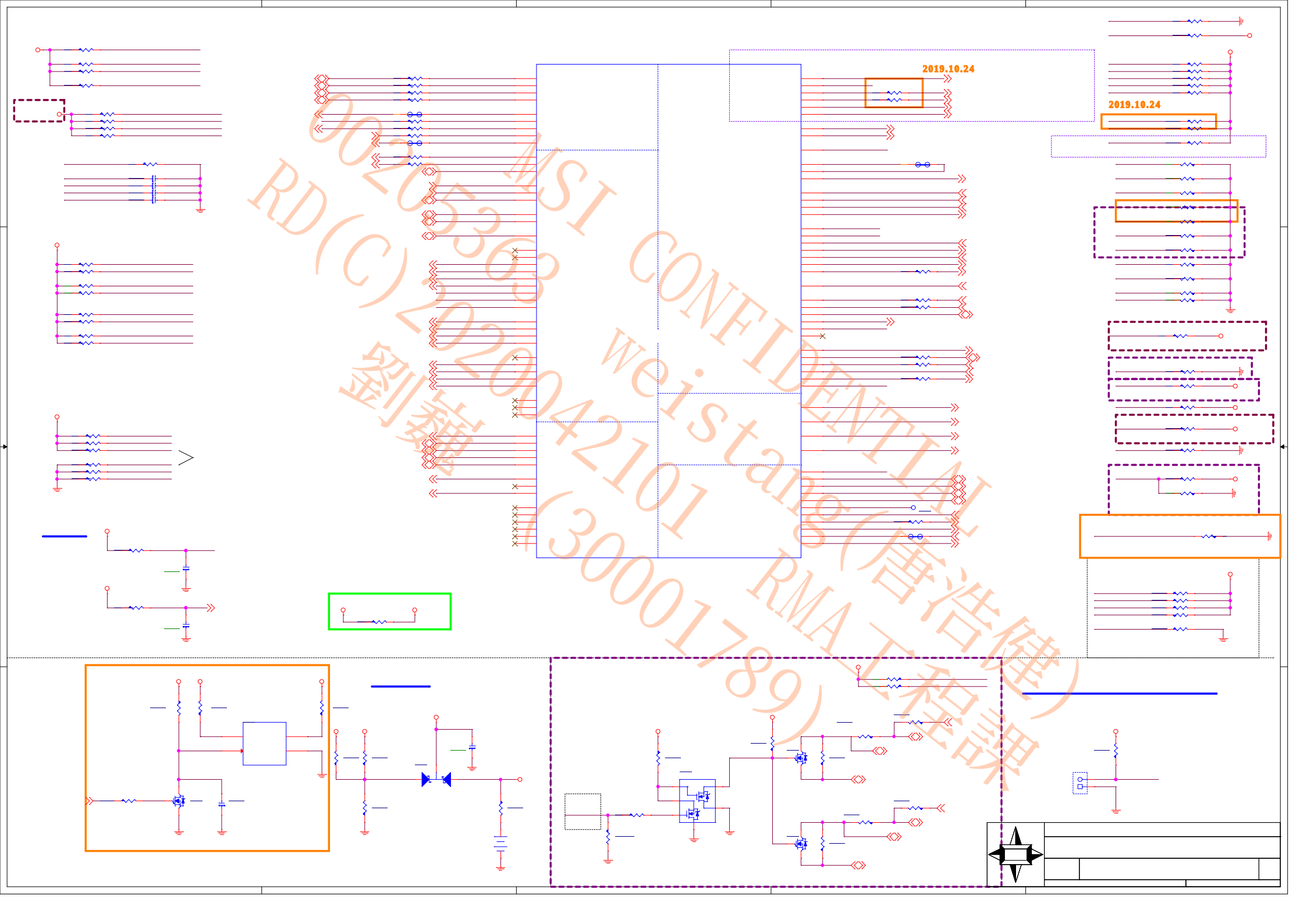
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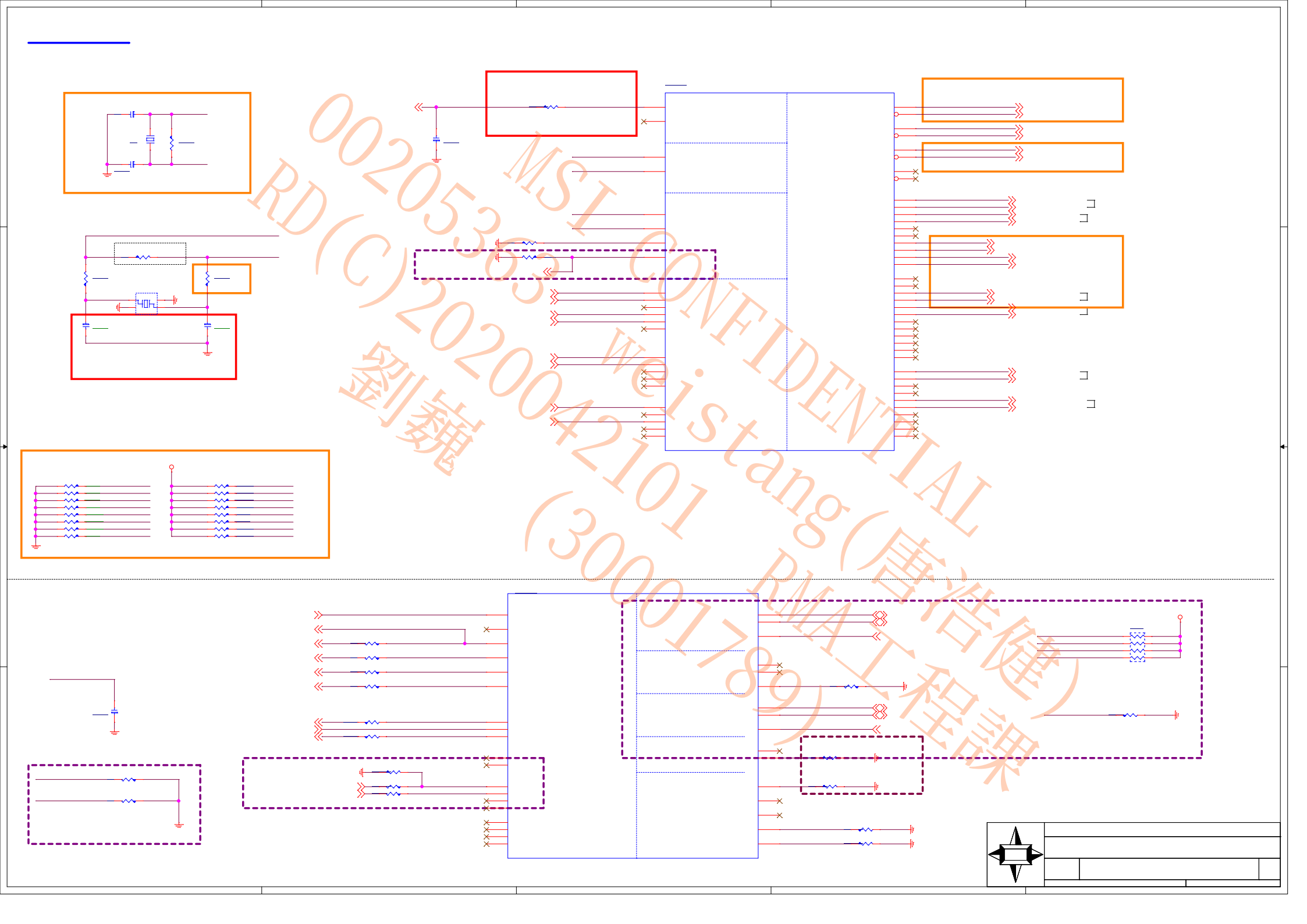
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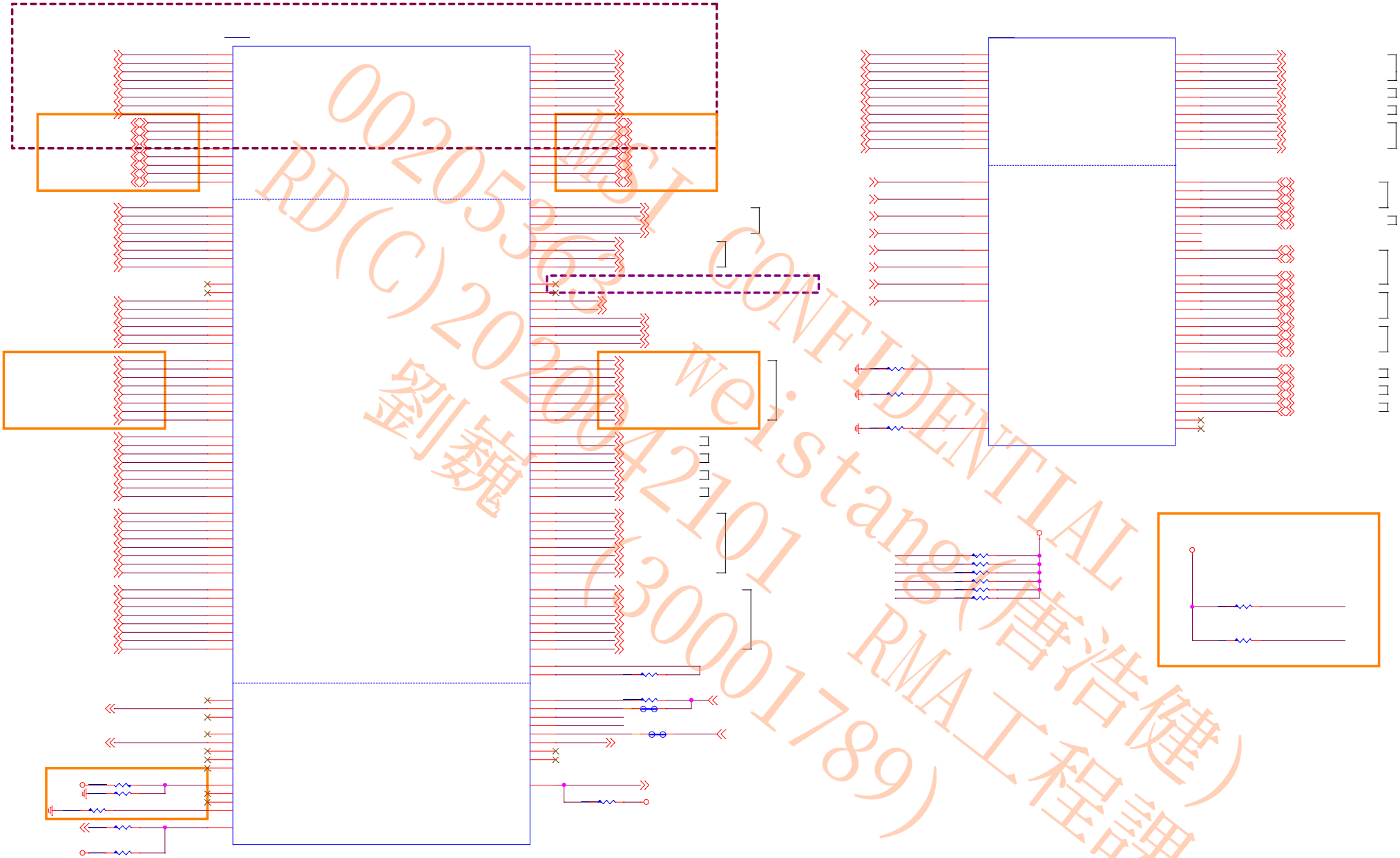
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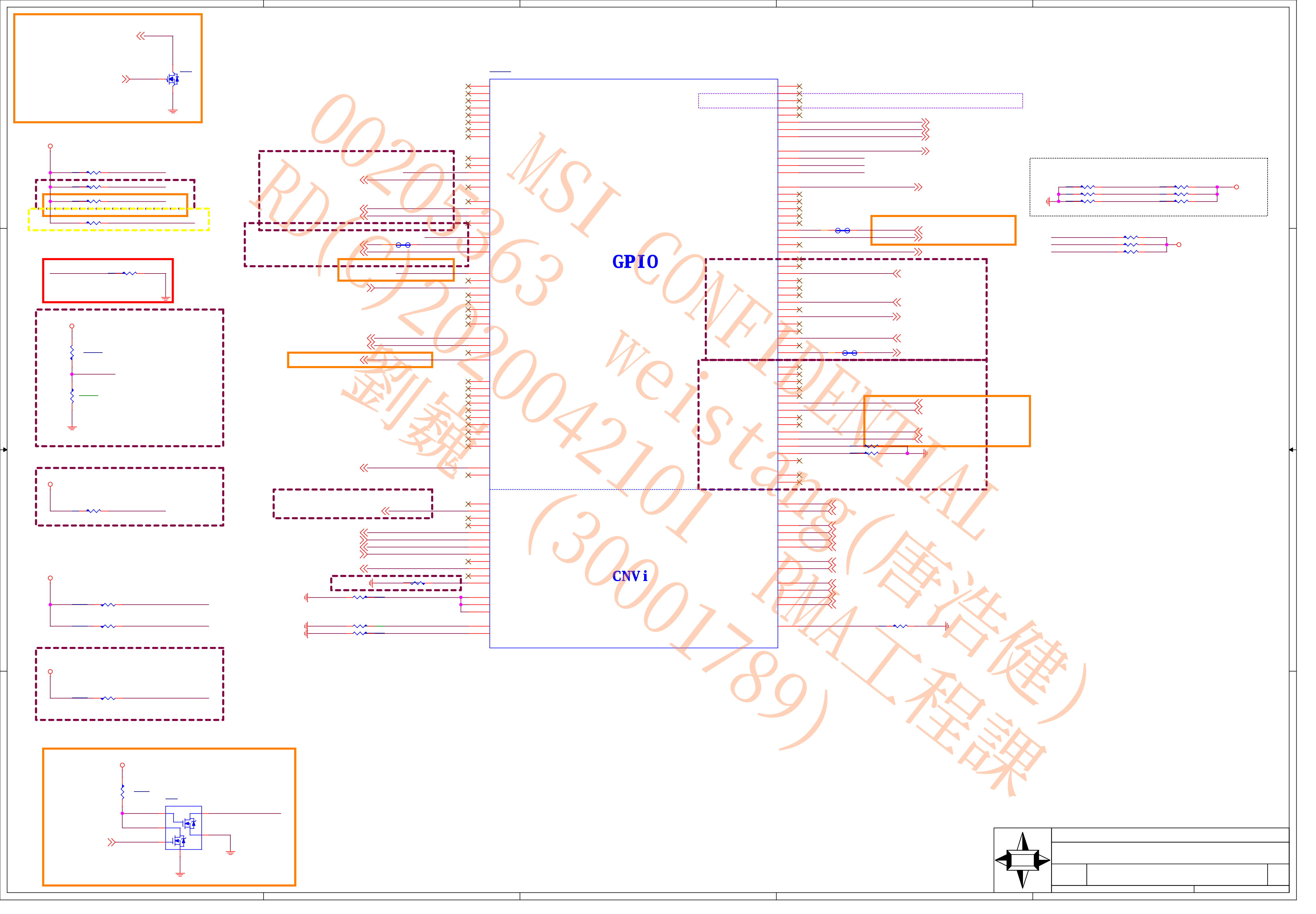

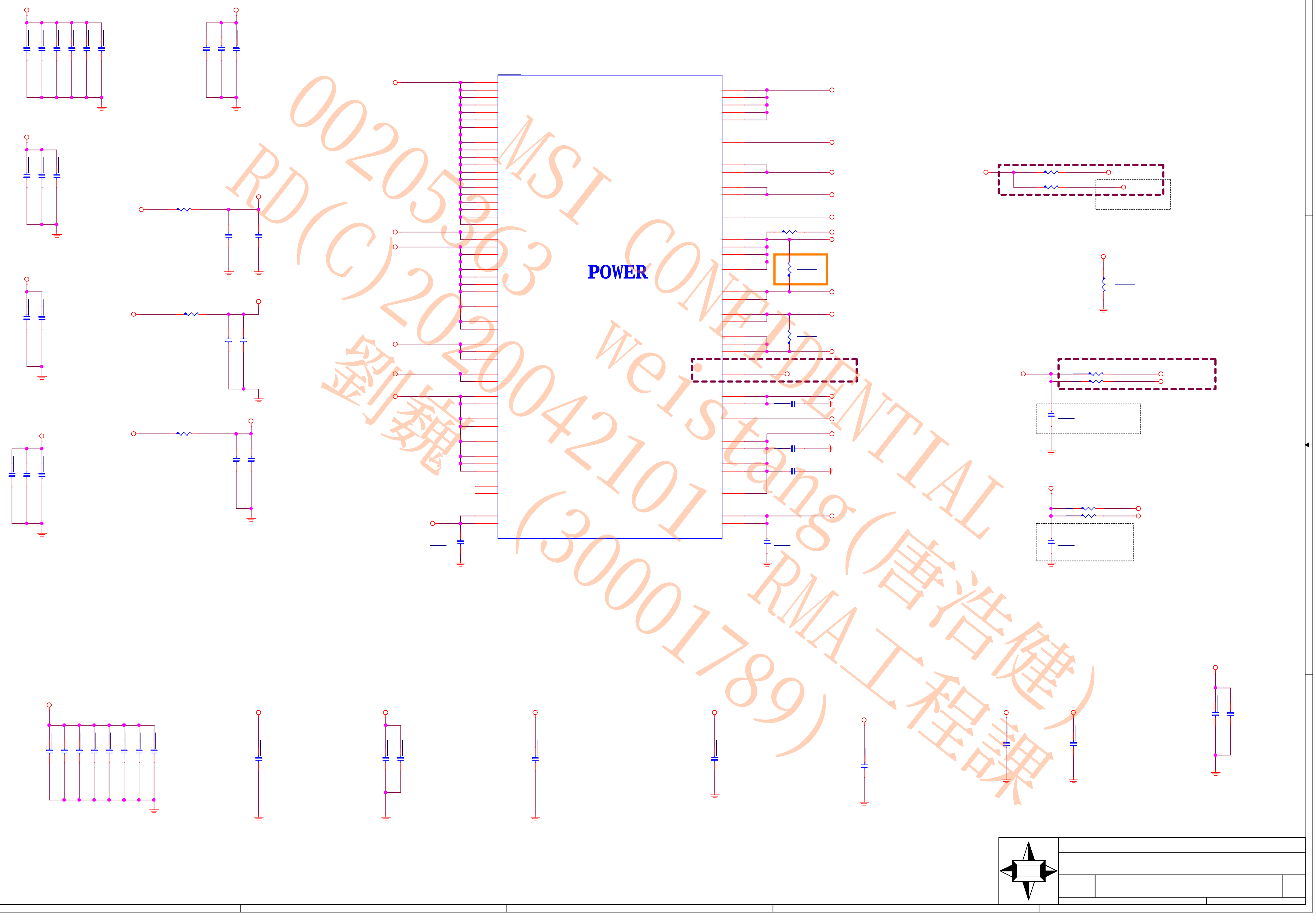

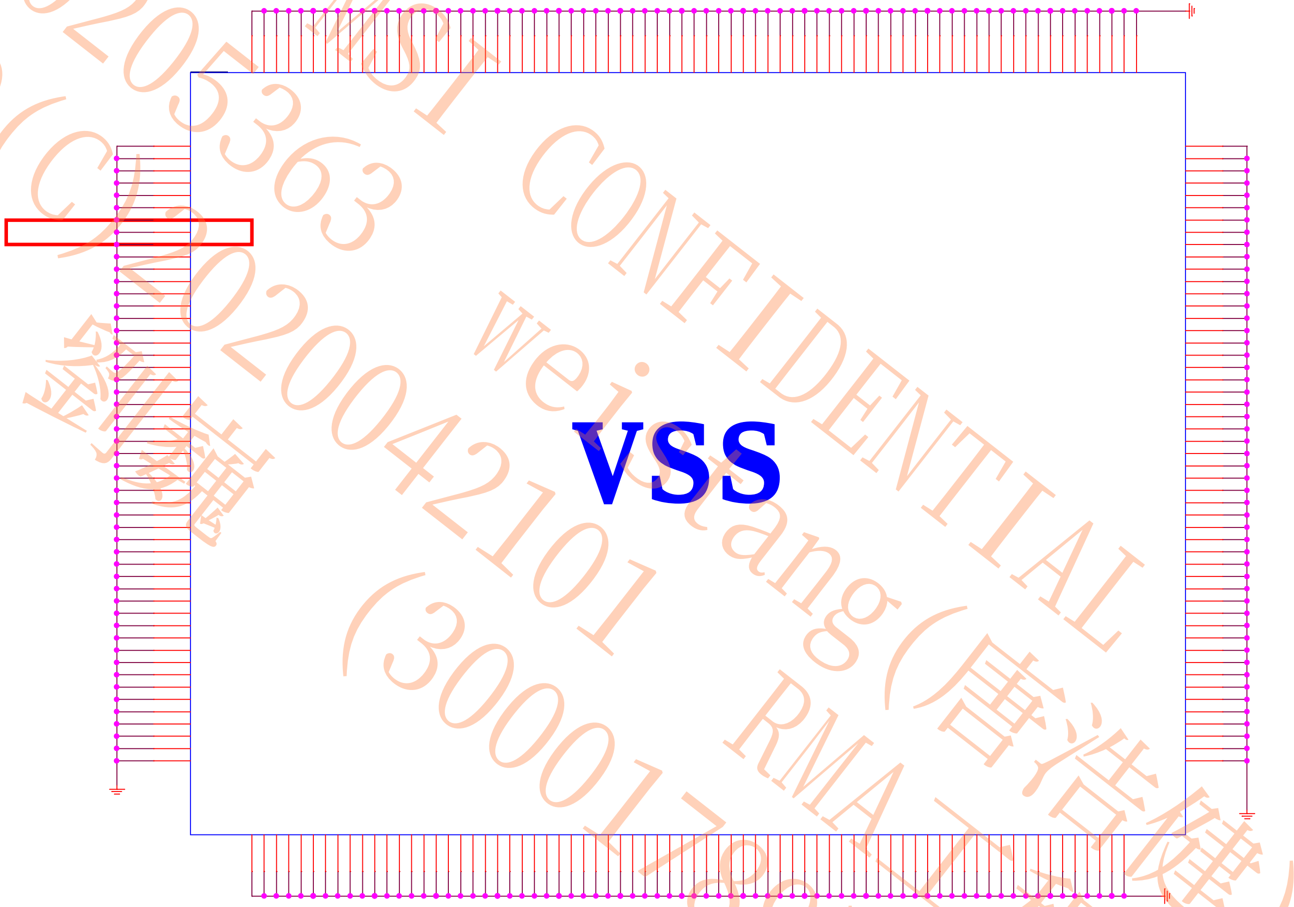

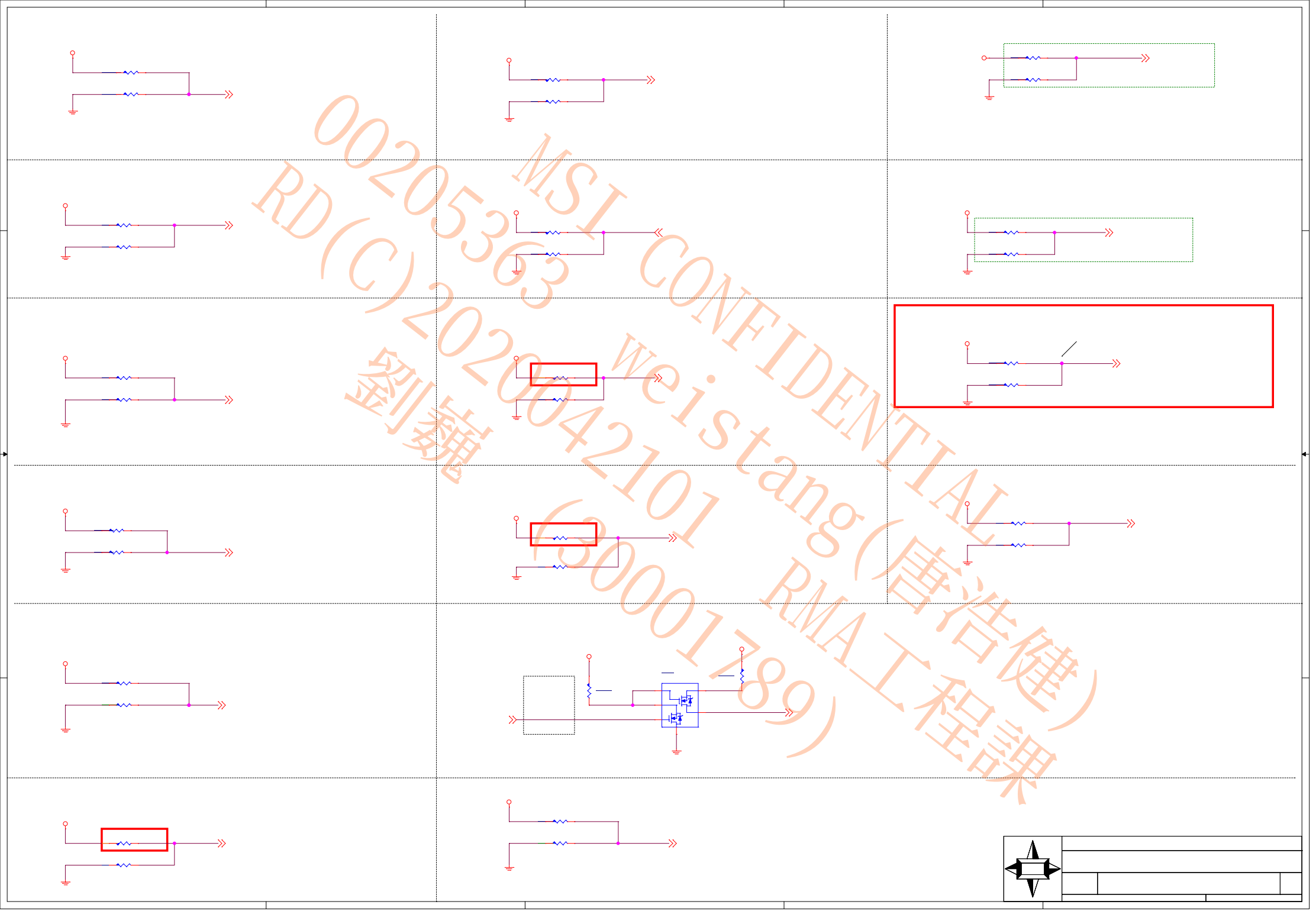
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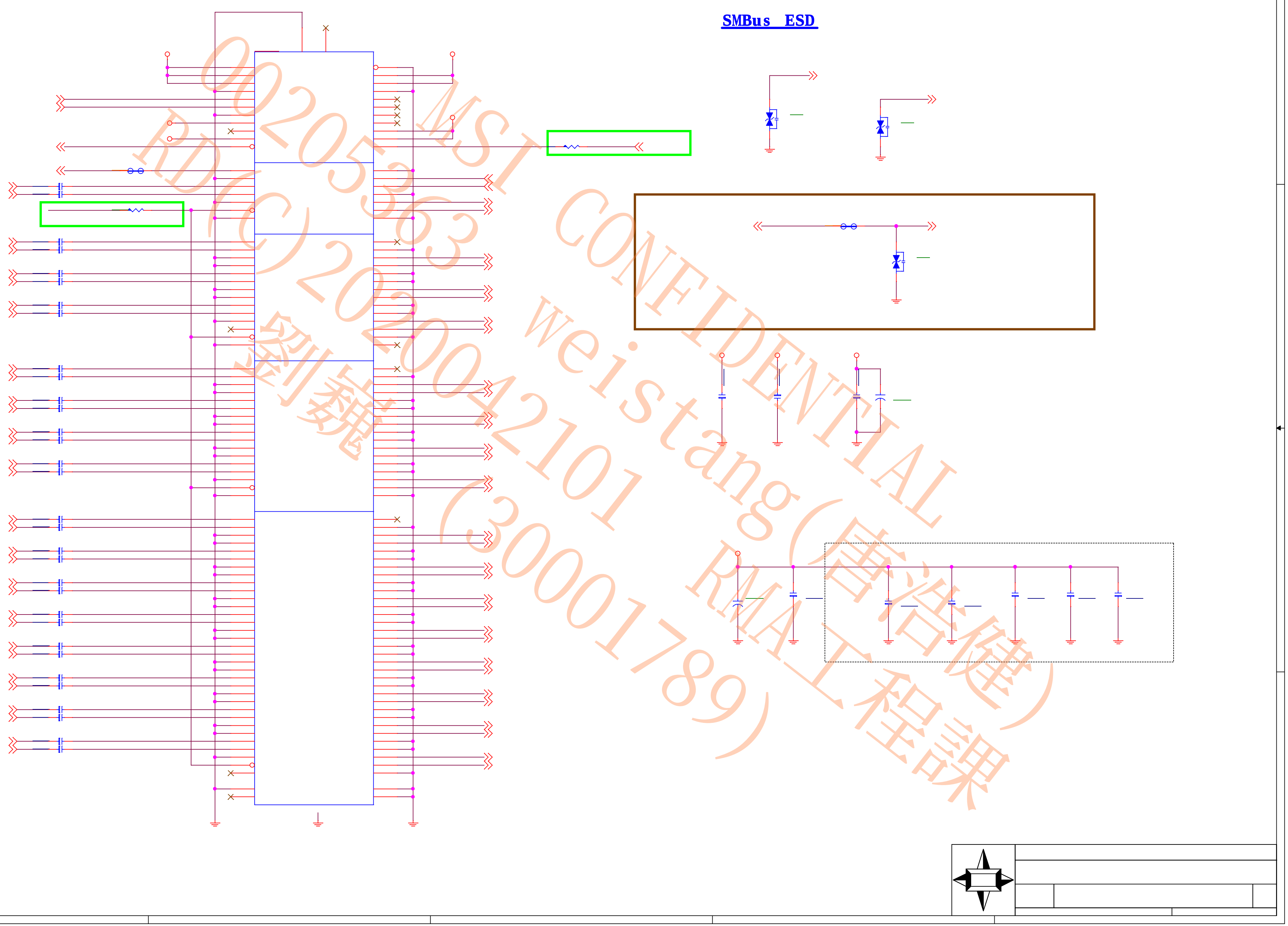

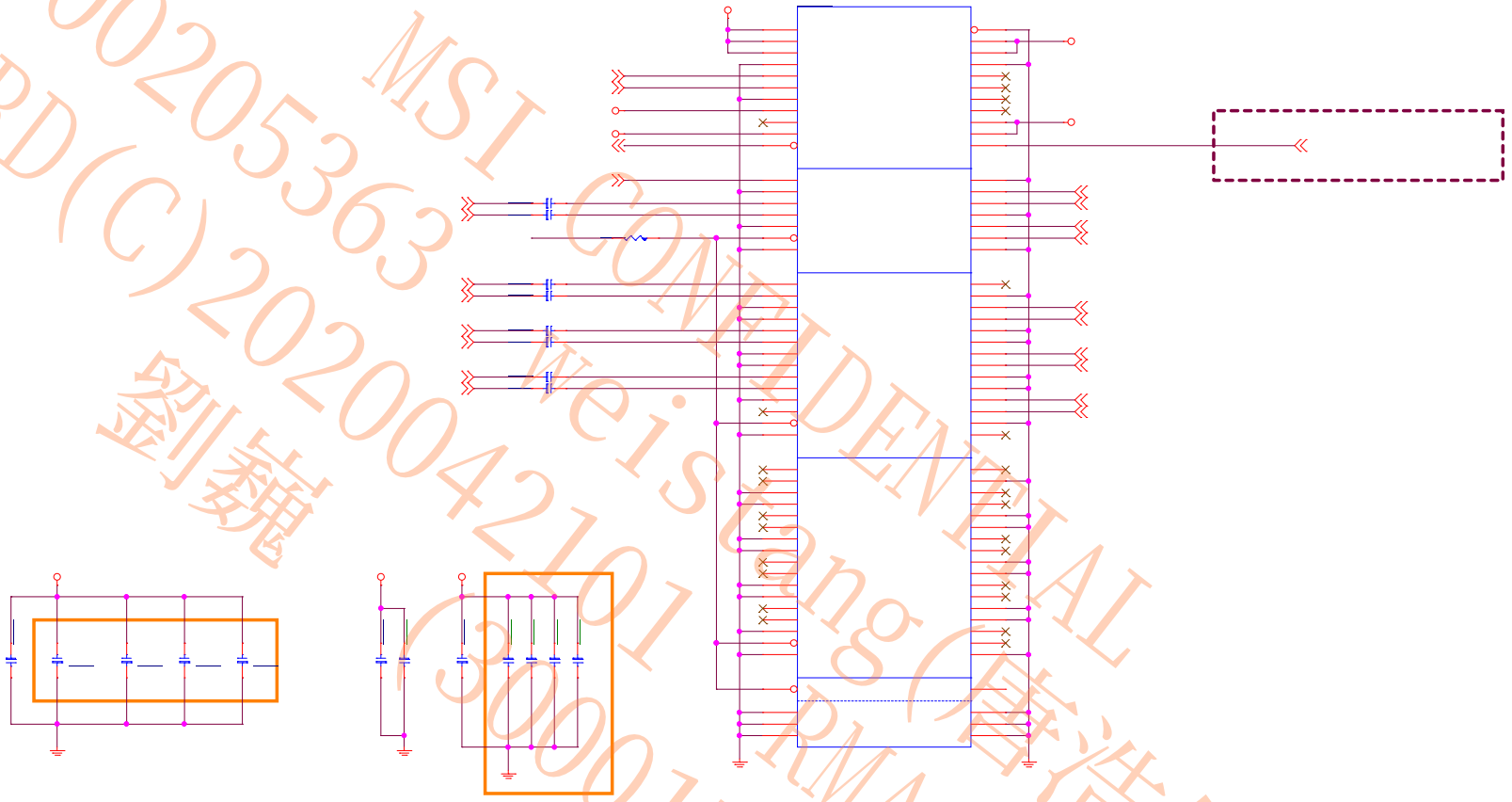

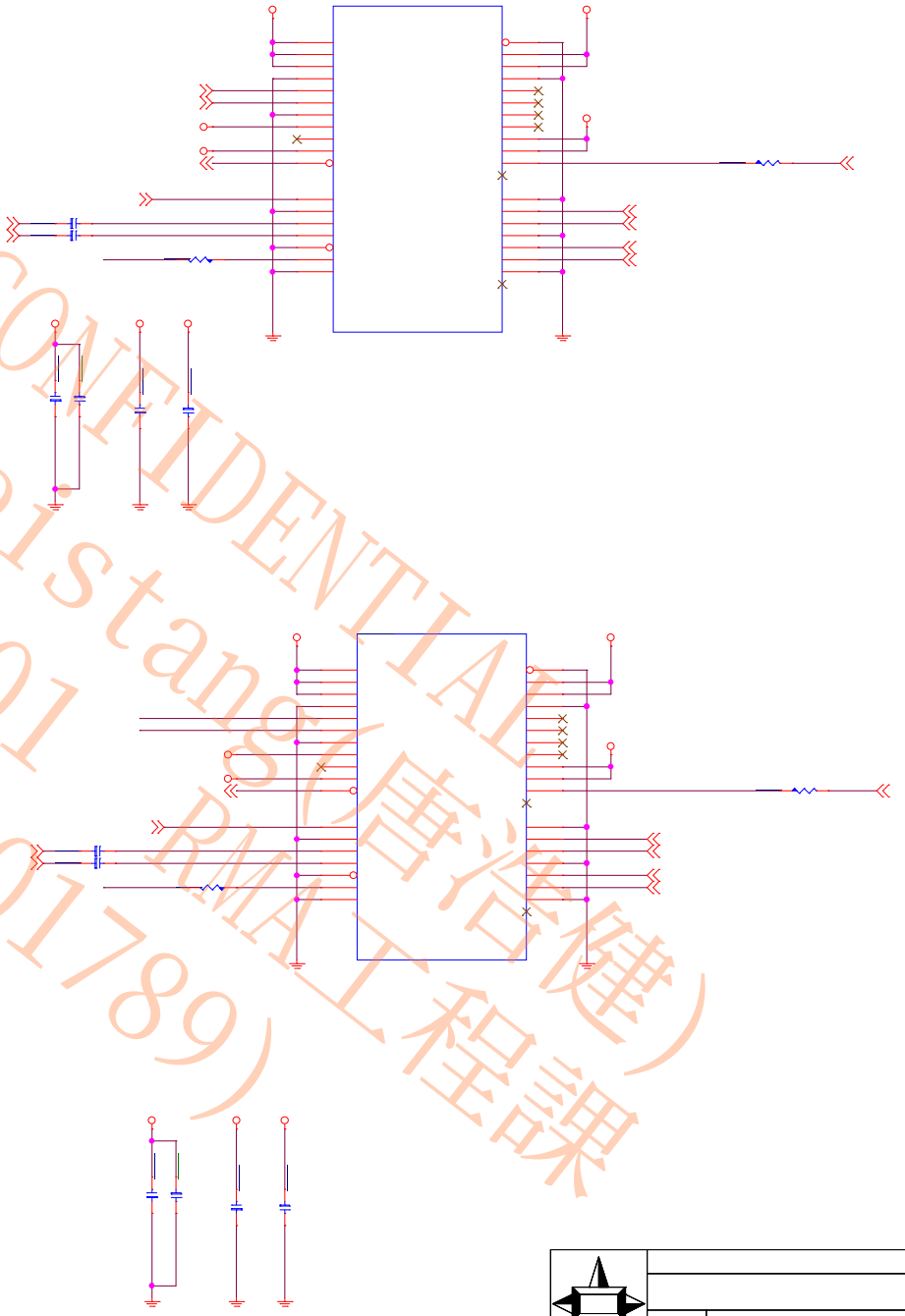

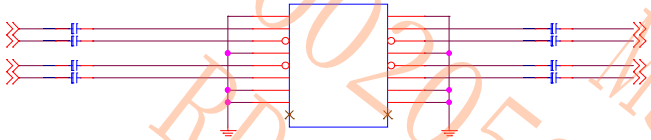

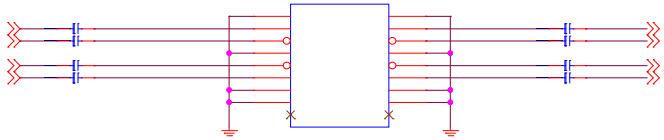

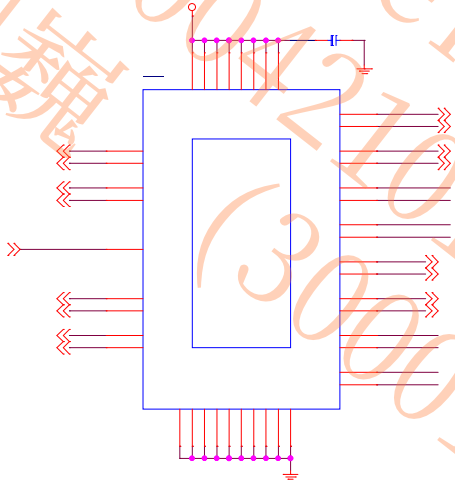

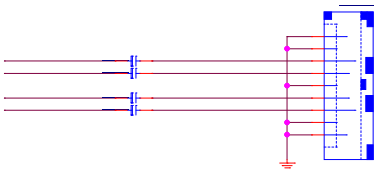

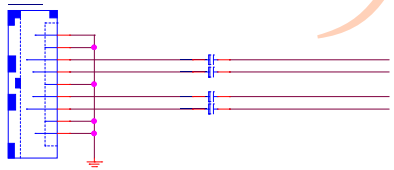

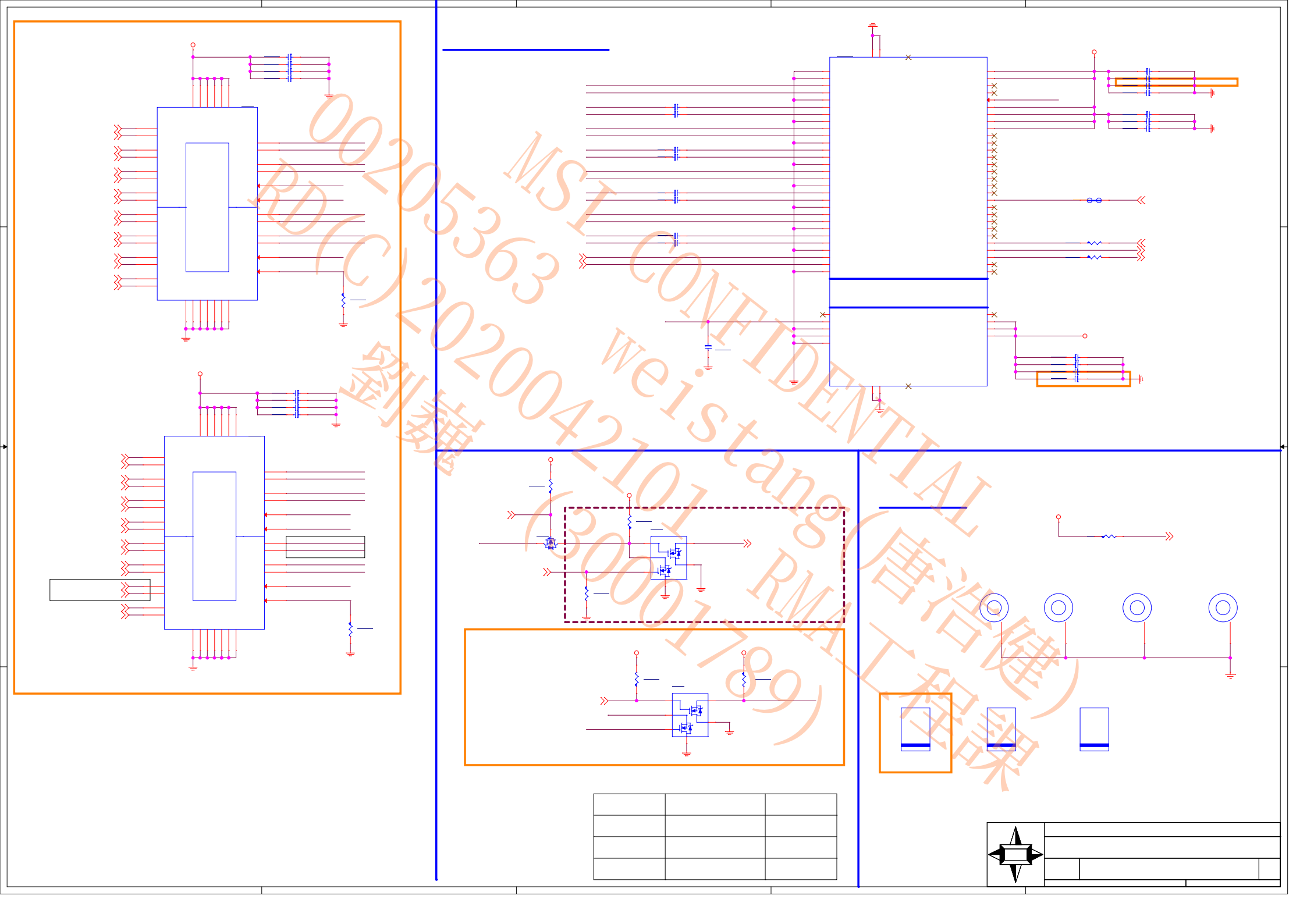
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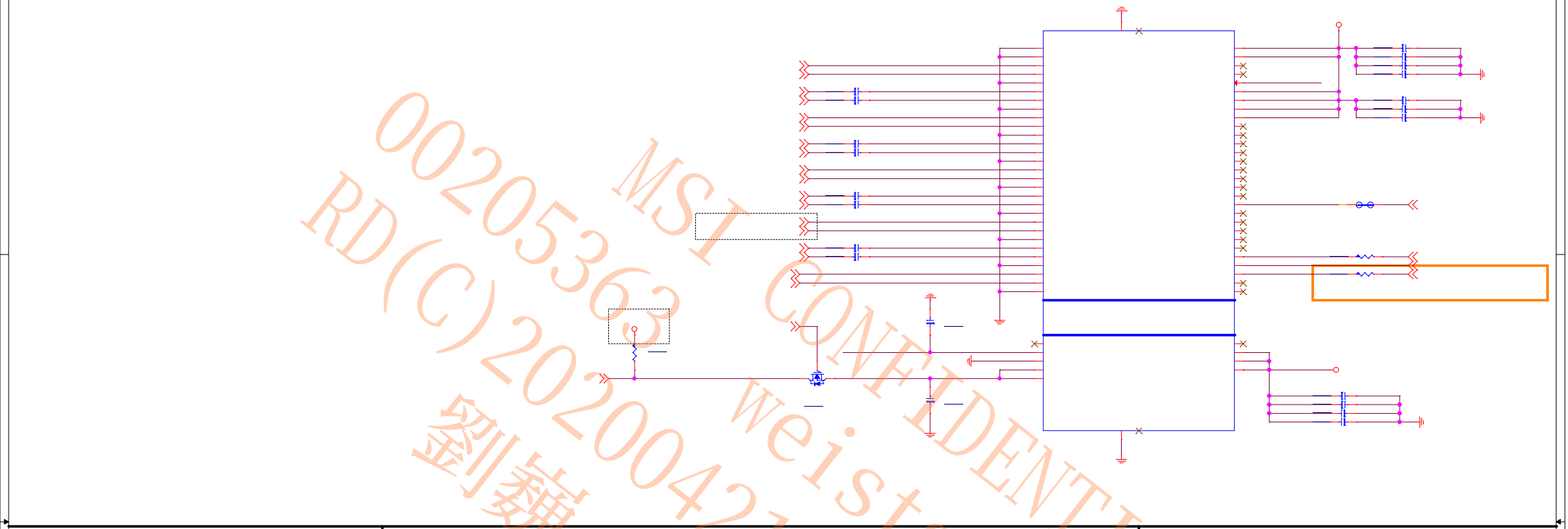

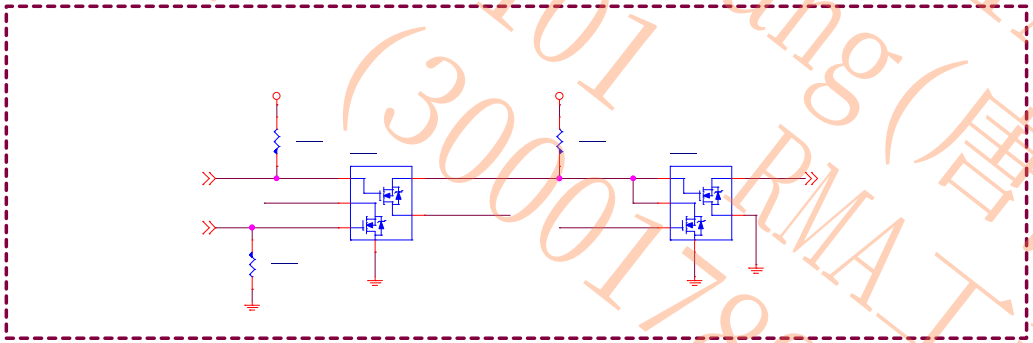

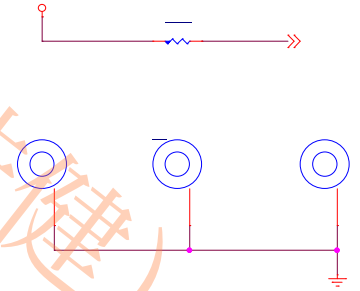

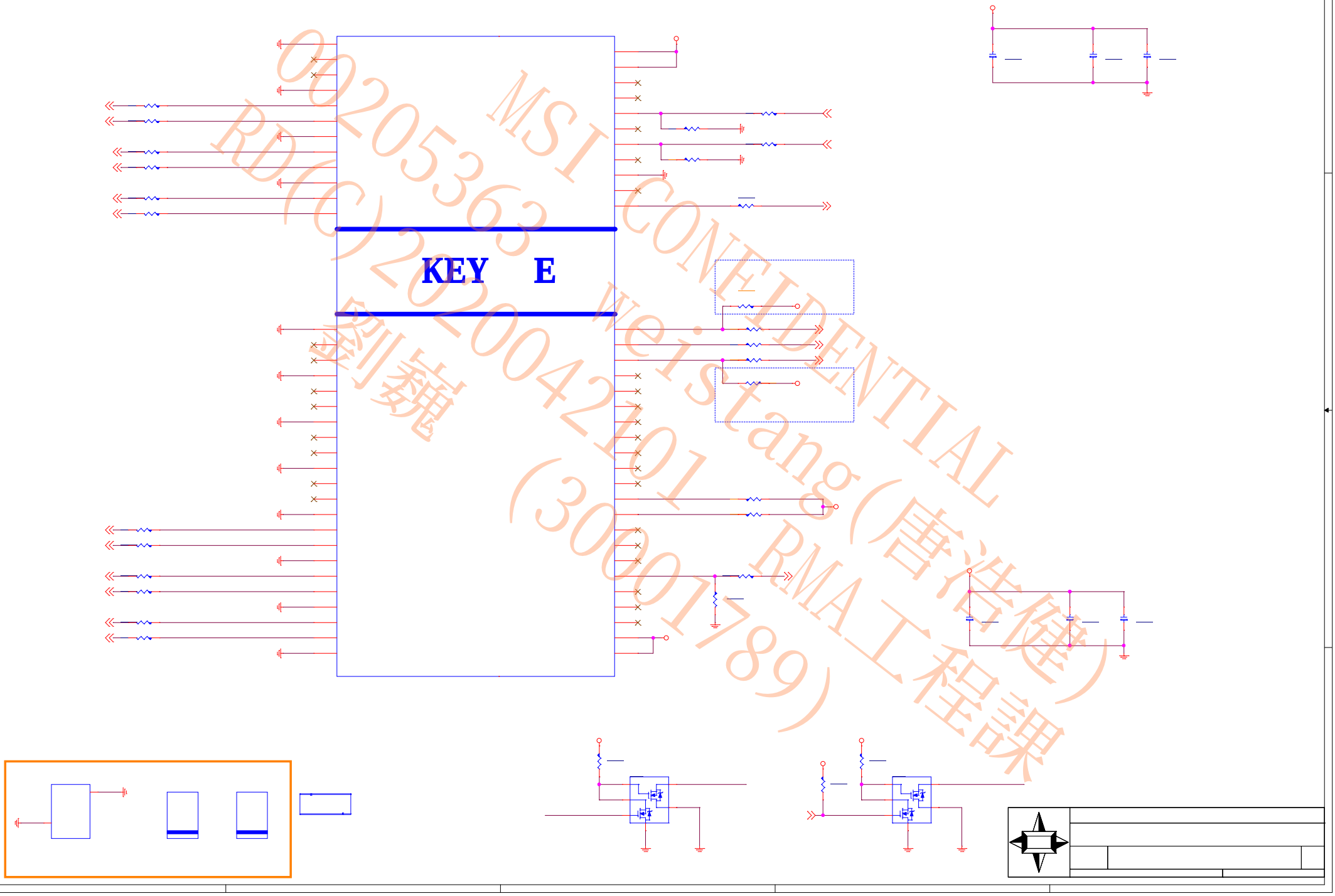

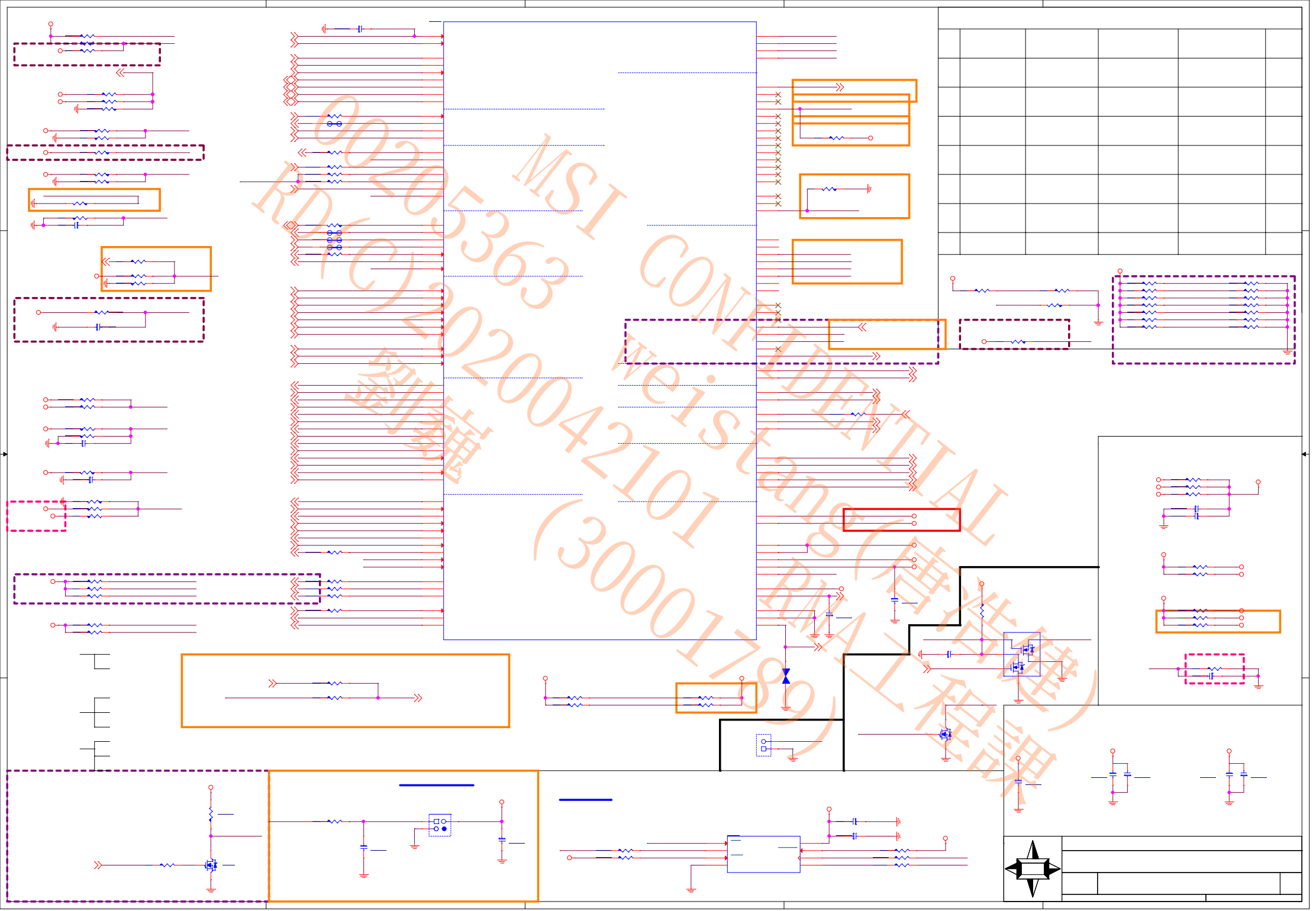
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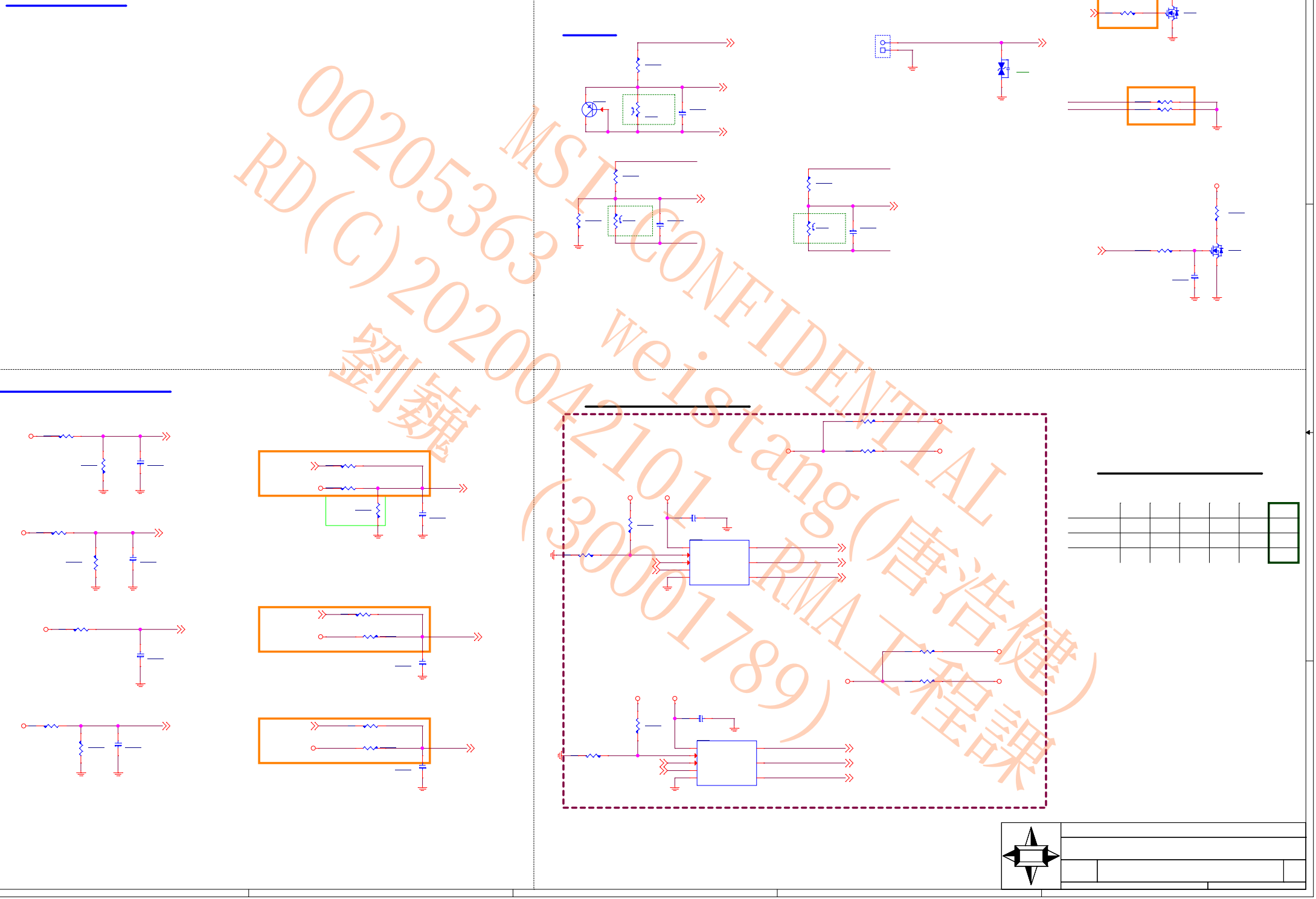
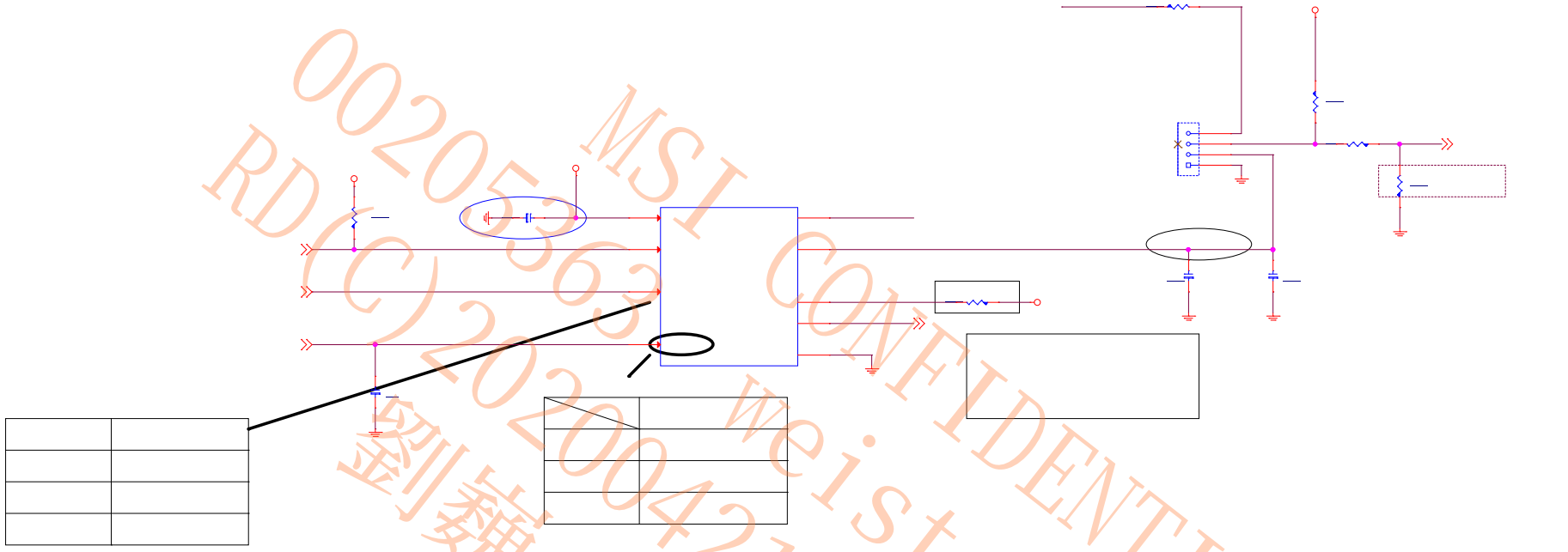
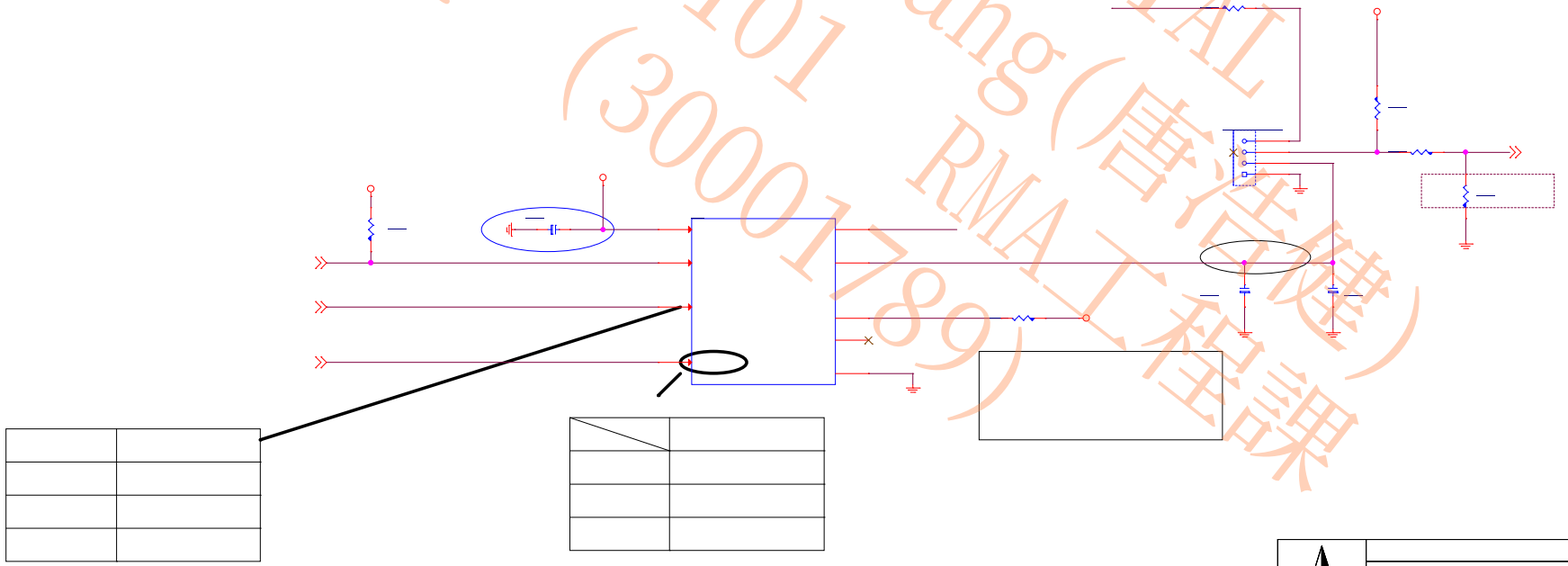
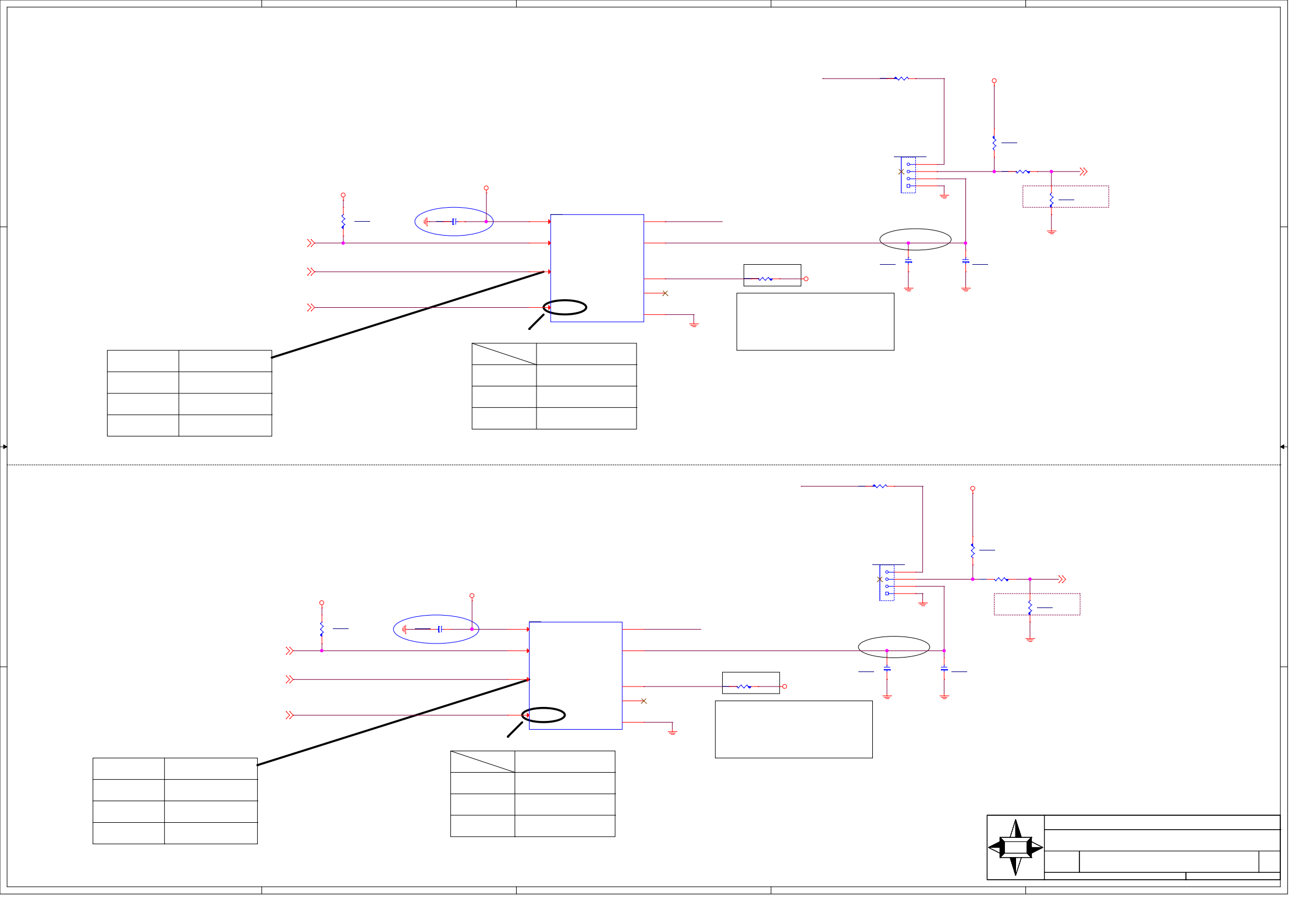
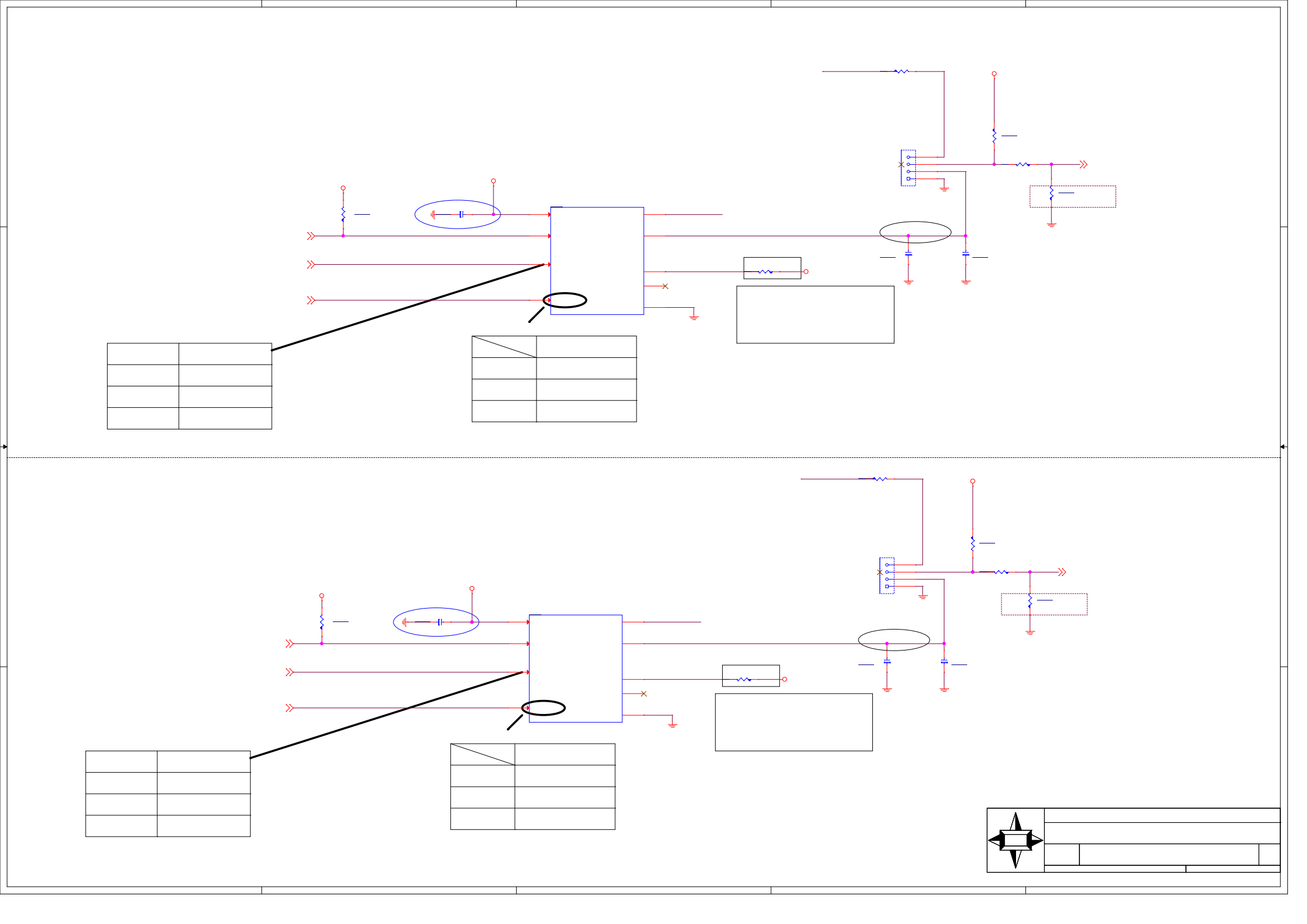
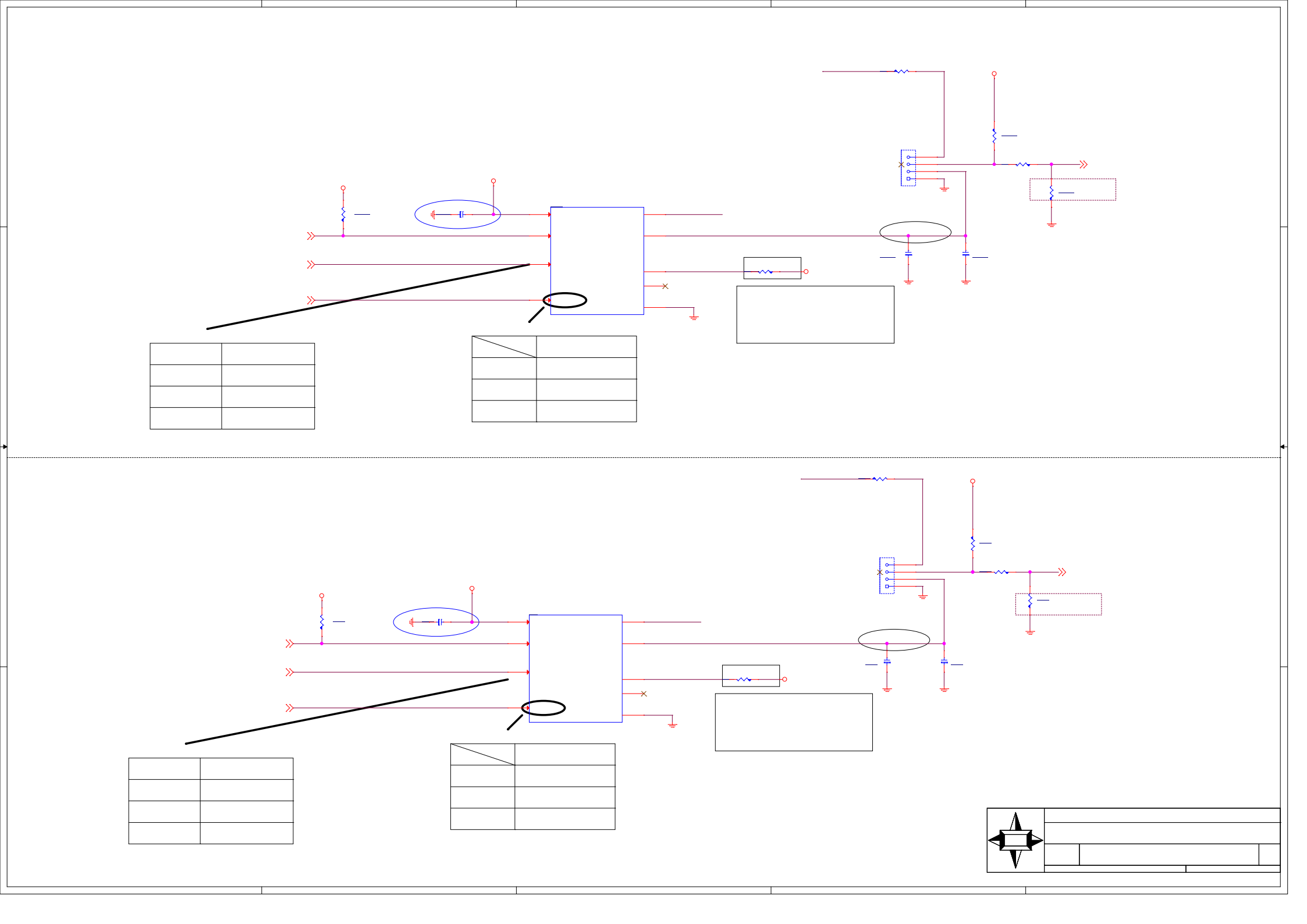


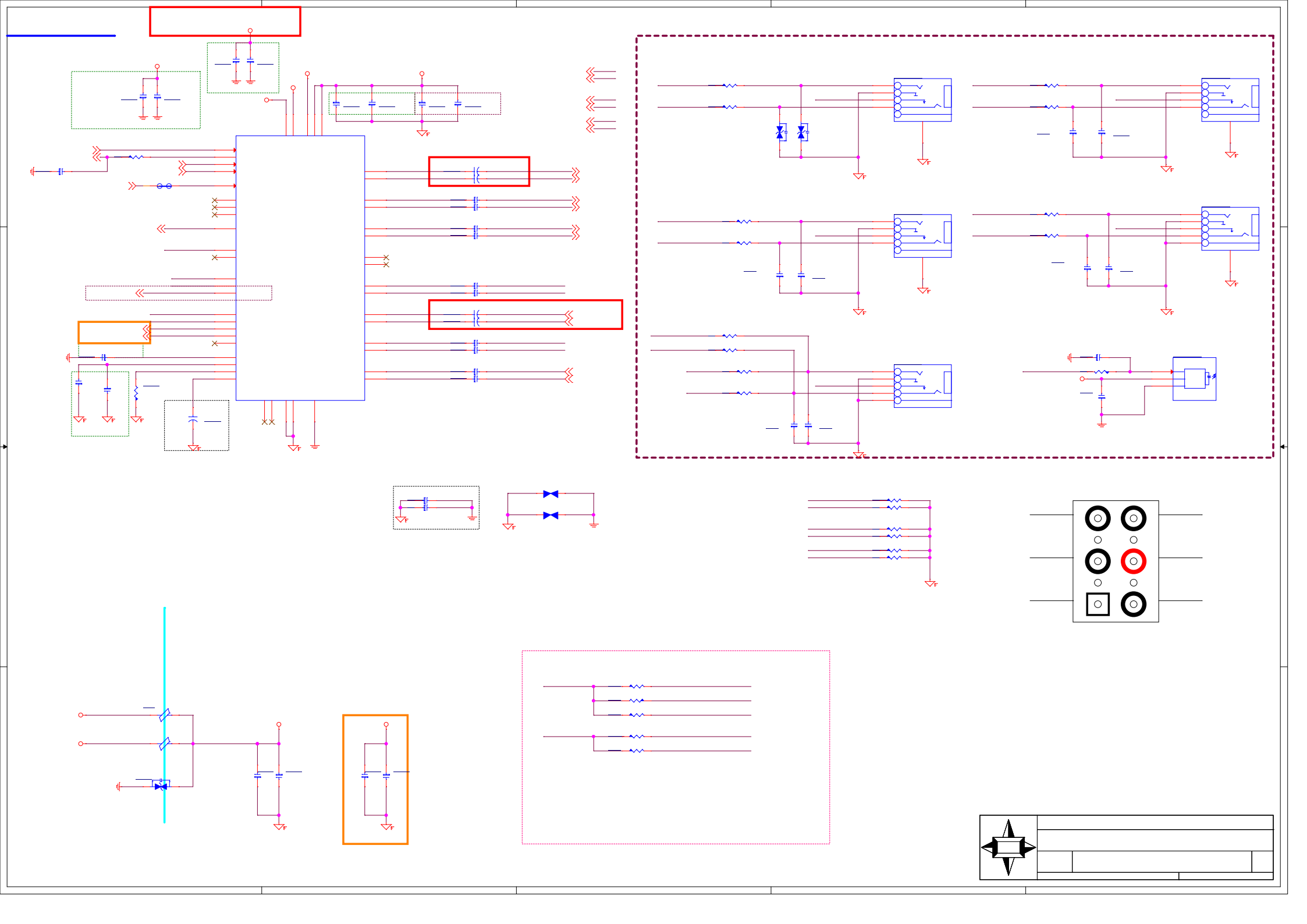


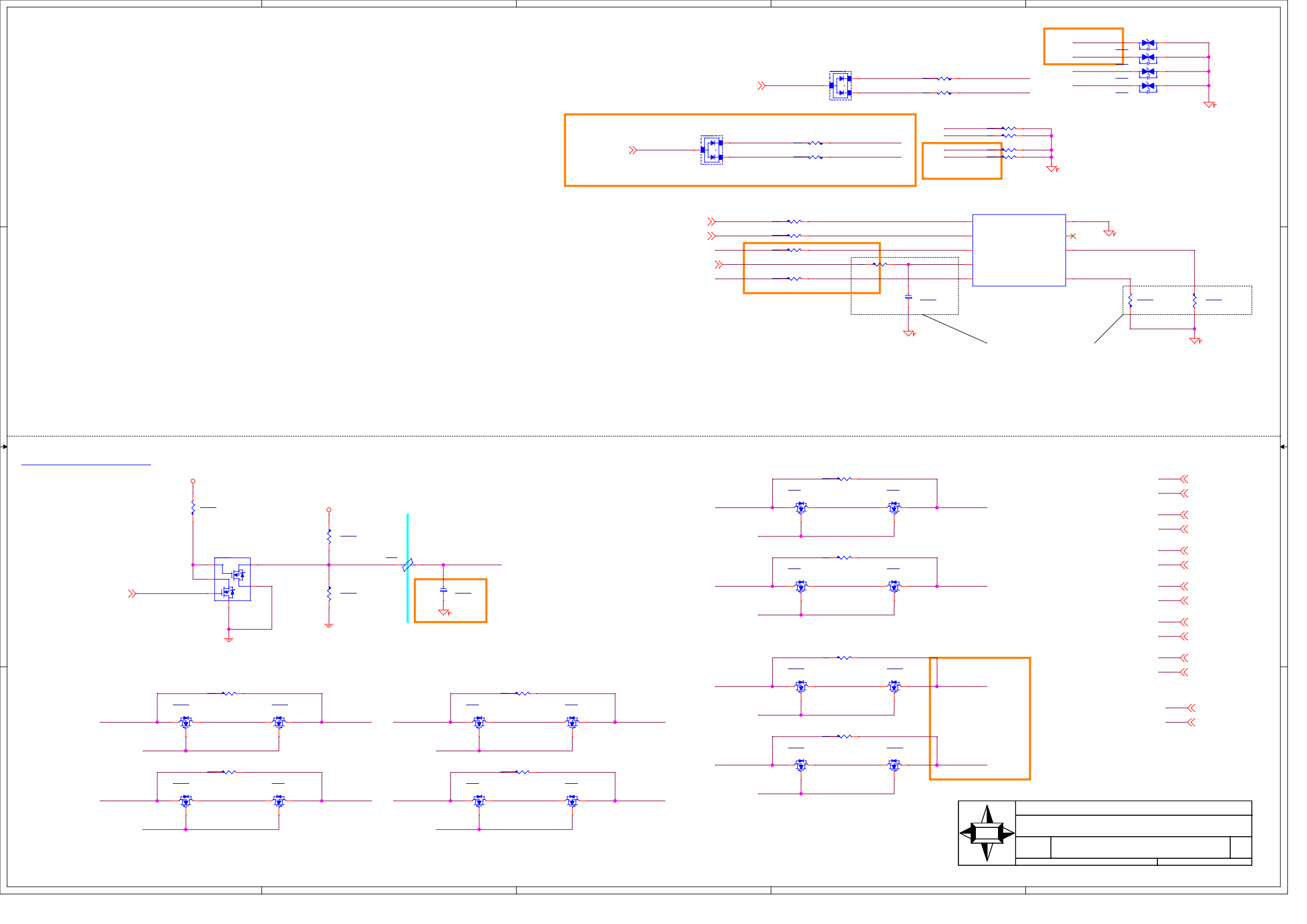


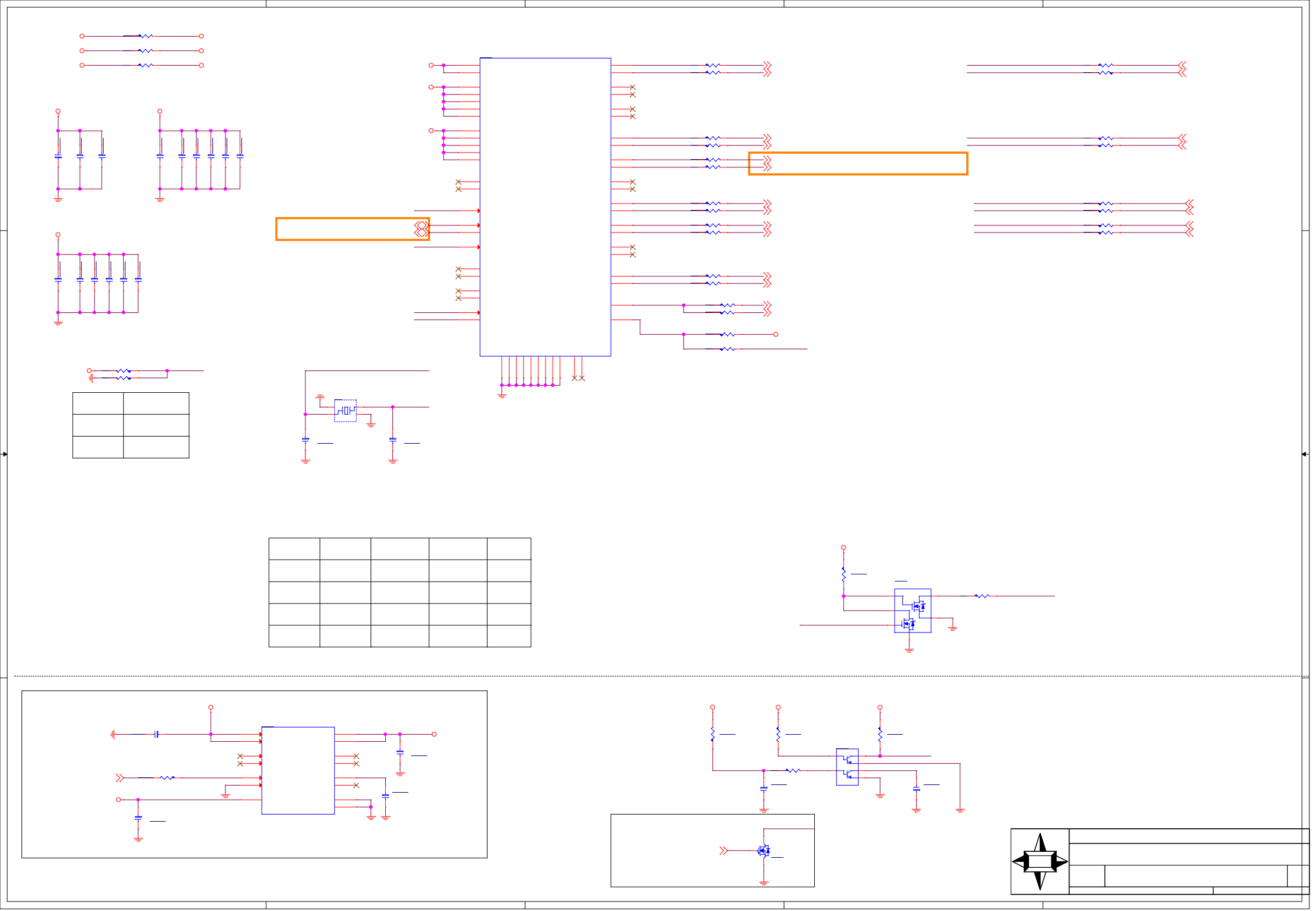


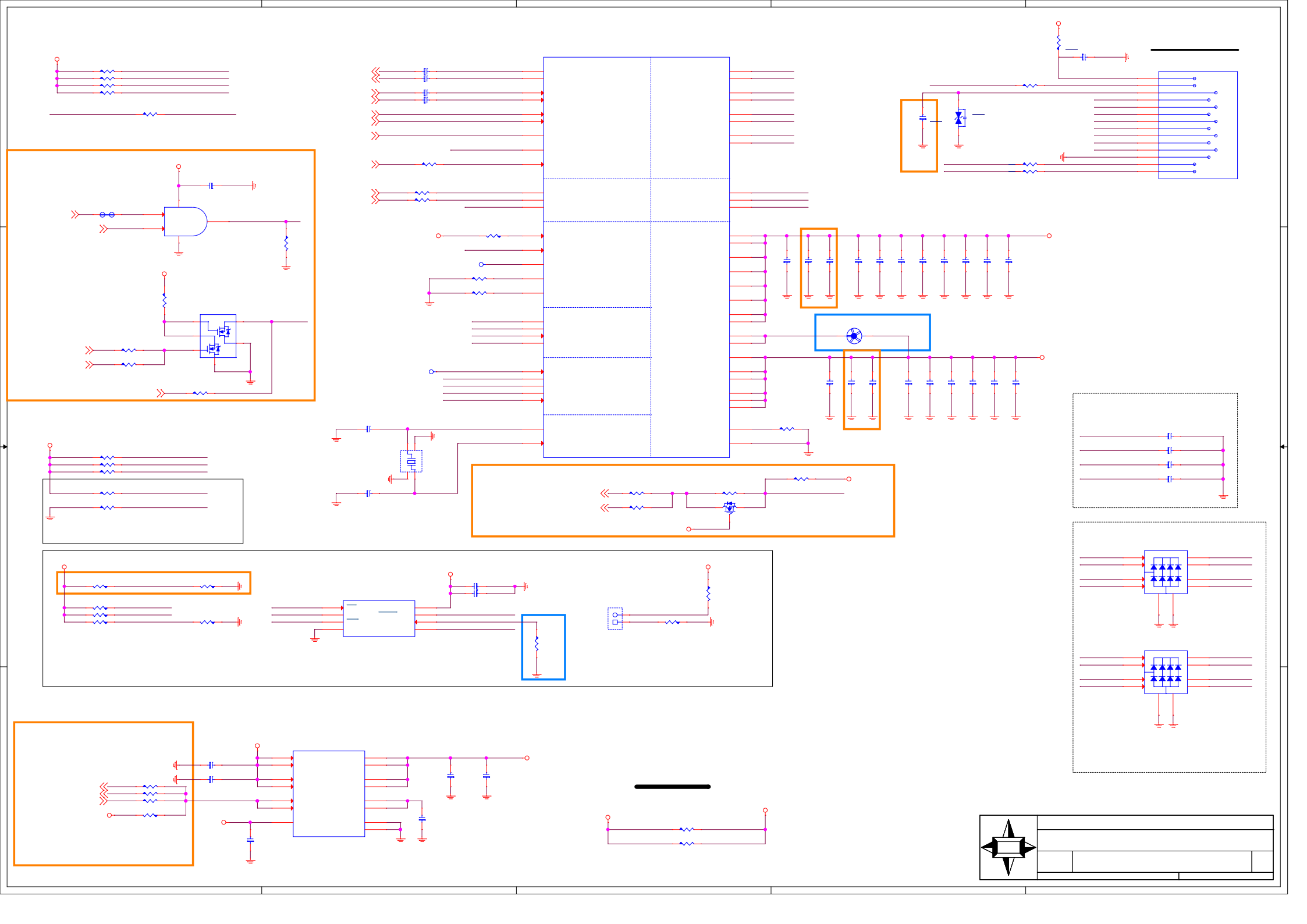


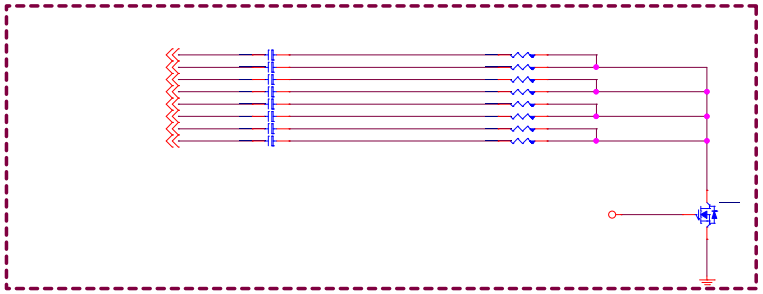
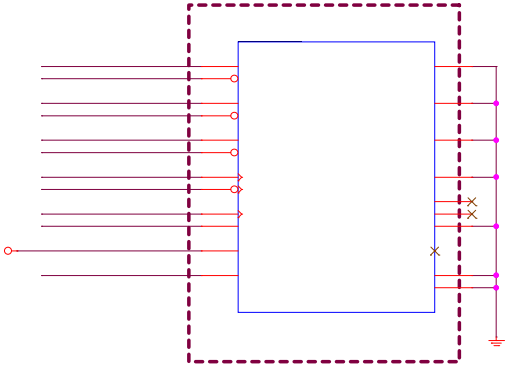
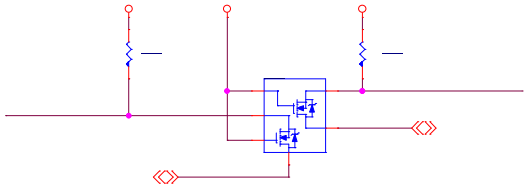
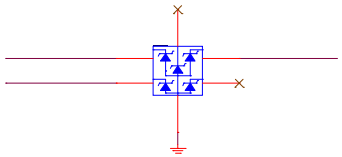
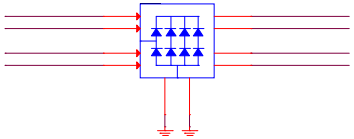
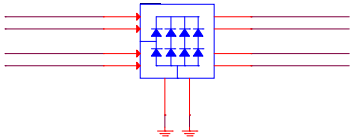
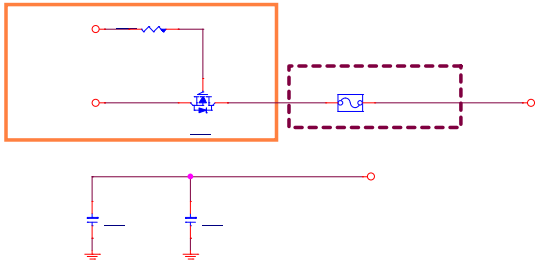
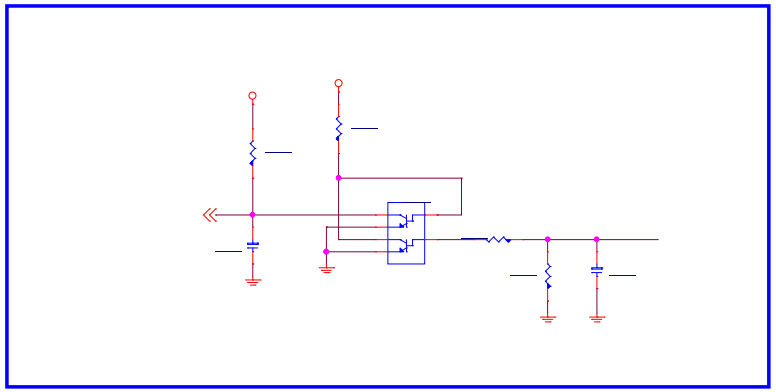
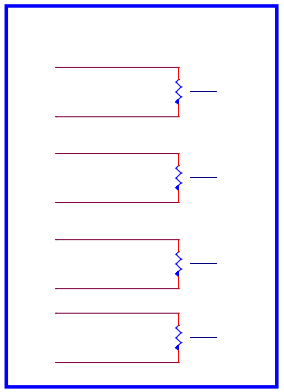
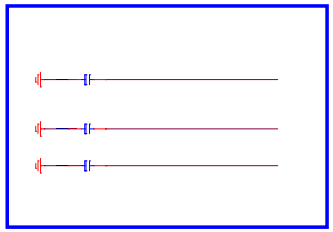


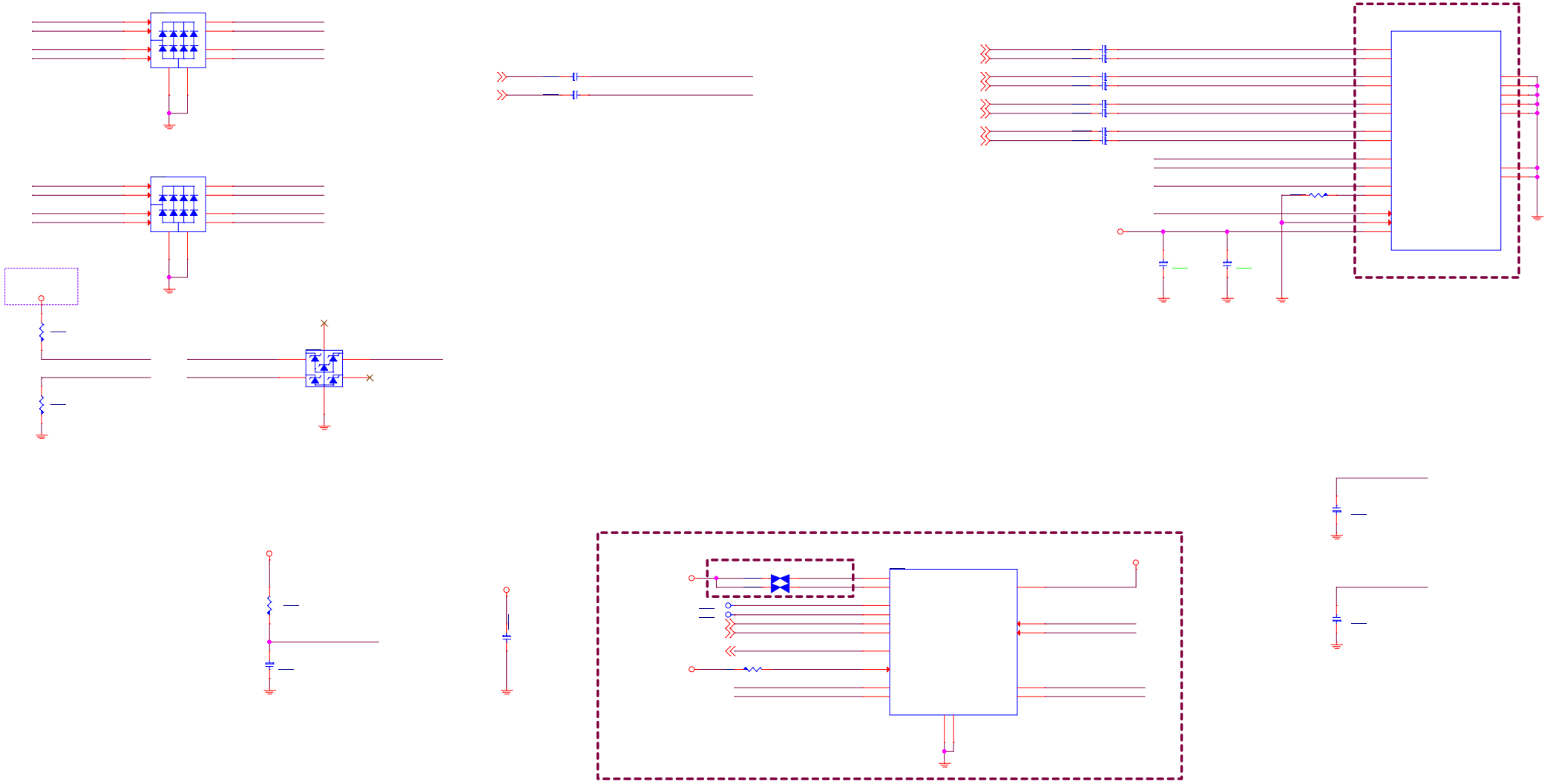


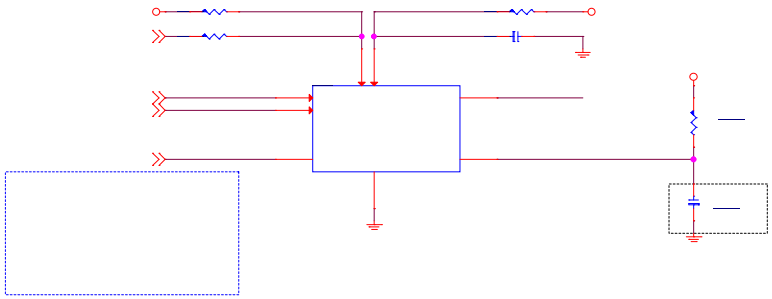


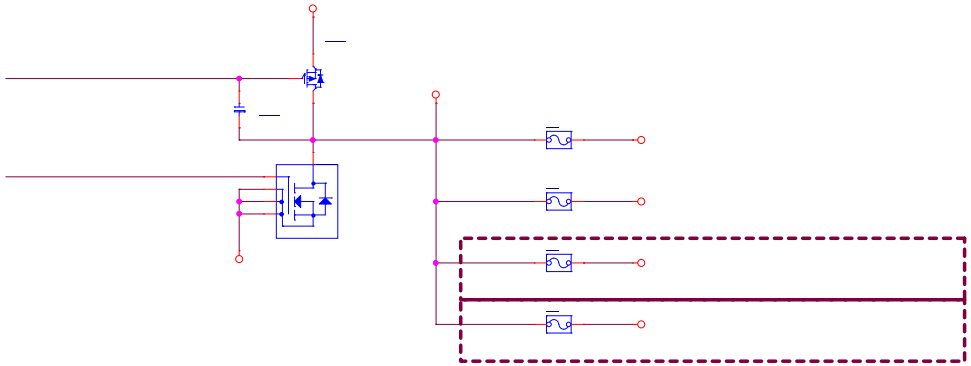


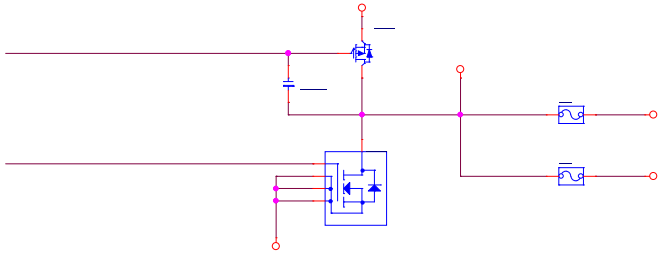


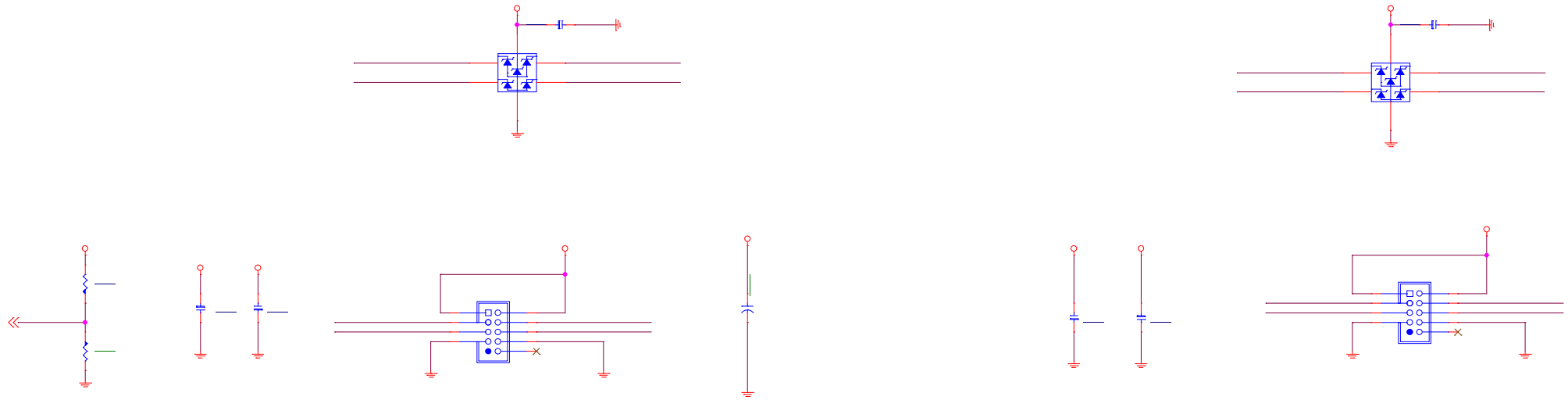











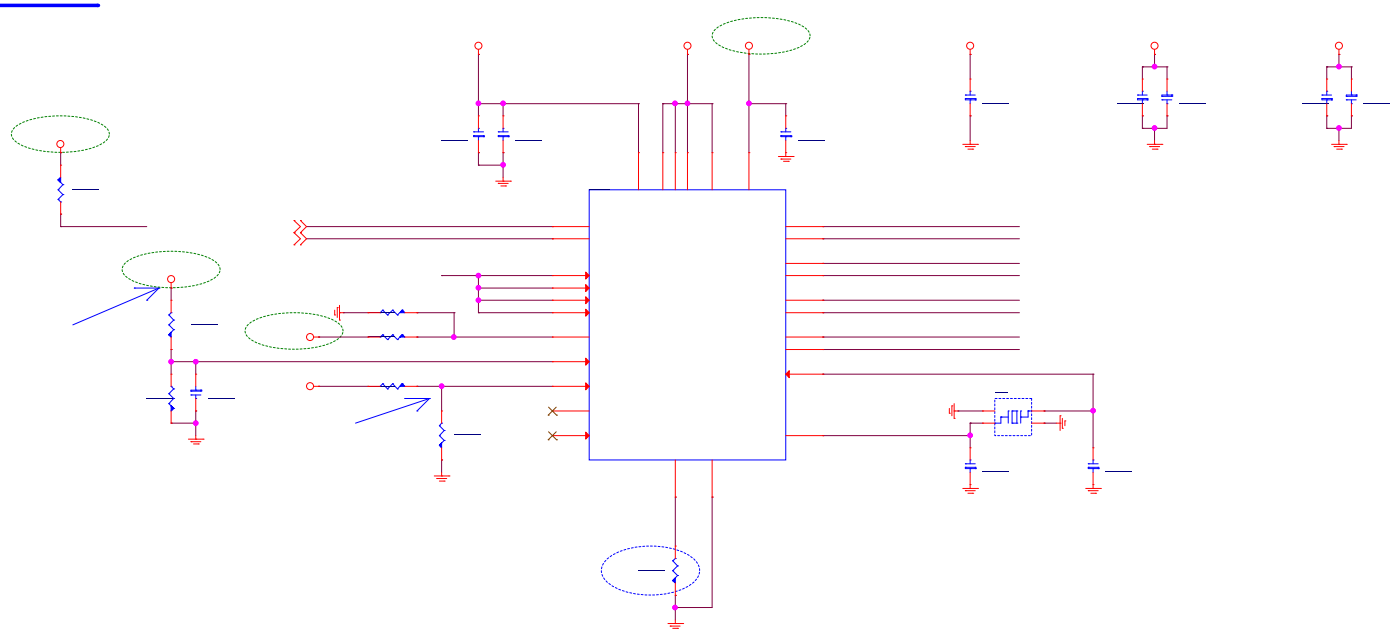


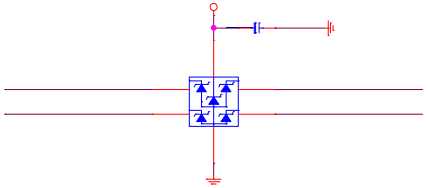


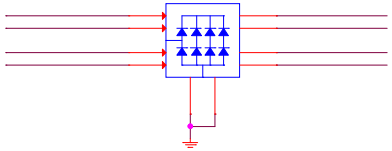


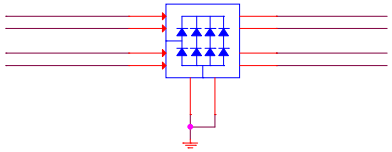


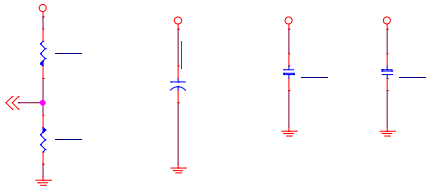


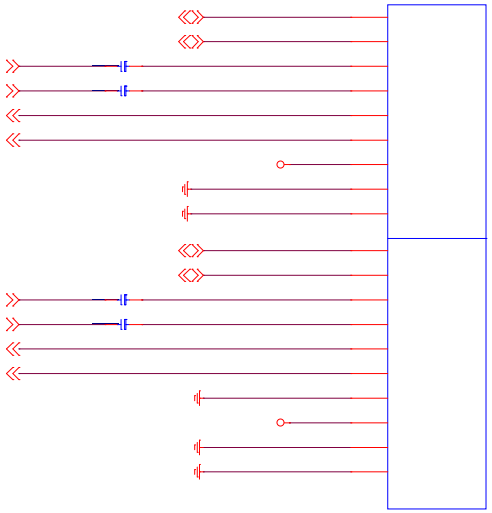


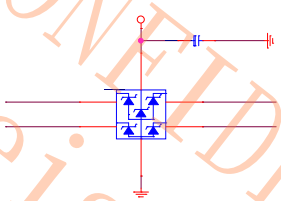


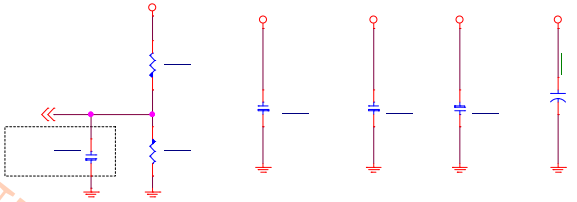


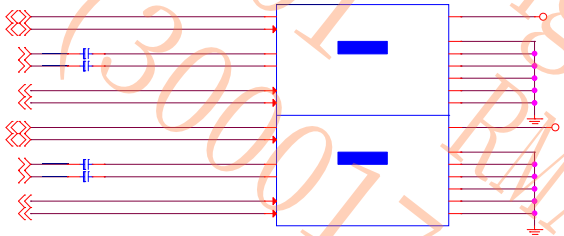


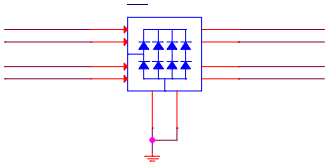


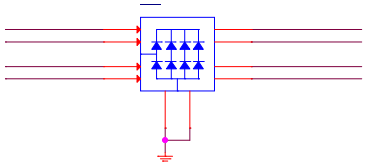


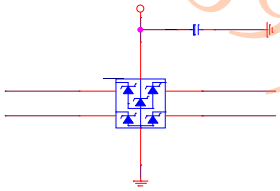


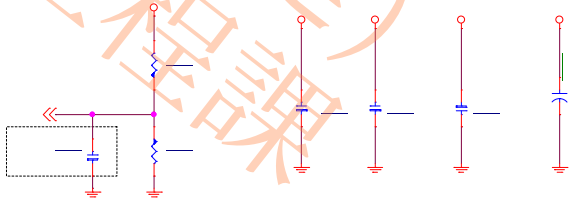


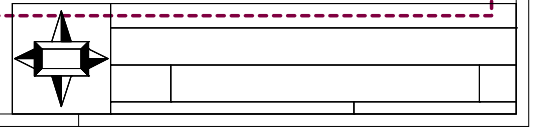




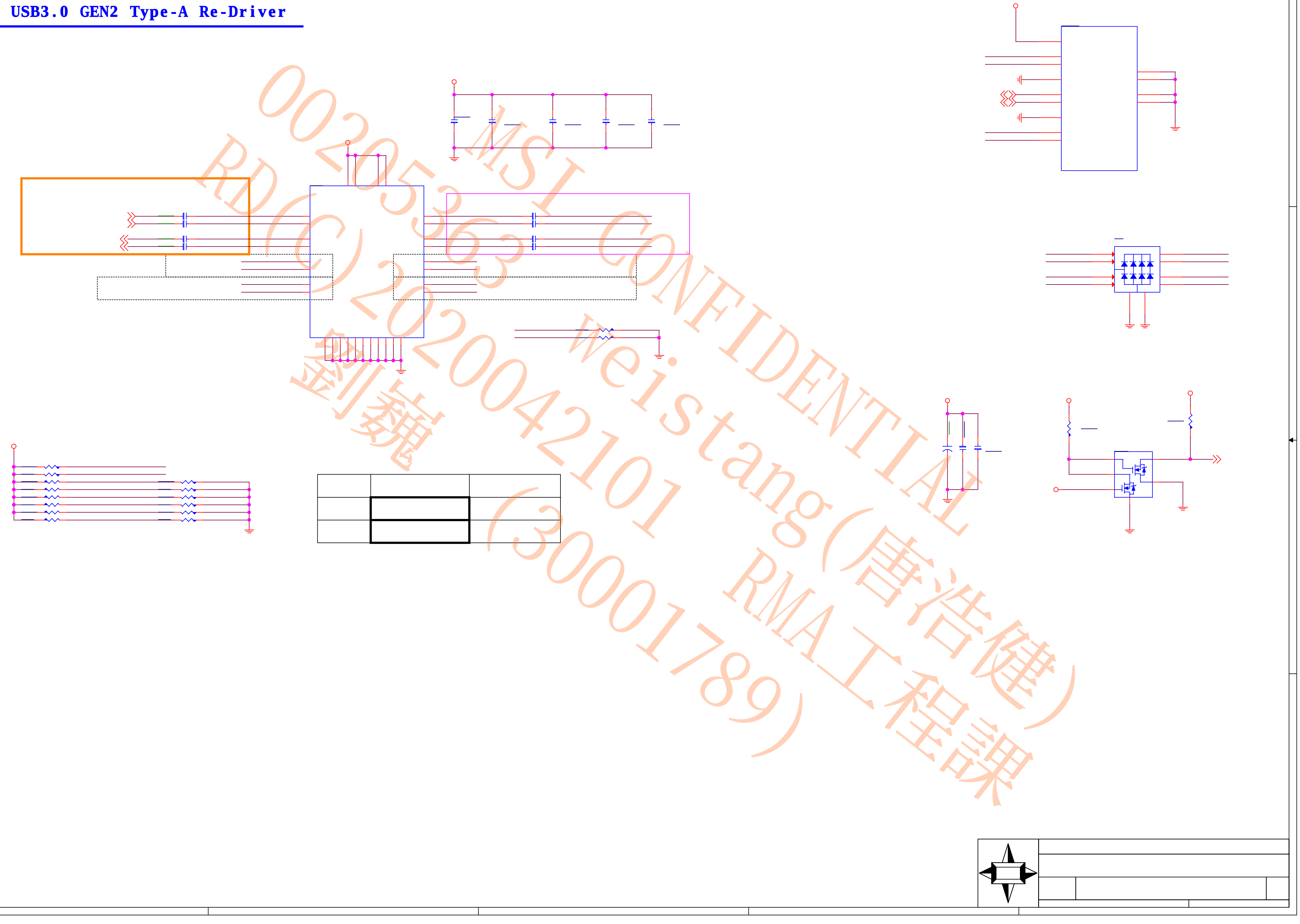






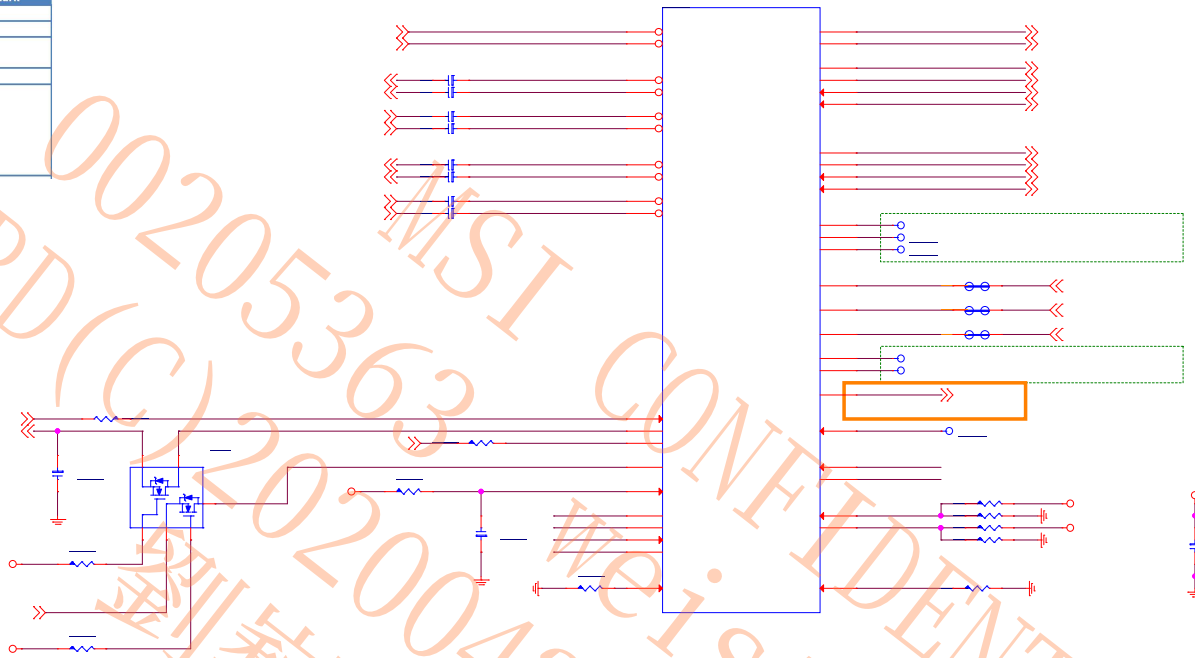


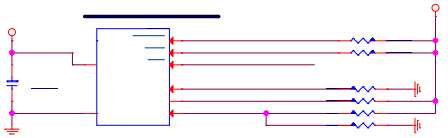
REAR USB3.0 GEN2 Type-A Re-Driver

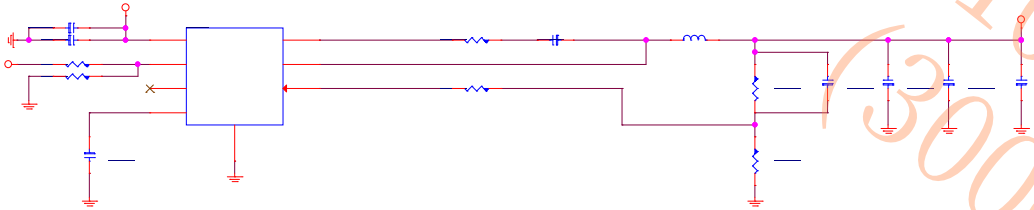


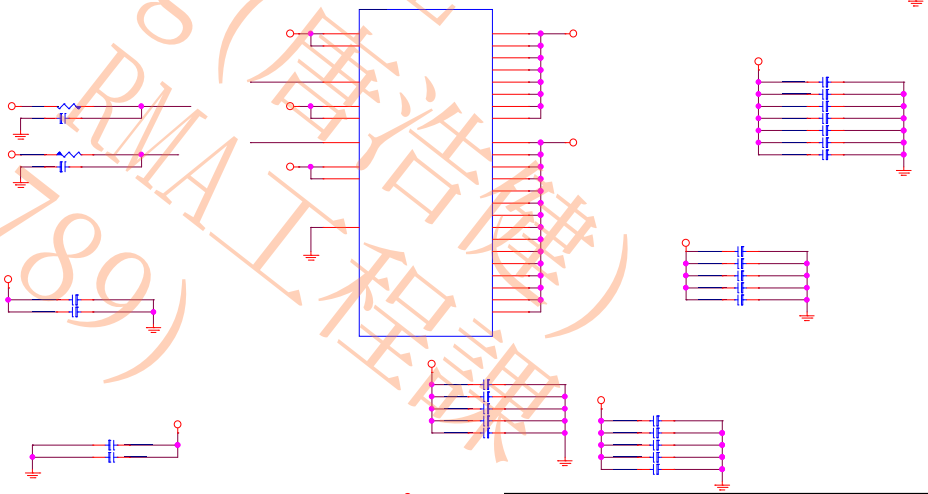
Power On Sequence Timing Specification for Asynchronous mode

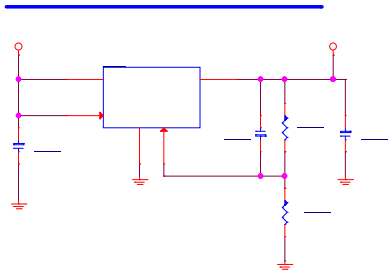
Symbols	Parameter	Min	Typ	Max	Unit	Remark
V_{TH_POR}	Threshold voltage for PORST# pins	1.38	1.6	1.8	V	
t_{OCI}	OCI# ready after Suspend Power Ready			12	ms	
t_{VSR}	Rising time for Suspend and normal Power Ready			10	ms	
t_{VNRDY}	Timing for all normal power Ready	50			ms	Note 1
t_{PORST1}	Timing for all normal Power Ready to Power On Reset (when suspend power domains are existed)	10		80	ms	
	Timing for all normal Power Ready to Power On Reset (when suspen powers connect to normal power directly)	60		80	ms	

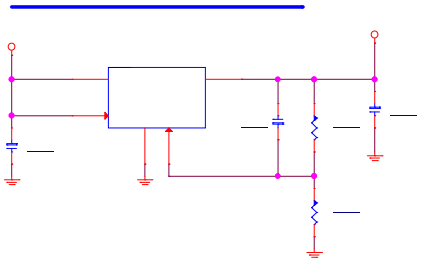


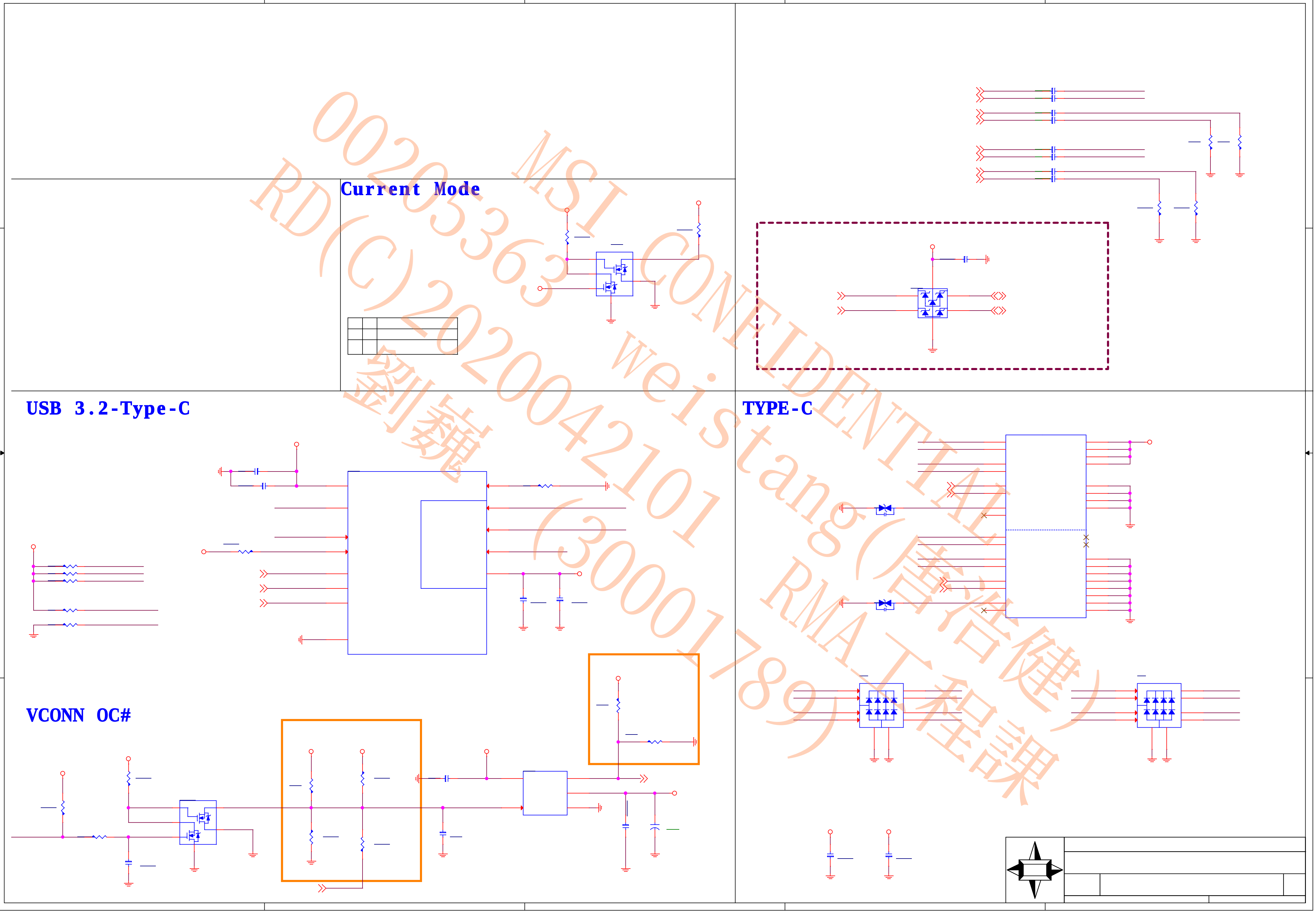


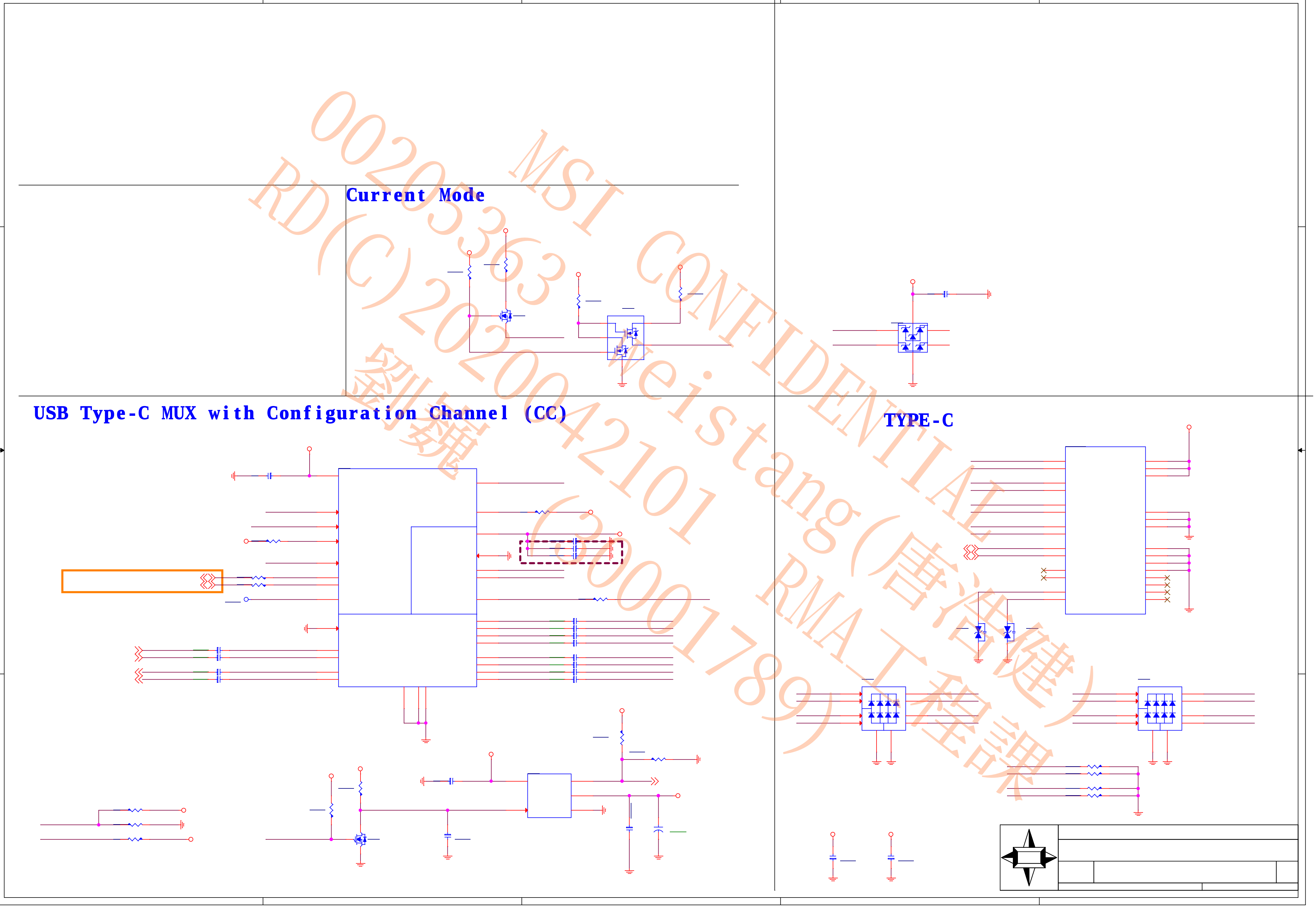


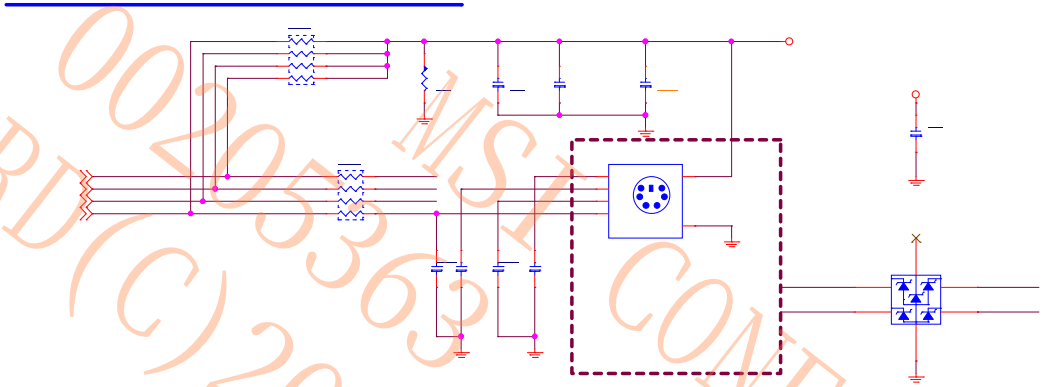


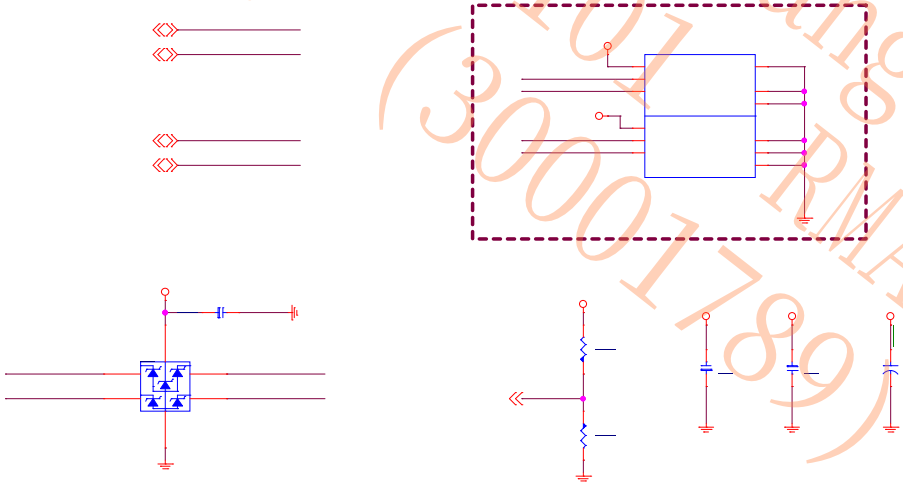




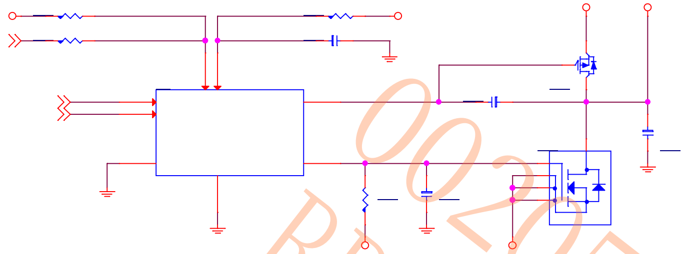




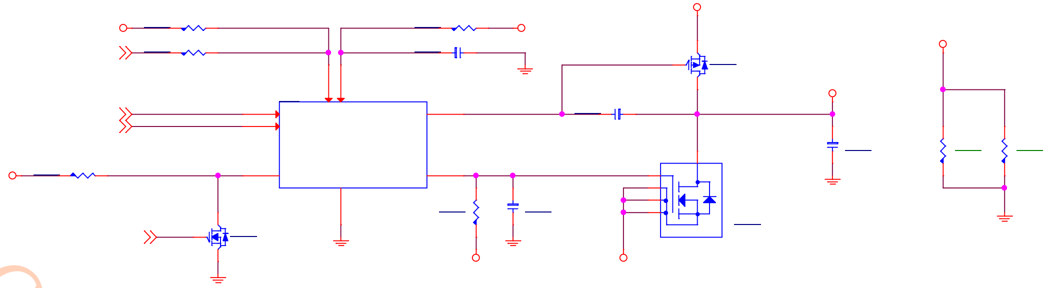




5VDIMM FOR DDR



5VDUAL



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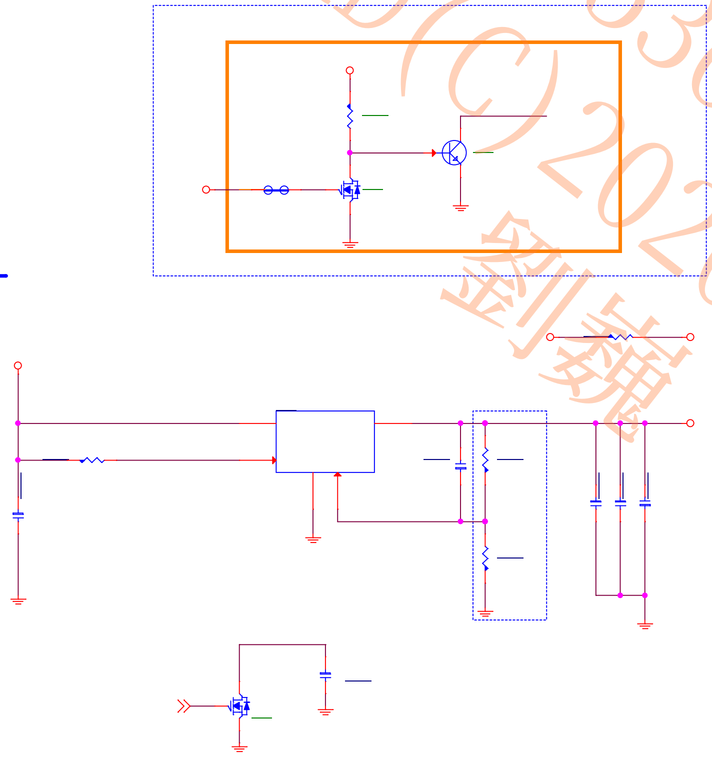

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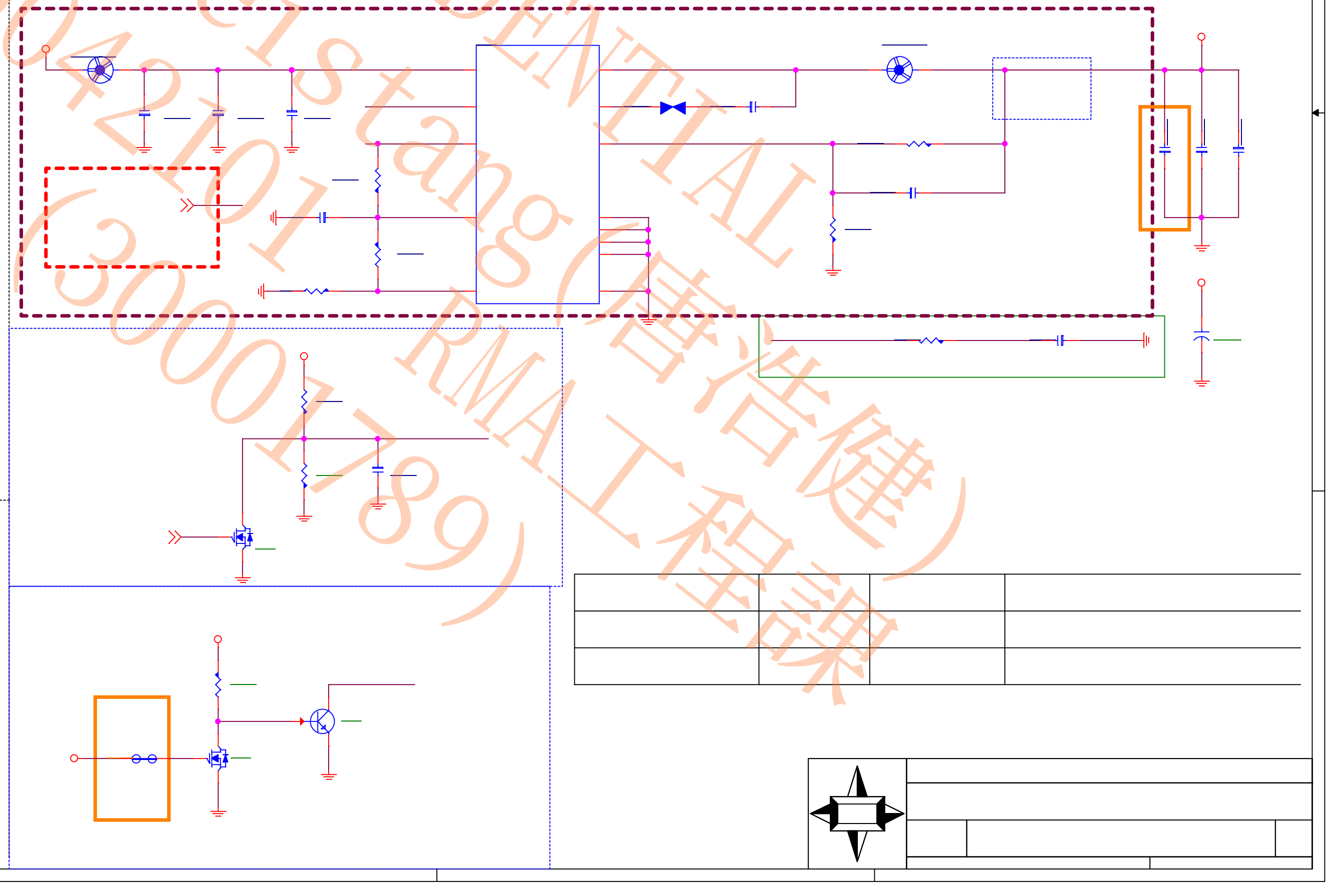
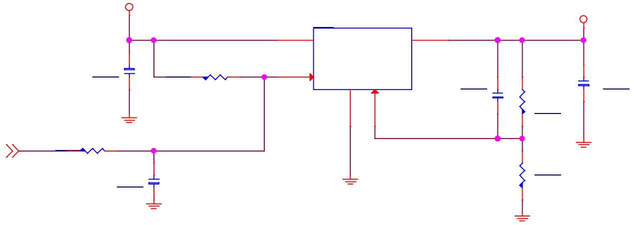
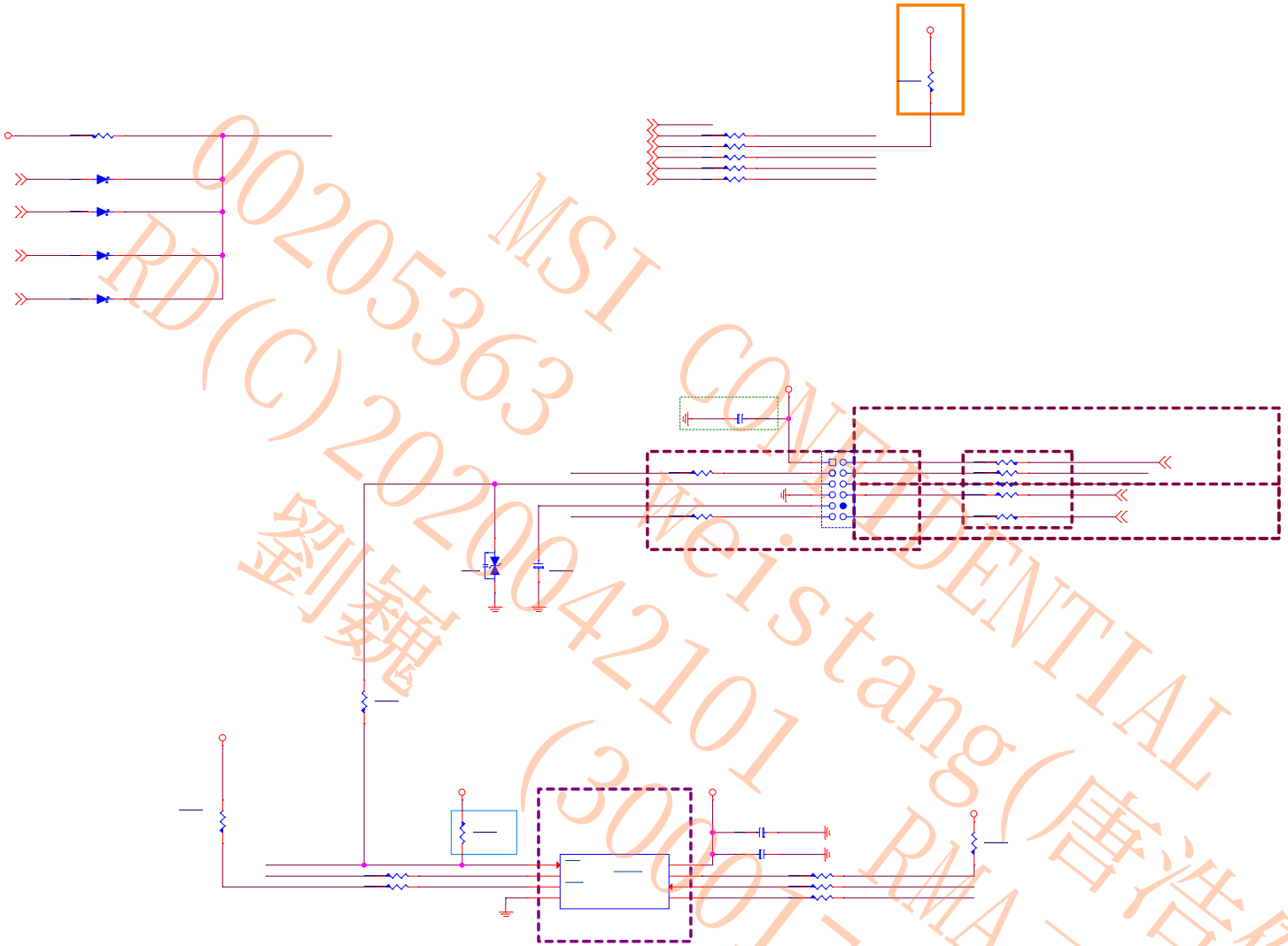
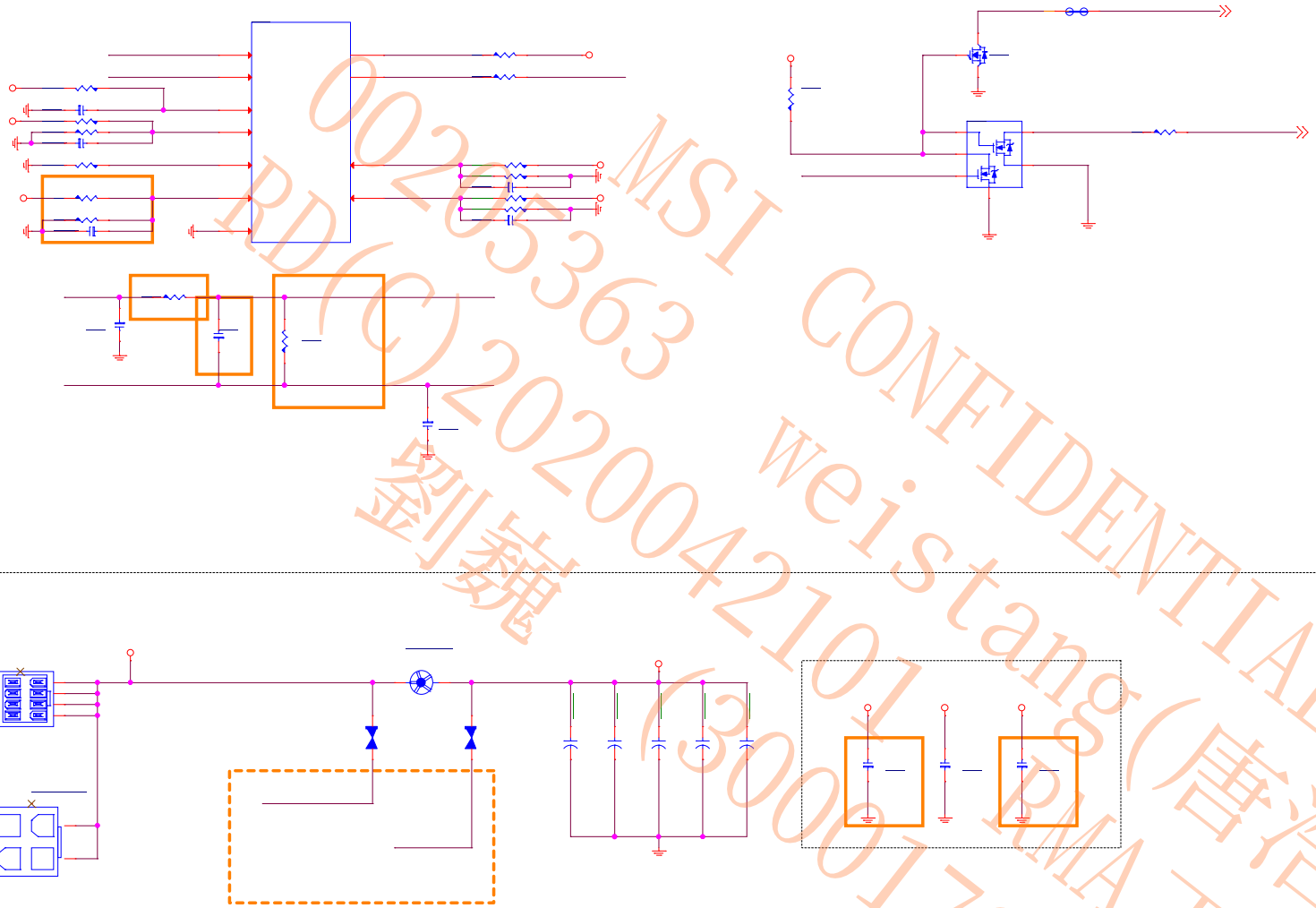


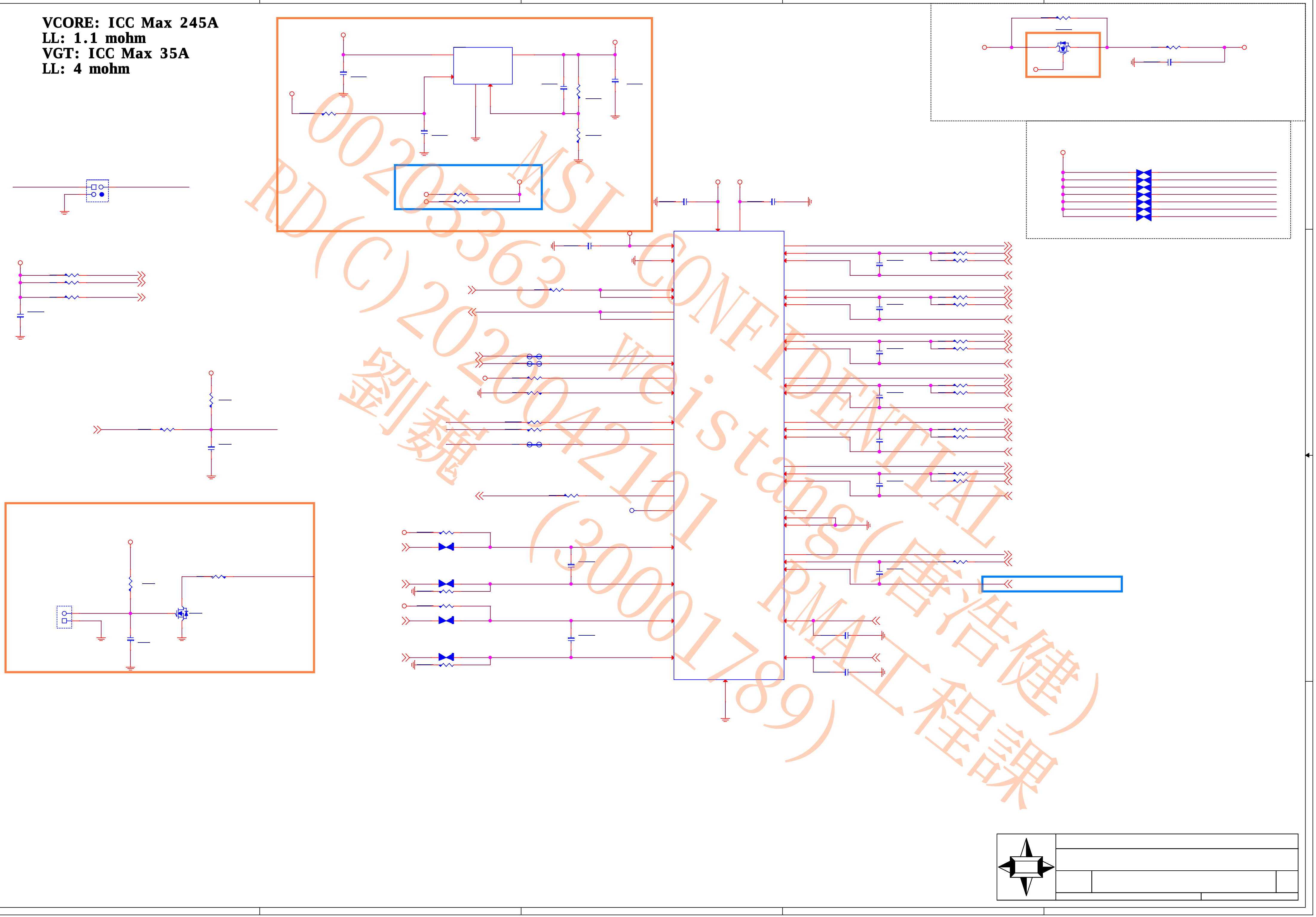
SIO_3VA

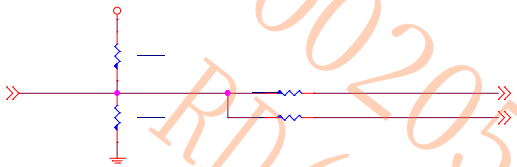
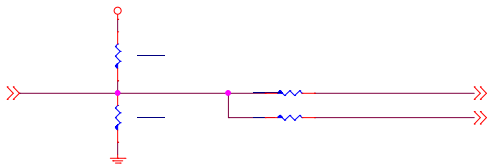
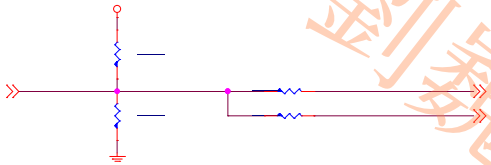
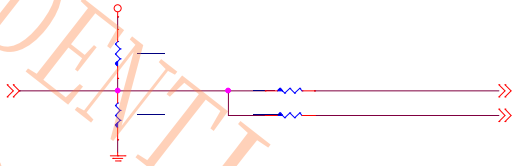
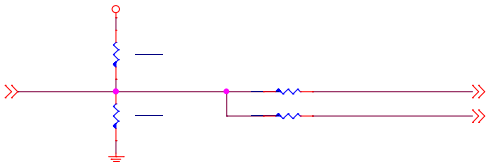
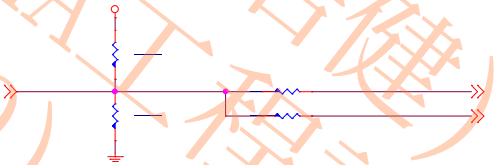


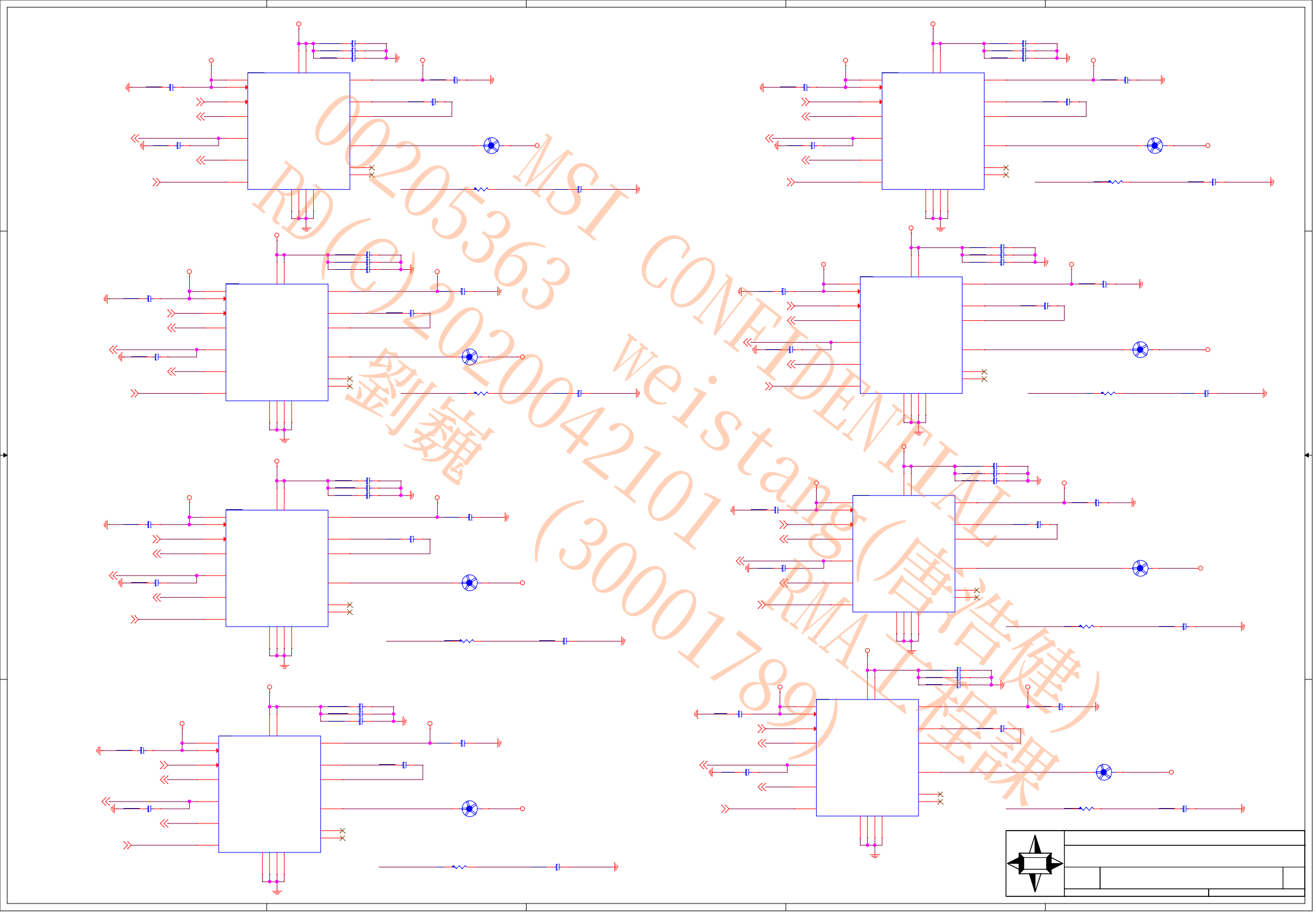




VCORE: ICC Max 245A
LL: 1.1 mohm
VGT: ICC Max 35A
LL: 4 mohm

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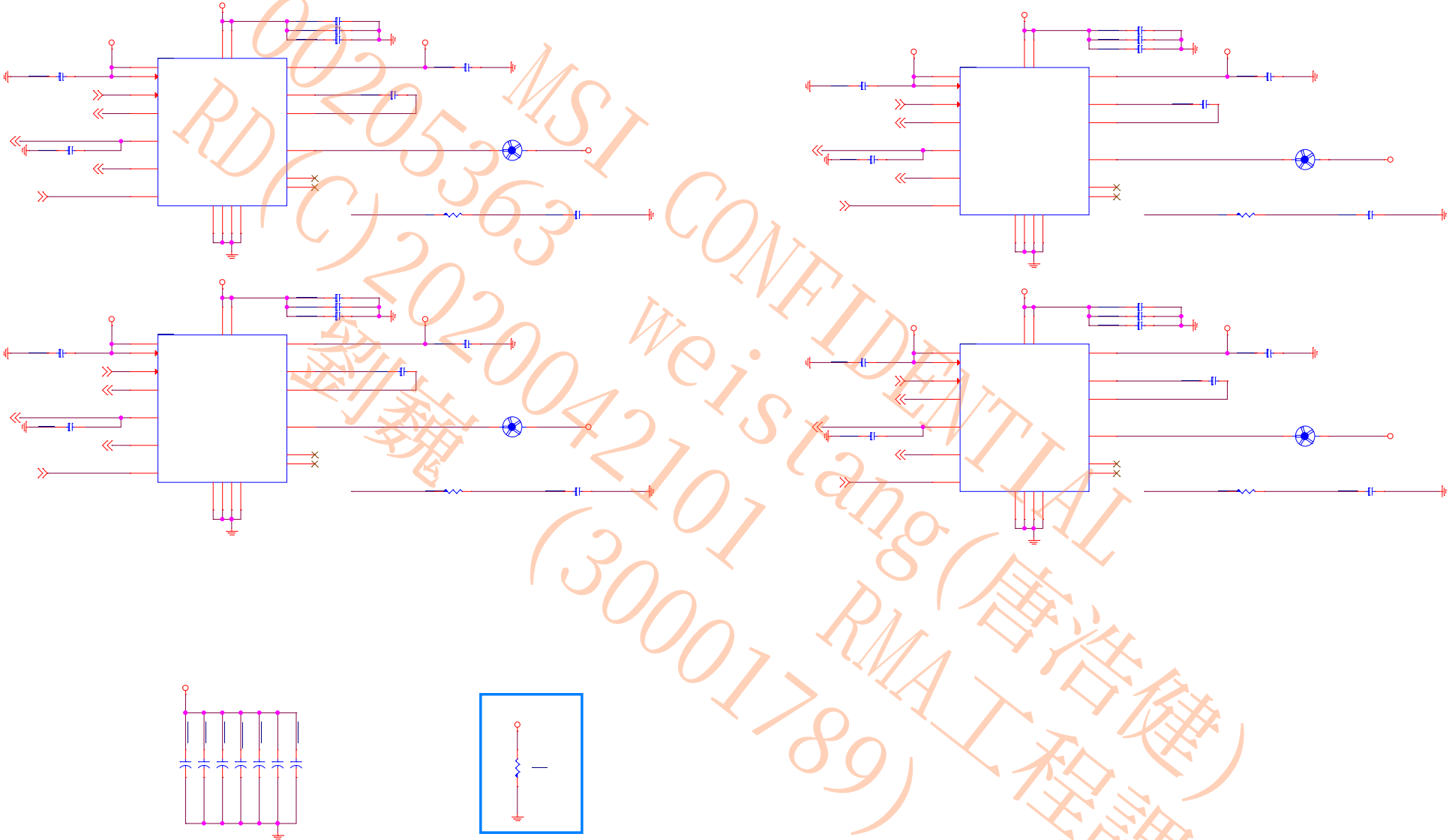

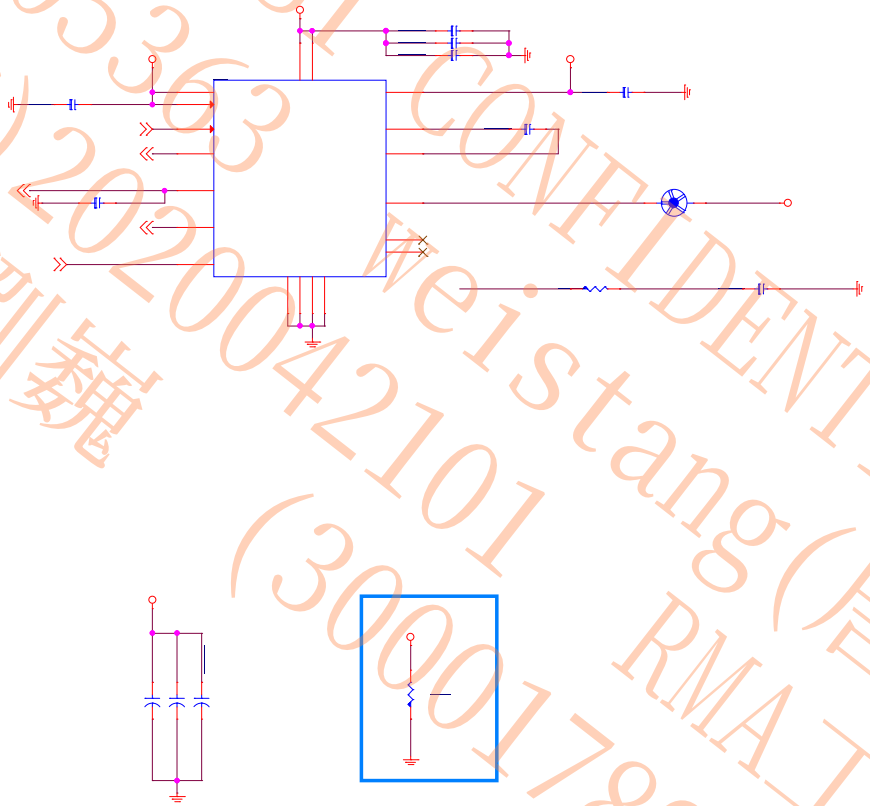

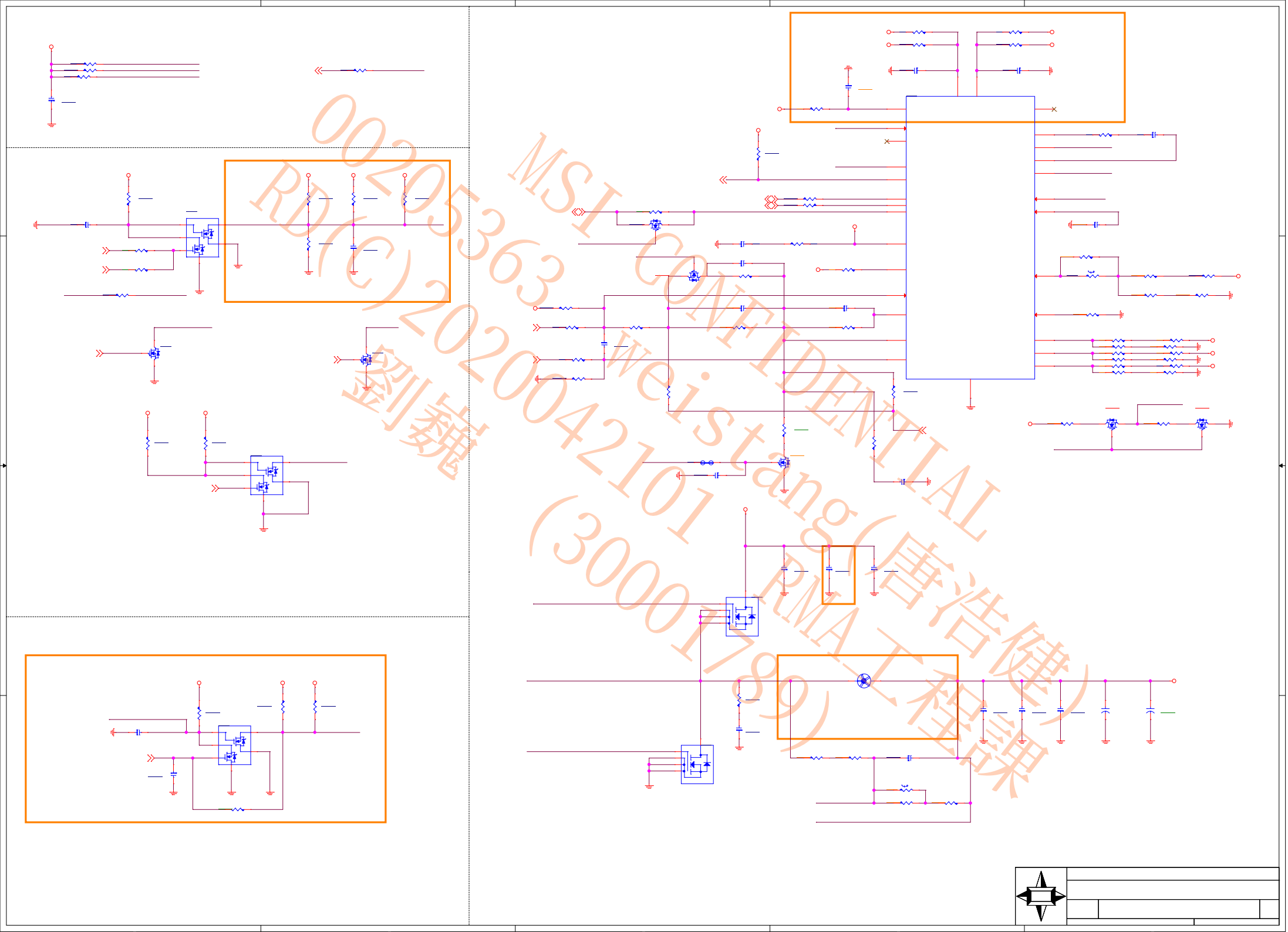

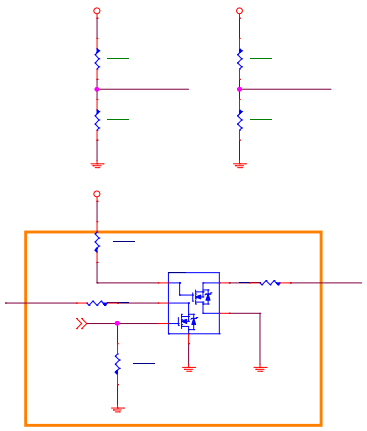

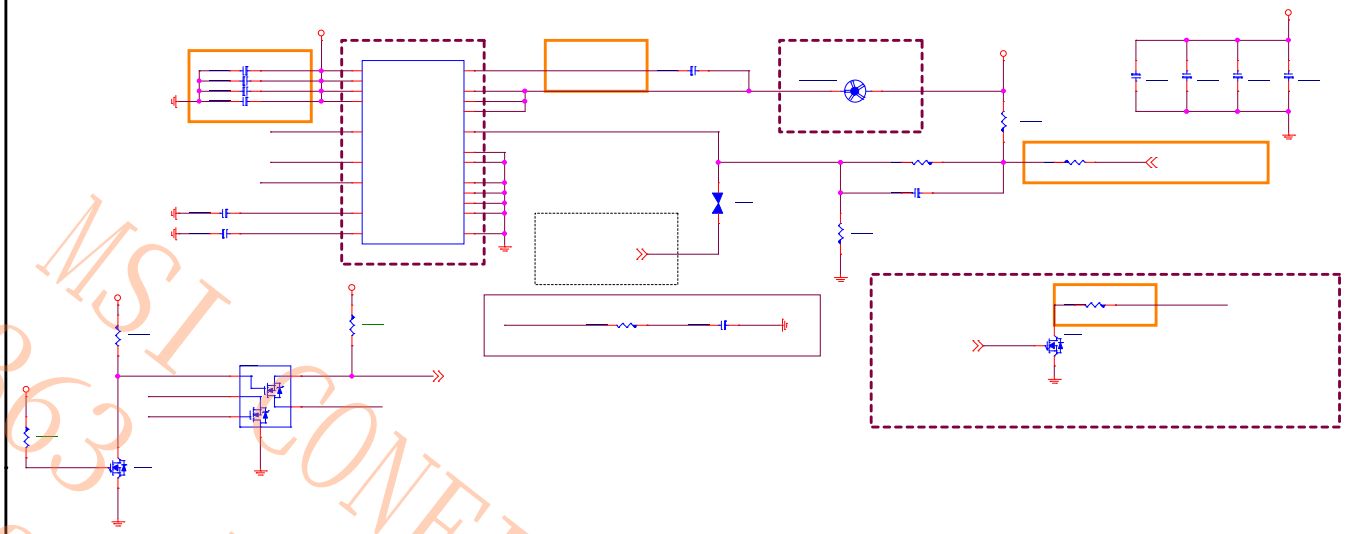

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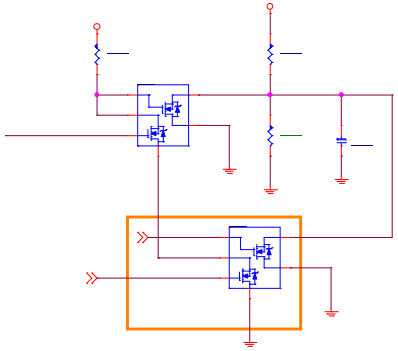

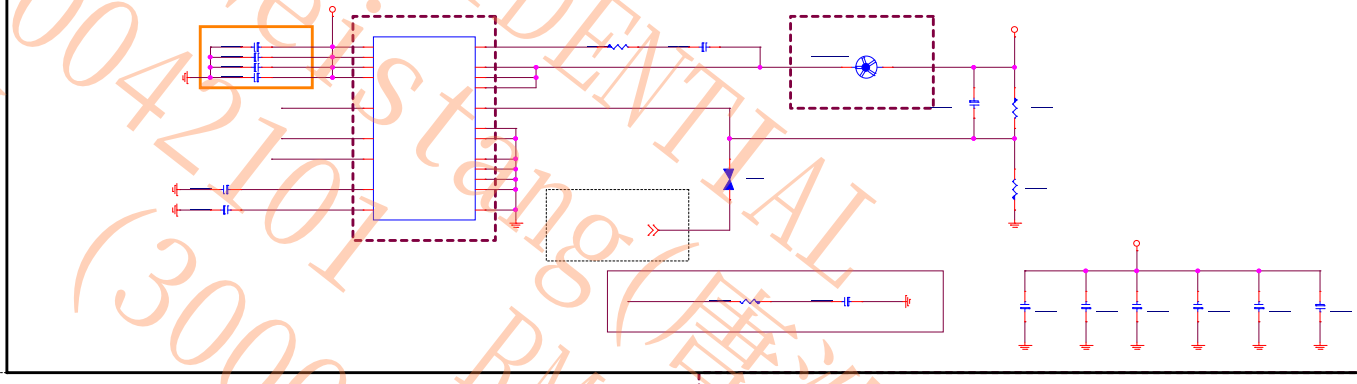

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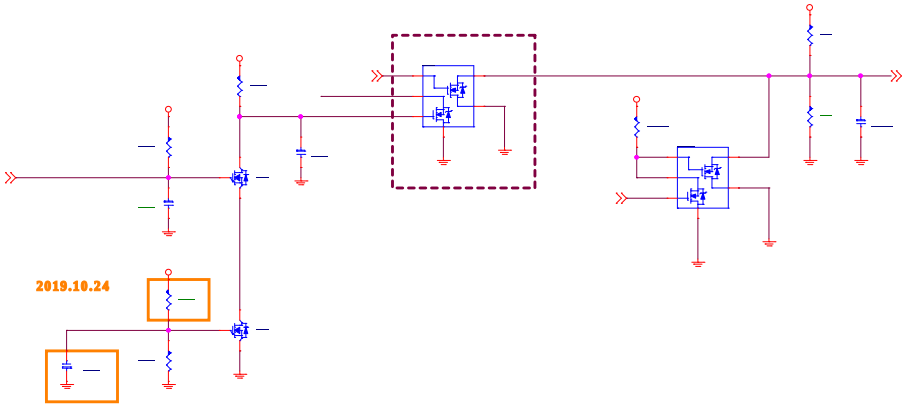
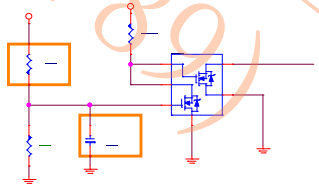


2019.10.24



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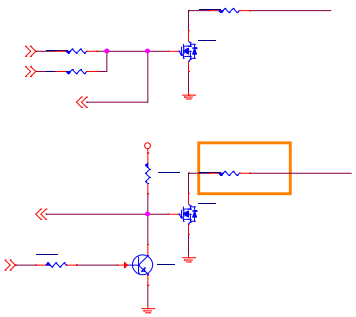
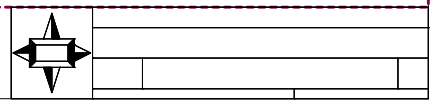
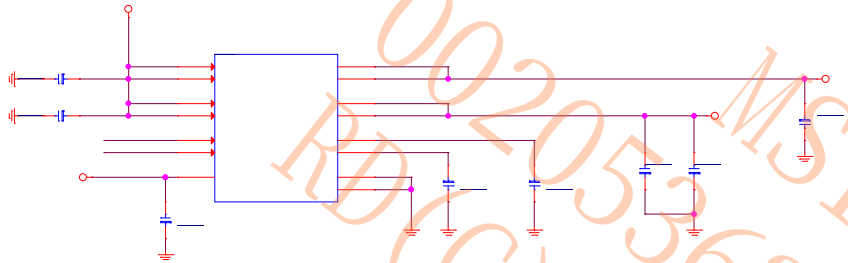
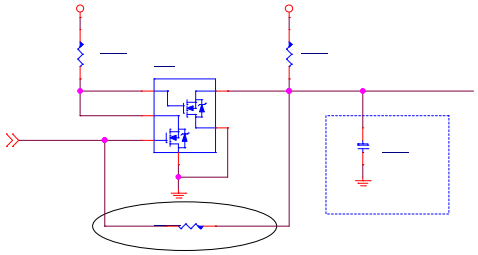
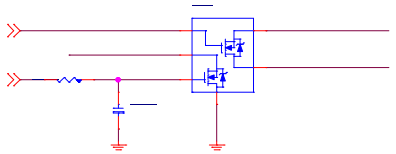
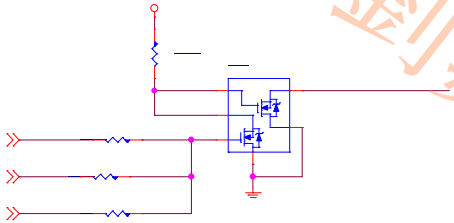


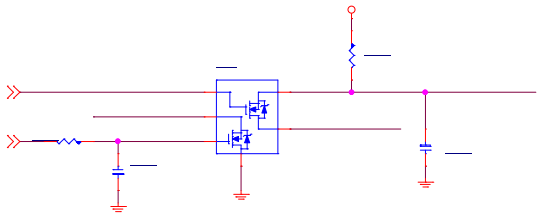


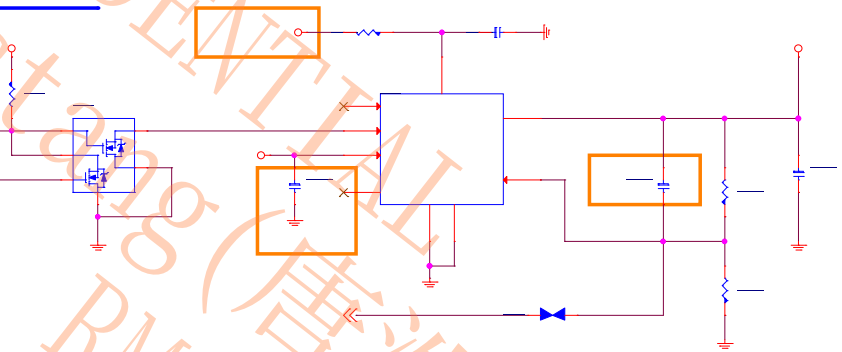


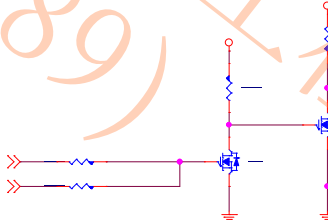


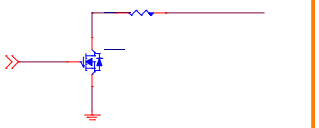


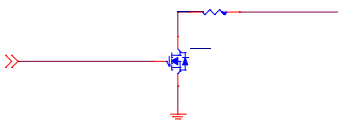


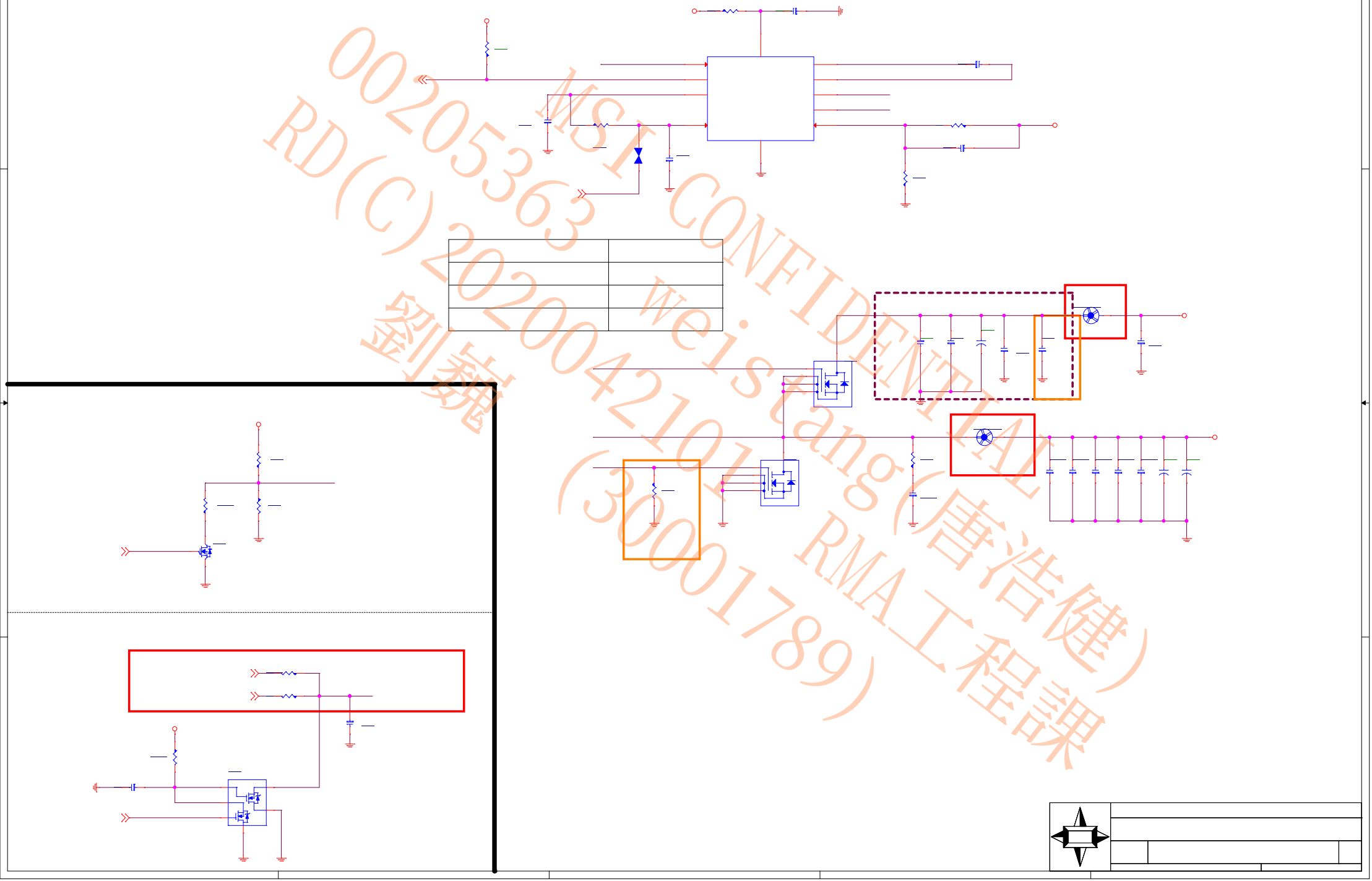


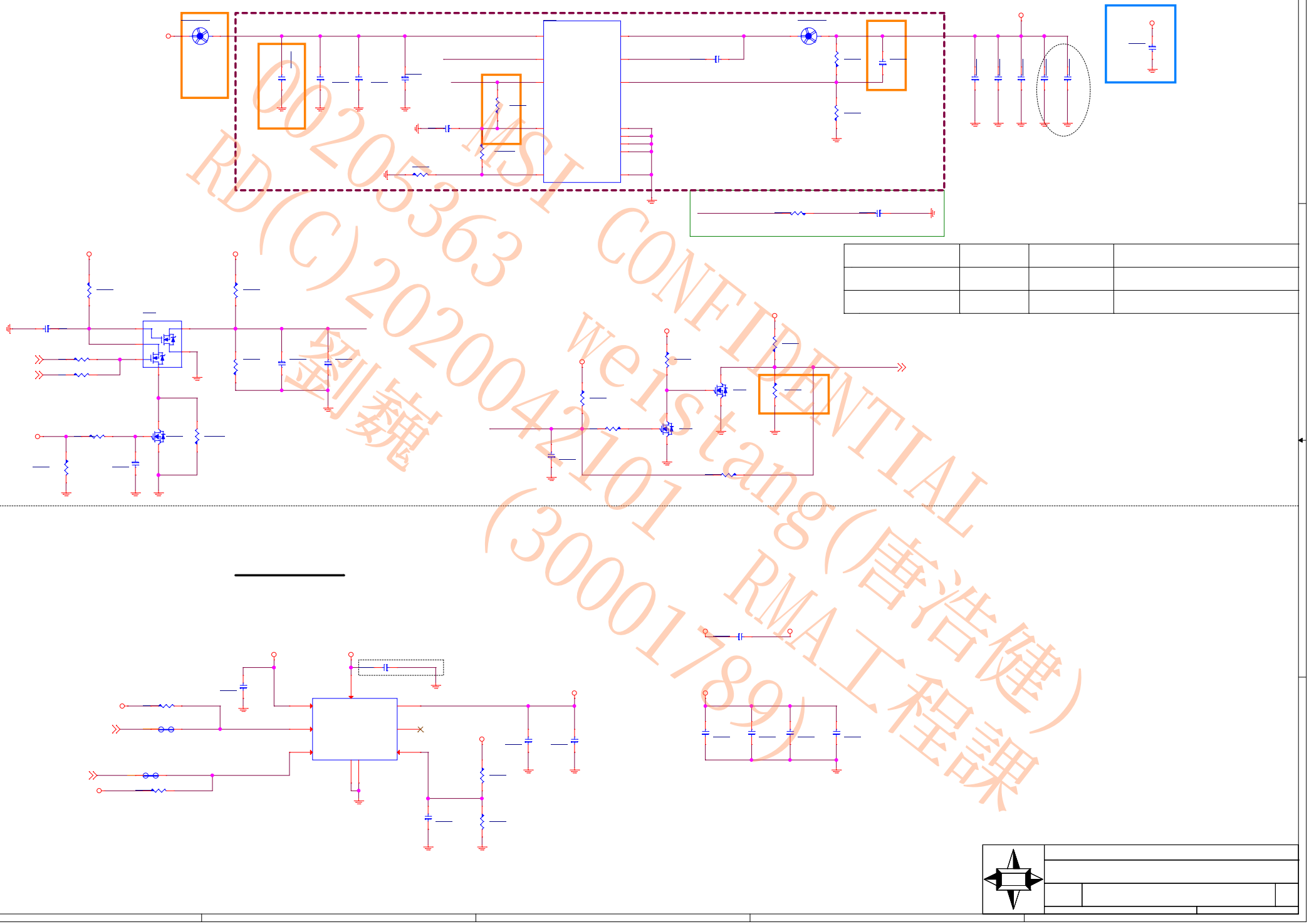






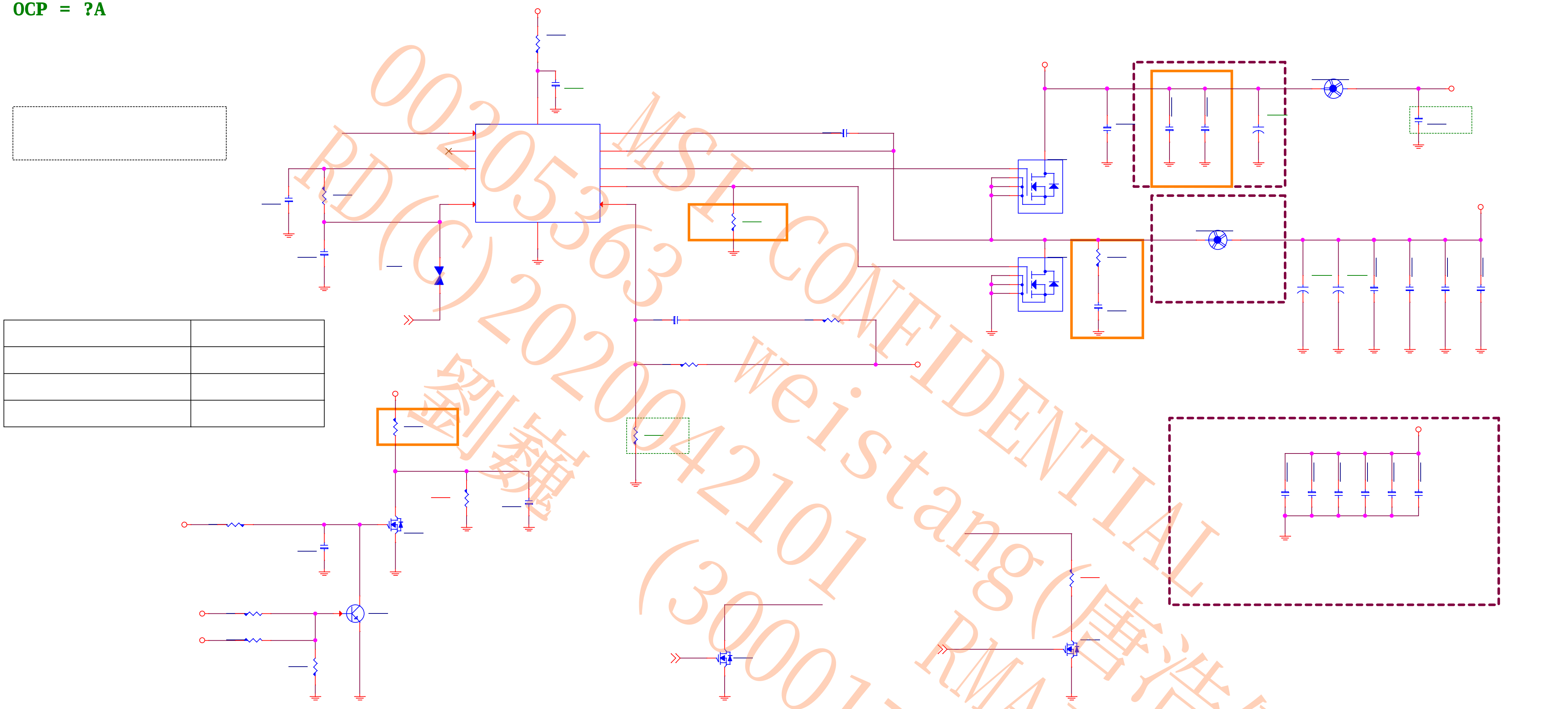




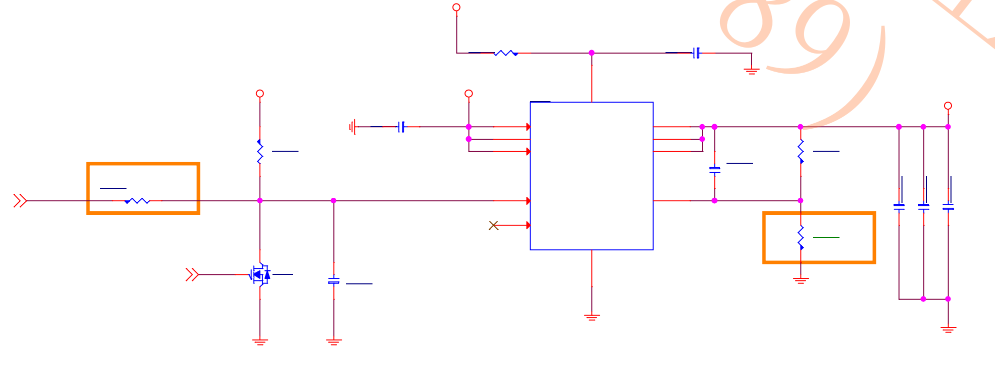


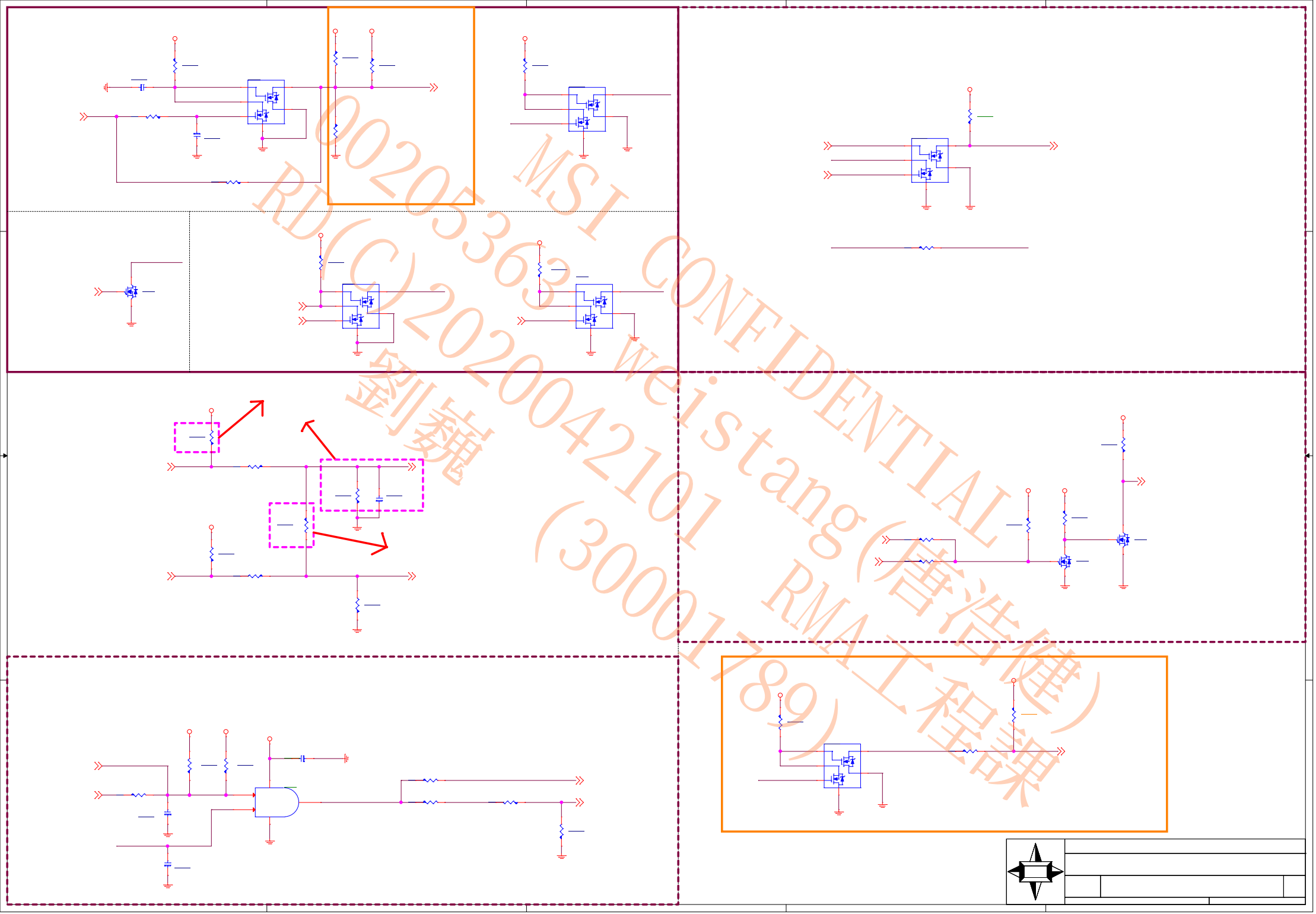
PCH_1P05_VSB Power:1.05V,17.651A

OCP = ?A



1P8_VSB Power:1.8V, 0.917A
(3.3V-1.8V)*0.917A=1.375W
QFN_10_3X3 PD 1.67W

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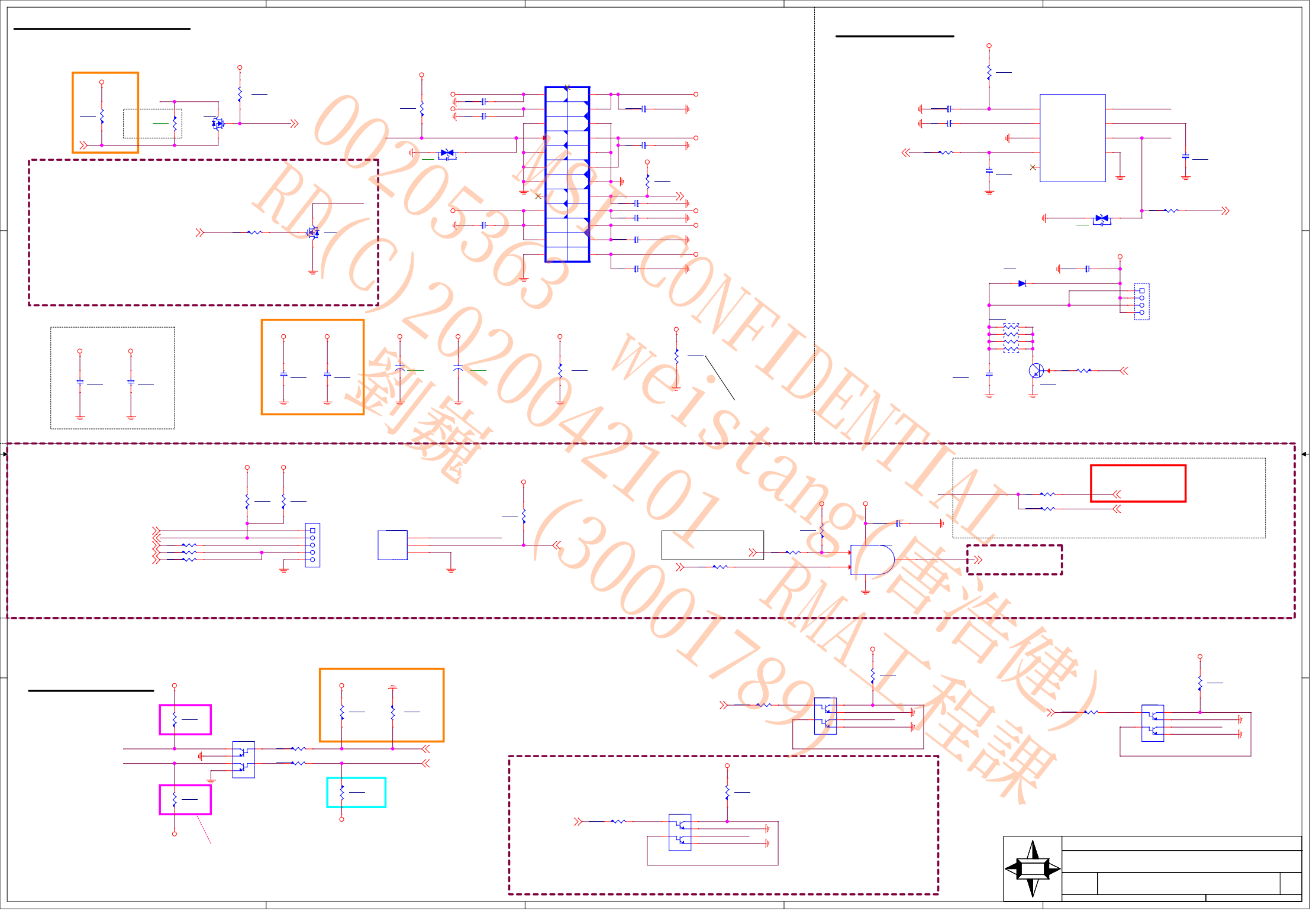
劉魏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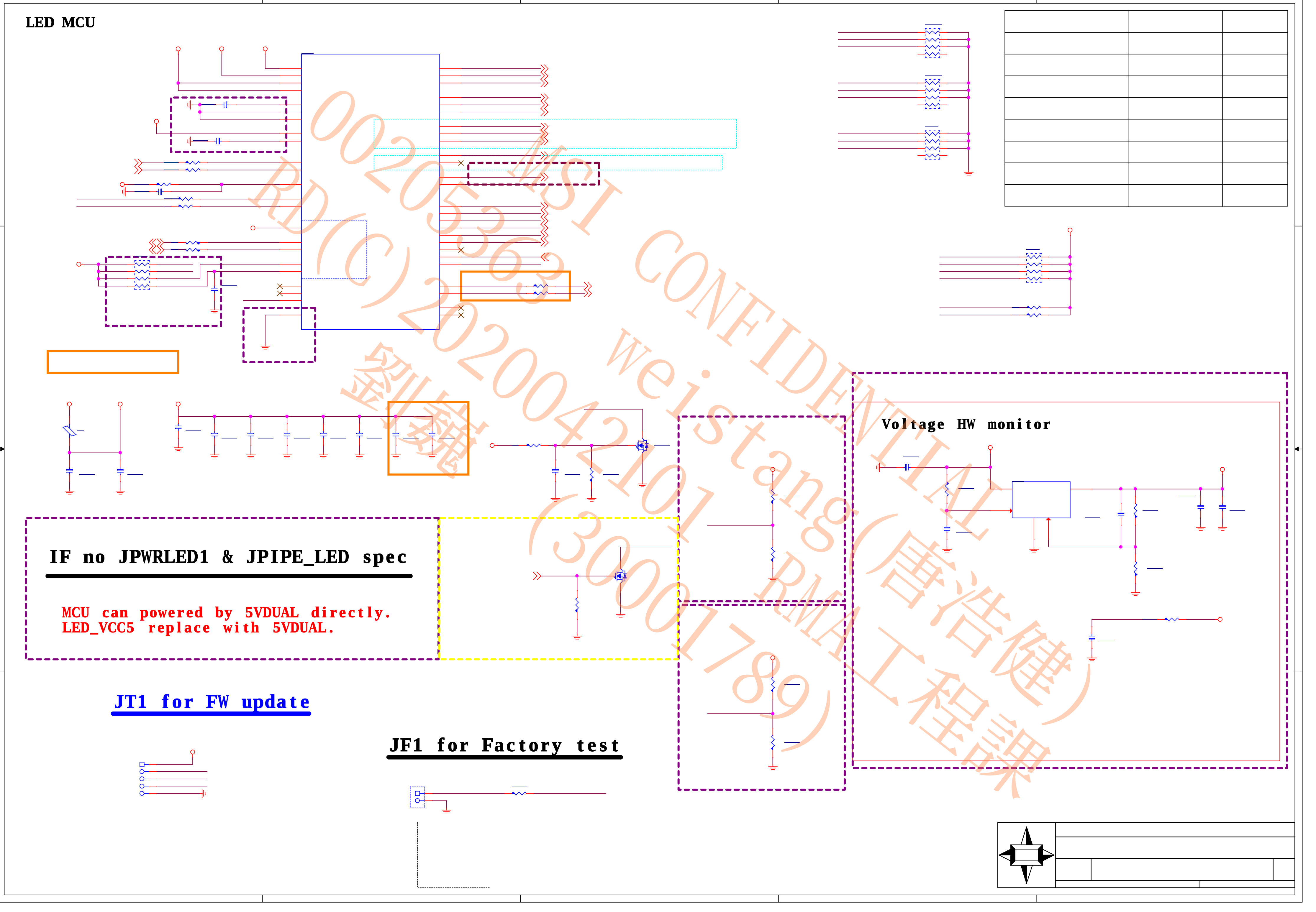
LED MCU
IF no JPWRLED1 & JPIPE_LED spec
MCU can powered by 5VDUAL directly.
LED_VCC5 replace with 5VDUAL.
Voltage HW monitor
JT1 for FW update
JF1 for Factory test
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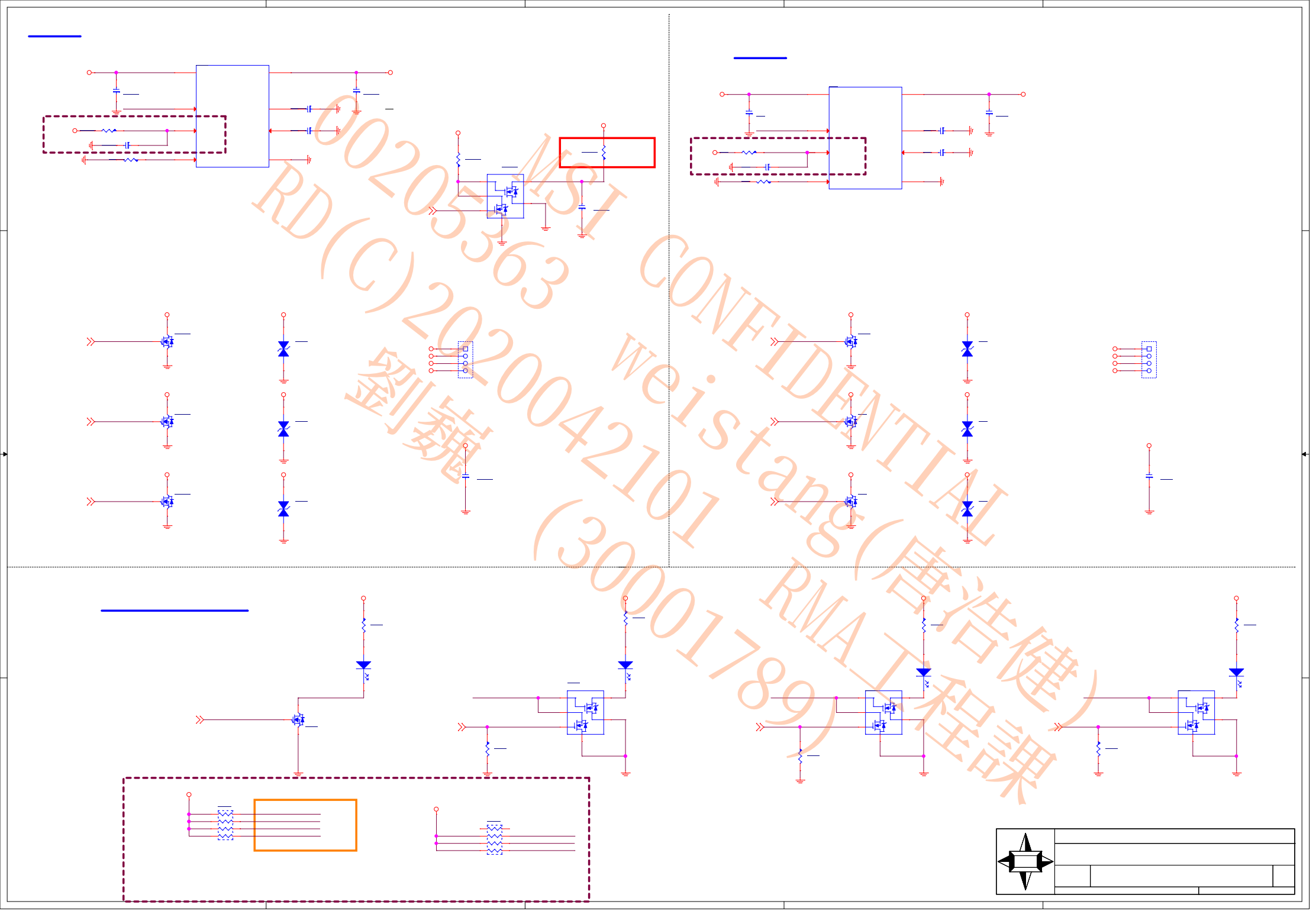
00205363 RD(C)2020042101 weistang(唐浩倢)
劉曉(30001789) RMA工程課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