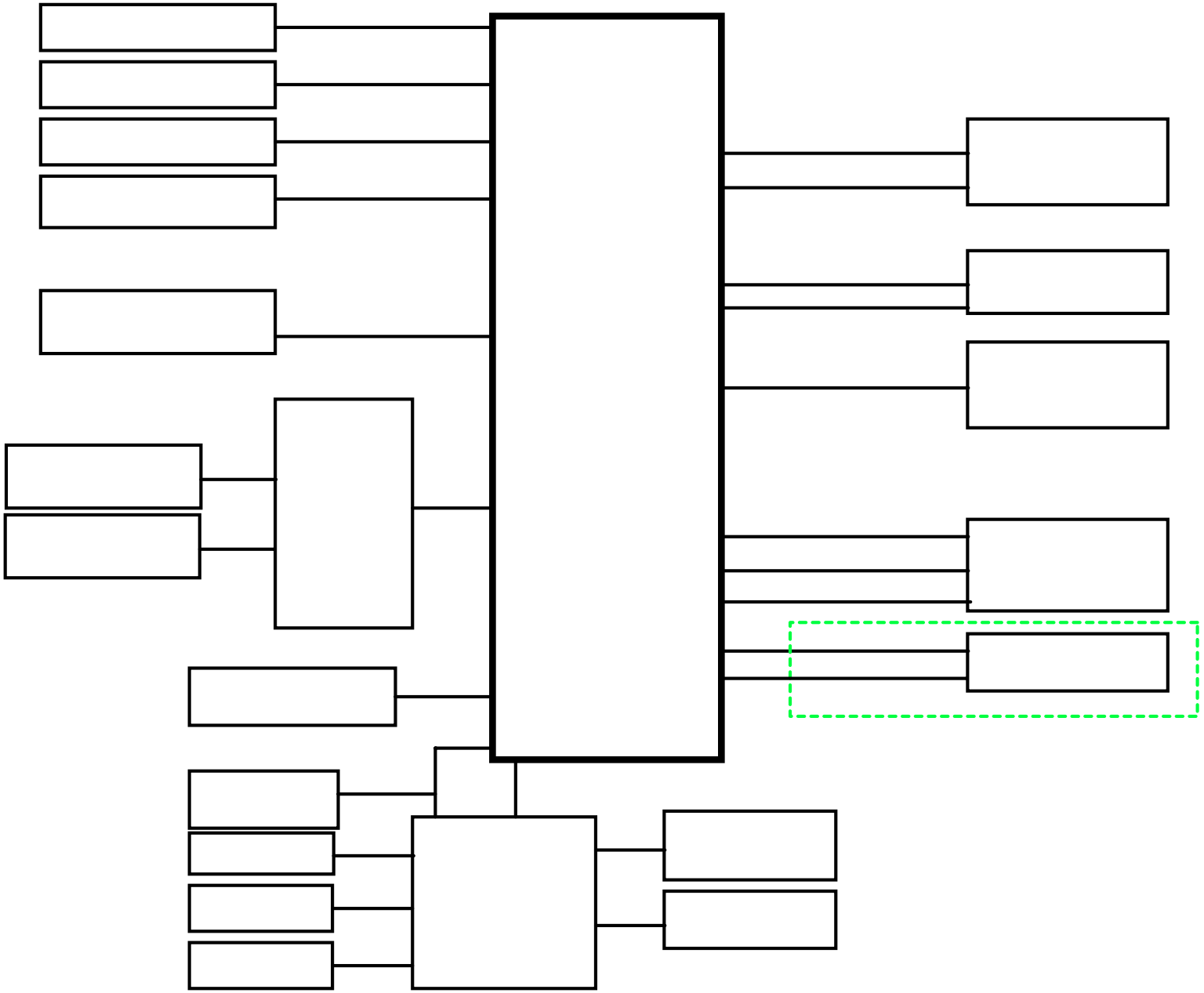

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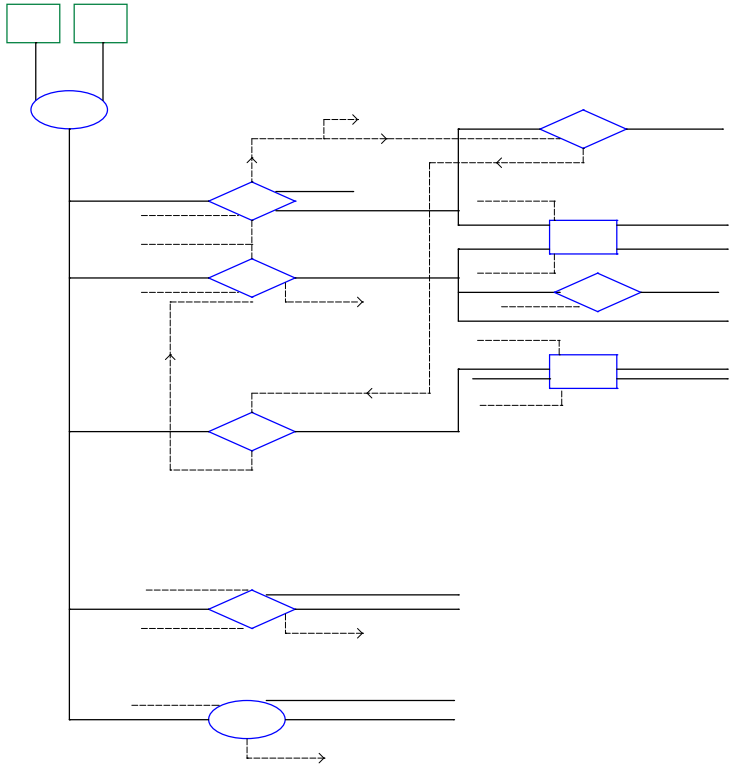

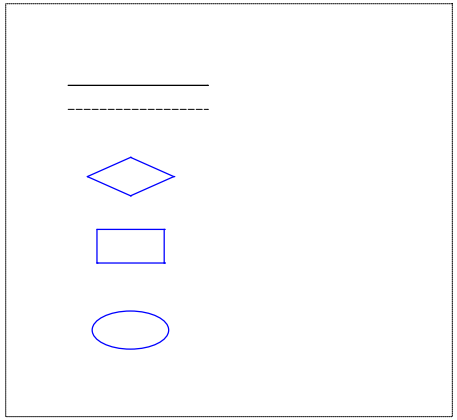

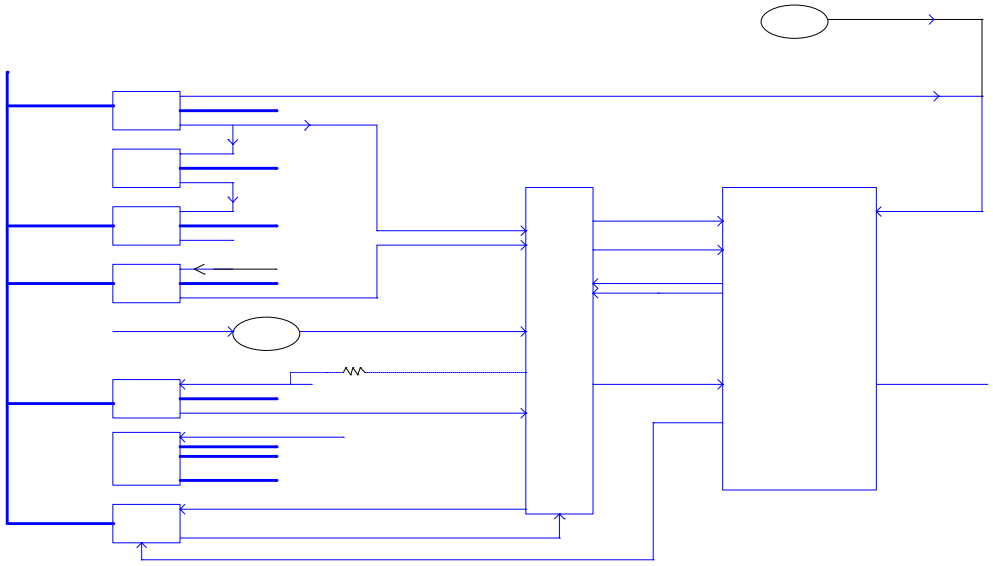

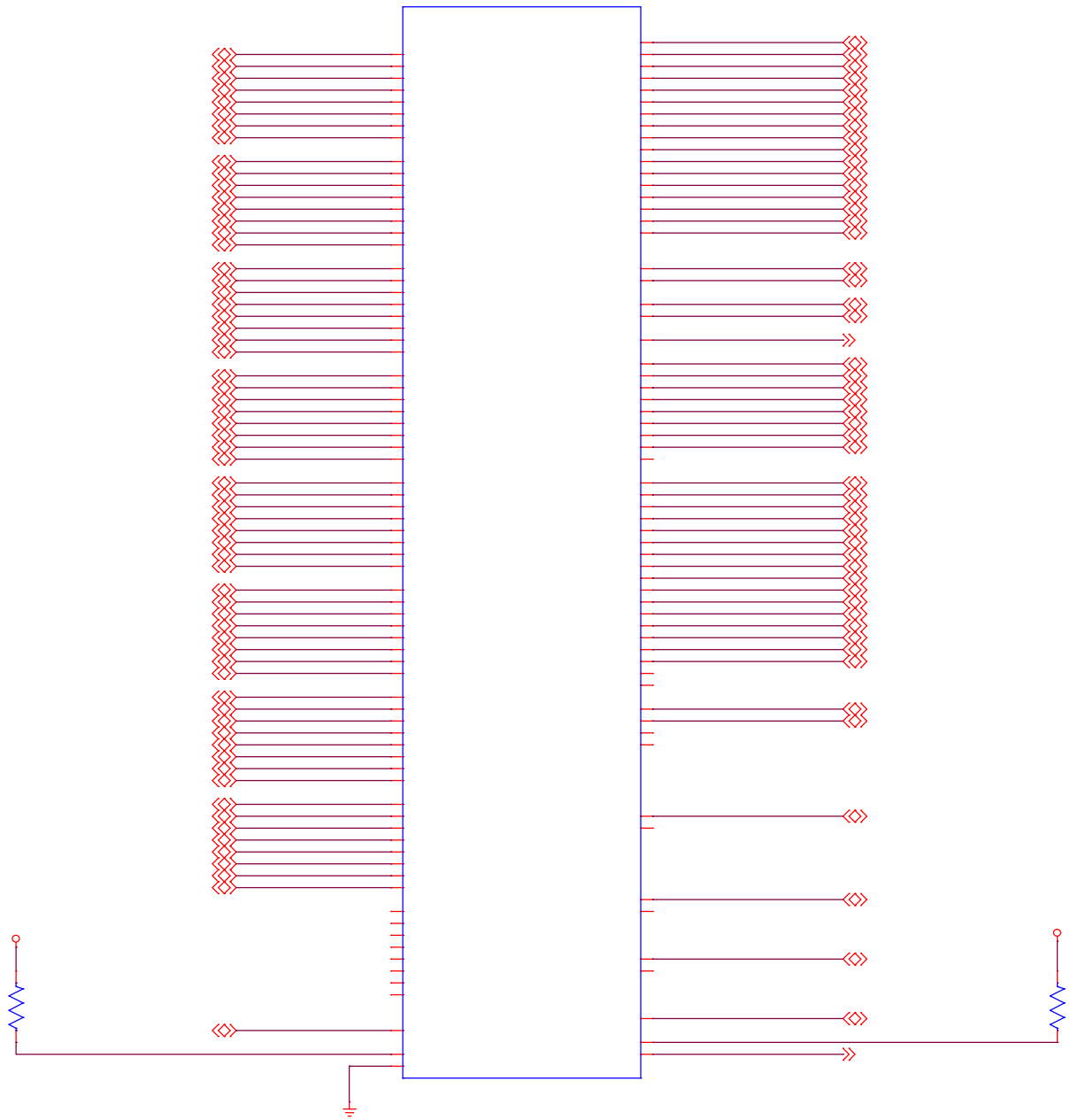

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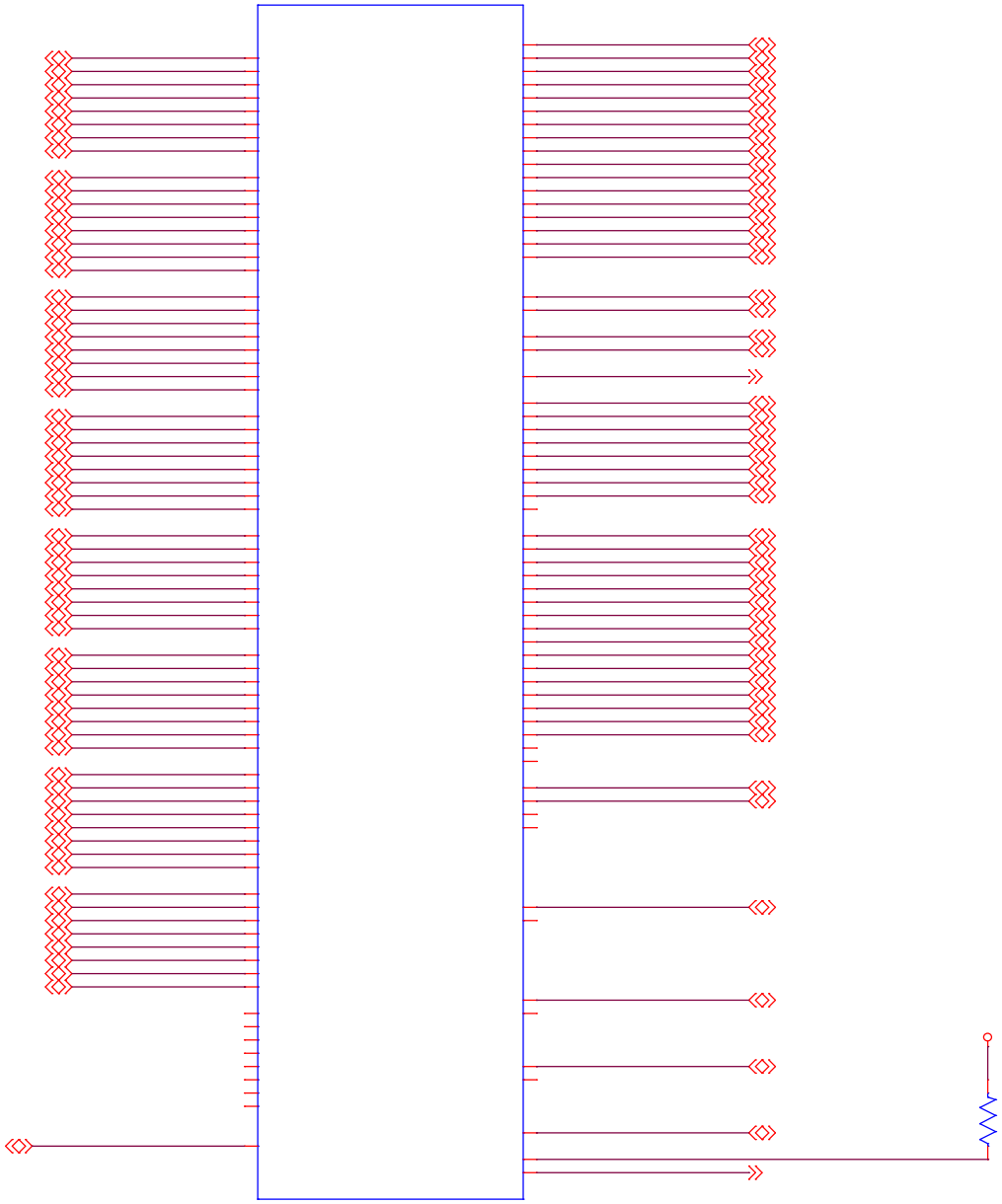

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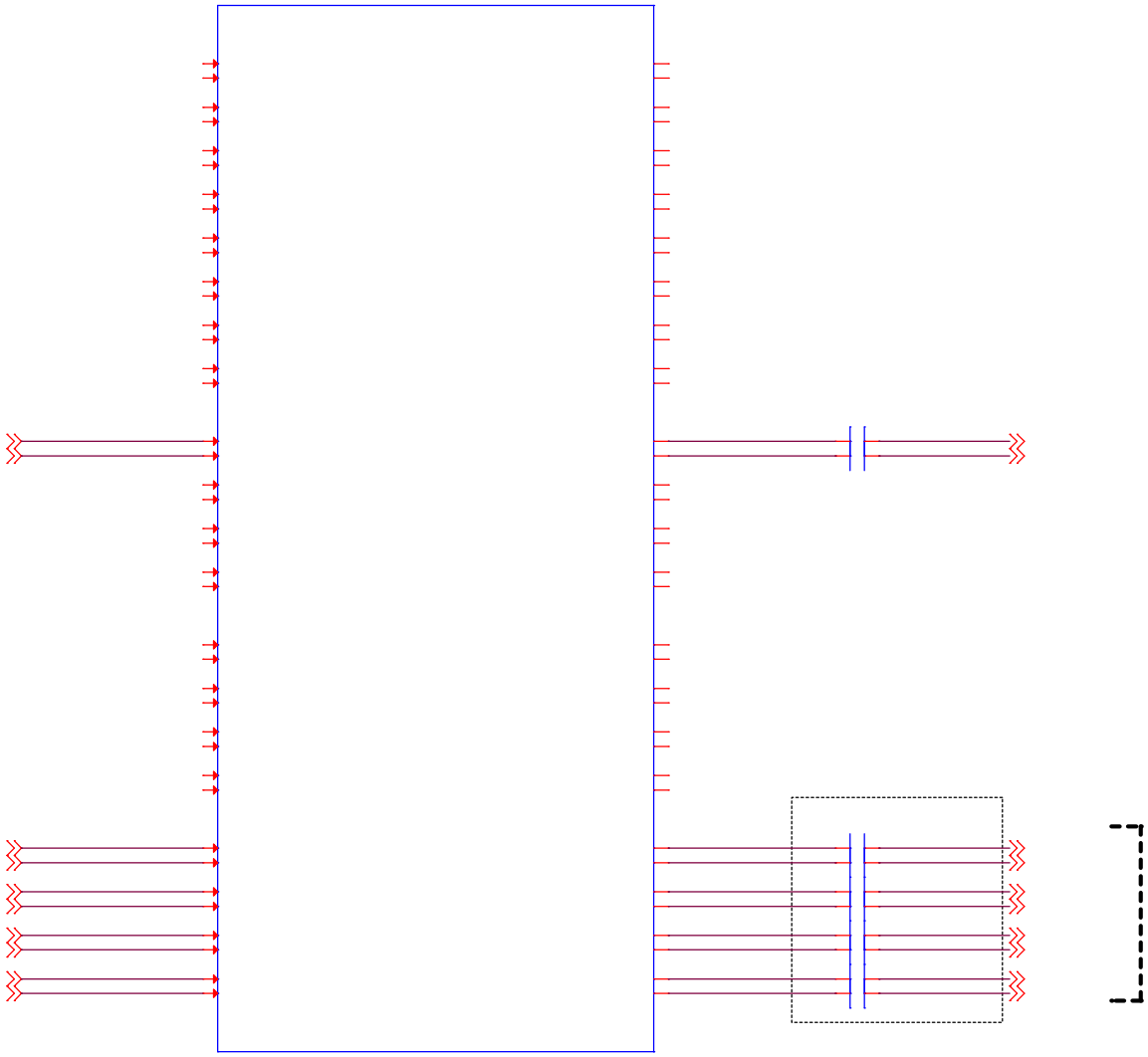

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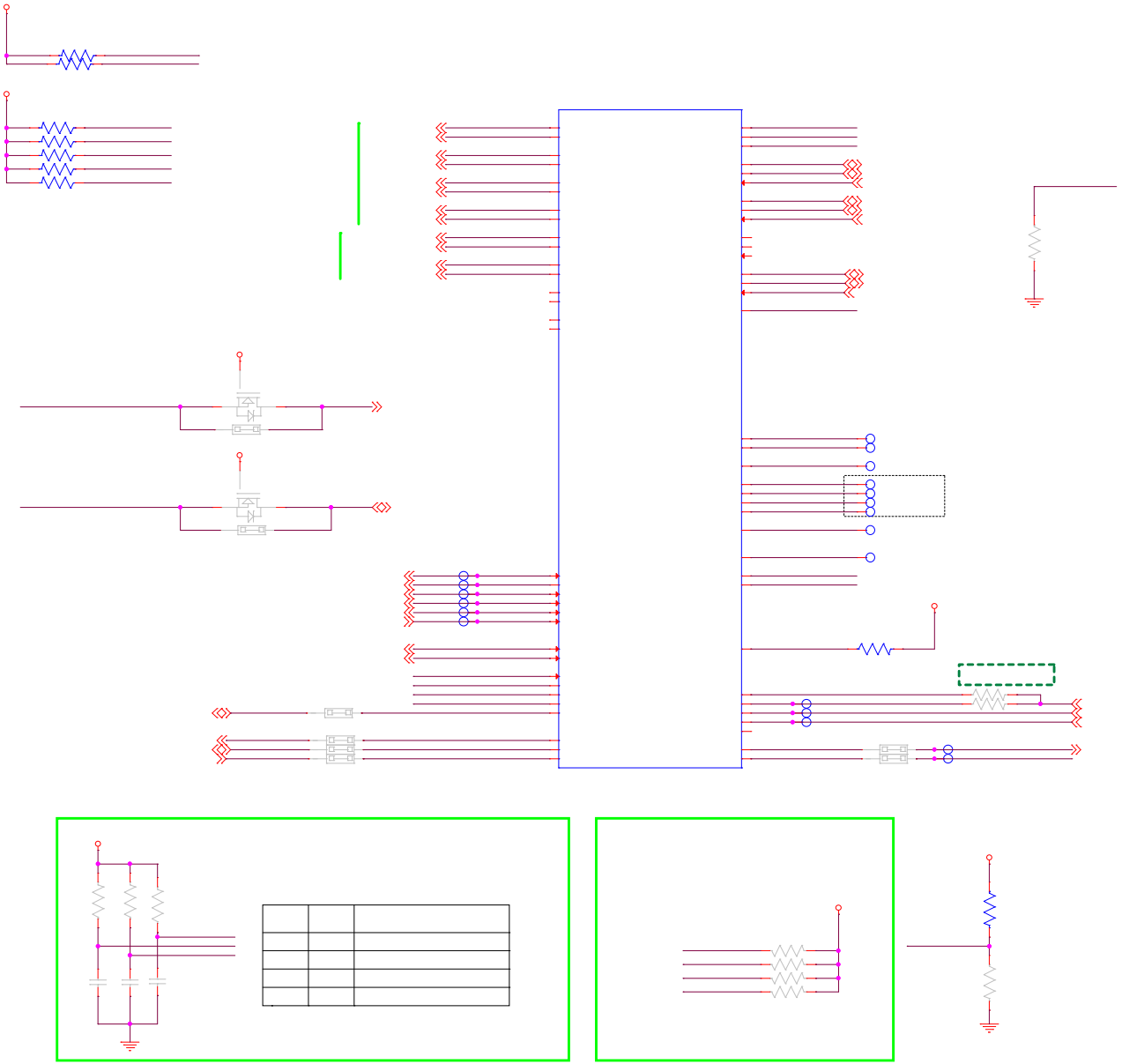

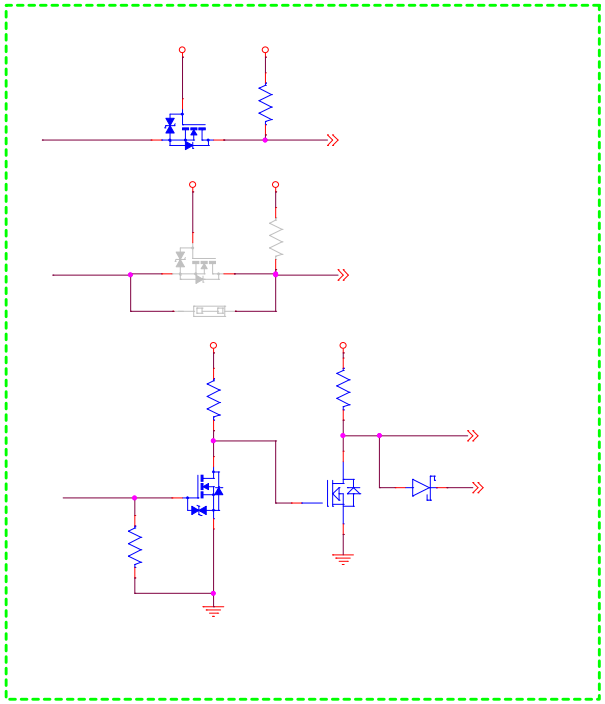

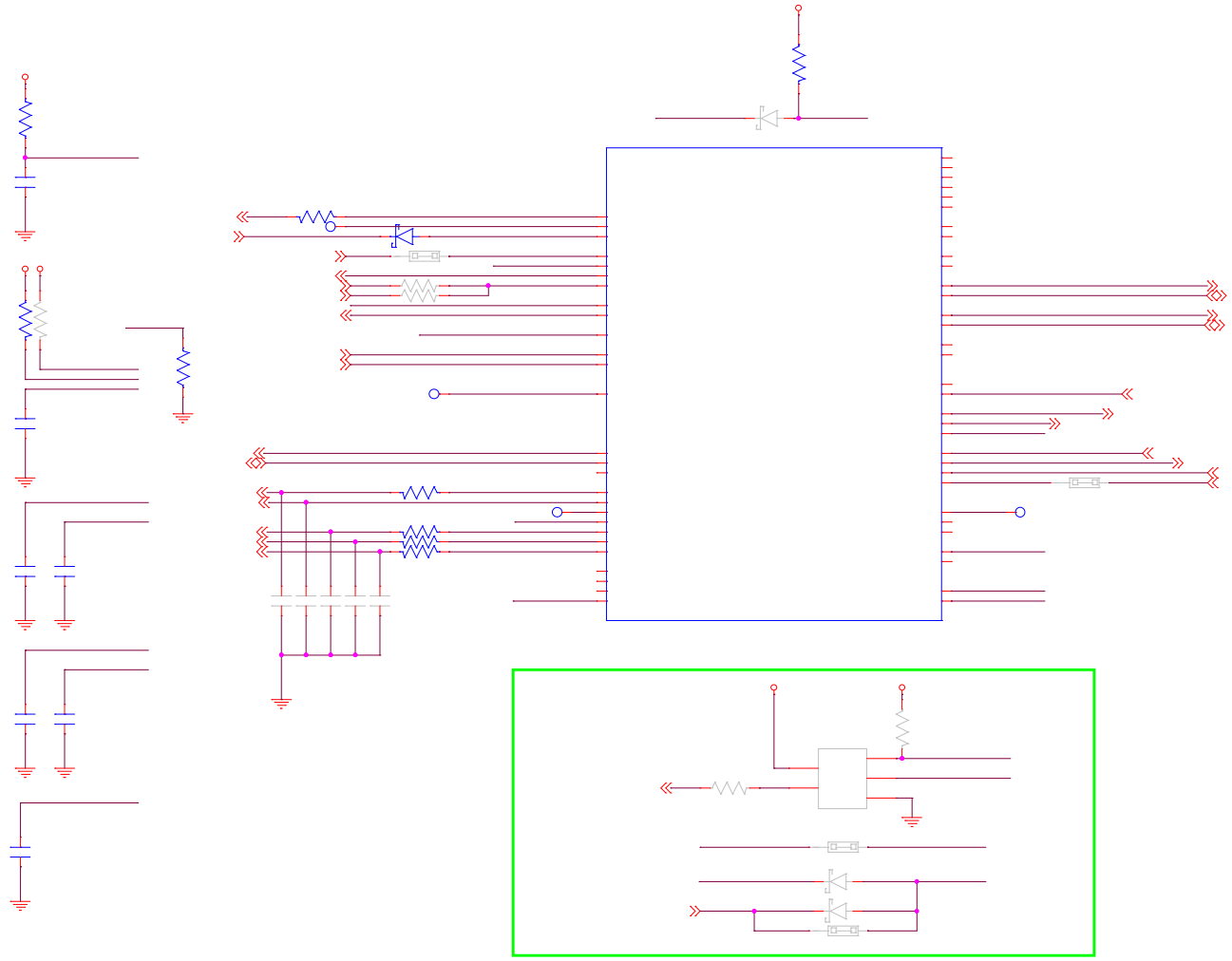

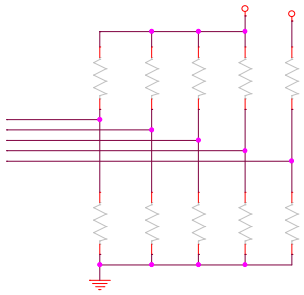

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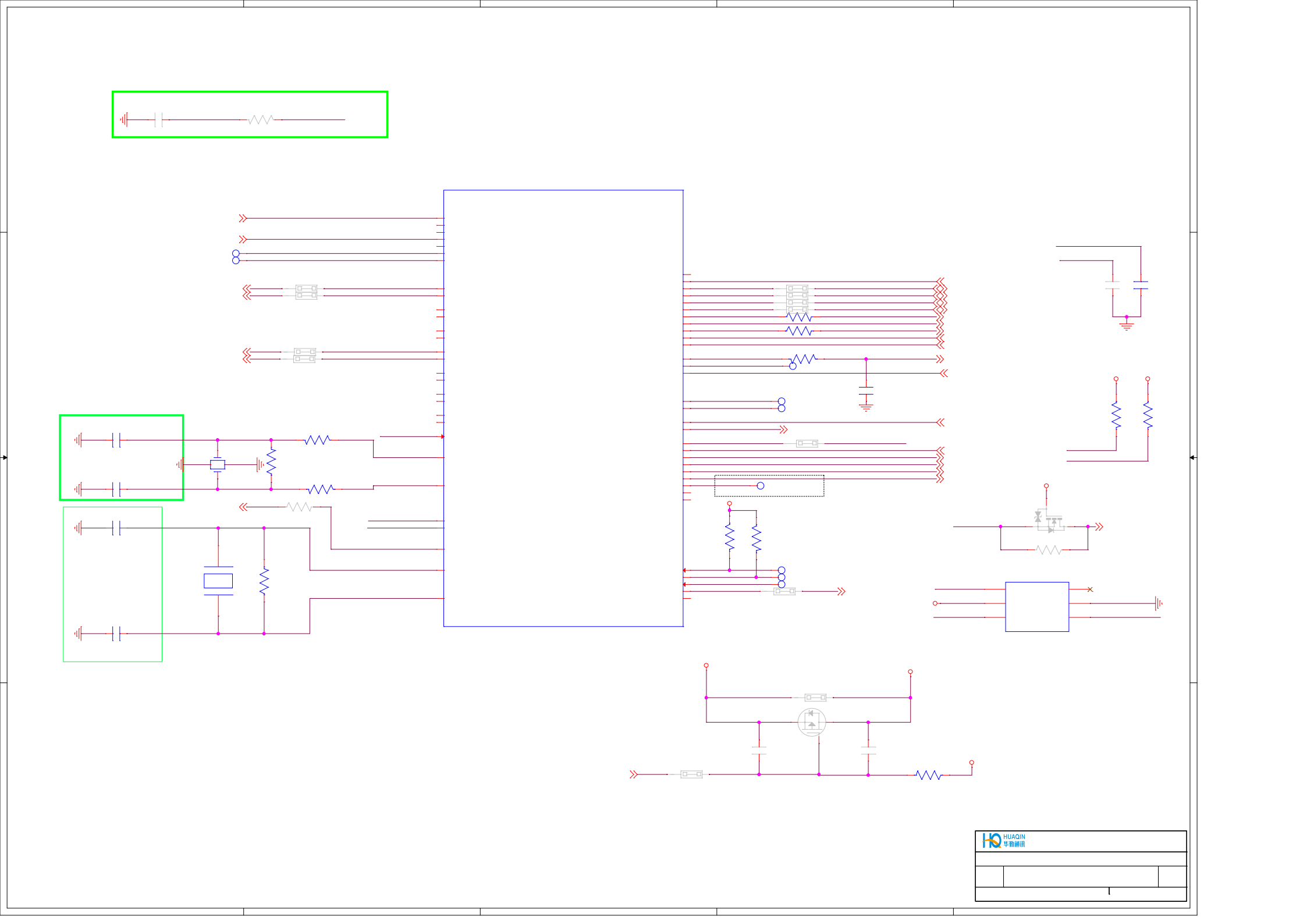
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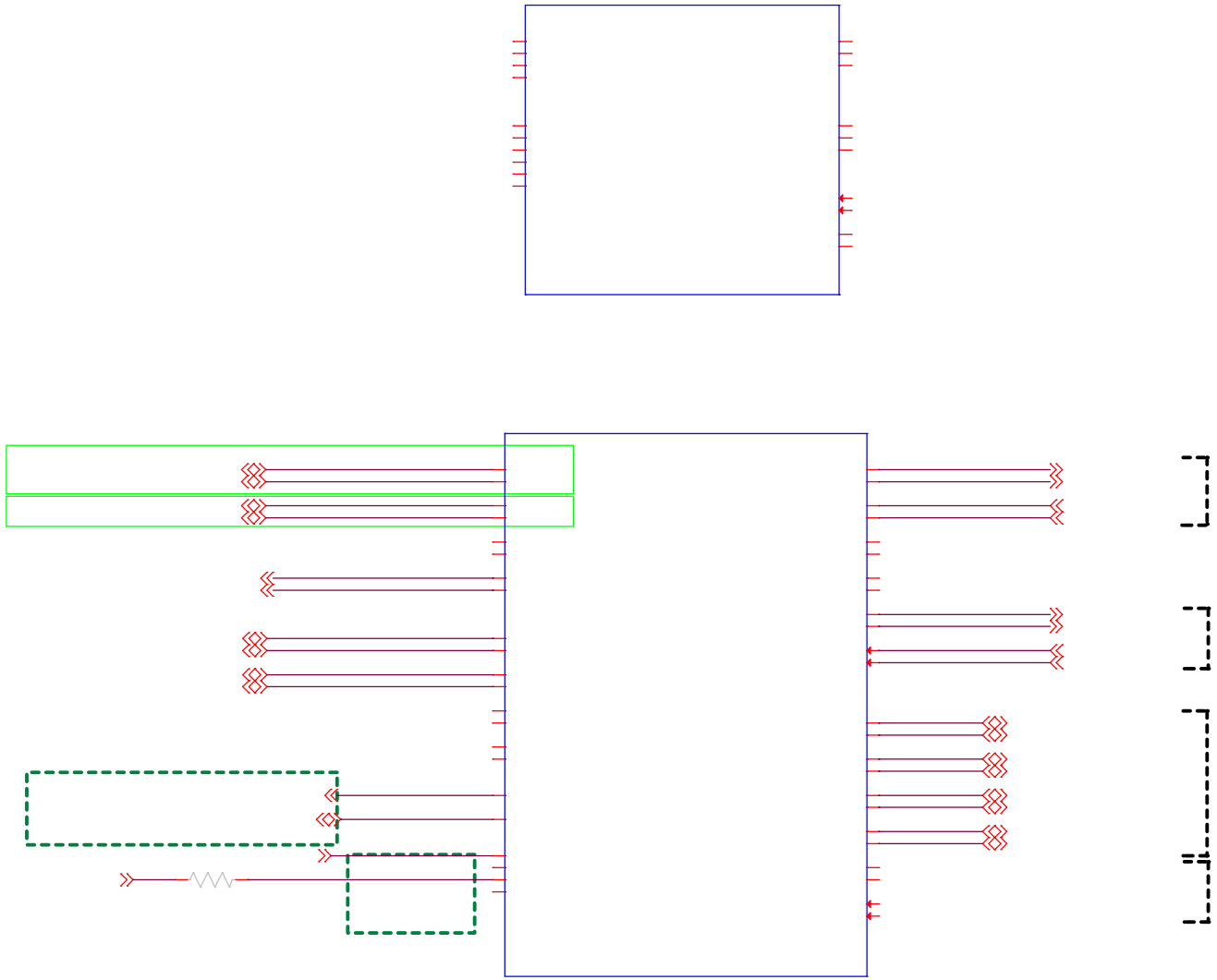

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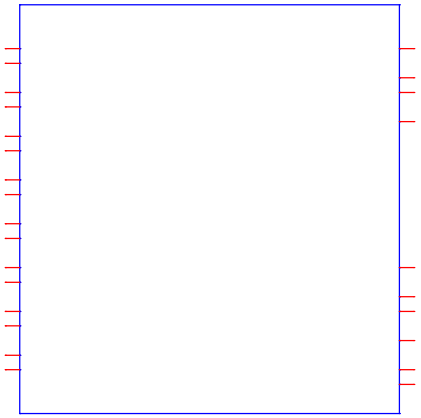

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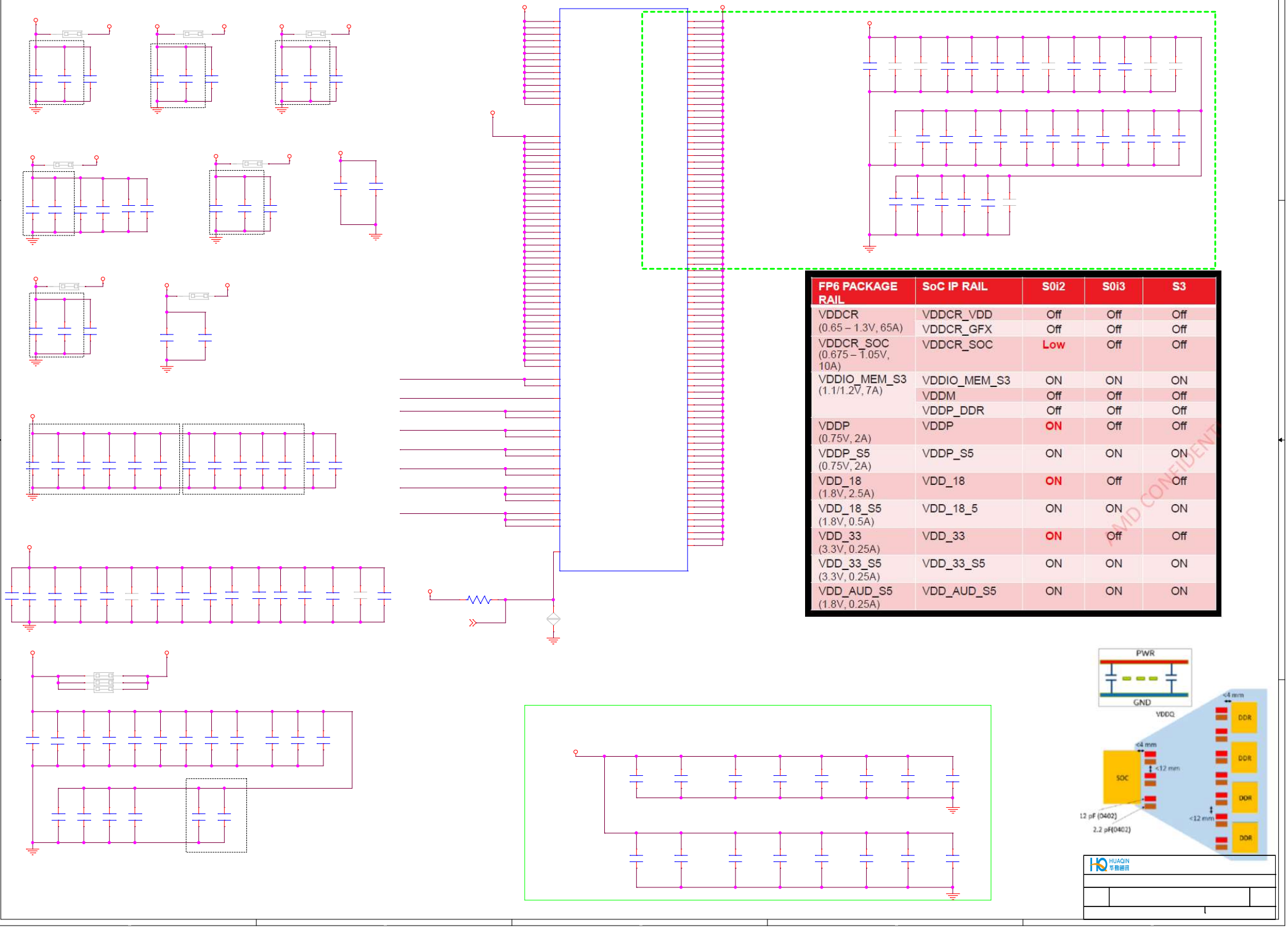

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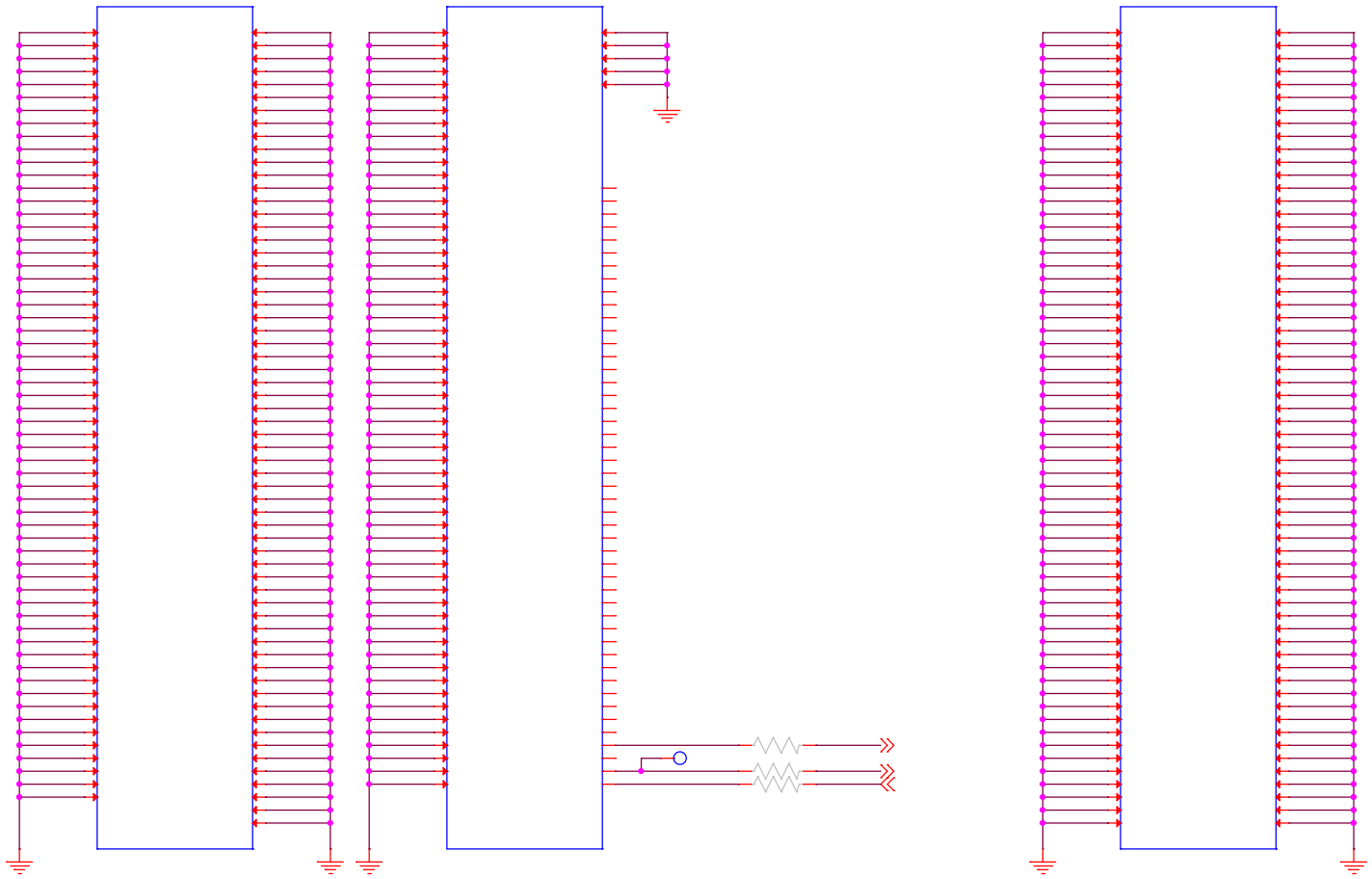
	VDDCR_GFX	Off	Off	Off
VDDCR_SOC (0.675 – 1.05V, 10A)	VDDCR_SOC	Low	Off	Off
VDDIO_MEM_S3 (1.1/1.2V, 7A)	VDDIO_MEM_S3	ON	ON	ON
	VDDM	Off	Off	Off
	VDDP_DDR	Off	Off	Off
VDDP (0.75V, 2A)	VDDP	ON	Off	Off
VDDP_S5 (0.75V, 2A)	VDDP_S5	ON	ON	ON
VDD_18 (1.8V, 2.5A)	VDD_18	ON	Off	Off
VDD_18_S5 (1.8V, 0.5A)	VDD_18_5	ON	ON	ON
VDD_33 (3.3V, 0.25A)	VDD_33	ON	Off	Off
VDD_33_S5 (3.3V, 0.25A)	VDD_33_S5	ON	ON	ON
VDD_AUD_S5 (1.8V, 0.25A)	VDD_AUD_S5	ON	ON	ON



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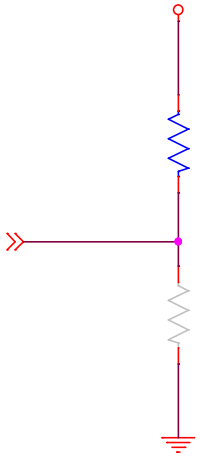

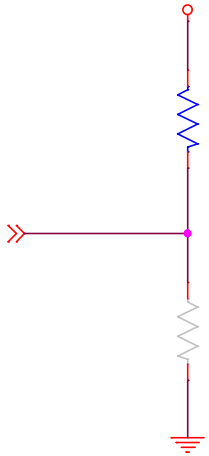

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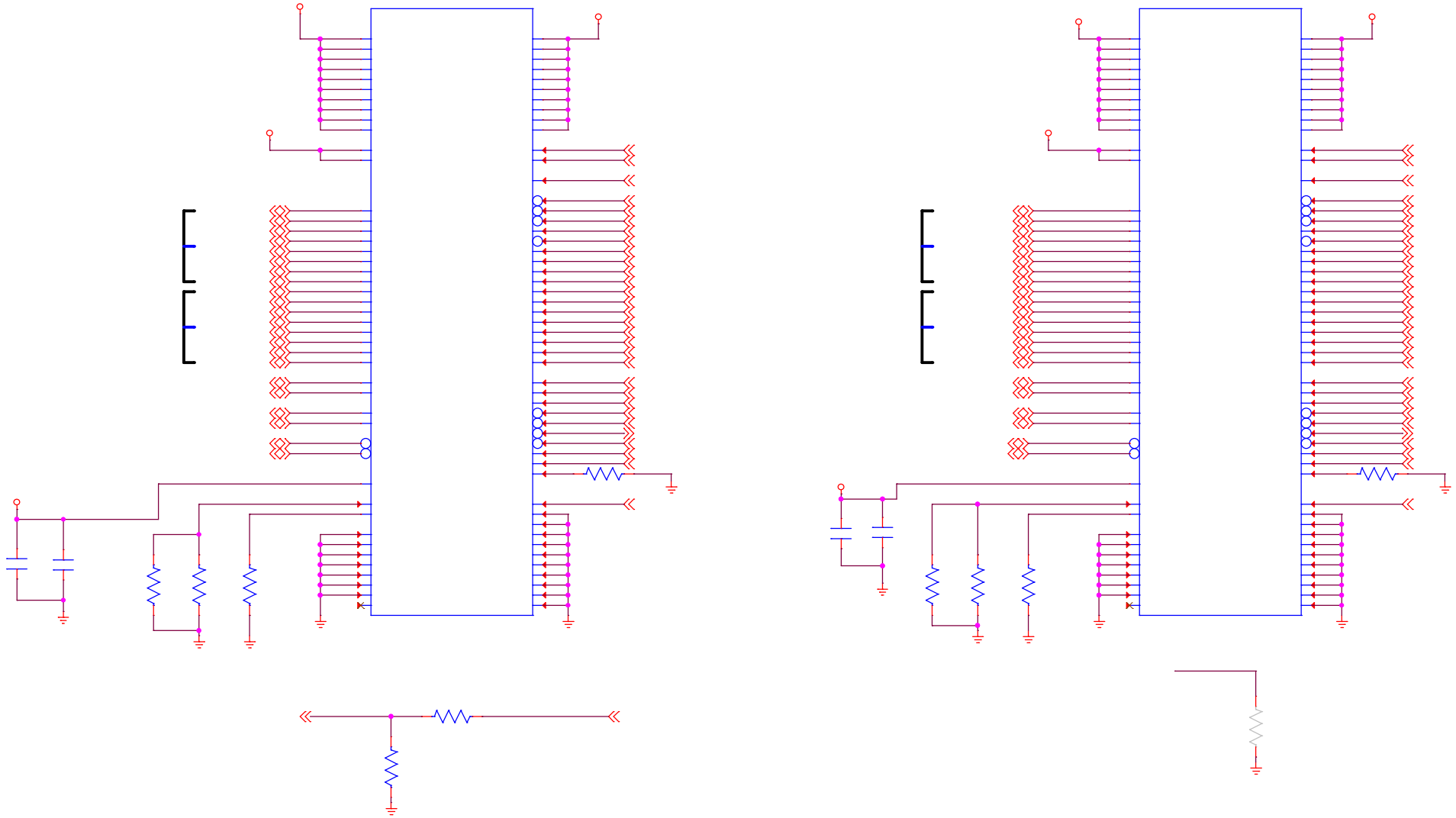

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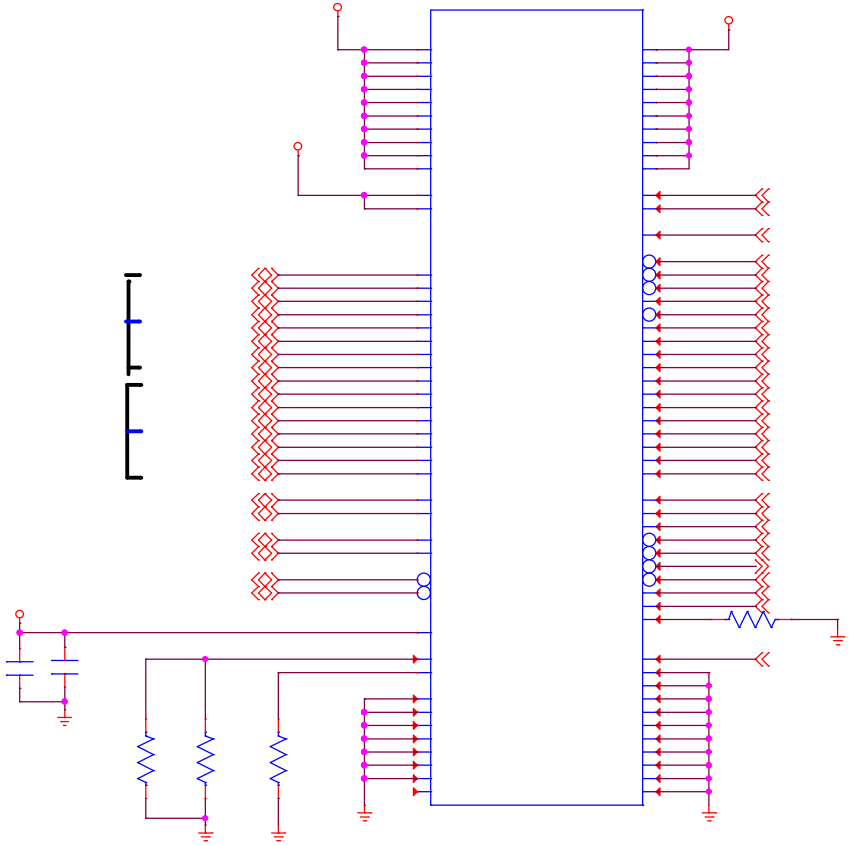

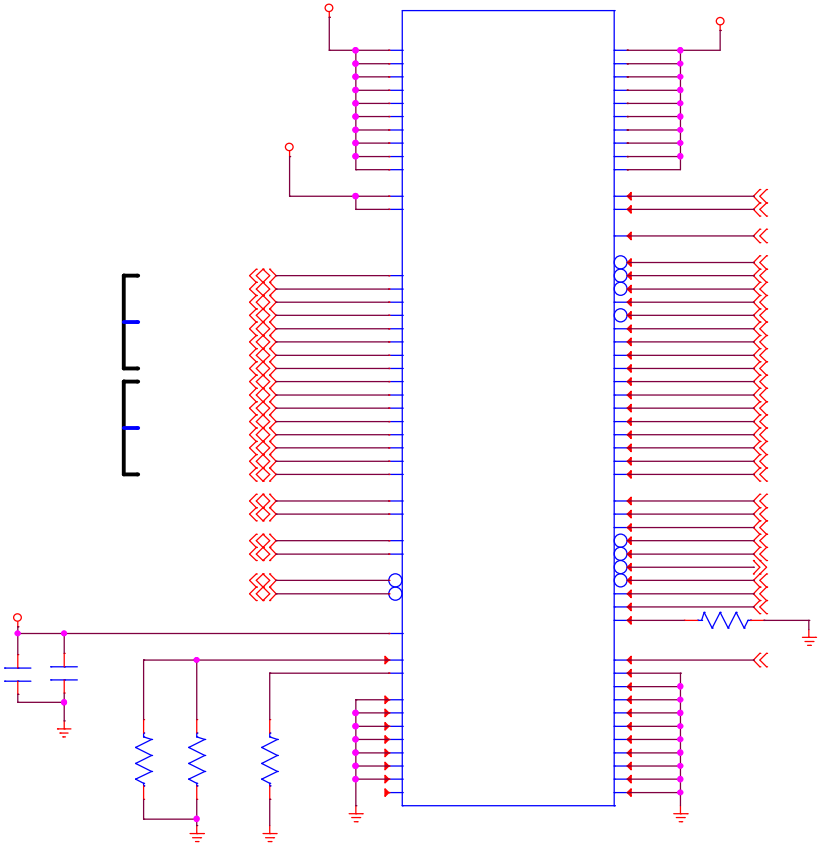

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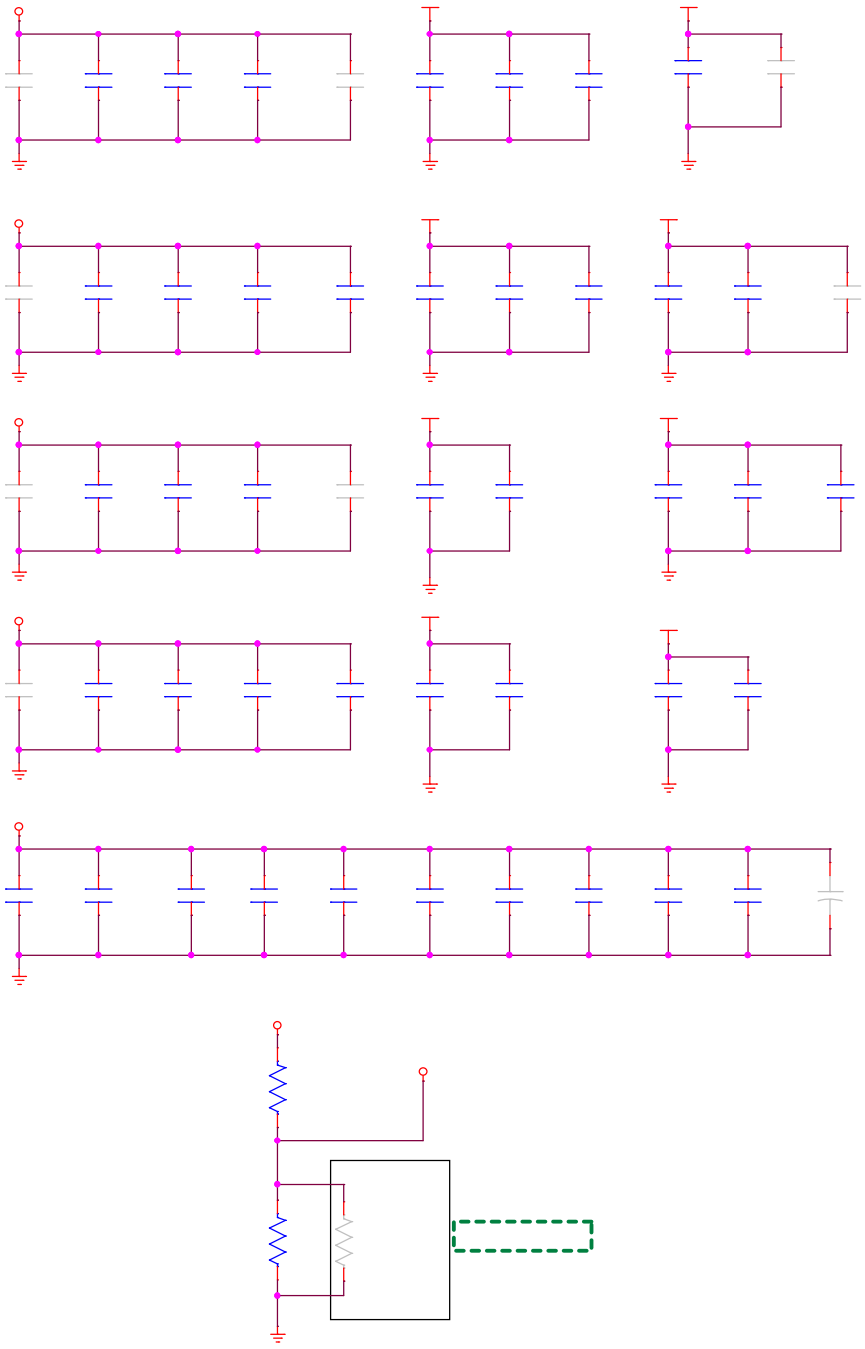

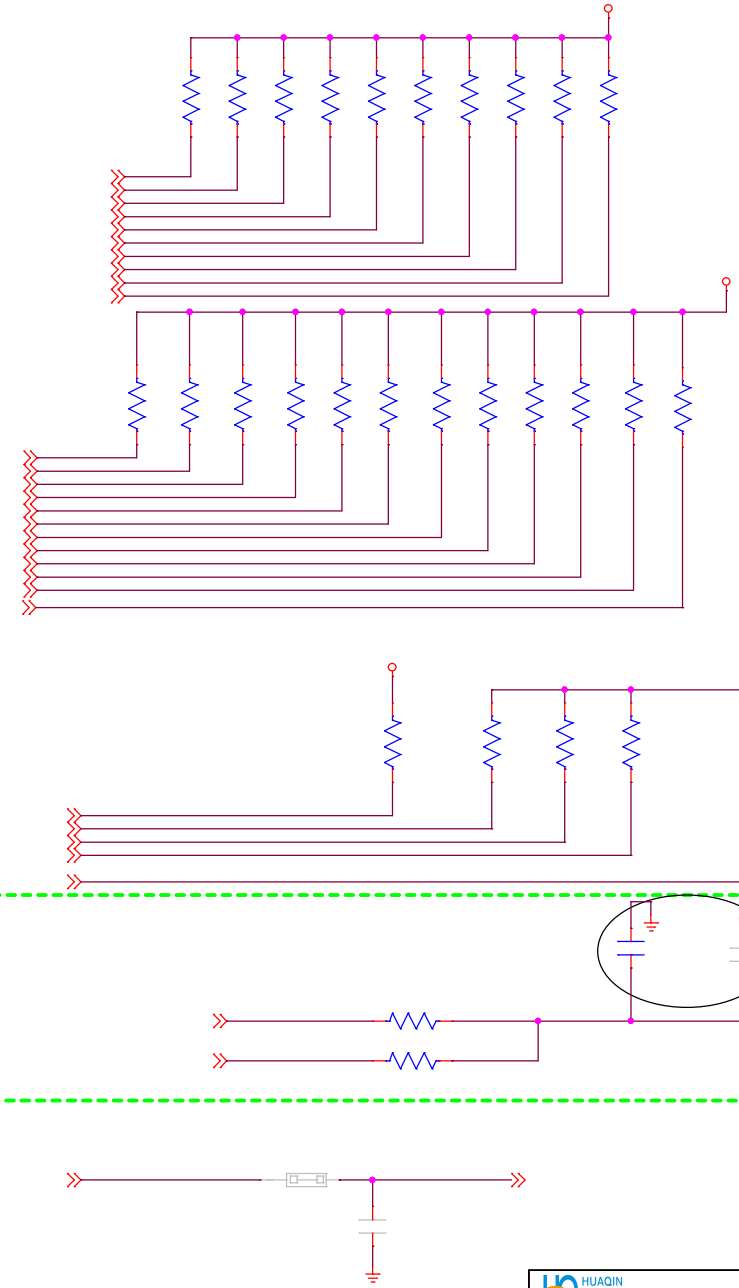

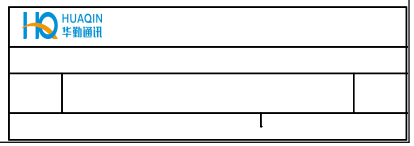
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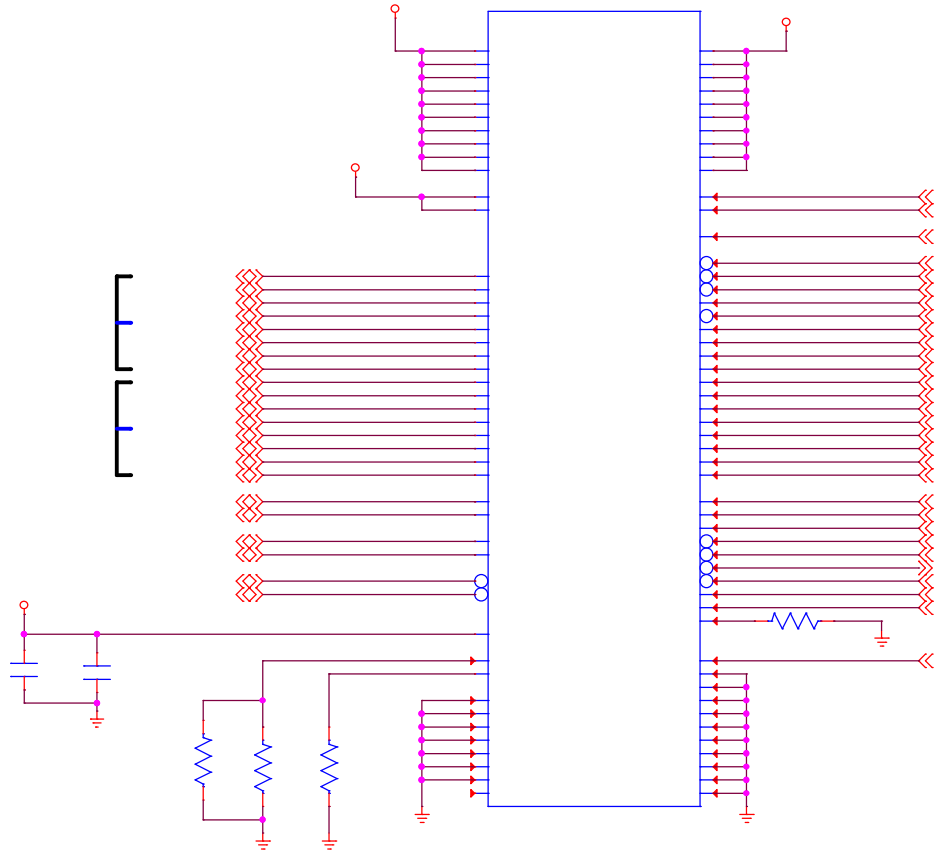

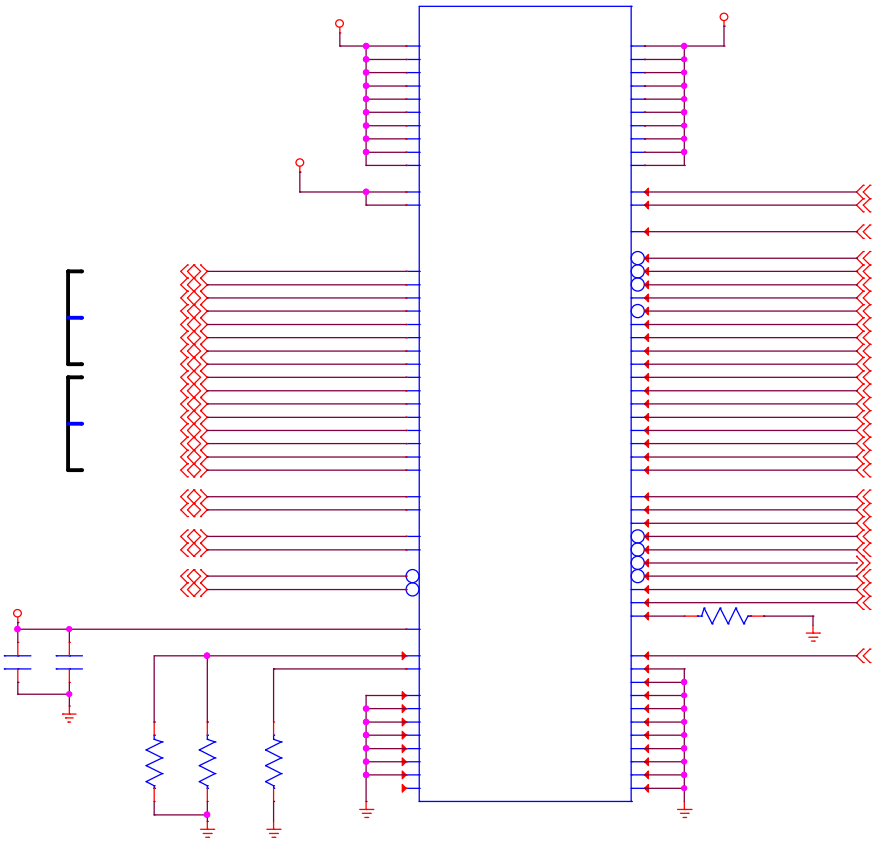

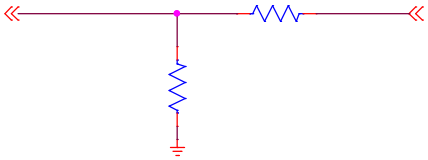

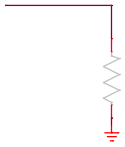

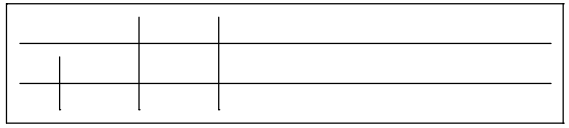

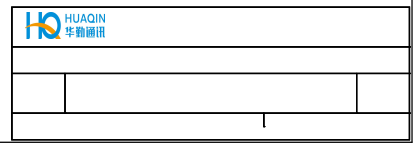
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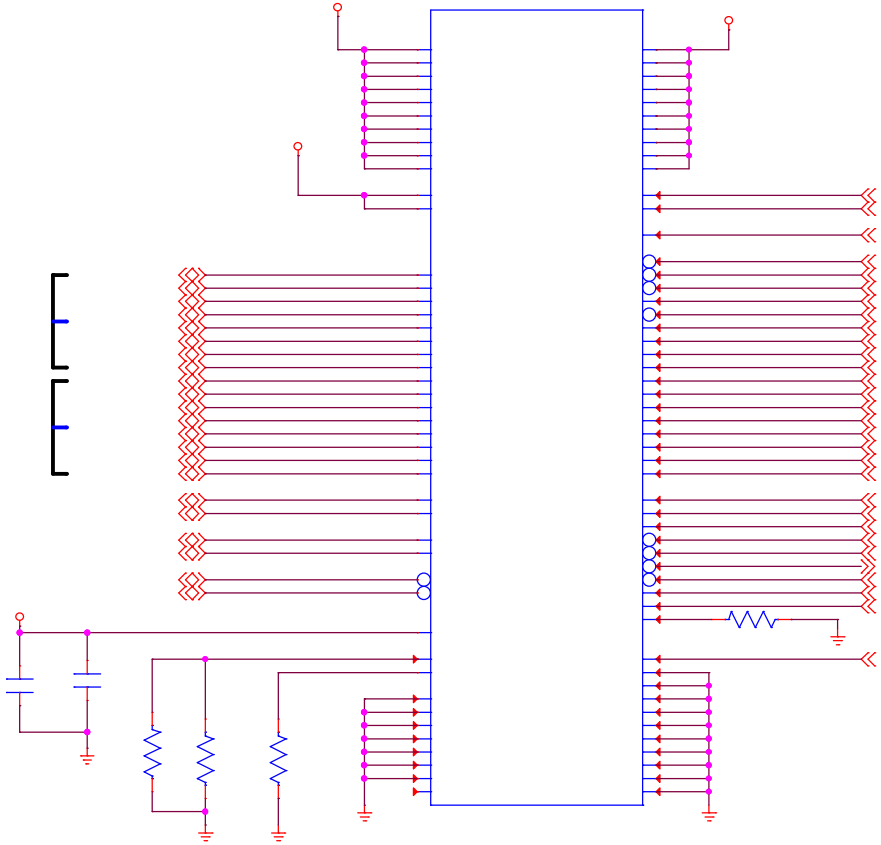

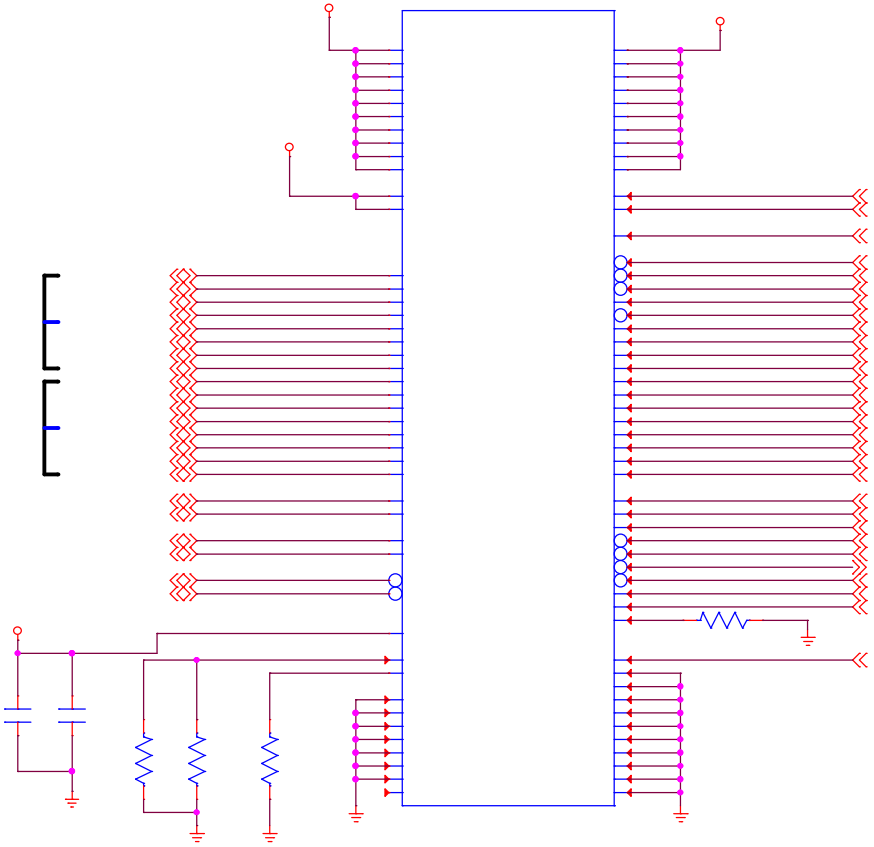

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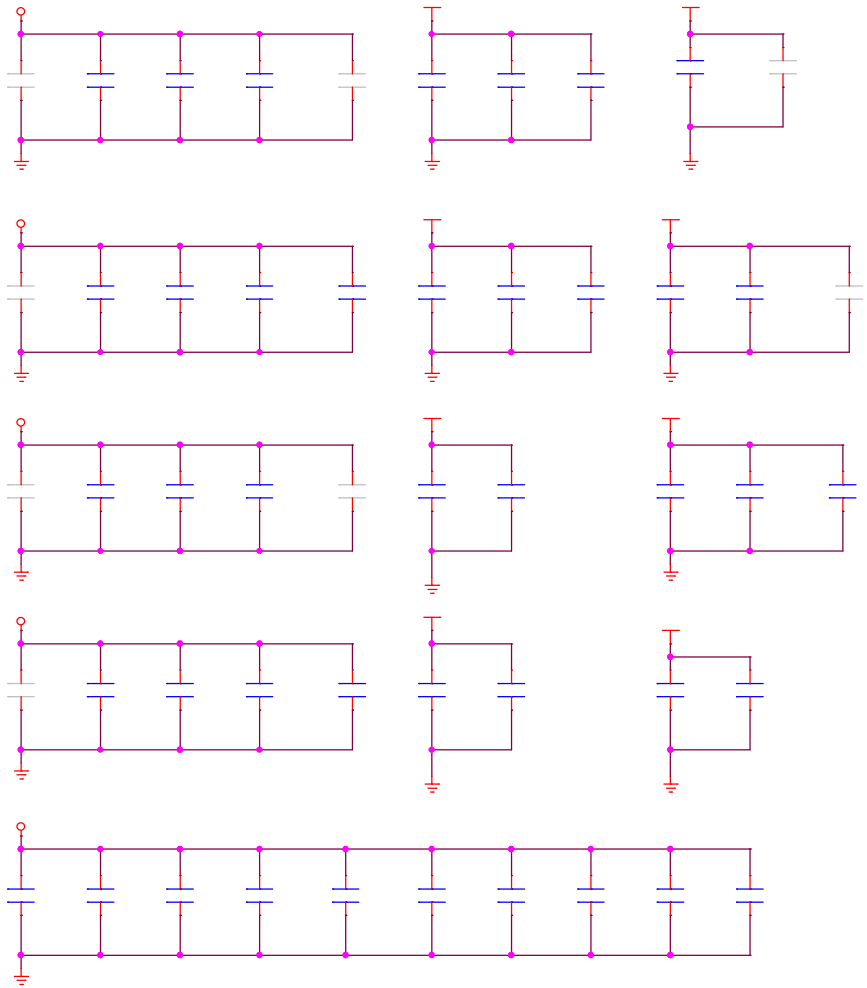

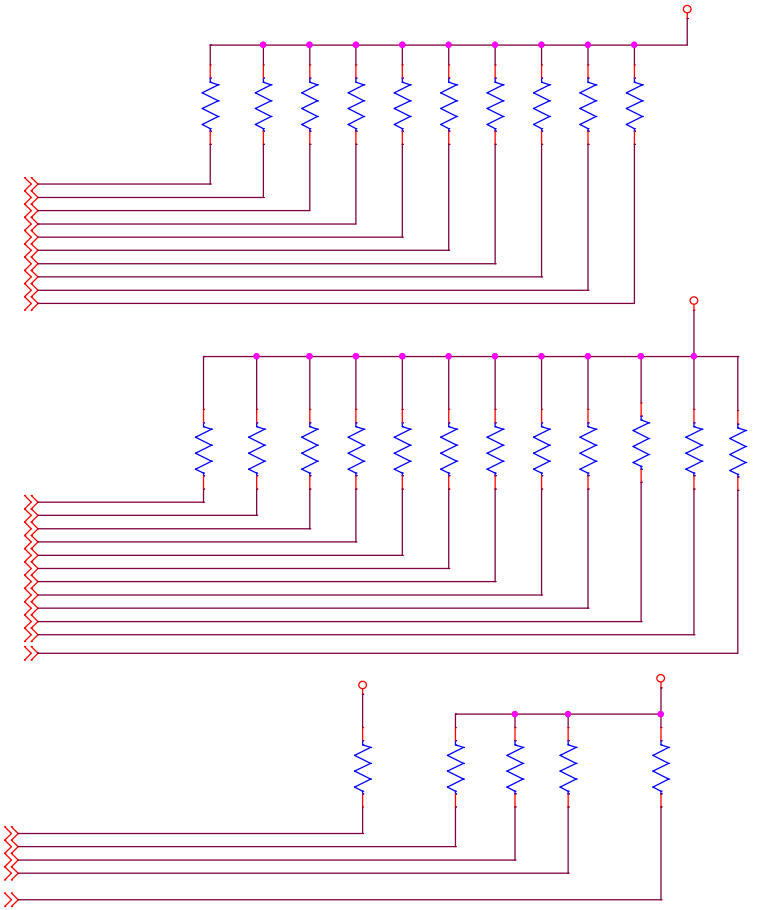

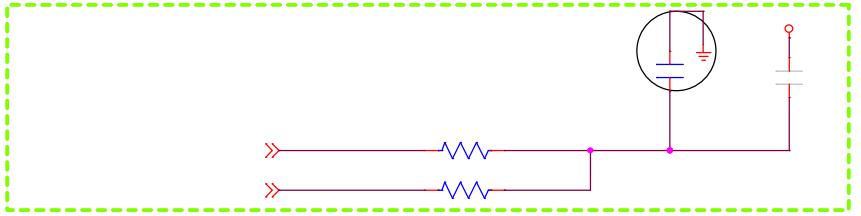

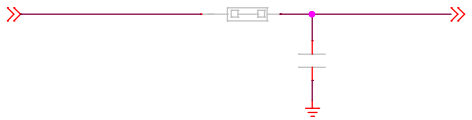

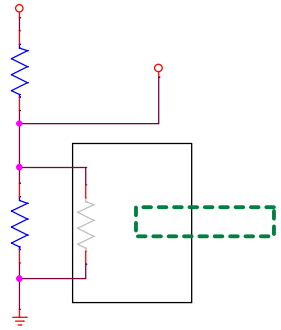

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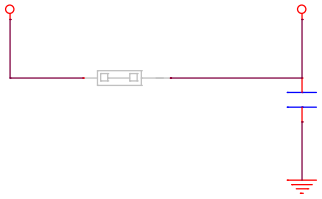

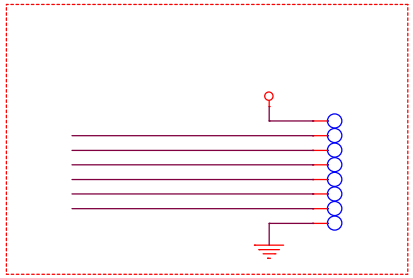

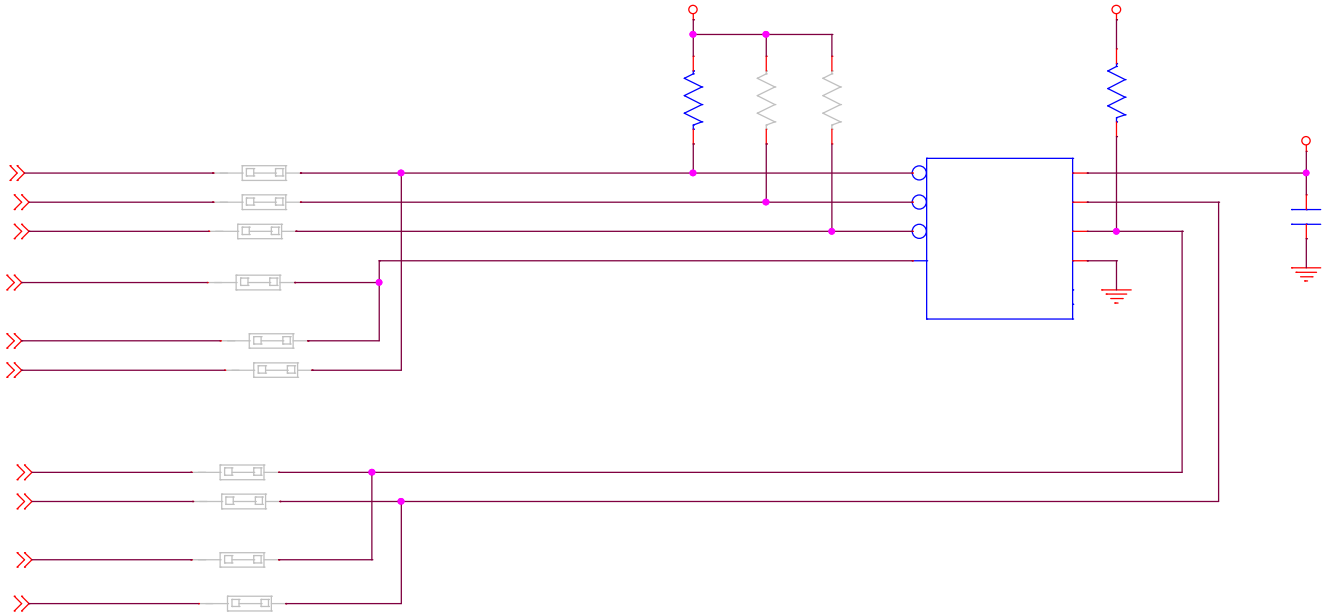

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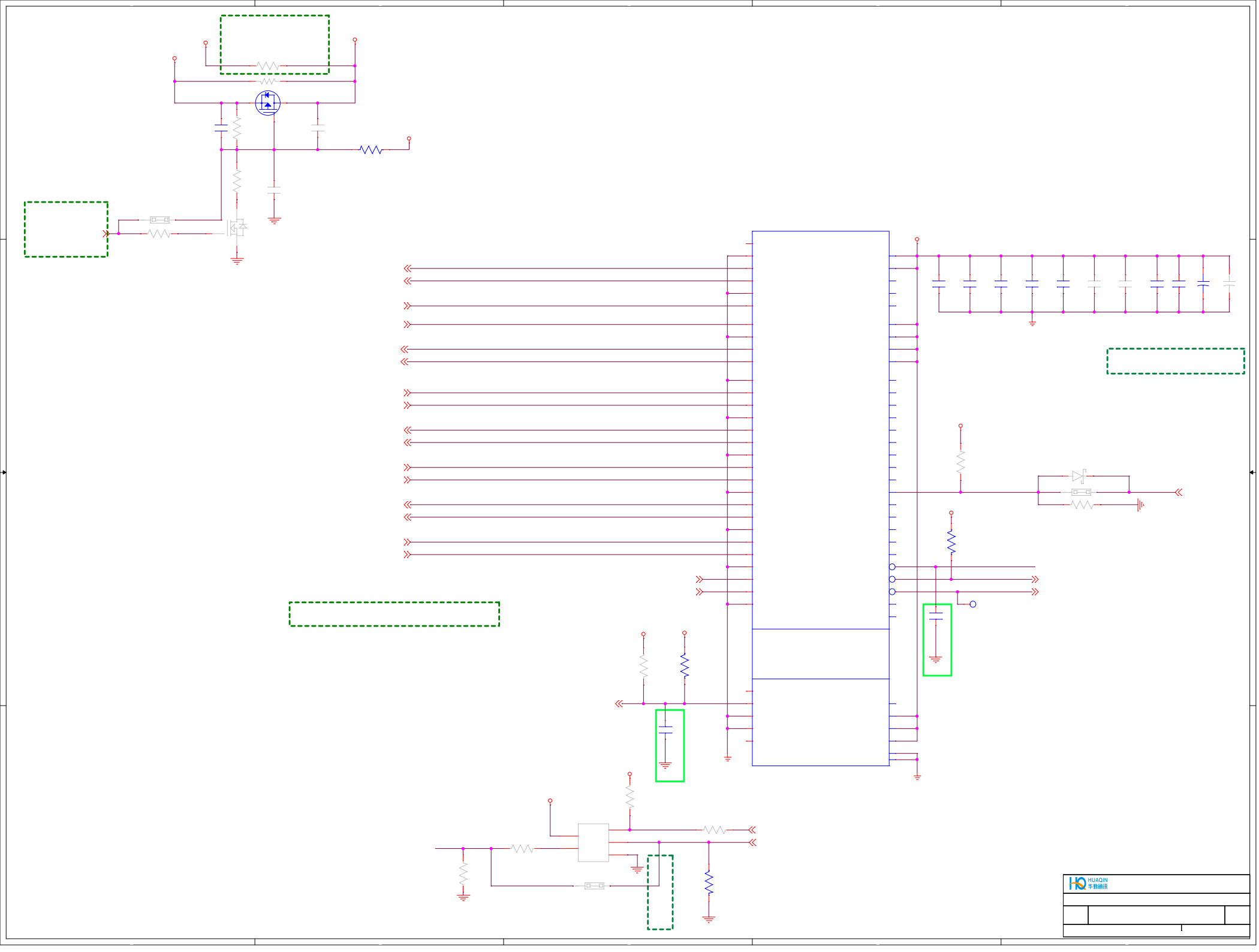

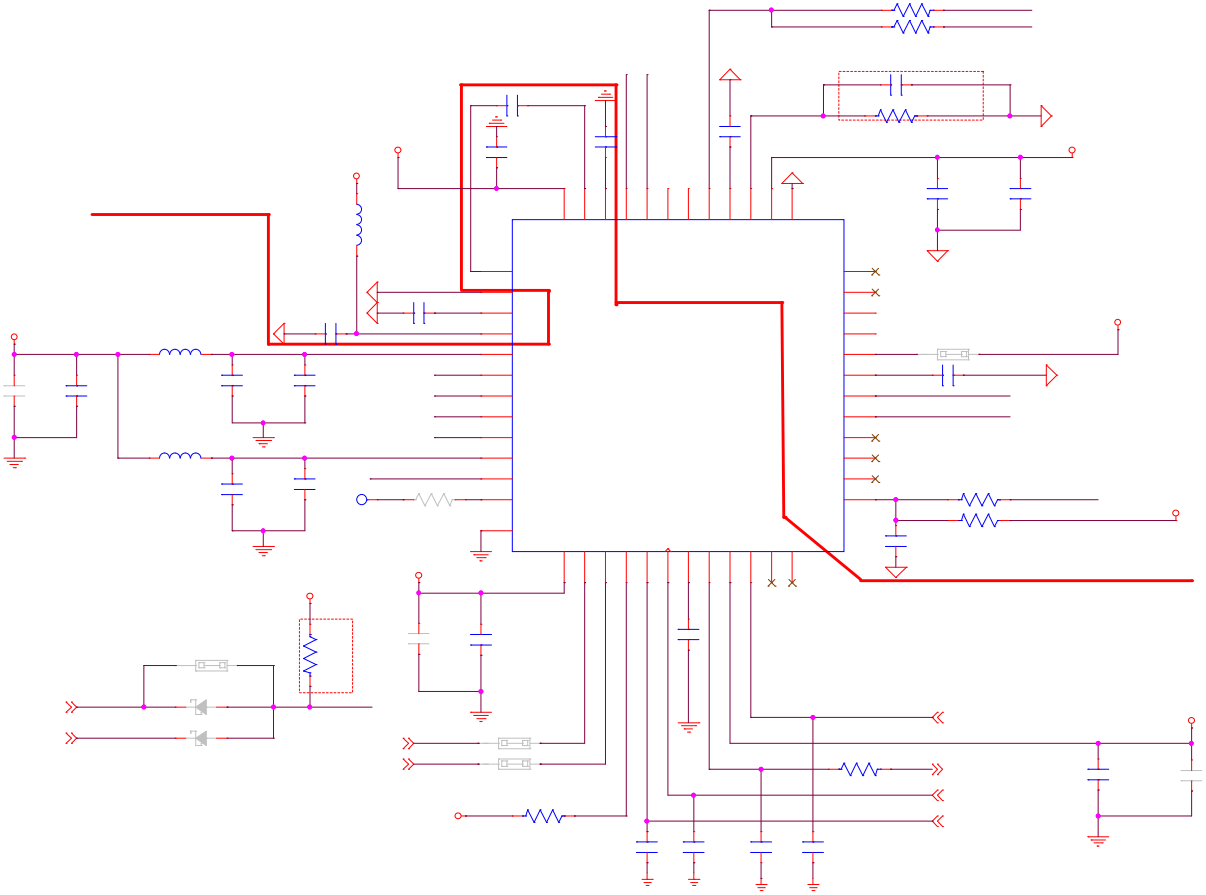

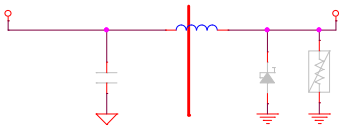

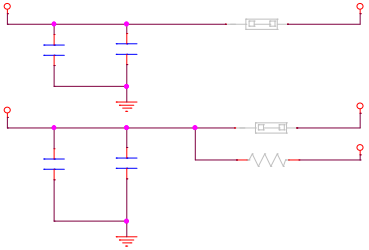

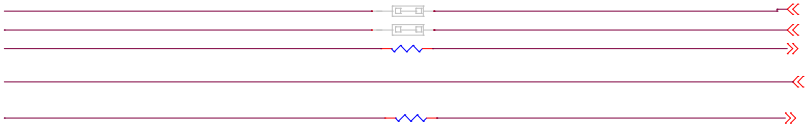

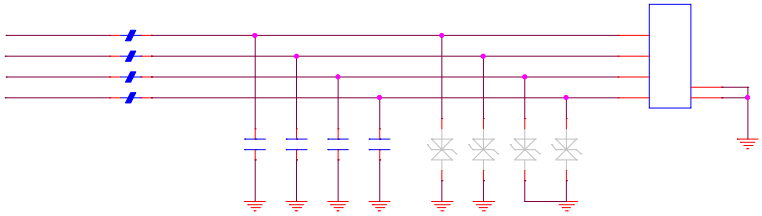

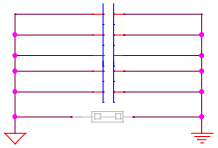

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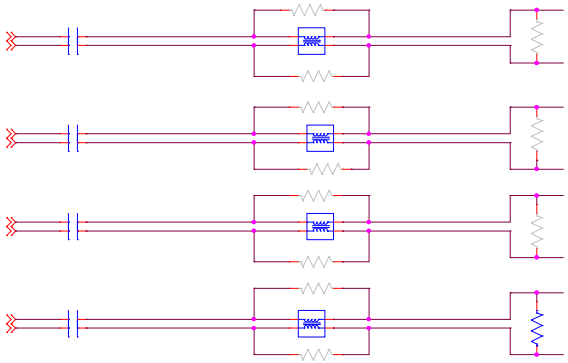

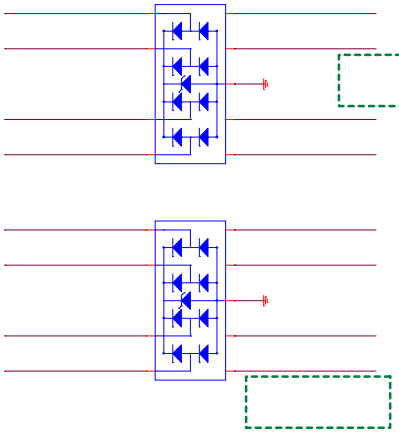

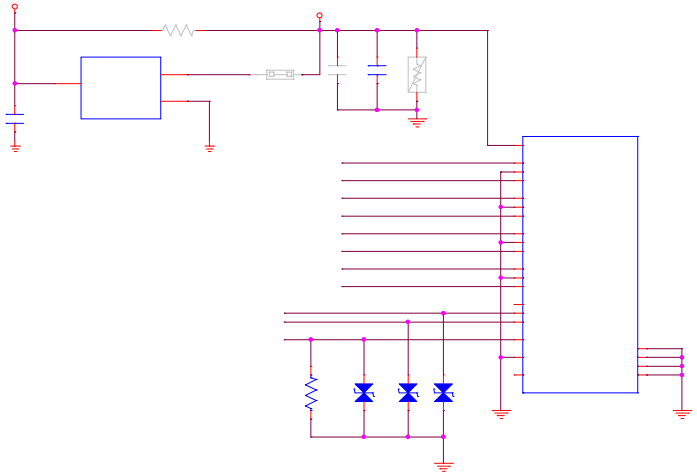

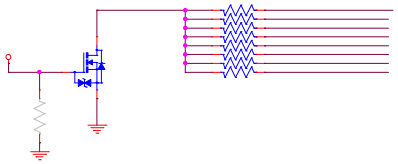

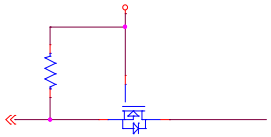

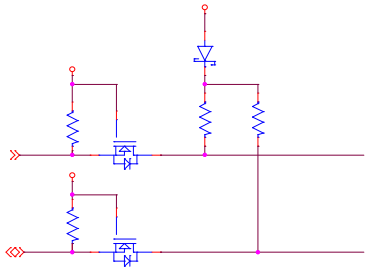

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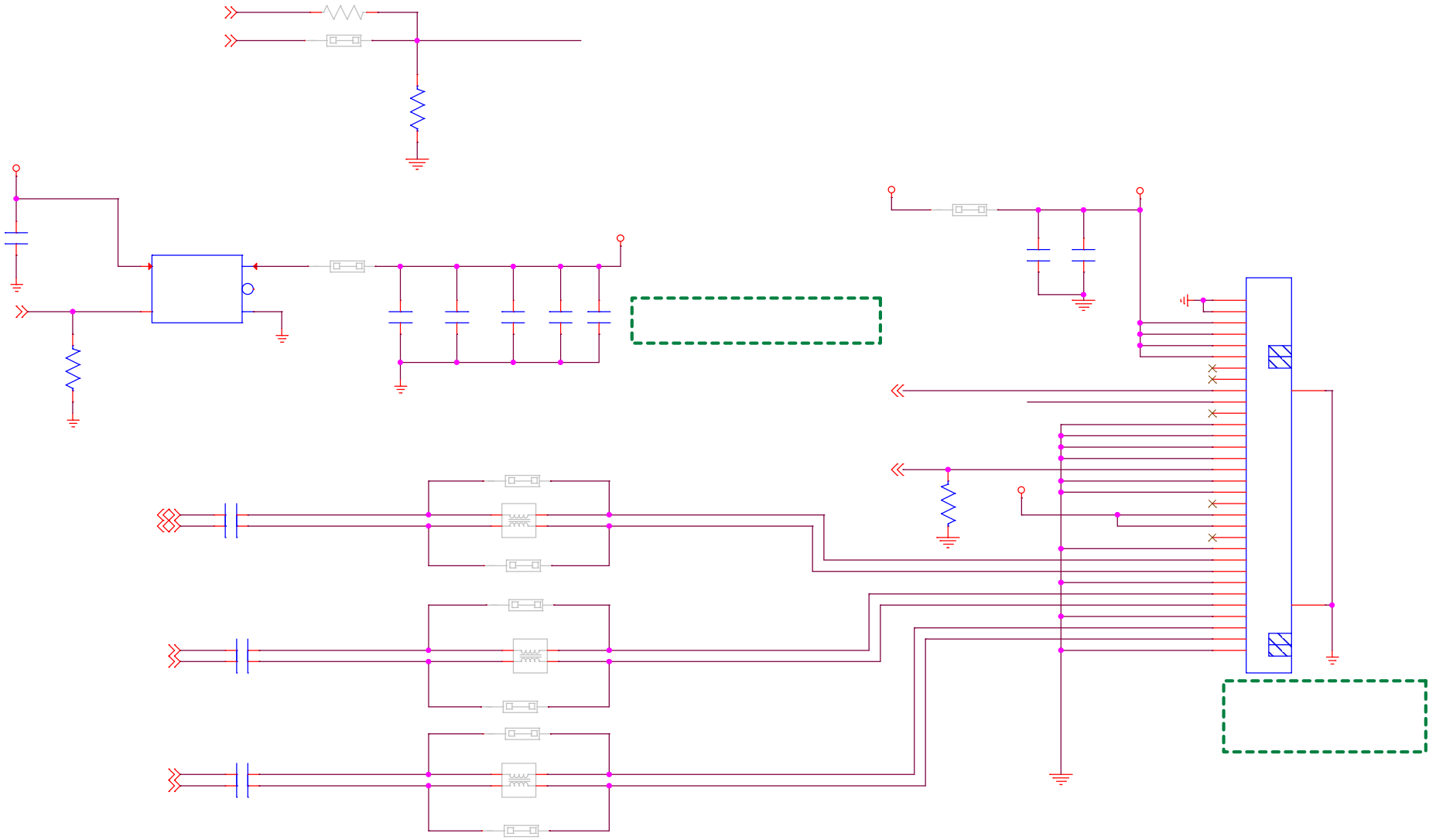

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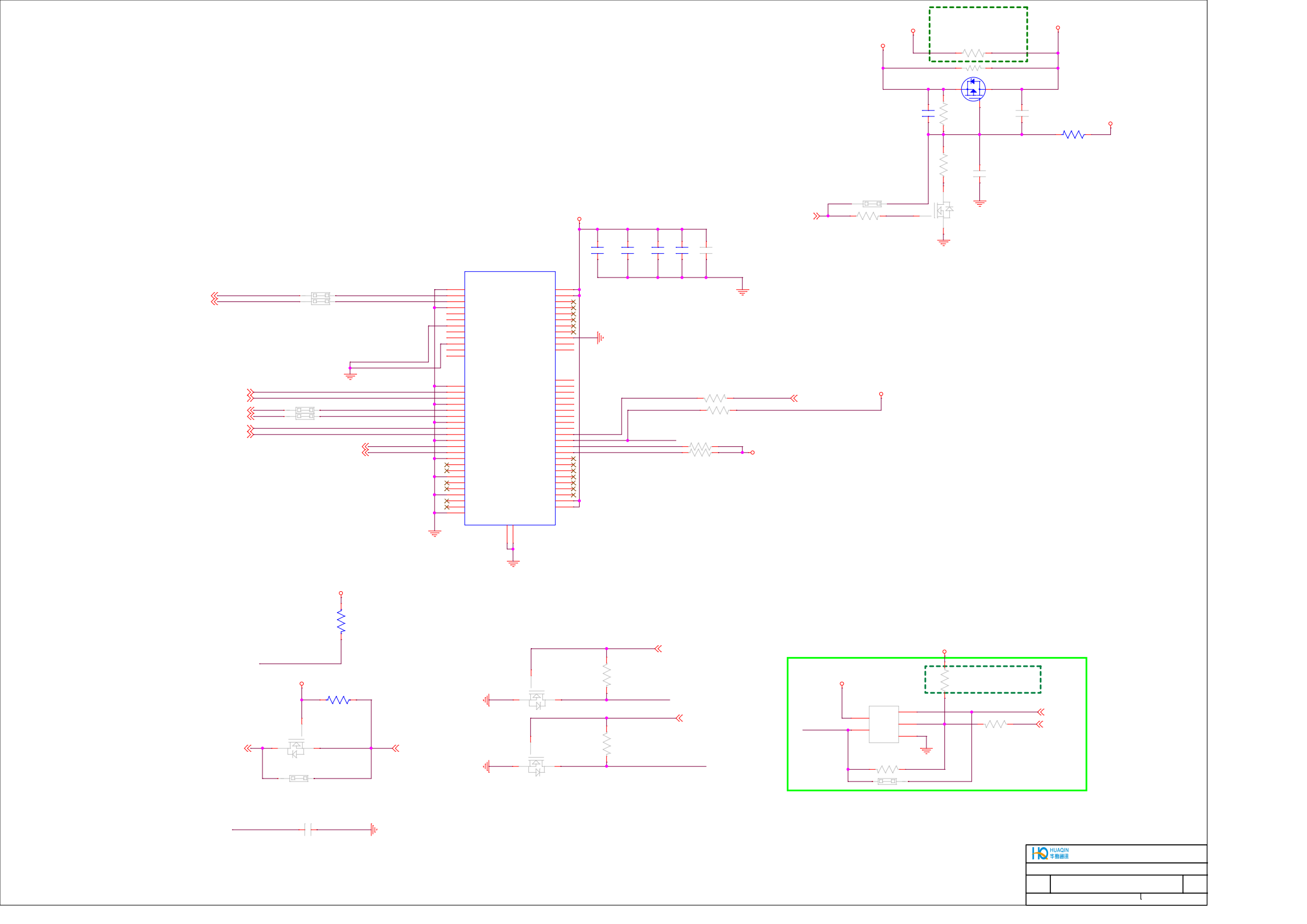

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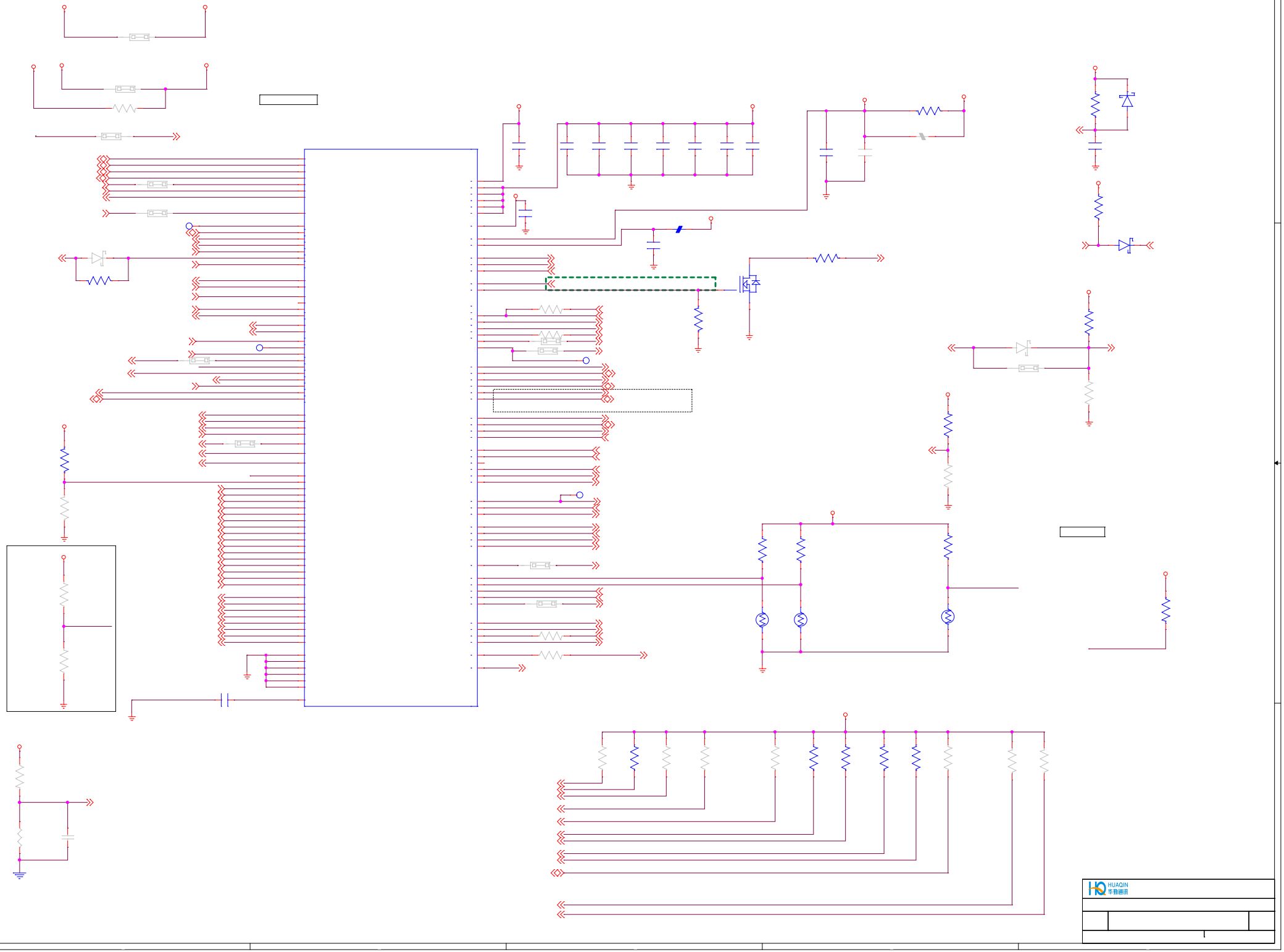

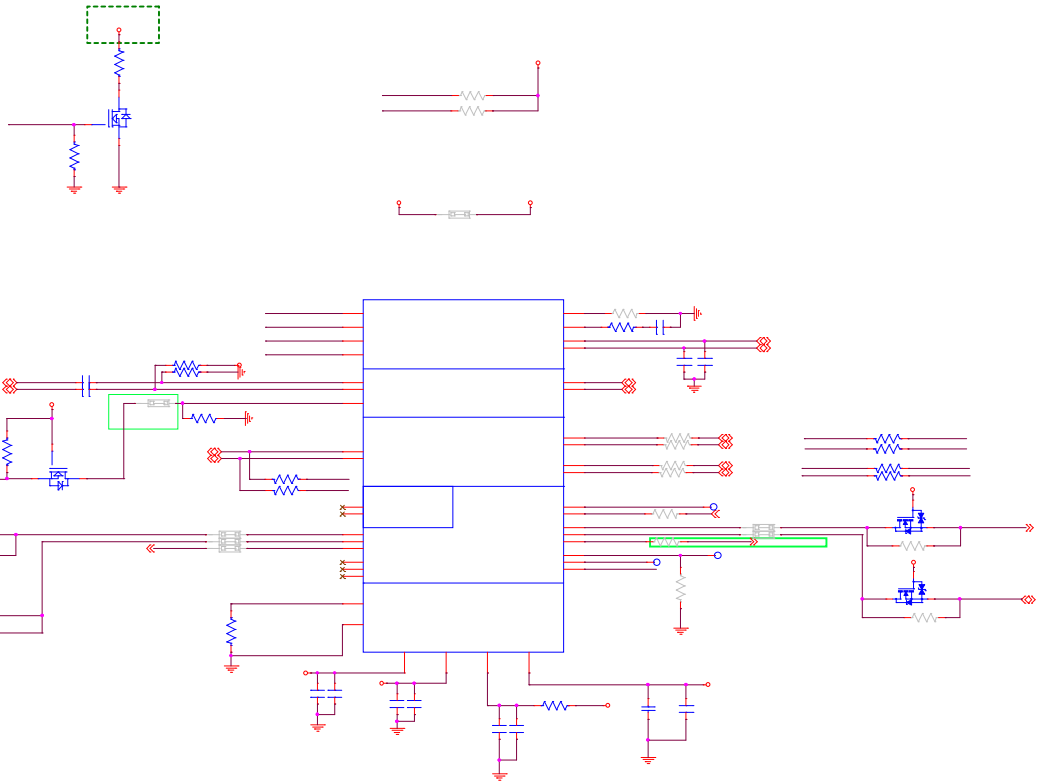

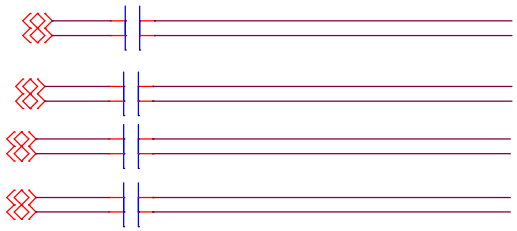

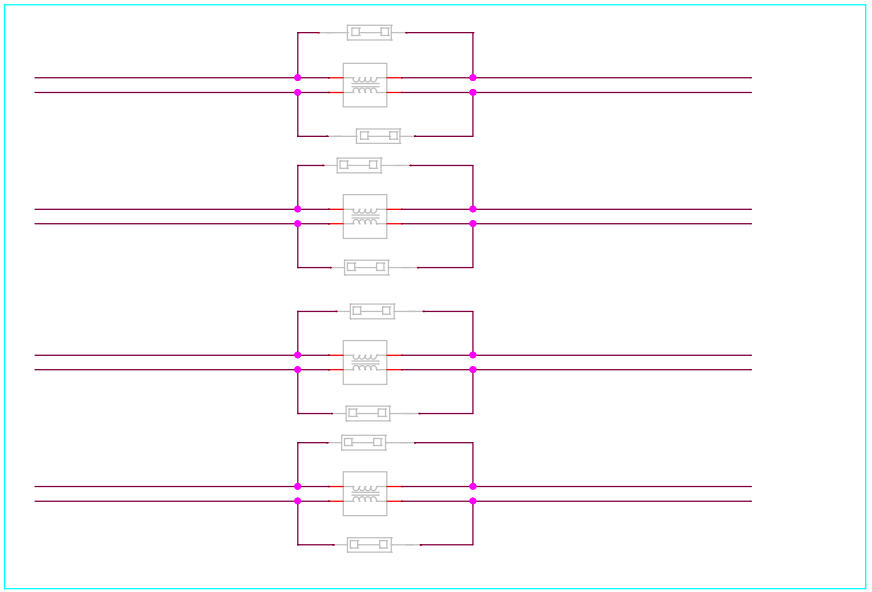

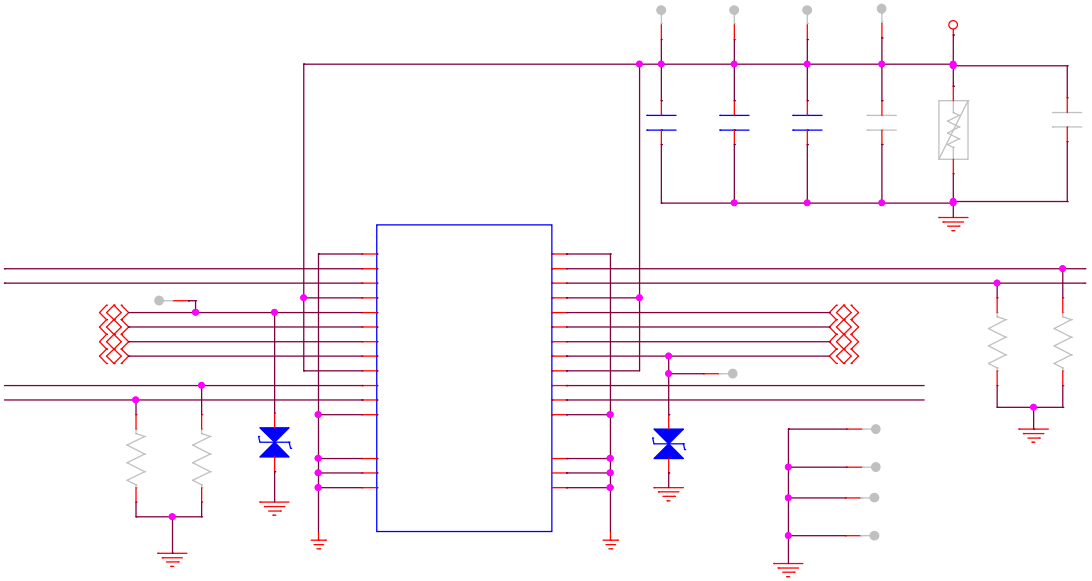

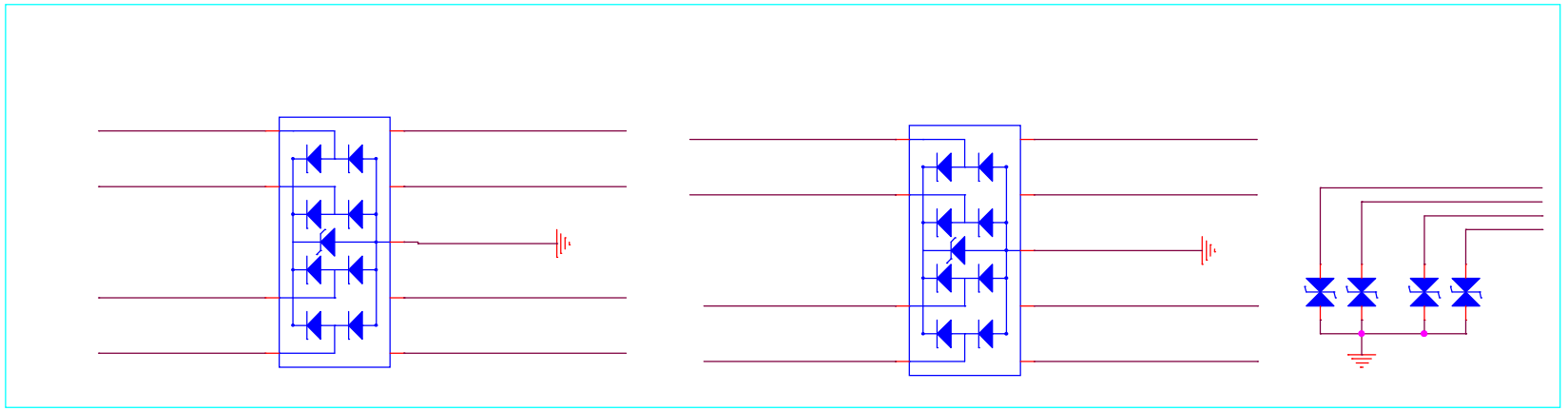

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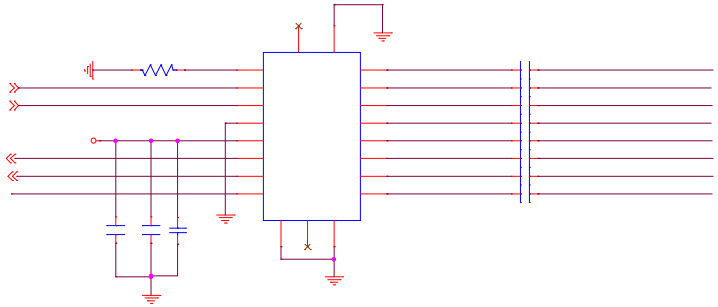

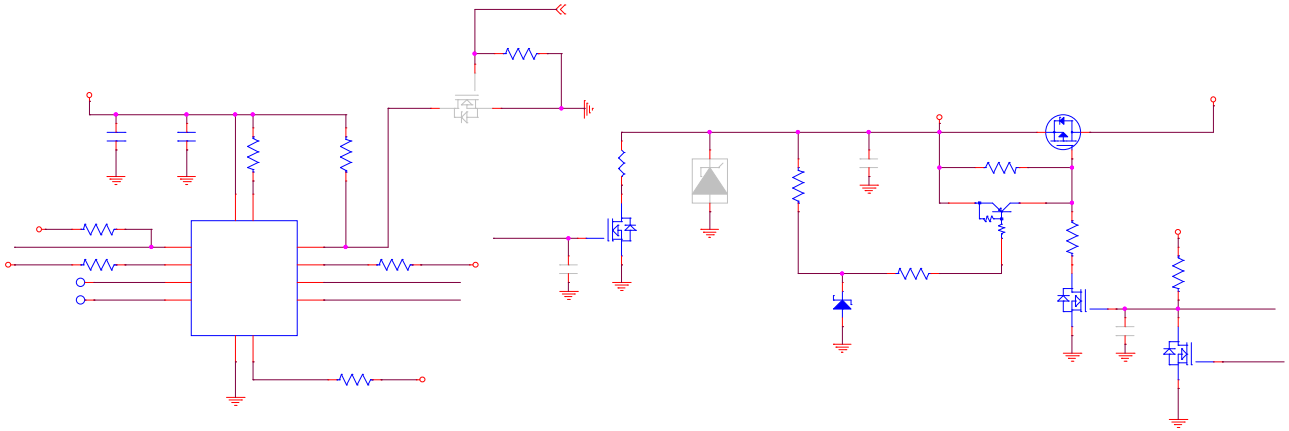

PORT A CHANNEL	PORT B OR PORT C CHANNEL CONNECTED TO PORT A CHANNEL	
	SEL = L	SEL = H
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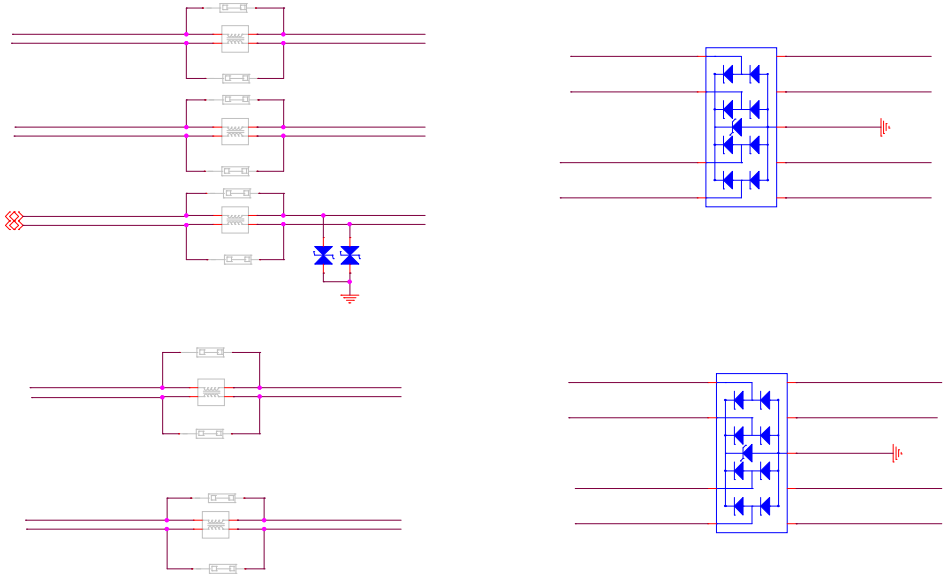
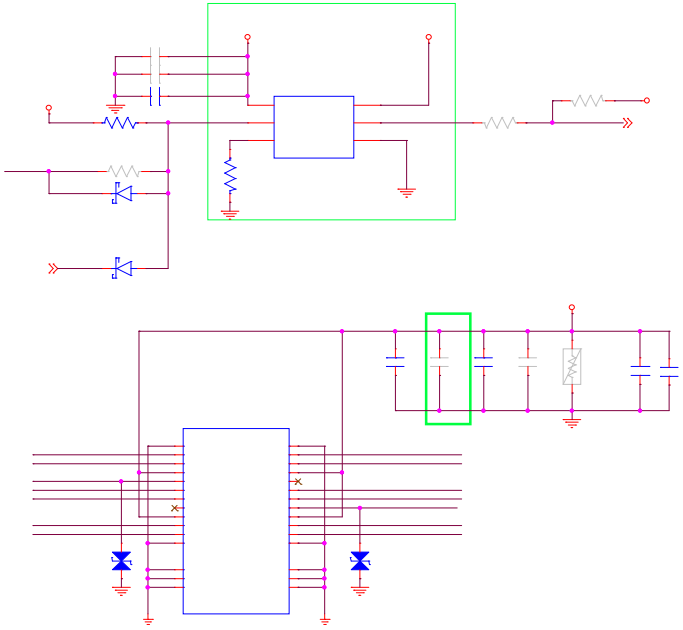
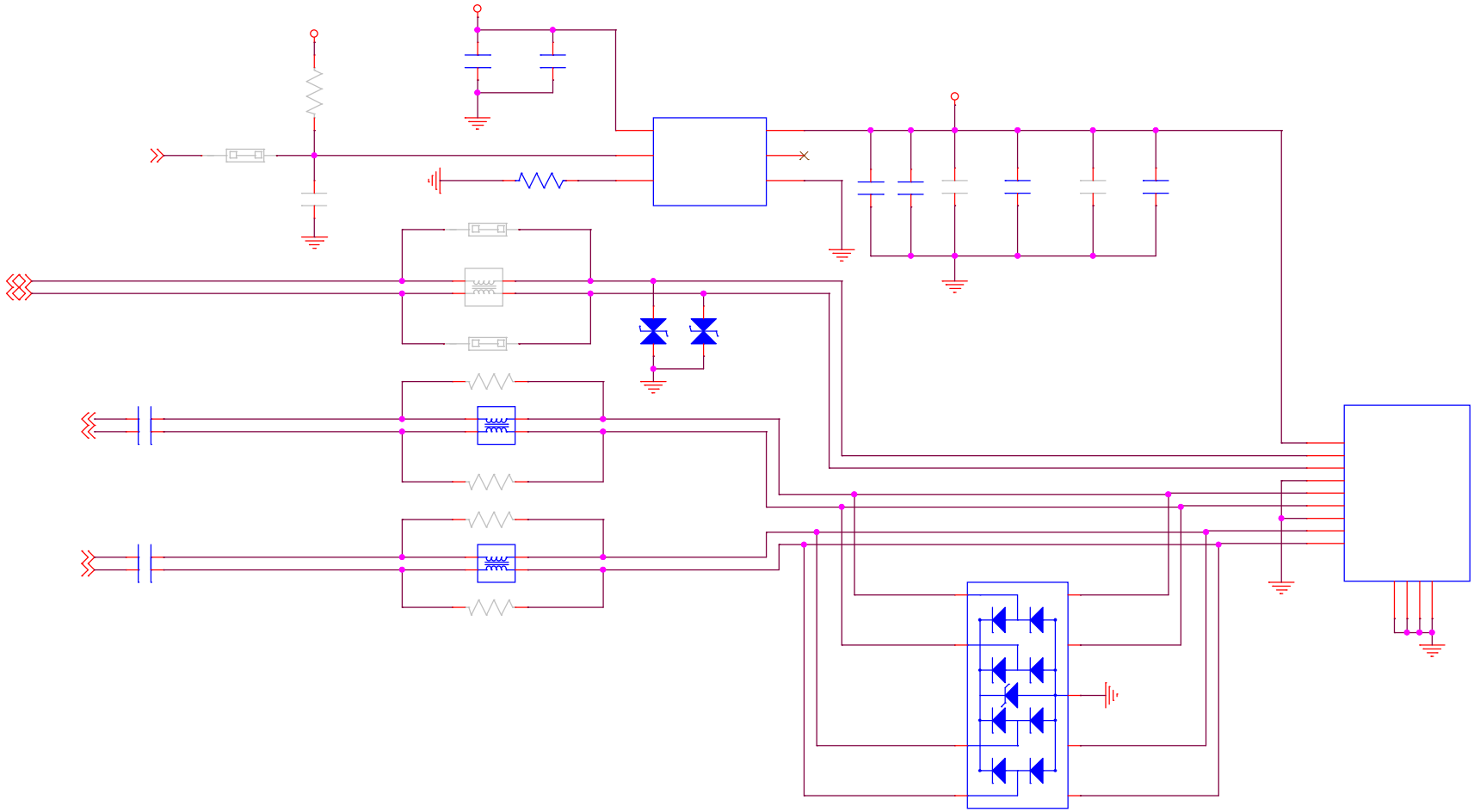
A0n	B0n	C0n
A1p	B1p	C1p
A1n	B1n	C1n







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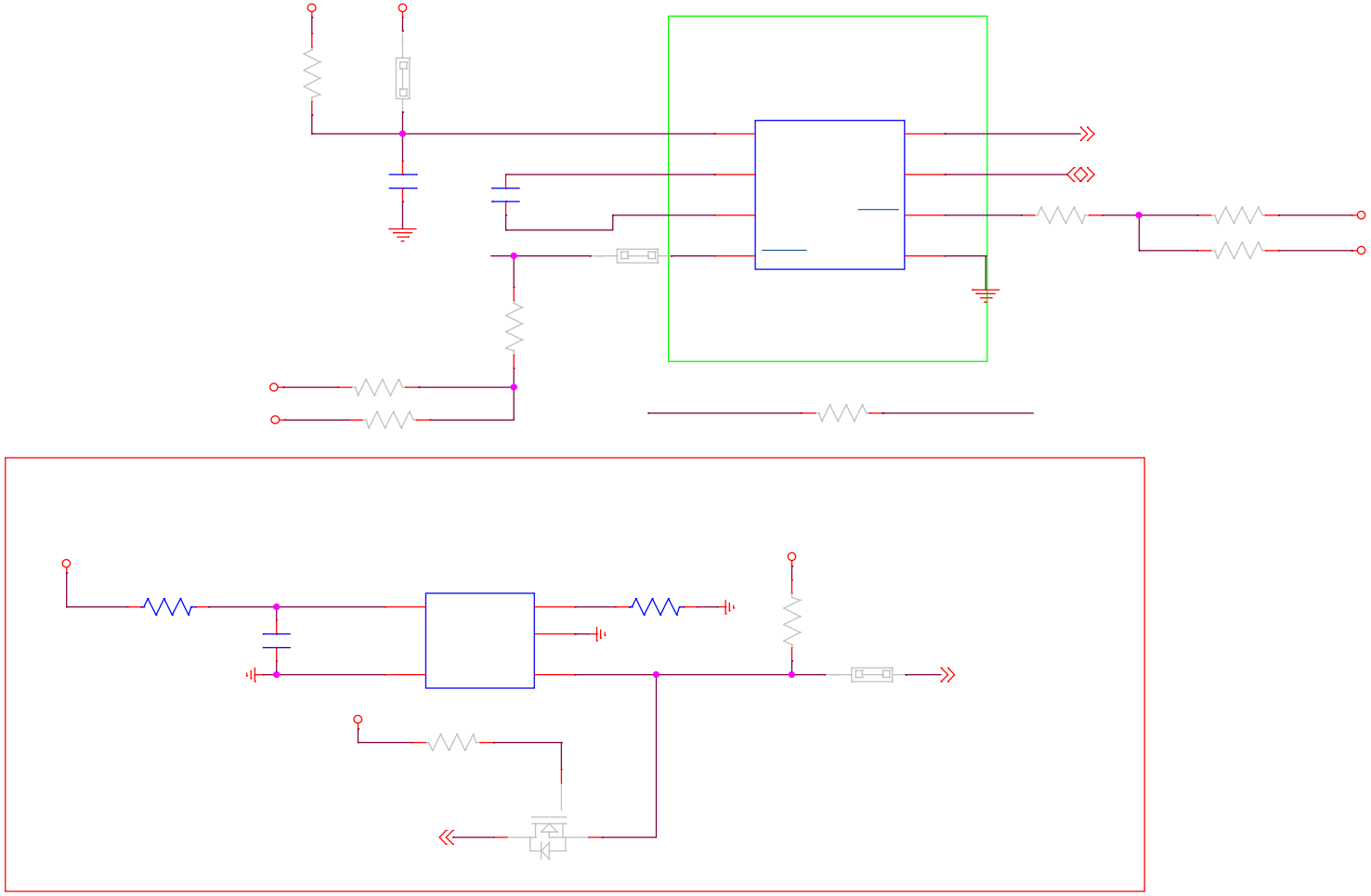

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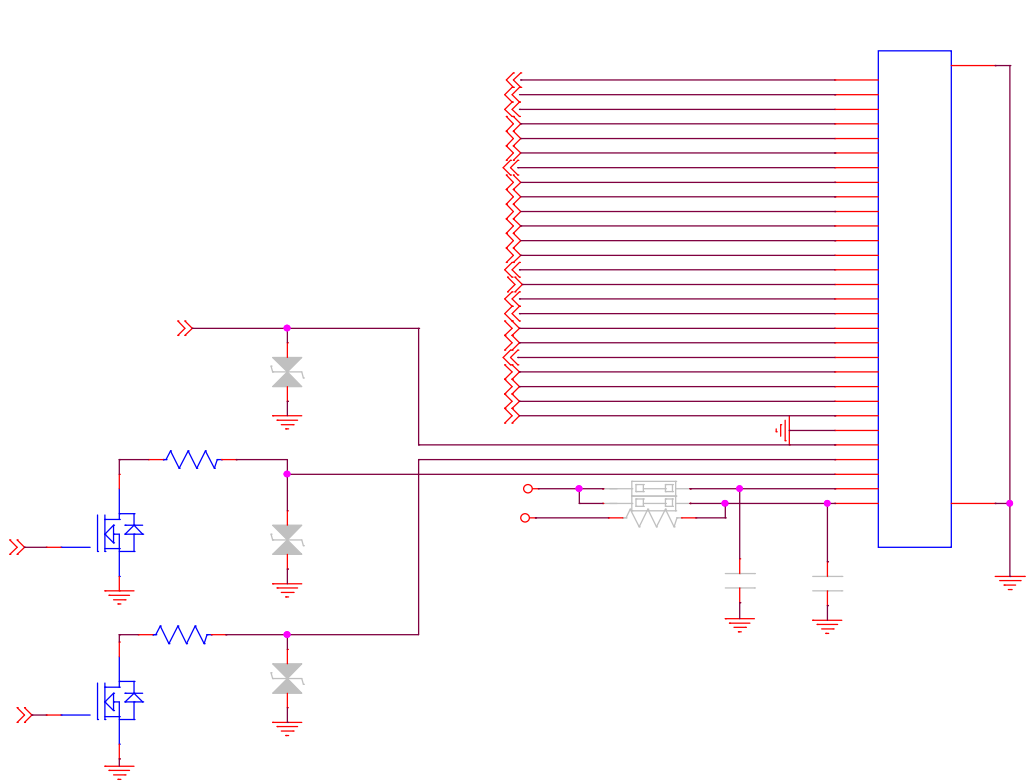

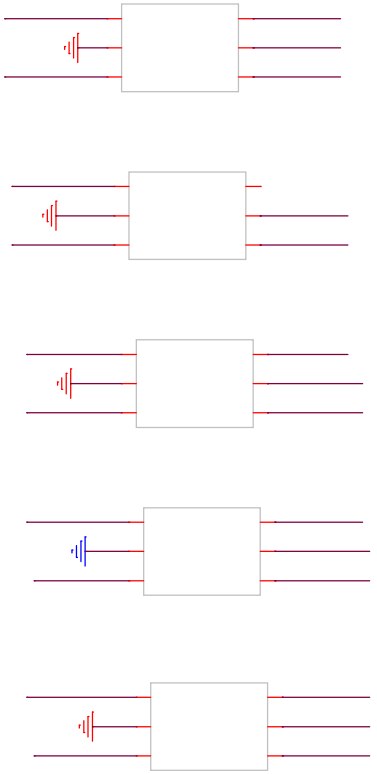

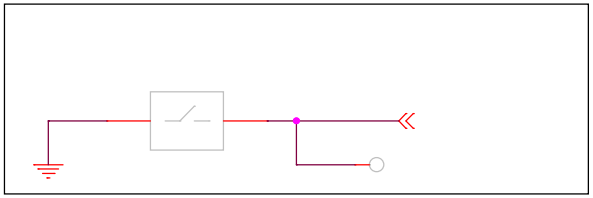

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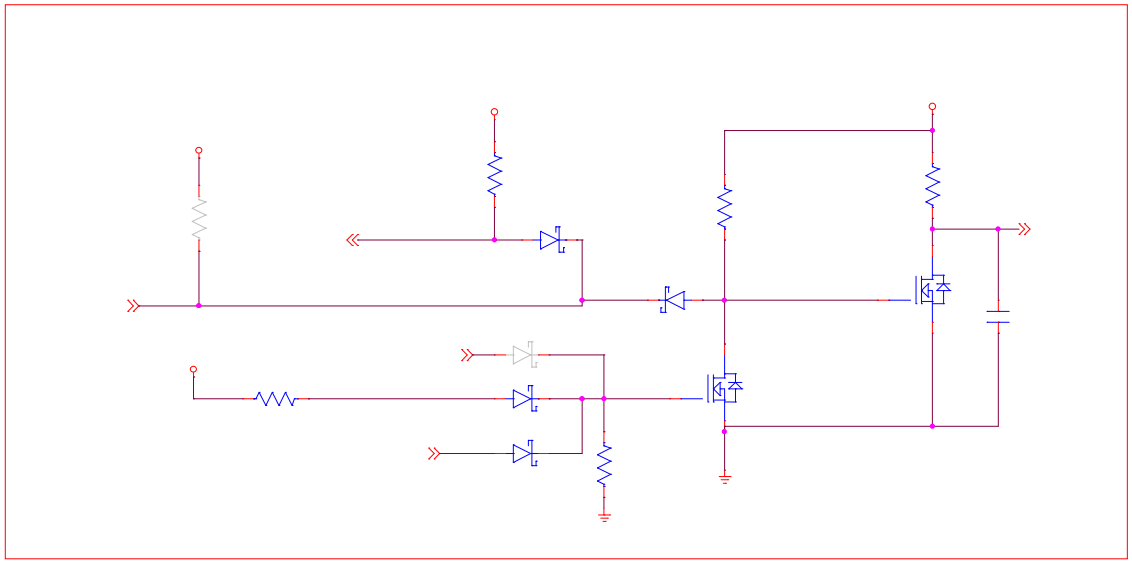

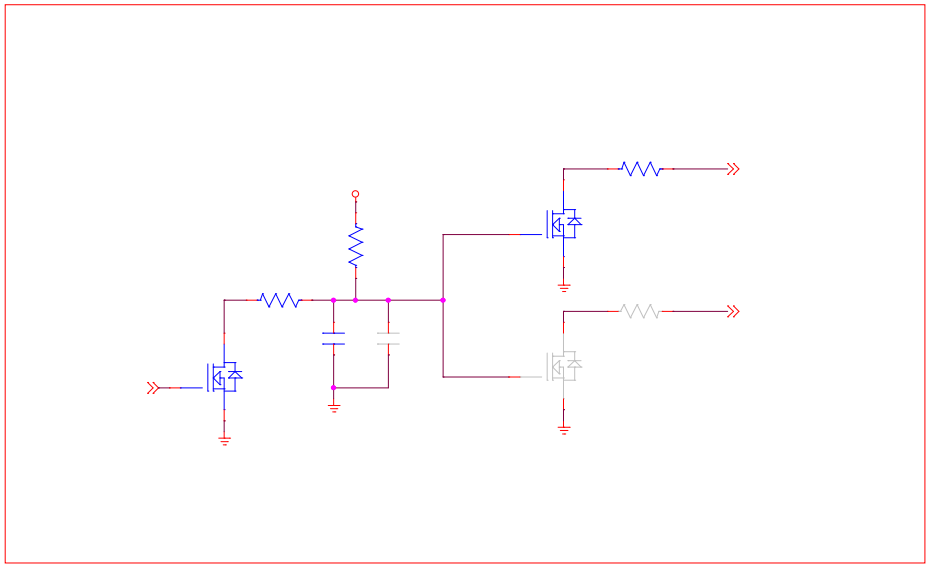

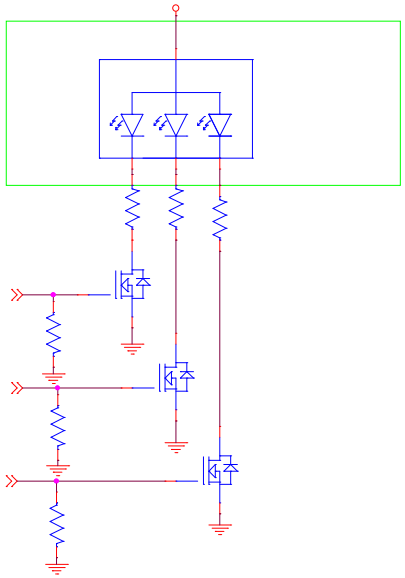

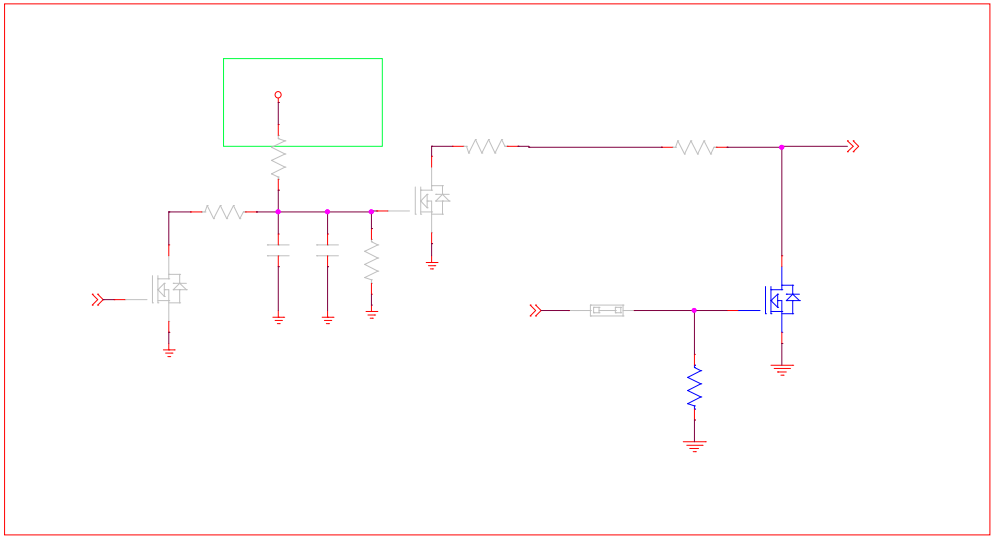

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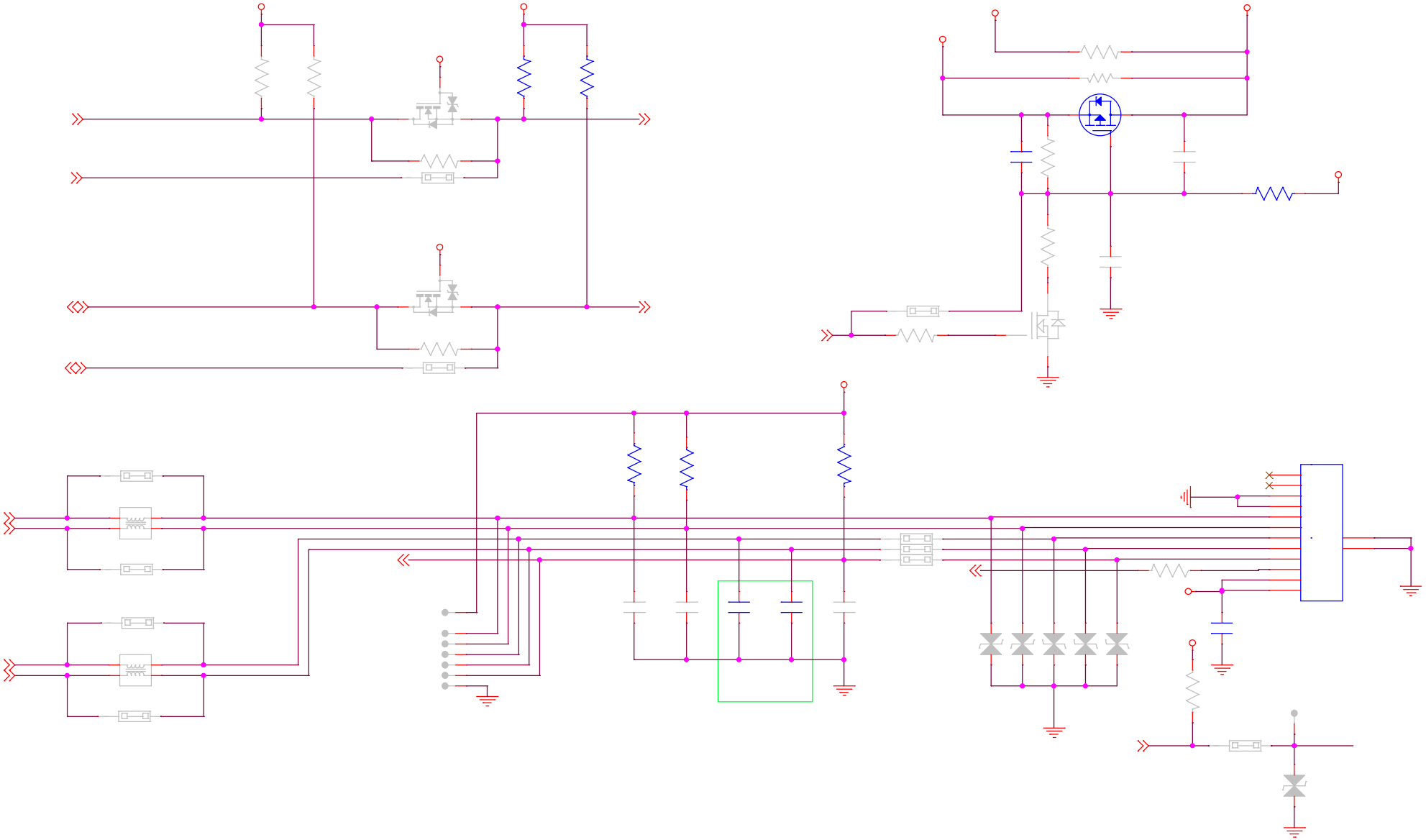

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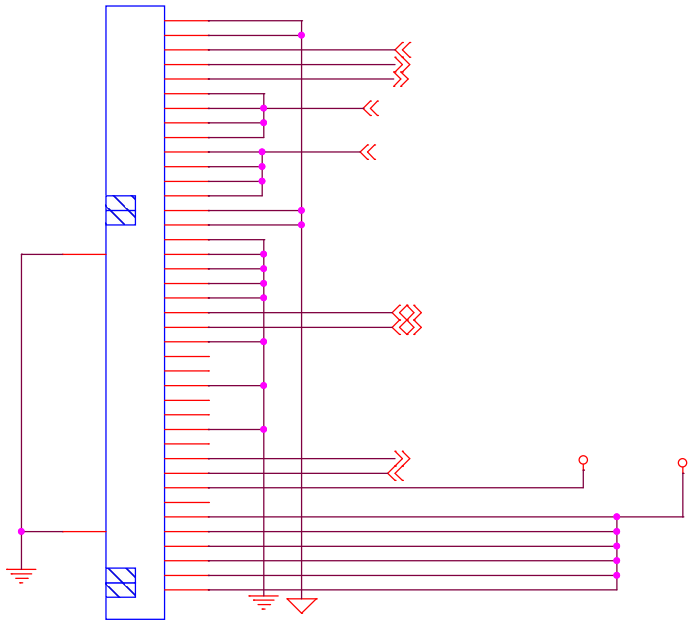

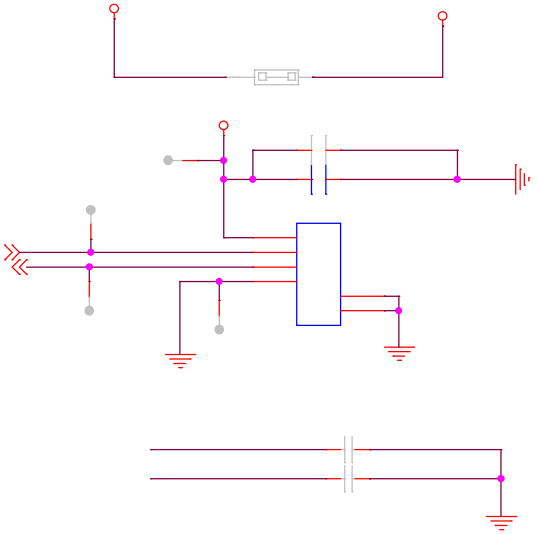

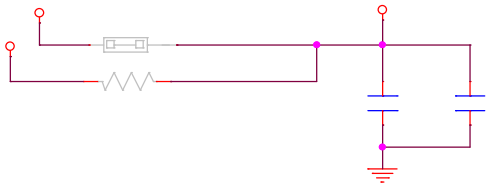

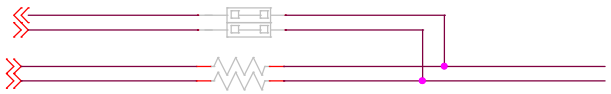

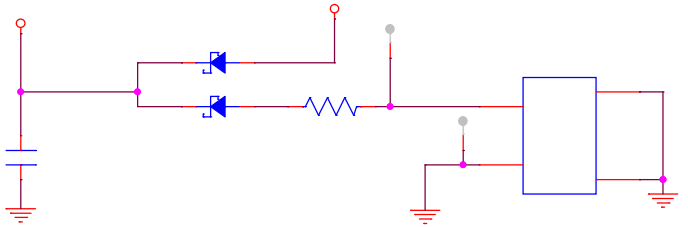

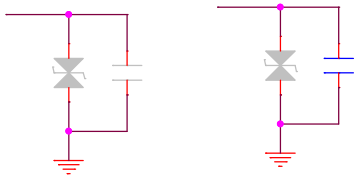

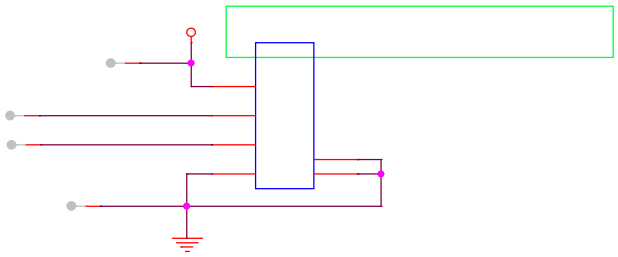

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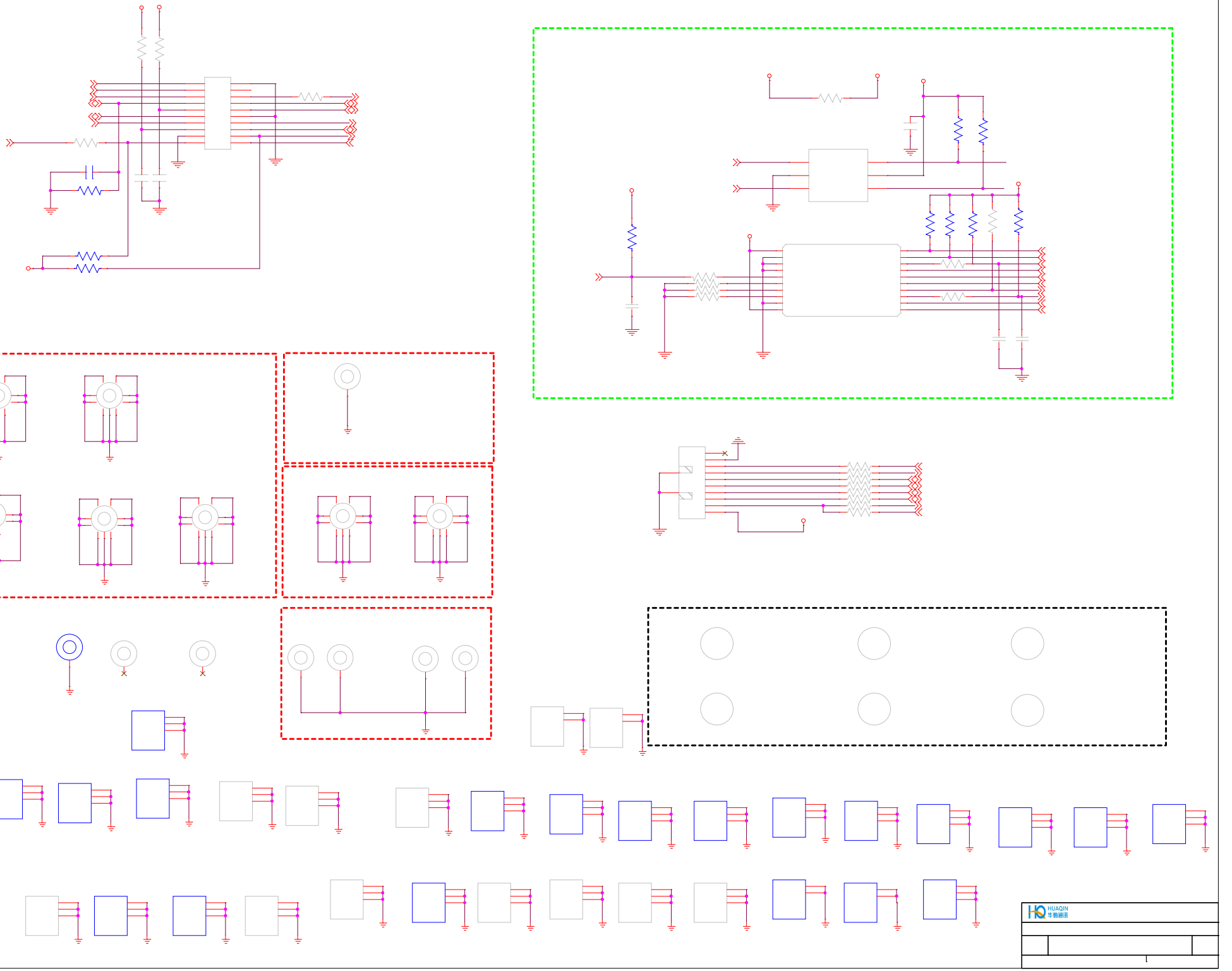

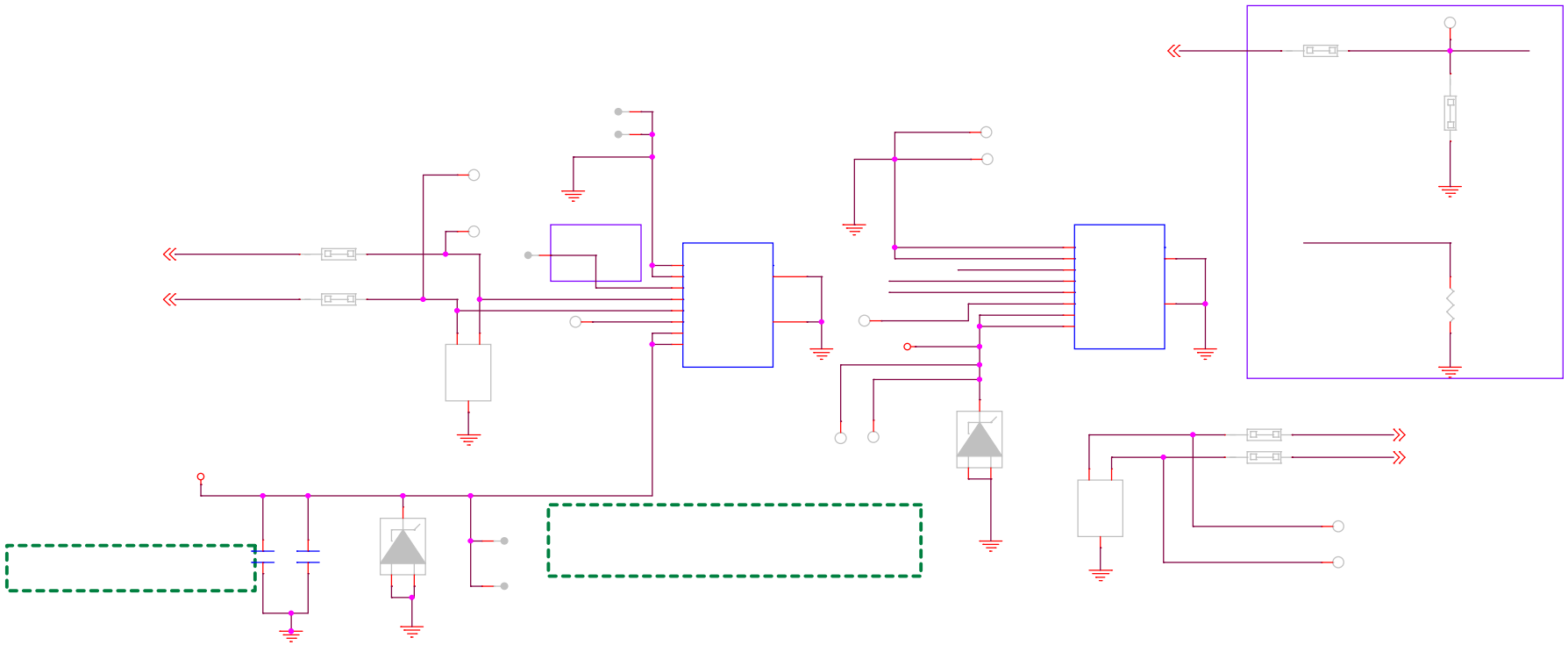

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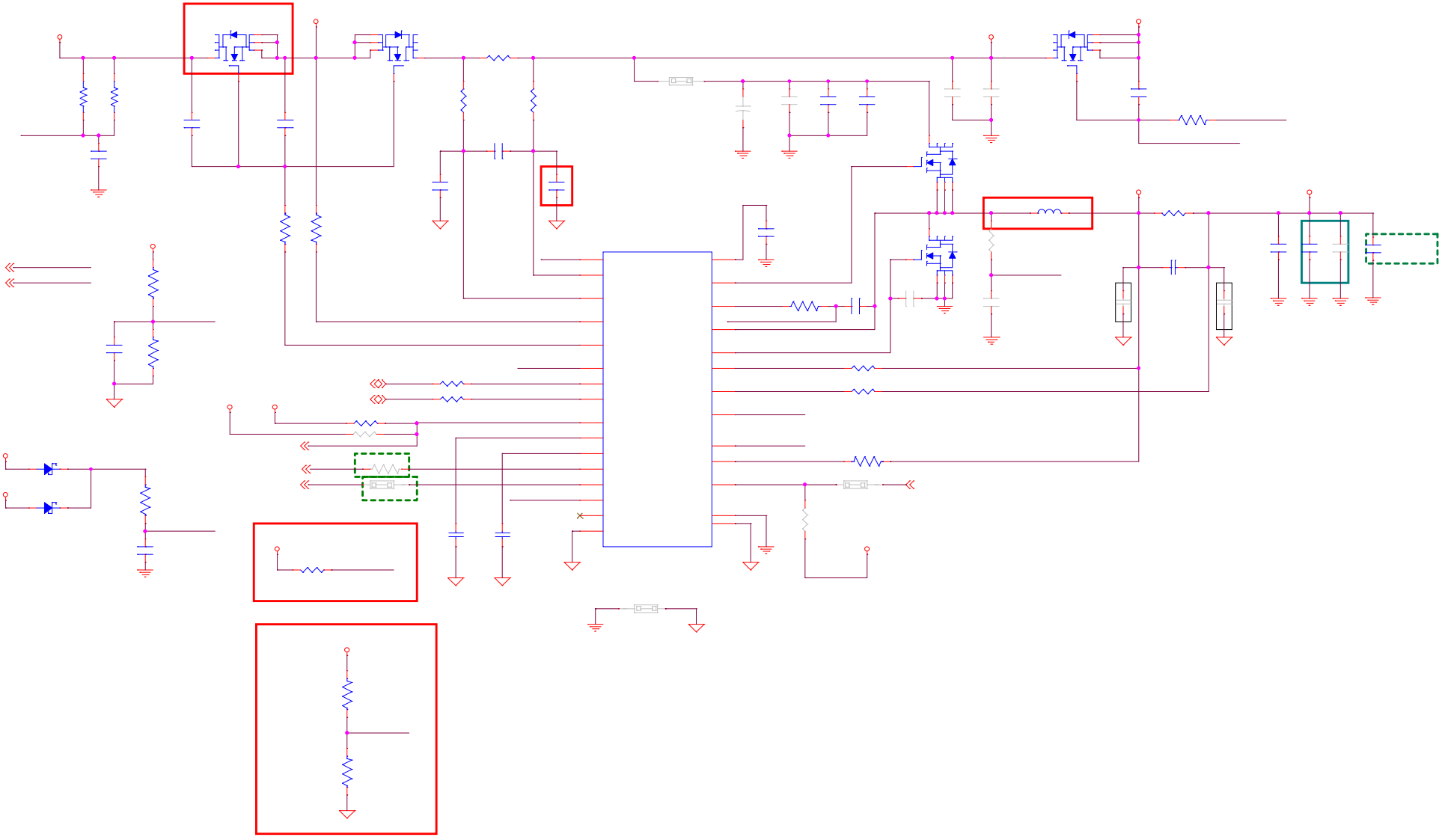

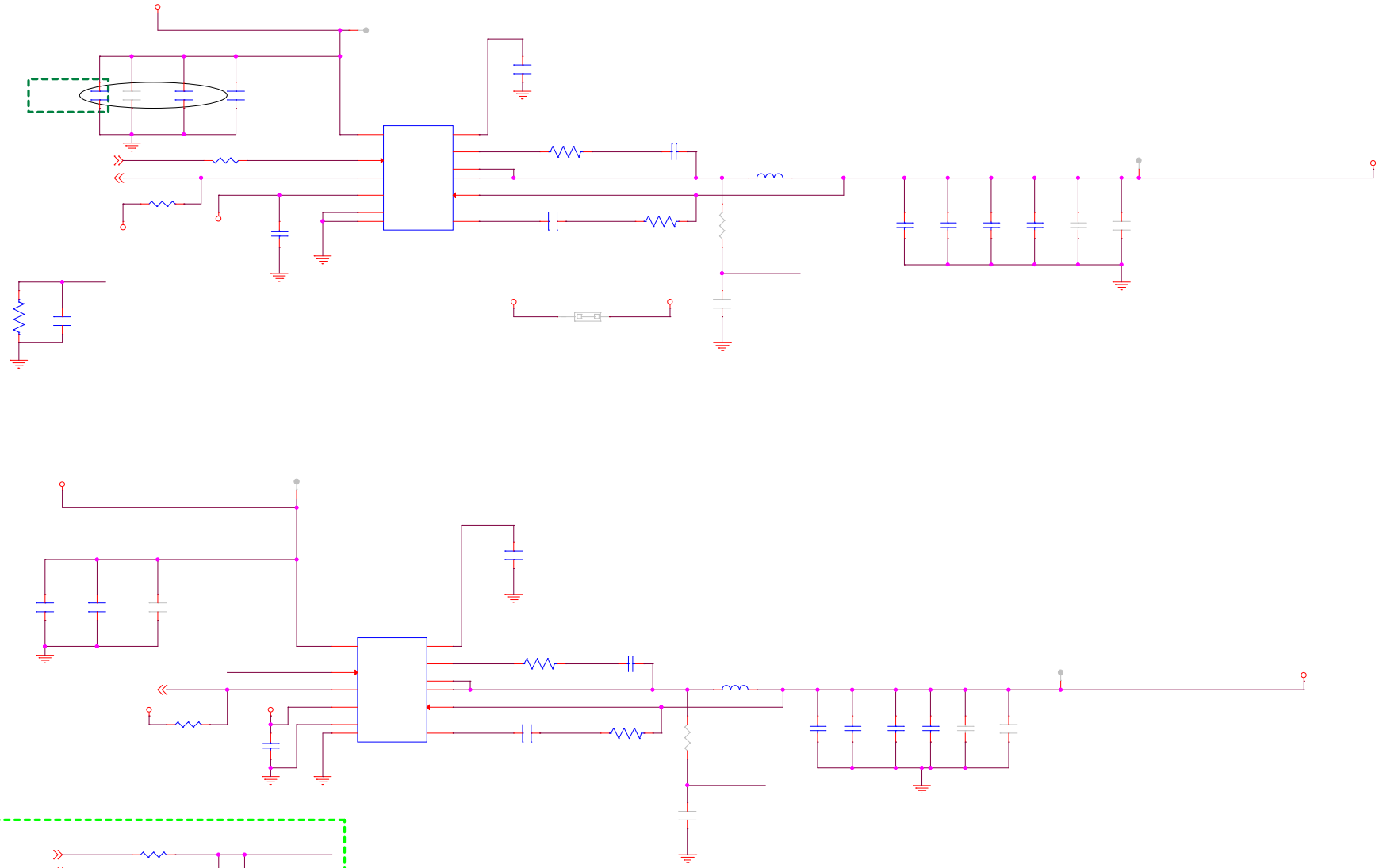

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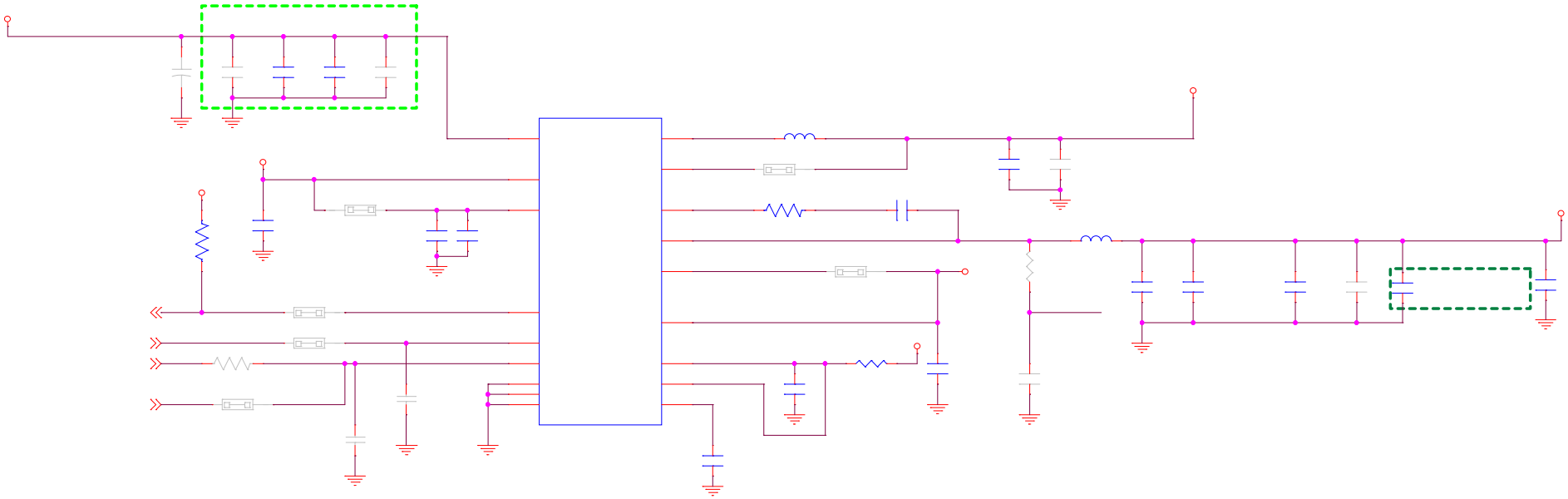

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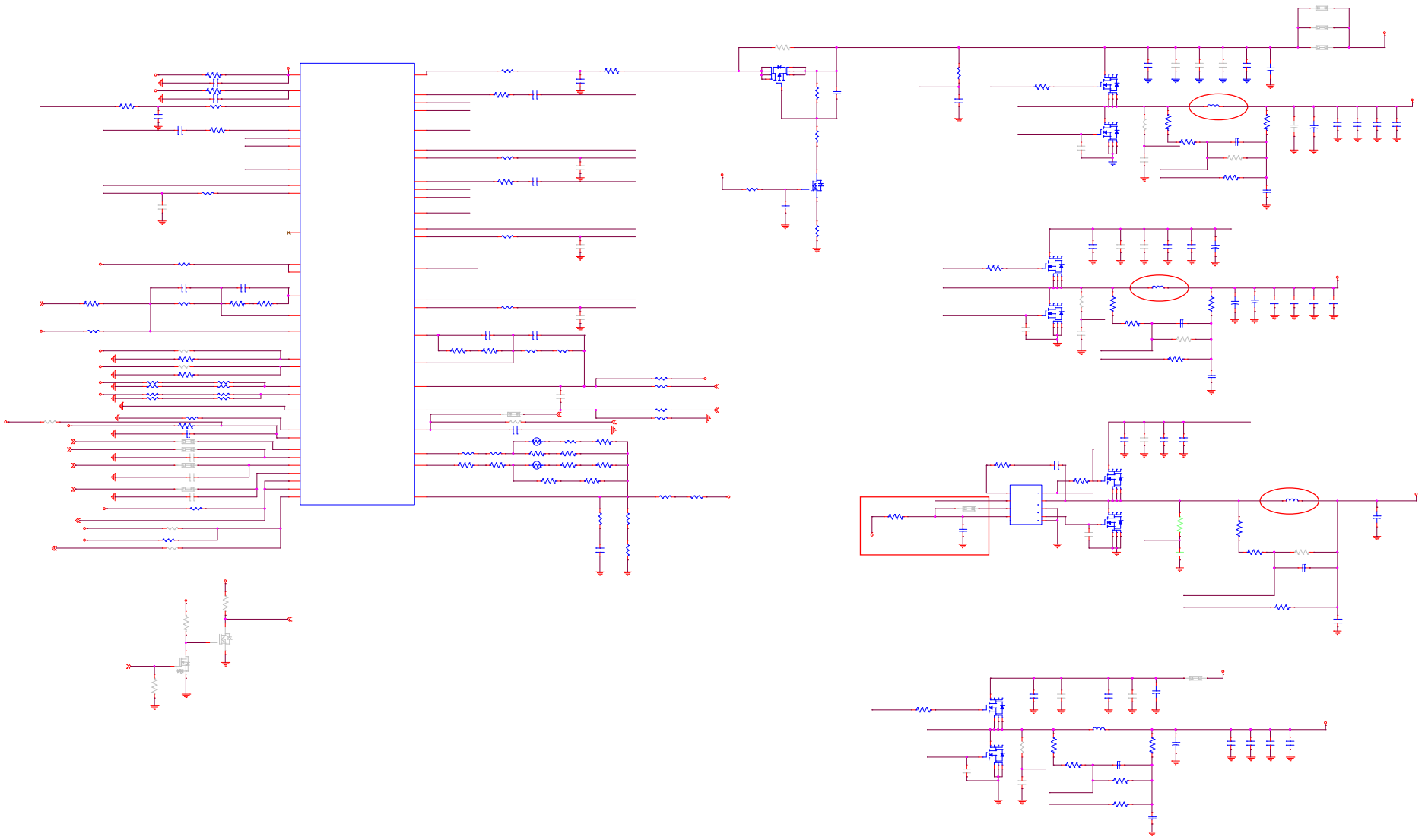

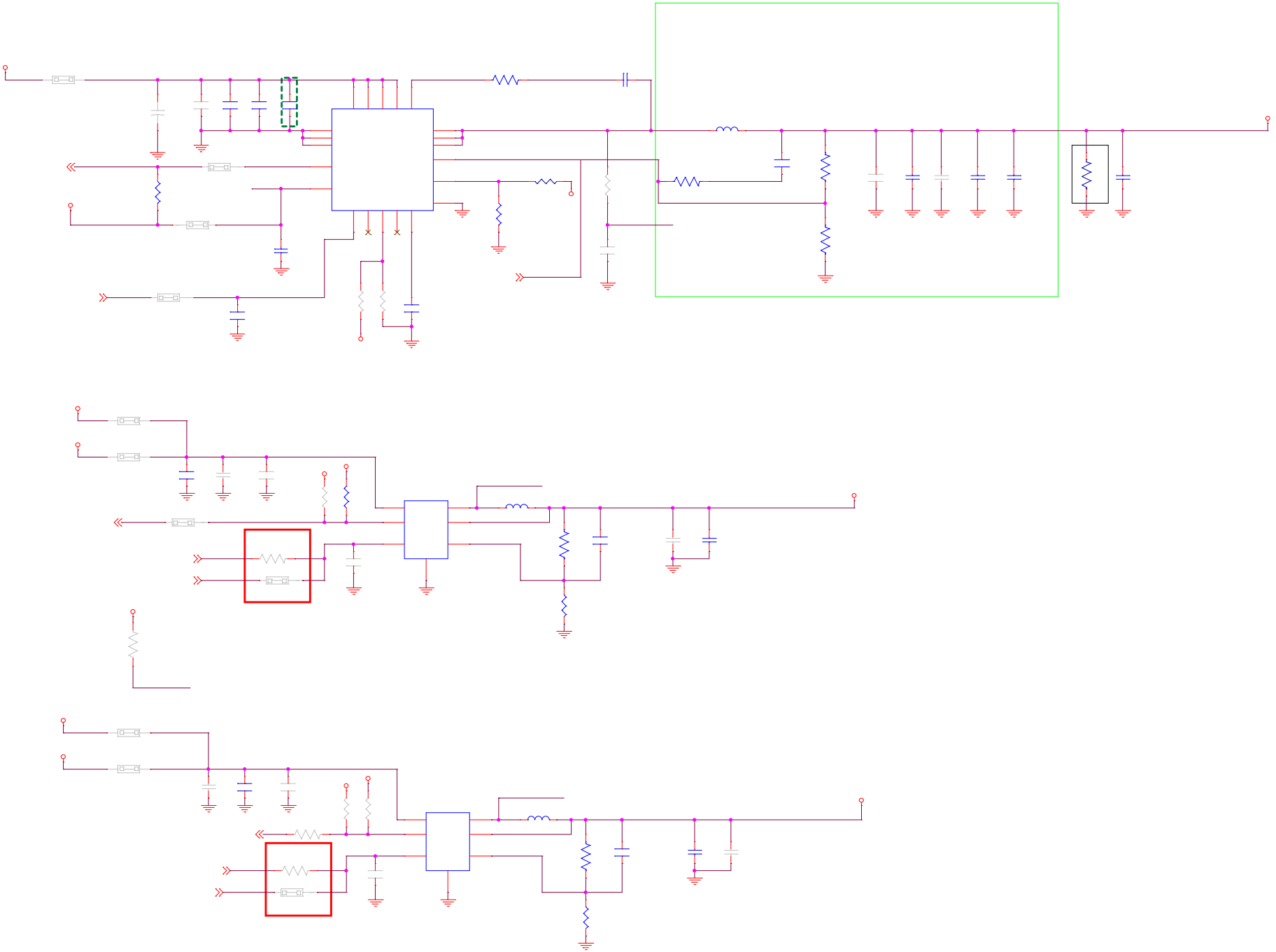

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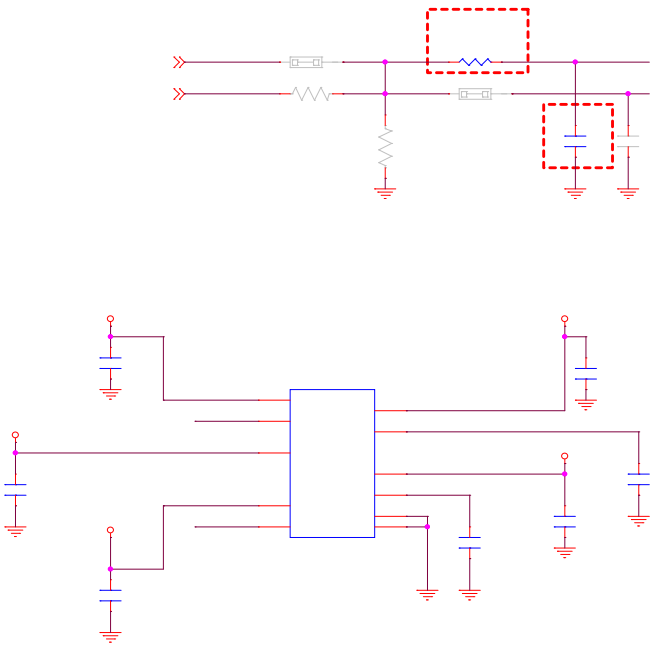

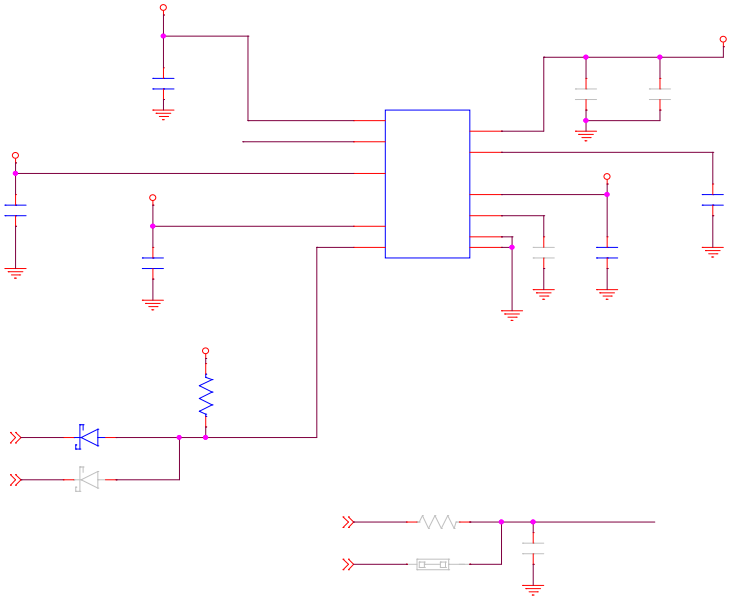

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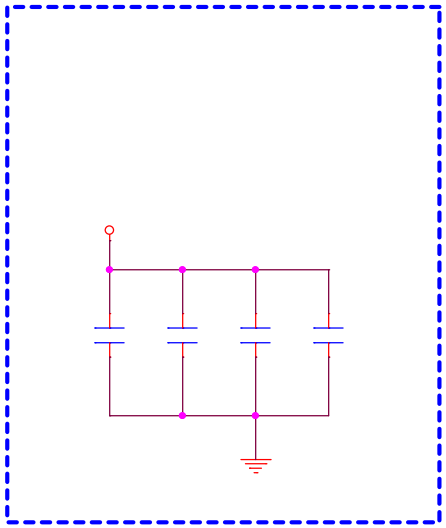

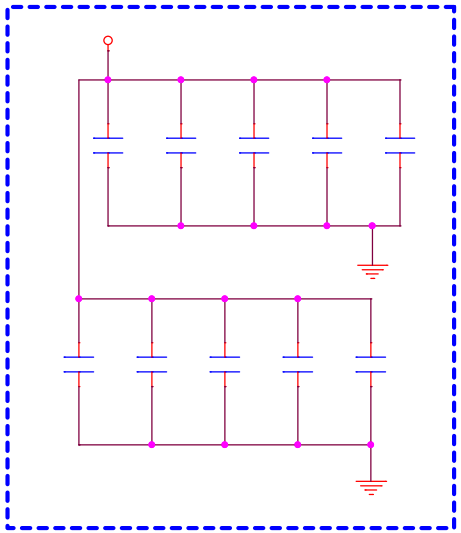

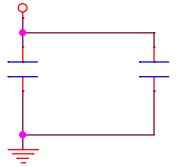

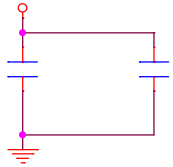

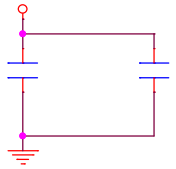

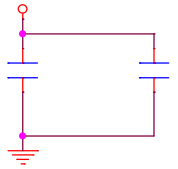

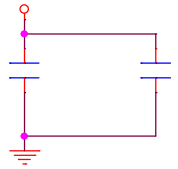

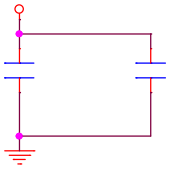

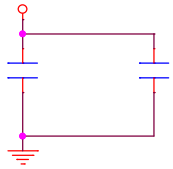

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