

# L14 L15 IO BD Schematics

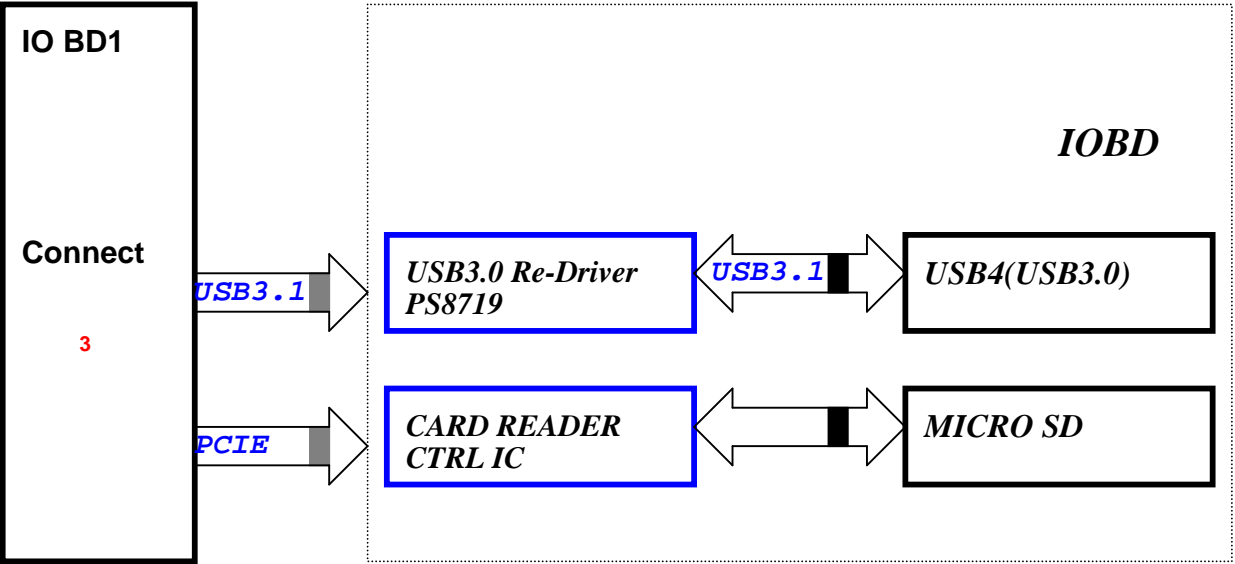
DY	DUMMY
GENESYS	CTRL IC DET PIN BOM CTRL
AMD/ INTEL	PLATFORM BOM CTRL AMD support modern standby

L14L15 IO

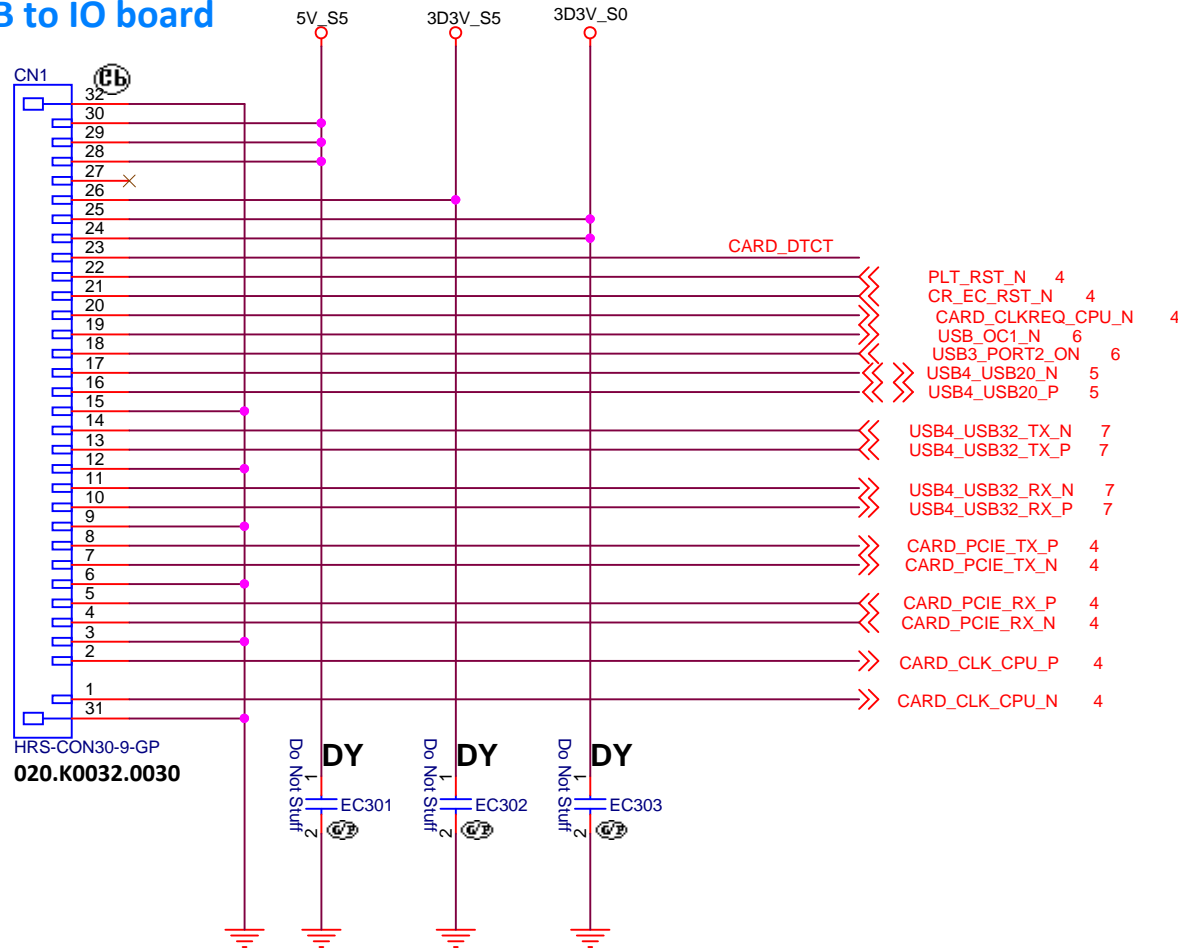
<div><div>緯創資通</div><div>Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title <div>Cover Page</div>		
Size <div>A4</div>	Document Number <div>L14/ L15 IO BD</div>	Rev <div>-1</div>
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# Block Diagram

Project code:  
L14-1 AMD: 4PD0PZ010001  
L15-1 AMD: 4PD0PZ01F001  
PCB P/N: 213602/ 213603  
Revision: -1

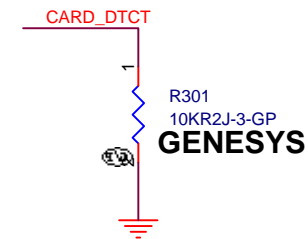


# MB to IO board



## CARD READER DTCT PIN

CARD_DTCT (R301)	
GENESYS	ASM
BAYHUB	DY



L14L15 IO

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Title

**IO BOARD CONN**

Size  
A4

Document Number

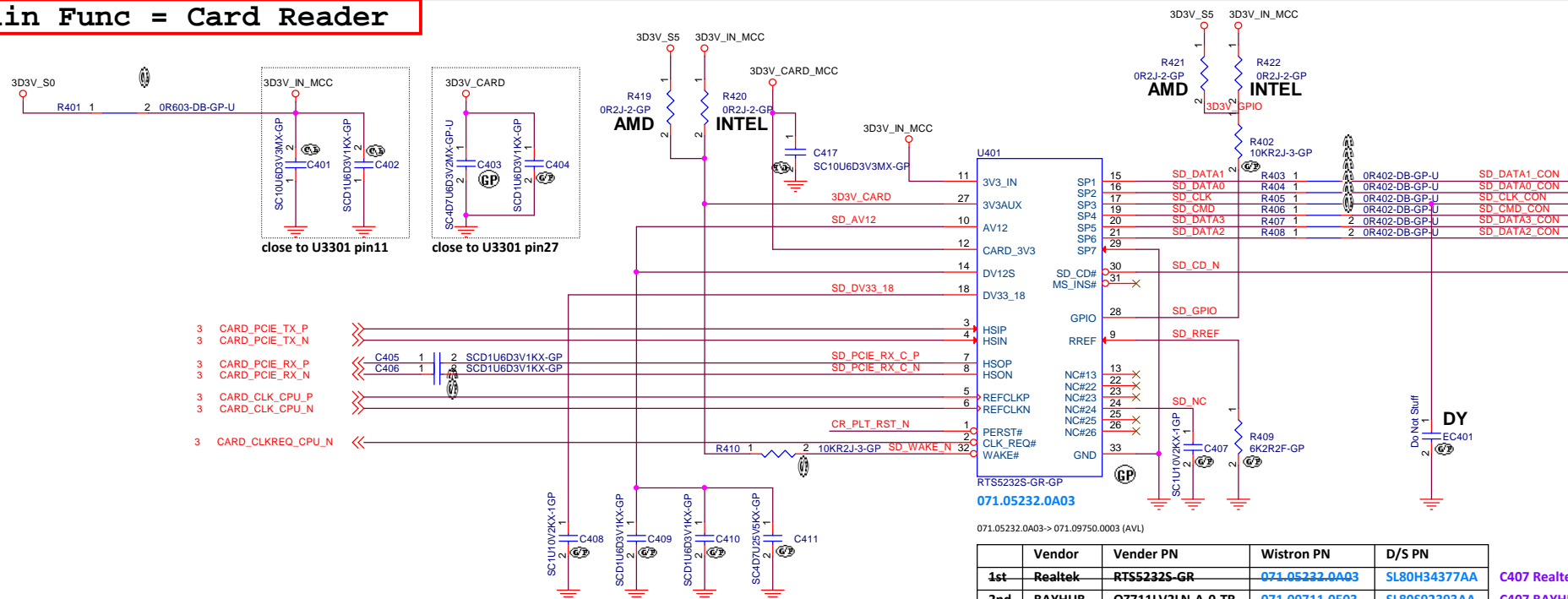
**L14/ L15 IO BD**

Rev  
**-1**

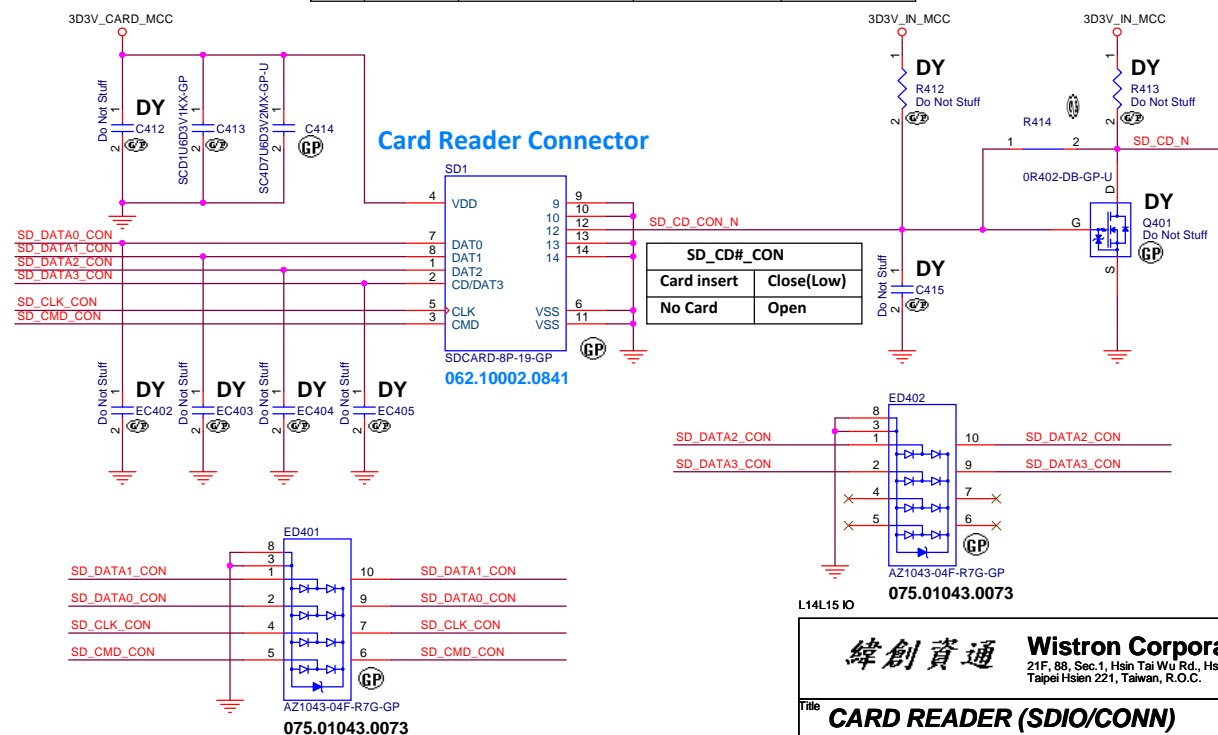
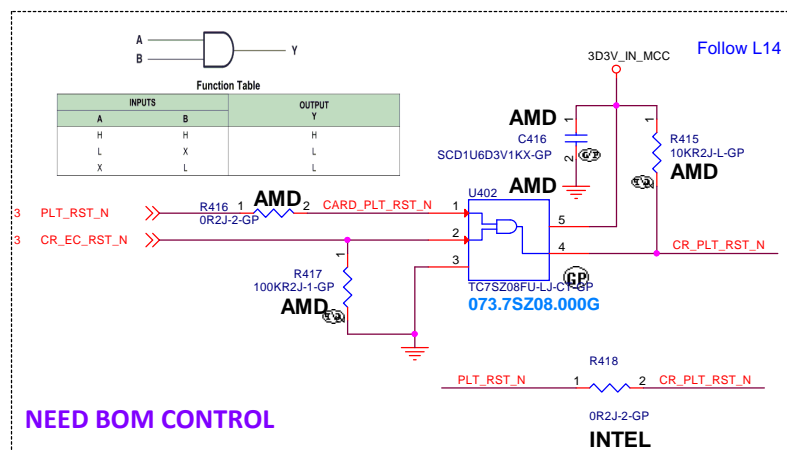
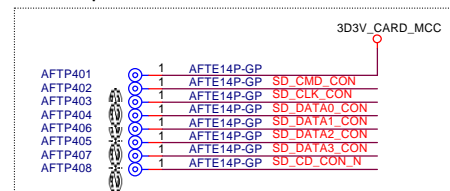
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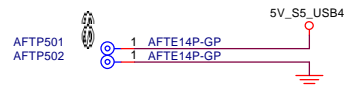
**Main Func = Card Reader**



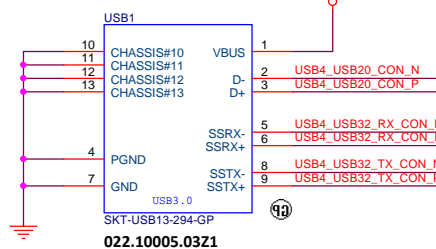
These test pins trace should be short and close to connector



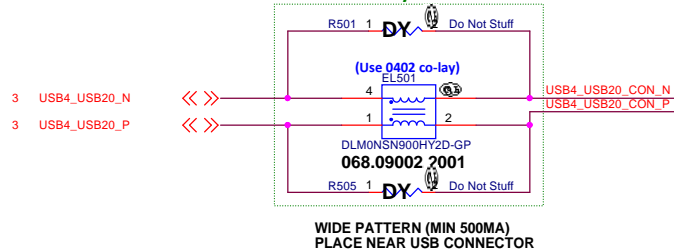
Main Func = USB3.0 Port1 w/ AOU



Port4  
USB3.0 Connector

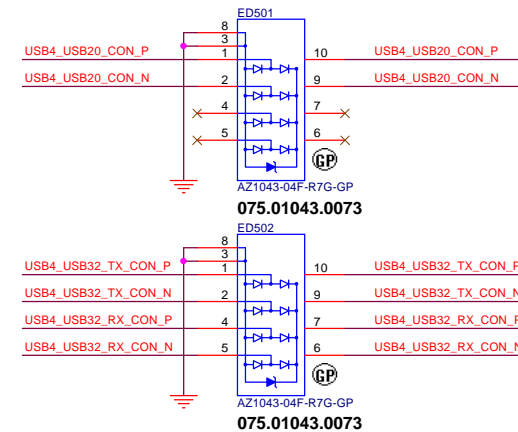


Co-layout



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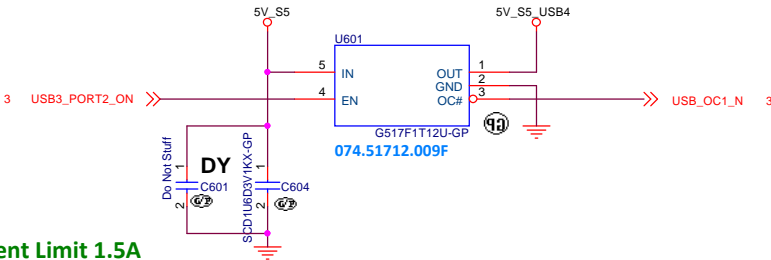
Del Co-layout CMC for -1 by PSE request



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Main Func = USB Charger

For USB3.0 System Port4



Continous Current Limit 1.5A

TABLE of USB 3.0 port: U3502

	Vendor	Vendor P/N	SY6288C20AACWistron P/N
1st	GMT	G517F1T12U	074.51712.009F
2nd	SILERGY		074.06288.007B

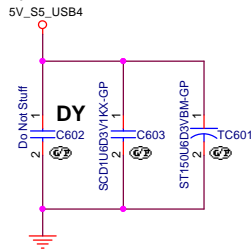
074.09742.0A9F

074.51712.009F

074.06288.007B

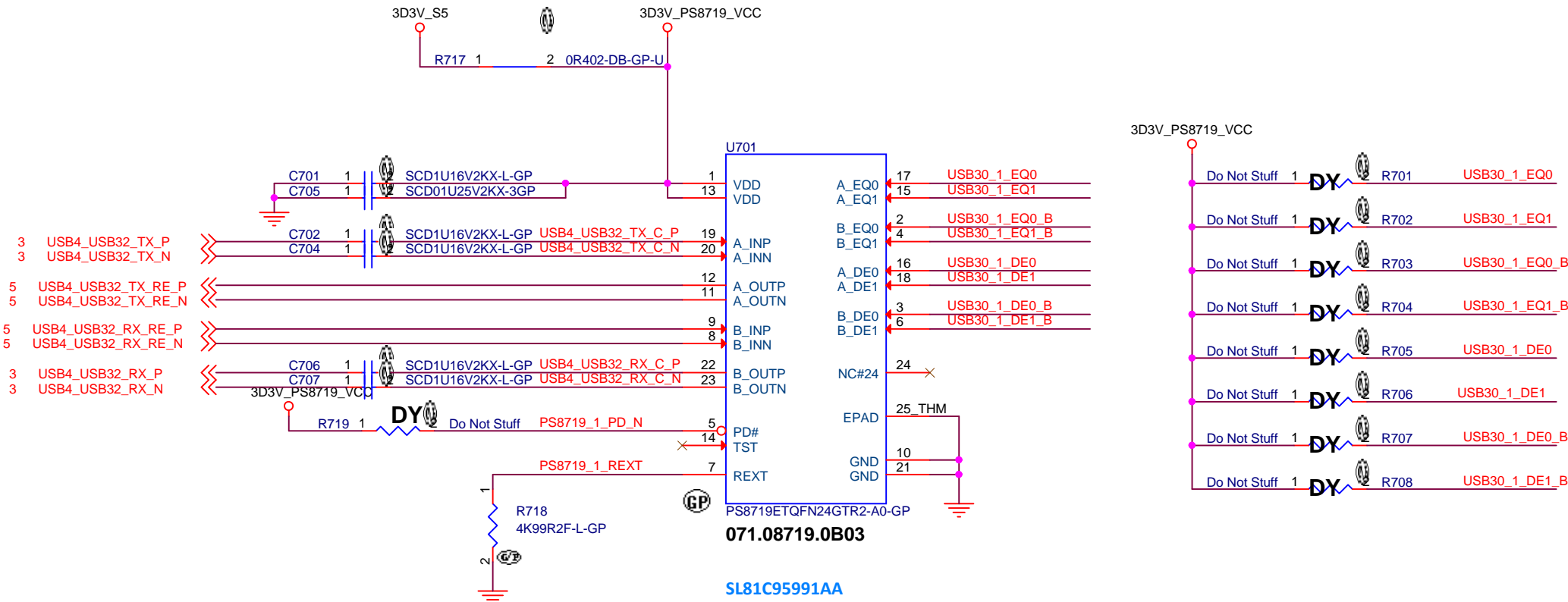
Please confirm VIH voltage level of G517F1T12U EN pin, DP\_DIGON is 1.8V level.

Layout Note: Close USB3.0 Port4



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Main Func = USB3.0 Re-driver



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Title

USB (USB Redriver/Hub)

SizeA4

Document Number

Rev

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