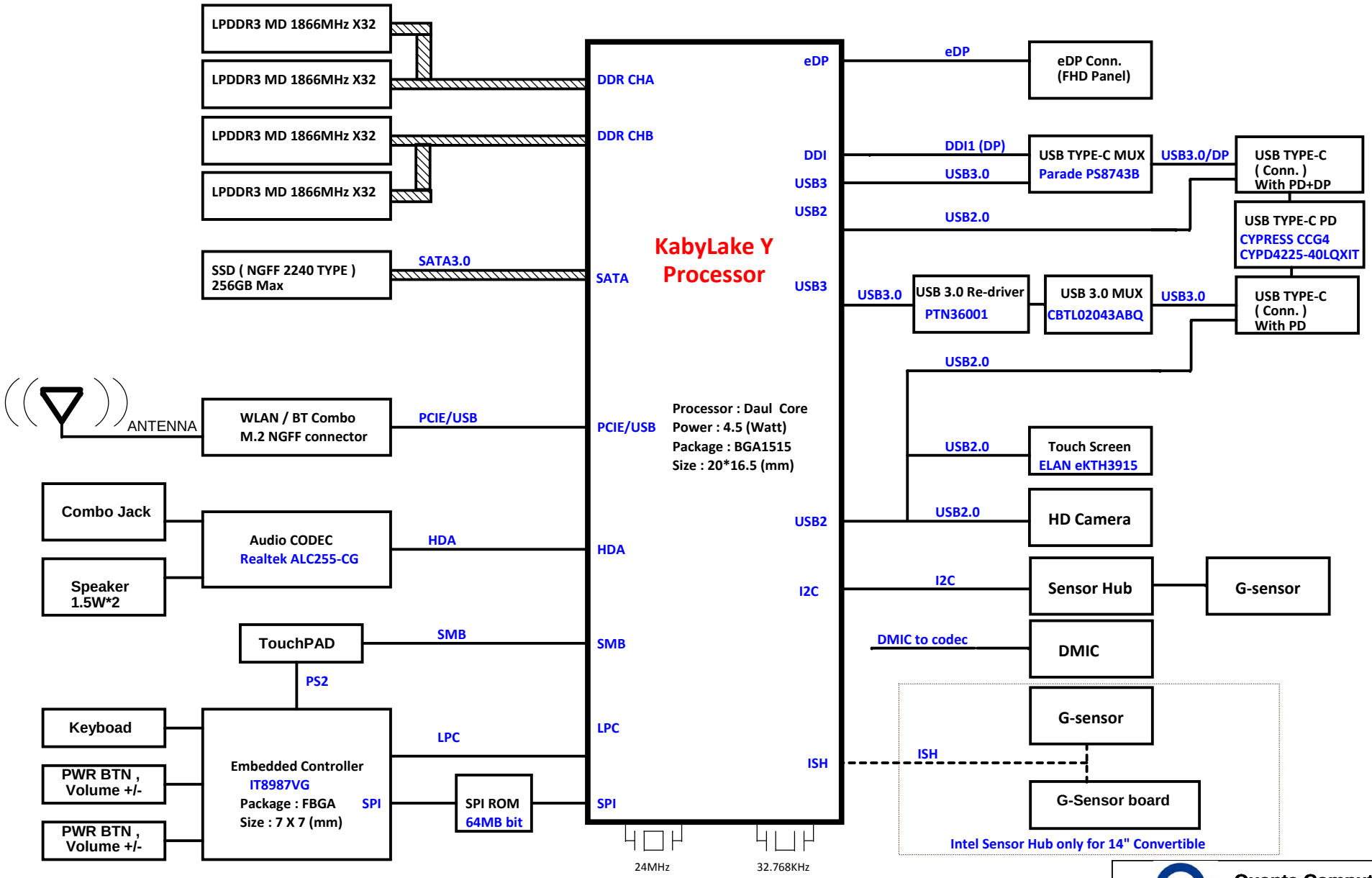
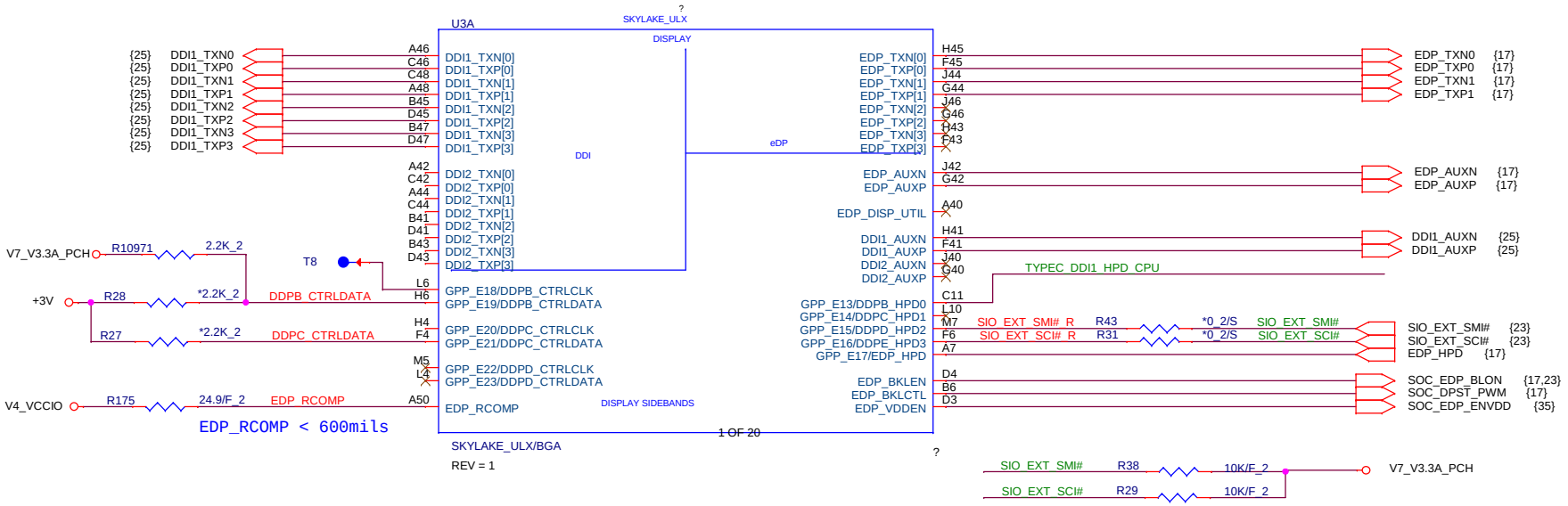
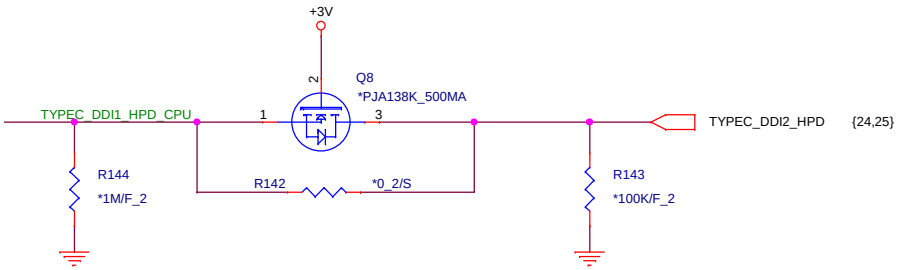
Block Diagram

01

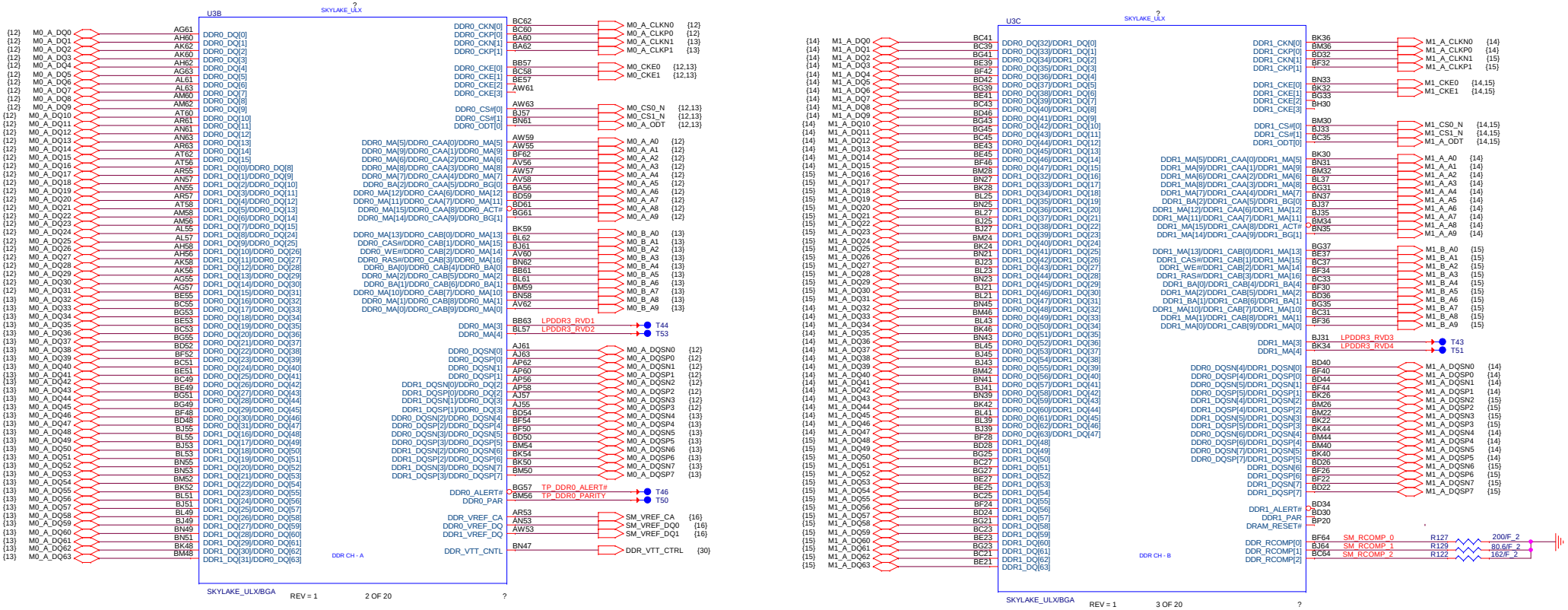


Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: Block Diagram	Rev: 1A
Date: Sunday, September 11, 2016	Sheet 1 of 42	





Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: SKL-Y CPU (DDI/EDP)	Rev: 1A
Date: Sunday, September 11, 2016	Sheet 2 of 42	

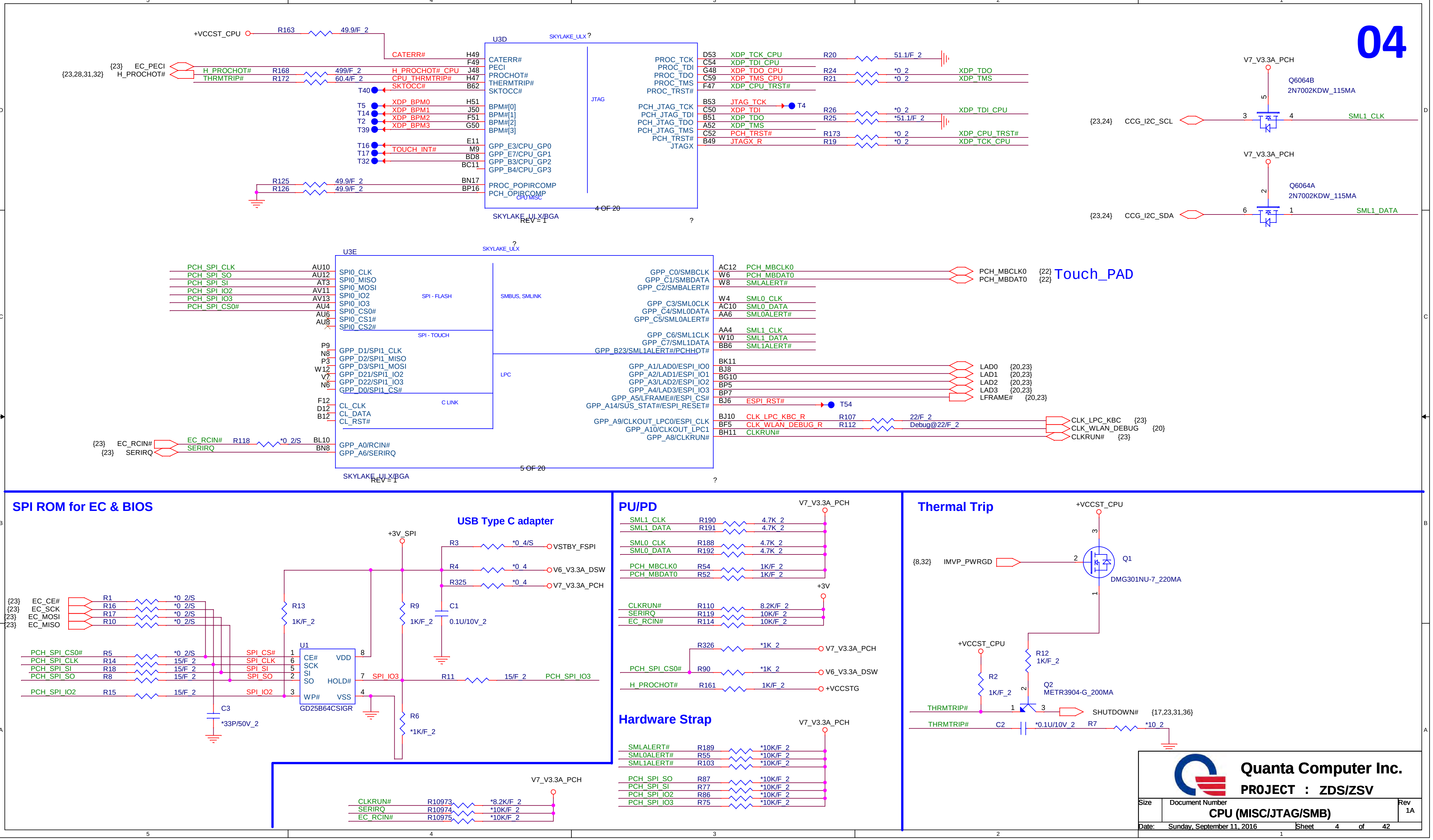


Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	SKL-Y CPU (Memory)	1A

Date: Sunday, September 11, 2016 Sheet 3 of 42



+VCCST_CPU
R163 49.9/F_2
U3D SKYLAKE_ULX
{23} EC_PECI
{23,28,31,32} H_PROCHOT#
H_PROCHOT# R168 499/F_2 H_PROCHOT#_CPU J48
THRMTRIP# R172 60.4/F_2 CPU_THRMTRIP# H47
CATERR# H49
F49
SKTOCC# B62
T40
T5 XDP_BPM0 H51
T14 XDP_BPM1 J50
T2 XDP_BPM2 F51
T39 XDP_BPM3 G50
T16 E11
T17 TOUCH_INT# M9
T32 BD8
BC11
R125 49.9/F_2 BN17
R126 49.9/F_2 BP16
CATERR#
PECI
PROCHOT#
THERMTRIP#
SKTOCC#
BPM#[0]
BPM#[1]
BPM#[2]
BPM#[3]
GPP_E3/CPU_GP0
GPP_E7/CPU_GP1
GPP_B3/CPU_GP2
GPP_B4/CPU_GP3
PROC_POPIRCOMP
PCH_OPIRCOMP
CPU MISC
JTAG
PROC_TCK
PROC_TDI
PROC_TDO
PROC_TMS
PROC_TRST#
PCH_JTAG_TCK
PCH_JTAG_TDI
PCH_JTAG_TDO
PCH_JTAG_TMS
PCH_TRST#
JTAGX
SKYLAKE_ULX/BGA
REV = 1
4 OF 20
D53 XDP_TCK_CPU R20 51.1/F_2
C54 XDP_TDI_CPU
G48 XDP_TDO_CPU R24 *0_2 XDP_TDO
C59 XDP_TMS_CPU R21 *0_2 XDP_TMS
F47 XDP_CPU_TRST#
B53 JTAG_TCK T4
C50 XDP_TDI R26 *0_2 XDP_TDI_CPU
B51 XDP_TDO R25 *51.1/F_2
A52 XDP_TMS
C52 PCH_TRST# R173 *0_2 XDP_CPU_TRST#
B49 JTAGX_R R19 *0_2 XDP_TCK_CPU
V7_V3.3A_PCH
Q6064B 2N7002KDW_115MA
{23,24} CCG_I2C_SCL SML1_CLK
V7_V3.3A_PCH
Q6064A 2N7002KDW_115MA
{23,24} CCG_I2C_SDA SML1_DATA
U3E SKYLAKE_ULX
PCH_SPI_CLK AU10
PCH_SPI_SO AU12
PCH_SPI_SI AT3
PCH_SPI_IO2 AV11
PCH_SPI_IO3 AV13
PCH_SPI_CS0# AU4
AU6
AU8
SPI0_CLK
SPI0_MISO
SPI0_MOSI
SPI0_IO2
SPI0_IO3
SPI0_CS0#
SPI0_CS1#
SPI0_CS2#
SPI - FLASH
SMBUS, SMLINK
SPI - TOUCH
P9 GPP_D1/SPI1_CLK
N8 GPP_D2/SPI1_MISO
P3 GPP_D3/SPI1_MOSI
W12 GPP_D21/SPI1_IO2
V7 GPP_D22/SPI1_IO3
N6 GPP_D0/SPI1_CS#
LPC
C LINK
F12 CL_CLK
D12 CL_DATA
B12 CL_RST#
{23} EC_RCIN# EC_RCIN# R118 *0_2/S BL10 GPP_A0/RCIN#
{23} SERIRQ SERIRQ BN8 GPP_A6/SERIRQ
SKYLAKE_ULX/BGA
REV = 1
5 OF 20
GPP_C0/SMBCLK AC12 PCH_MBCLK0
GPP_C1/SMBDATA W6 PCH_MBDAT0
GPP_C2/SMBALERT# W8 SMLALERT#
GPP_C3/SML0CLK W4 SML0_CLK
GPP_C4/SML0DATA AC10 SML0_DATA
GPP_C5/SML0ALERT# AA6 SML0ALERT#
GPP_C6/SML1CLK AA4 SML1_CLK
GPP_C7/SML1DATA W10 SML1_DATA
GPP_B23/SML1ALERT#/PCHHOT# BB6 SML1ALERT#
PCH_MBCLK0 {22}
PCH_MBDAT0 {22}
Touch_PAD
GPP_A1/LAD0/ESPI_IO0 BK11 LAD0 {20,23}
GPP_A2/LAD1/ESPI_IO1 BJ8 LAD1 {20,23}
GPP_A3/LAD2/ESPI_IO2 BG10 LAD2 {20,23}
GPP_A4/LAD3/ESPI_IO3 BP5 LAD3 {20,23}
GPP_A5/LFRAME#/ESPI_CS# BP7 LFRAME# {20,23}
GPP_A14/SUS_STAT#/ESPI_RESET# BJ6 ESPI_RST# T54
GPP_A9/CLKOUT_LPC0/ESPI_CLK BJ10 CLK_LPC_KBC_R R107 22/F_2 CLK_LPC_KBC {23}
GPP_A10/CLKOUT_LPC1 BF5 CLK_WLAN_DEBUG_R R112 Debug@22/F_2 CLK_WLAN_DEBUG {20}
GPP_A8/CLKRUN# BH11 CLKRUN# CLKRUN# {23}
SPI ROM for EC & BIOS
USB Type C adapter
+3V_SPI
R3 *0_4/S VSTBY_FSPI
R4 *0_4 V6_V3.3A_DSW
R325 *0_4 V7_V3.3A_PCH
{23} EC_CE# R1 *0_2/S
{23} EC_SCK R16 *0_2/S
{23} EC_MOSI R17 *0_2/S
{23} EC_MISO R10 *0_2/S
R13 1K/F_2
R9 1K/F_2
C1 0.1U/10V_2
PCH_SPI_CS0# R5 *0_2/S SPI_CS#
PCH_SPI_CLK R14 15/F_2 SPI_CLK
PCH_SPI_SI R18 15/F_2 SPI_SI
PCH_SPI_SO R8 15/F_2 SPI_SO
PCH_SPI_IO2 R15 15/F_2 SPI_IO2
U1 GD25B64CSIGR
CE# SCK SI SO WP# VDD HOLD# VSS
SPI_IO3 R11 15/F_2 PCH_SPI_IO3
C3 *33P/50V_2
R6 *1K/F_2
PU/PD
V7_V3.3A_PCH
SML1_CLK R190 4.7K_2
SML1_DATA R191 4.7K_2
SML0_CLK R188 4.7K_2
SML0_DATA R192 4.7K_2
PCH_MBCLK0 R54 1K/F_2
PCH_MBDAT0 R52 1K/F_2
+3V
CLKRUN# R110 8.2K/F_2
SERIRQ R119 10K/F_2
EC_RCIN# R114 10K/F_2
R326 *1K_2 V7_V3.3A_PCH
PCH_SPI_CS0# R90 *1K_2 V6_V3.3A_DSW
H_PROCHOT# R161 1K/F_2 +VCCSTG
Hardware Strap
V7_V3.3A_PCH
SMLALERT# R189 *10K/F_2
SML0ALERT# R55 *10K/F_2
SML1ALERT# R103 *10K/F_2
PCH_SPI_SO R87 *10K/F_2
PCH_SPI_SI R77 *10K/F_2
PCH_SPI_IO2 R86 *10K/F_2
PCH_SPI_IO3 R75 *10K/F_2
V7_V3.3A_PCH
CLKRUN# R10973 *8.2K/F_2
SERIRQ R10974 *10K/F_2
EC_RCIN# R10975 *10K/F_2
Thermal Trip
+VCCST_CPU
{8,32} IMVP_PWRGD
Q1 DMG301NU-7_220MA
+VCCST_CPU
R12 1K/F_2
R2 1K/F_2
Q2 METR3904-G_200MA
THRMTRIP# SHUTDOWN# {17,23,31,36}
THRMTRIP# C2 *0.1U/10V_2 R7 *10_2
PCH_SPI_CS0#

Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	CPU (MISC/JTAG/SMB)	1A

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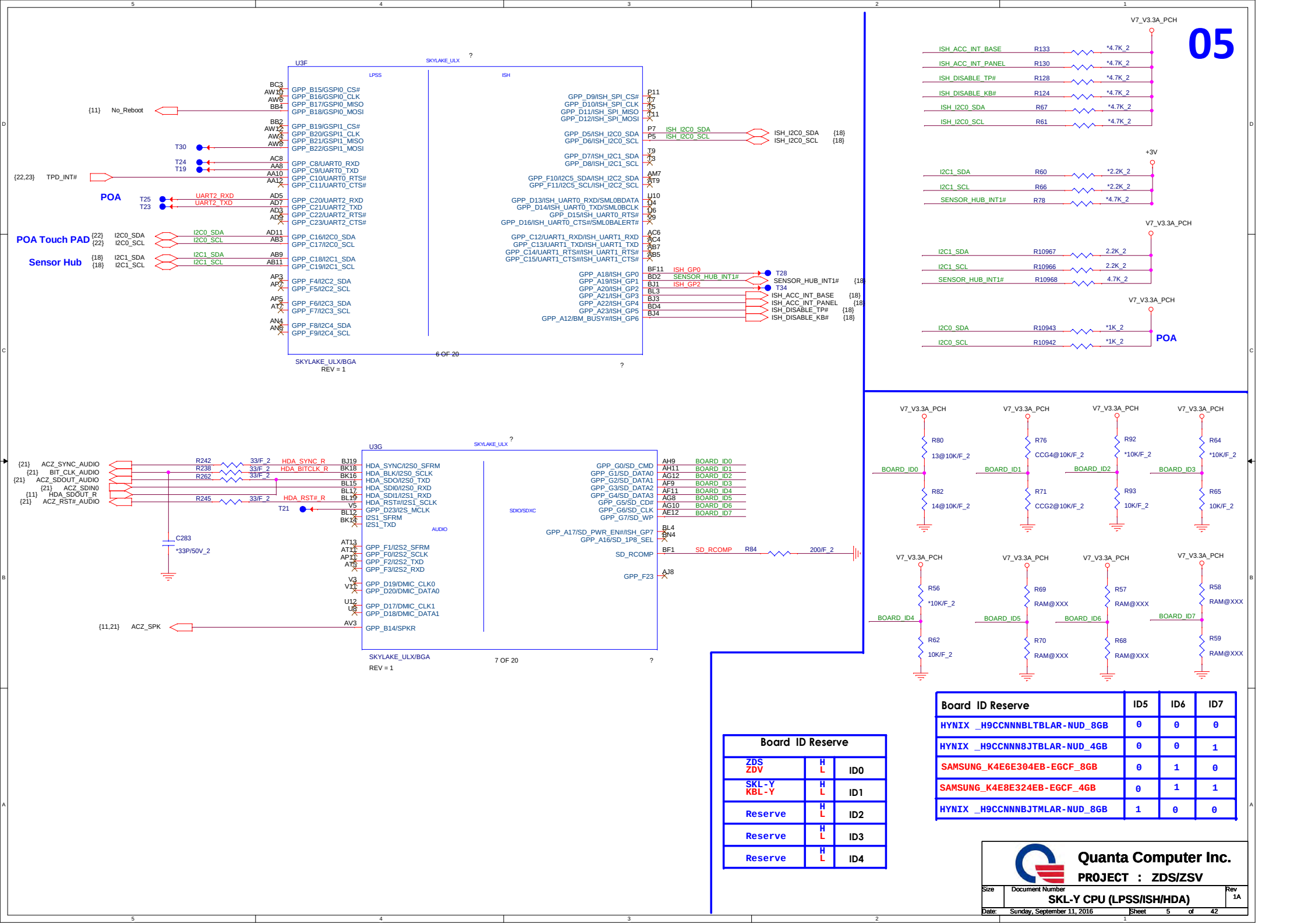

Board ID Reserve		
ZDS ZDV	H L	ID0
SKL-Y KBL-Y	H L	ID1
Reserve	H L	ID2
Reserve	H L	ID3
Reserve	H L	ID4

Board ID Reserve	ID5	ID6	ID7
HYNIX _H9CCNNNBLTBLAR-NUD_8GB	0	0	0
HYNIX _H9CCNNN8JTBLAR-NUD_4GB	0	0	1
SAMSUNG_K4E6E304EB-EGCF_8GB	0	1	0
SAMSUNG_K4E8E324EB-EGCF_4GB	0	1	1
HYNIX _H9CCNNNBJTMLAR-NUD_8GB	1	0	0

Quanta Computer Inc.
PROJECT : ZDS/ZSV

Size	Document Number	Rev
	SKL-Y CPU (LPSS/ISH/HDA)	1A
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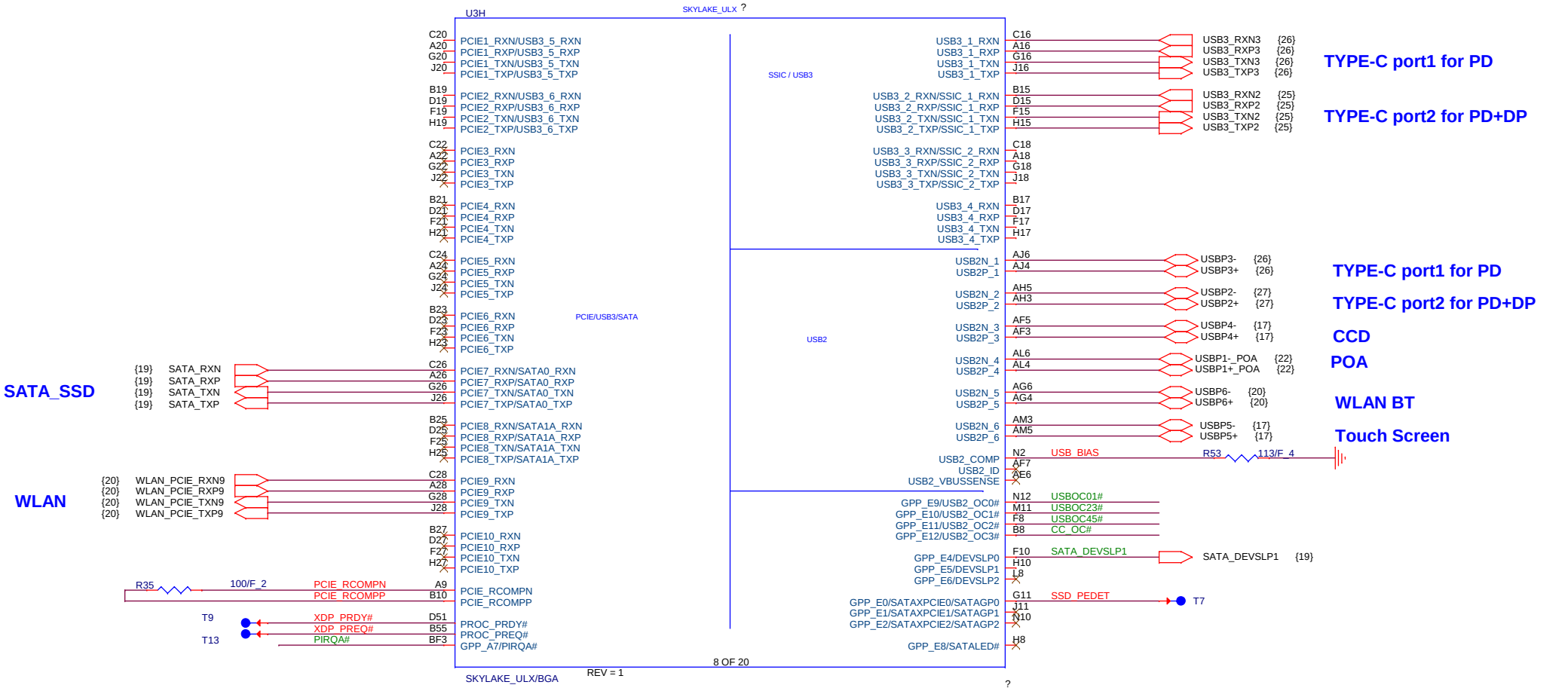


SATA_SSD

WLAN

TYPE-C port1 for PD

TYPE-C port2 for PD+DP

TYPE-C port1 for PD

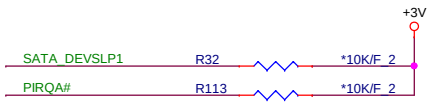
TYPE-C port2 for PD+DP

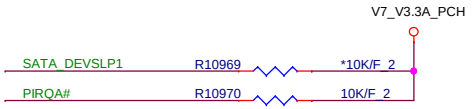
CCD

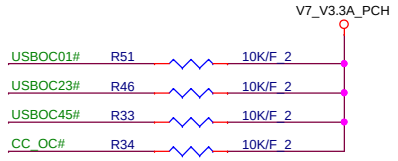
POA

WLAN BT

Touch Screen







Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: SKL-Y CPU (PCIE/USB3)	Rev: 1A
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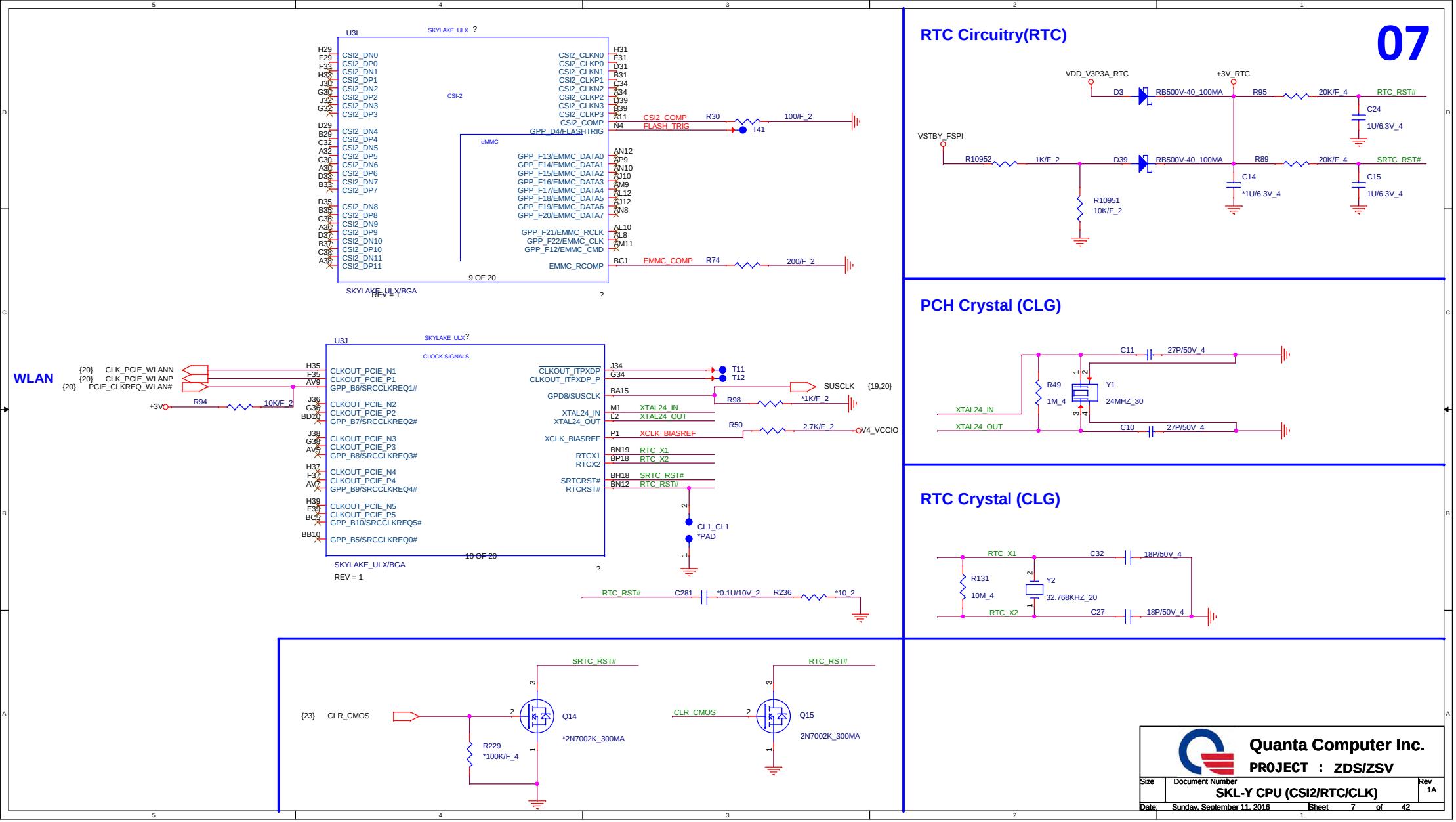

RTC Circuitry(RTC)

07

PCH Crystal (CLG)

RTC Crystal (CLG)

Quanta Computer Inc. PROJECT : ZDS/ZSV		
Size	Document Number: SKL-Y CPU (CSI2/RTC/CLK)	Rev: 1A
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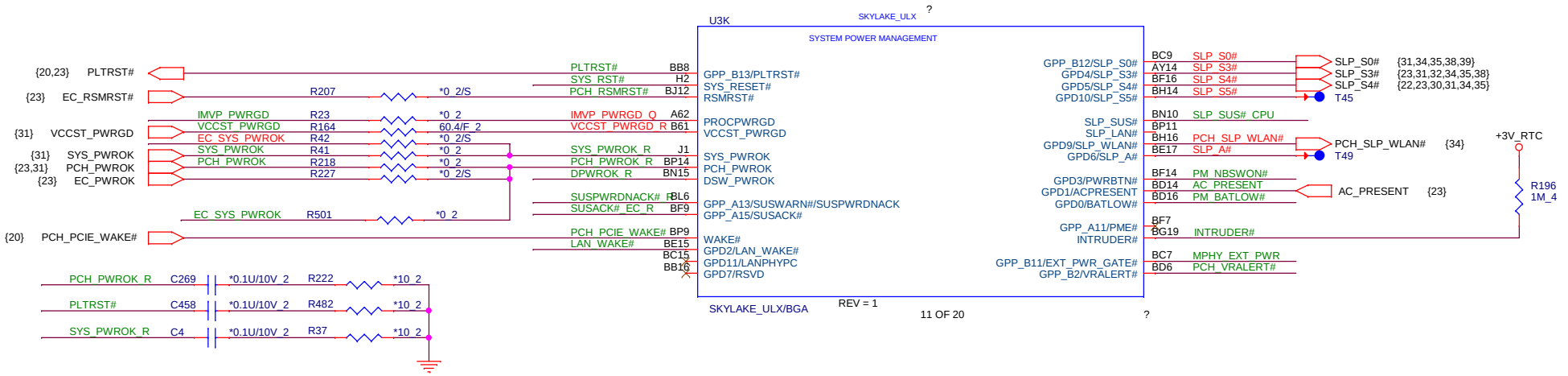
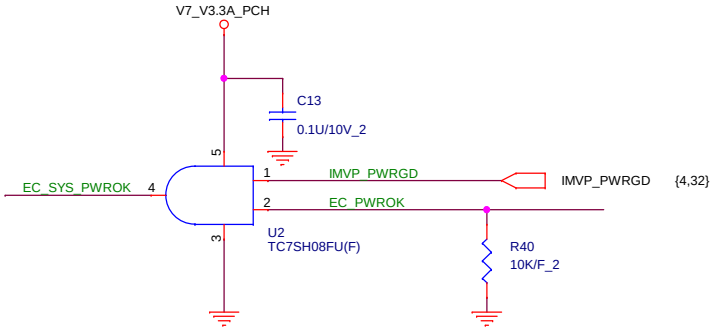
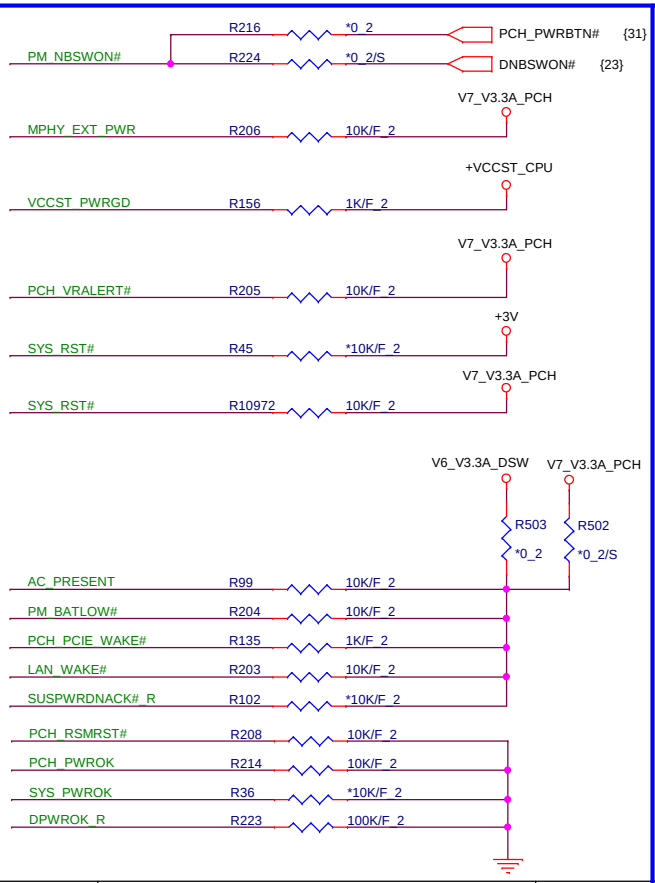
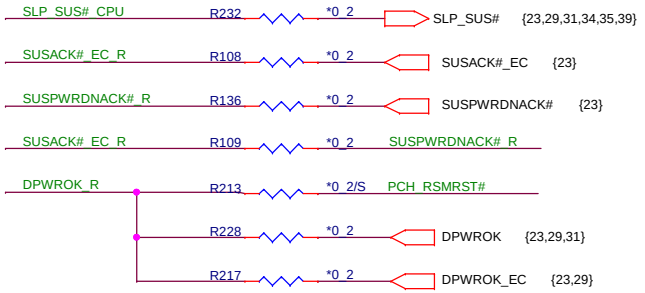




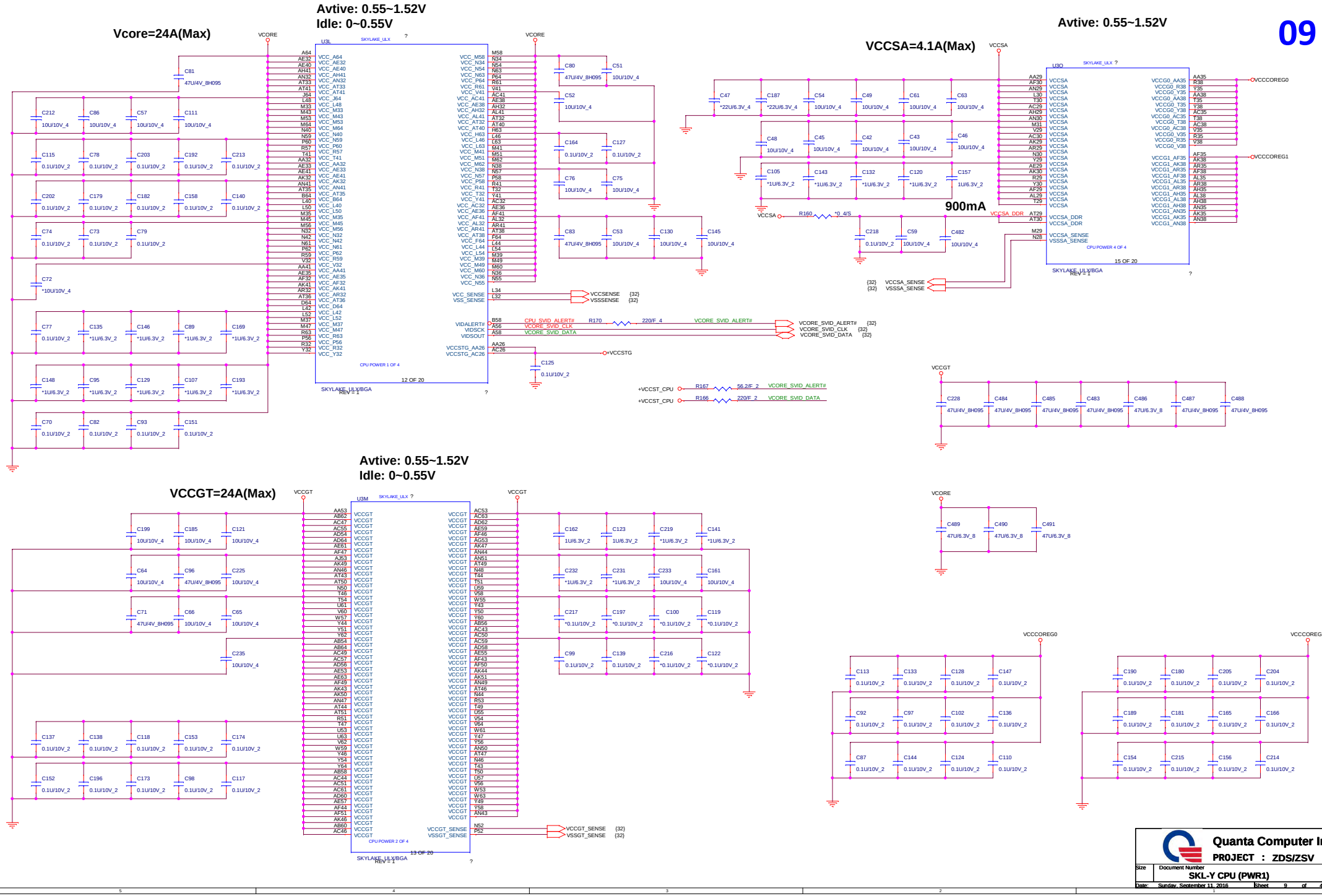




Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	SKL-Y CPU (PWR MANAGE)	1A
Date: Sunday, September 11, 2016	Sheet 8 of 42	


Avtive: 0.55~1.52V
Idle: 0~0.55V
Vcore=24A(Max)
VCORE
U3L
SKYLAKE_ULX
CPU POWER 1 OF 4
12 OF 20
SKYLAKE_ULX/BGA
REV = 1
VCCSENSE {32}
VSSSENSE {32}
VIDALERT#
VIDSCK
VIDSOUT
CPU_SVID_ALERT#
VCORE_SVID_CLK
VCORE_SVID_DATA
R170 220/F_4
VCORE_SVID_ALERT#
VCORE_SVID_ALERT# {32}
VCORE_SVID_CLK {32}
VCORE_SVID_DATA {32}
VCCSTG_AA26
VCCSTG_AC26
+VCCSTG
+VCCST_CPU
R167 56.2/F_2 VCORE_SVID_ALERT#
R166 220/F_2 VCORE_SVID_DATA
Avtive: 0.55~1.52V
VCCSA=4.1A(Max)
VCCSA
U3O
CPU POWER 4 OF 4
15 OF 20
SKYLAKE_ULX/BGA
REV = 1
900mA
VCCSA_DDR
R160 *0_4/S
VCCSA_SENSE {32}
VSSSA_SENSE {32}
VCCCOREG0
VCCCOREG1
VCCGT
VCCGT=24A(Max)
U3M
CPU POWER 2 OF 4
13 OF 20
VCCGT_SENSE {32}
VSSGT_SENSE {32}
Avtive: 0.55~1.52V
Idle: 0~0.55V
09
Quanta Computer Inc.
PROJECT : ZDS/ZSV
SKL-Y CPU (PWR1)
Rev 1A
Date: Sunday, September 11, 2016
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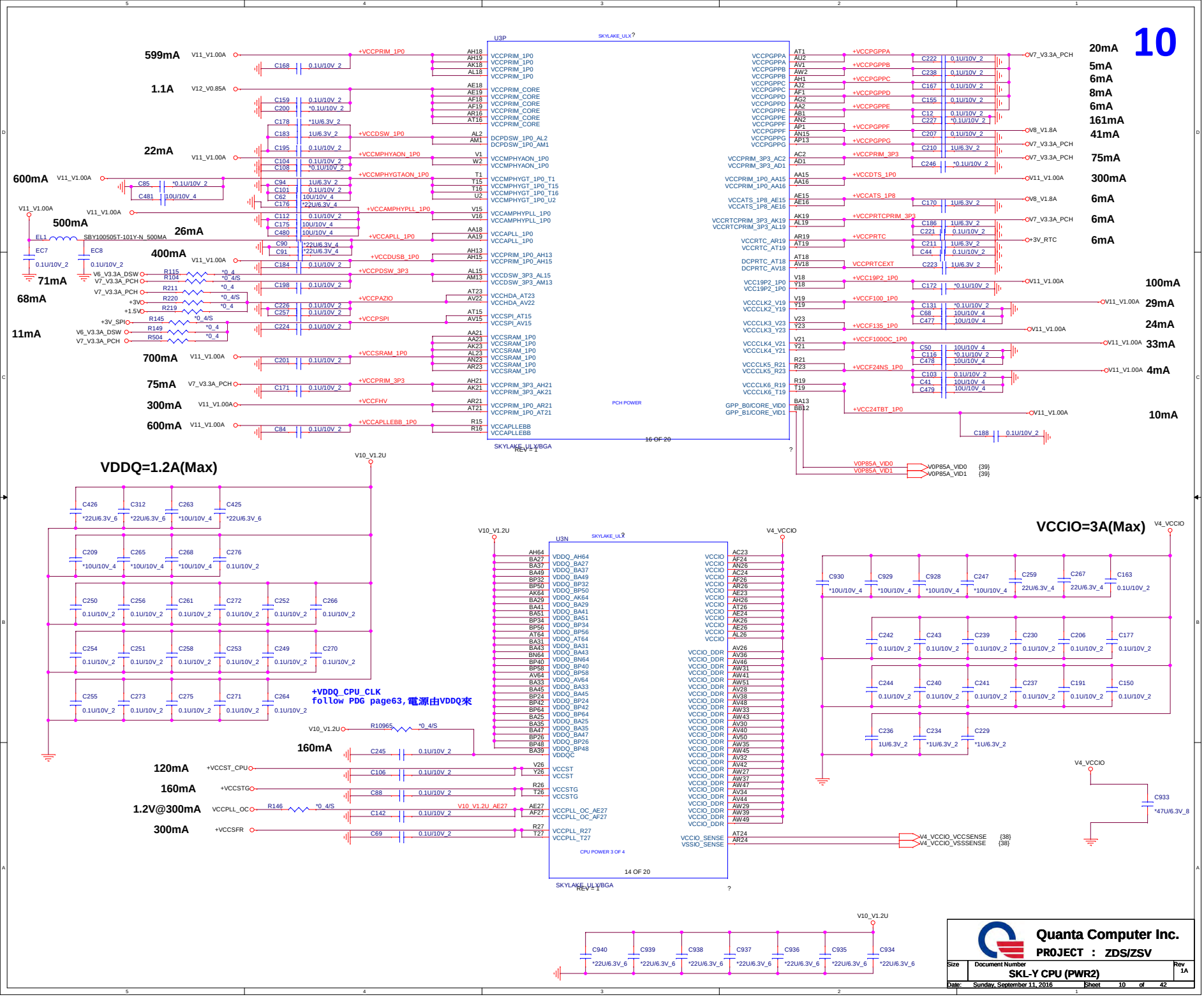
10
U3P
SKYLAKE_ULX
PCH POWER
16 OF 20
SKYLAKE_ULX/BGA
REV - 1
599mA
1.1A
22mA
600mA
500mA
26mA
400mA
71mA
68mA
11mA
700mA
75mA
300mA
600mA
20mA
5mA
6mA
8mA
6mA
161mA
41mA
75mA
300mA
6mA
6mA
6mA
100mA
29mA
24mA
33mA
4mA
10mA
VDDQ=1.2A(Max)
+VDDQ_CPU_CLK
follow PDG page63,電源由VDDQ來
U3N
CPU POWER 3 OF 4
14 OF 20
160mA
120mA
160mA
1.2V@300mA
300mA
VCCIO=3A(Max)
Quanta Computer Inc.
PROJECT : ZDS/ZSV
SKL-Y CPU (PWR2)
Size
Document Number
Rev
1A
Date: Sunday, September 11, 2016
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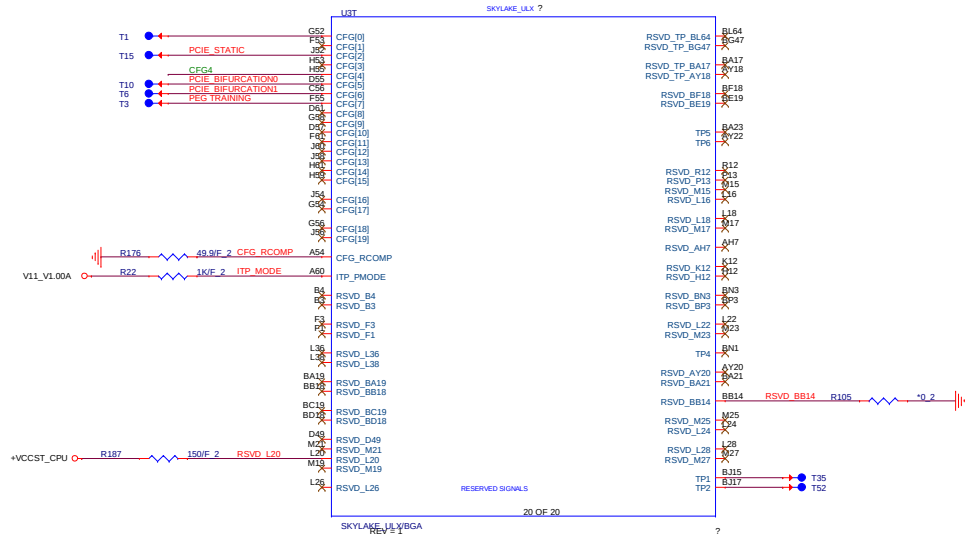

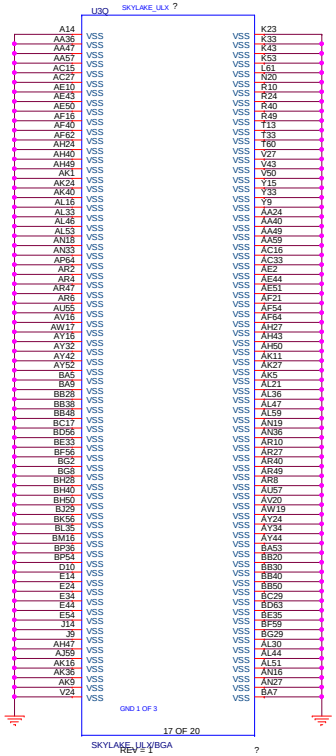

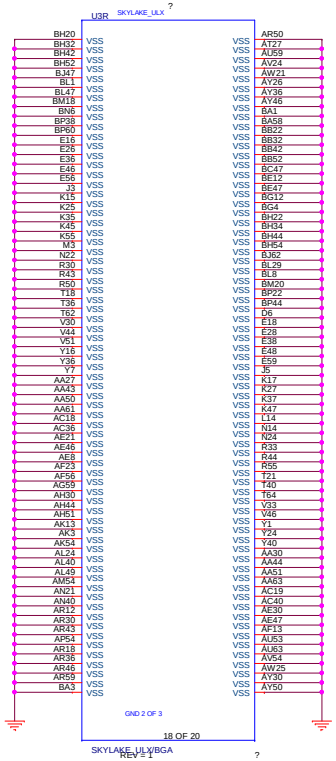

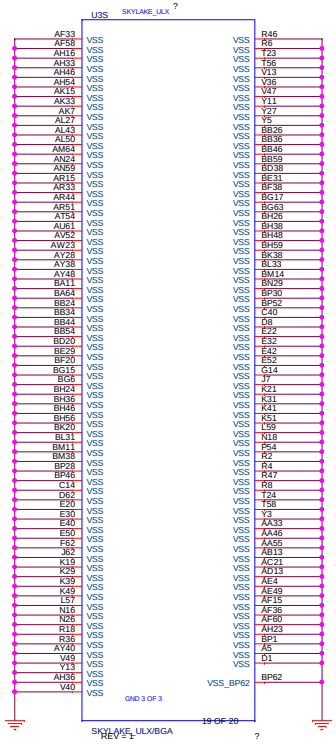

Processor Strapping

Pin Name	Strap description	Configuration	Note
CFG[0]	Stall reset sequence after PCU PLL lock until de-asserted	**1 = *Normal Operation** 0 = Stall	
CFG[1]	Reserved Configuration lane		
CFG[2]	PCI Express* Static x16 Lane Numbering Reversal	**1 = *Normal Operation** 0 = Lan number reversed	
CFG[3]	Reserved Configuration lane		
CFG[4]	eDP enable	1 = Disabled **0 = Enabled**	CFG4 R183 1K/F_2
CFG[6:5]	PCI Express* Bifunction	00 = 1x8, 2x4 PCI Express* 01 = reserved 10 = 2x8 PCI Express* 11 = 1x16 PCI Express*	
CFG[7]	PEG Training	**1 = *PEG Train immediatedly follow RESET# de-assertion** 0 = PEG wait for BIOS for training	
CFG[19:8]	Reserved Configuration lane		
SPKR / GPP_B14	Top-Block Swap Override	1 = Enable **0 = Disable**	{5,21} ACZ_SPK R83 *1K_2 +3V
GSPI0_MOSI /GPP_B18	No Reboot	1 = Enable **0 = Disable**	{5} No_Reboot R97 *1K_2 +3V
SMBALERT# /GPP_C2	eSPI or LPC	1 = Enable **0 = Disable**	Reserve Pull-high on Page.4
Boot BIOS Strap Bit /GPP_B22	Boot BIOS Strap Bit (BBS)	1 = LPC **0 = SPI**	default:SPI
SML0ALERT# / GPP_C5	eSPI or LPC	1 = eSPI **0 = LPC**	Reserve Pull-high on Page.4(default:LPC)
SML1ALERT# / PCHHOT#/ GPP_B23			Reserve Pull-high on Page.6
SPI0_MOSI			Reserve Pull-high on Page.4
SPI0_MISO			Reserve Pull-high on Page.4
SPI0_IO2			Reserve Pull-high on Page.4
SPI0_IO3			Reserve Pull-high on Page.4
HDA_SDO/ I2S_TXD0	Flash Descriptor Security Override	1 = Disable **0 = Enable**	{5} HDA_SDOUT_R R244 1K/F_2 ME_WR# {23}
DDPB_CTRLDATA / GPP_E19	Port B Detected	1 = Enable **0 = Disable**	Already Pull-high on Page.2
DDPC_CTRLDATA / GPP_E21	Port C Detected	1 = Enable **0 = Disable**	Reserve Pull-high on Page.2

Quanta Computer Inc.
PROJECT : ZDS/ZSV

Size	Document Number	Rev
	SKL-Y CPU (VSS/CFG/RSVD)	1A

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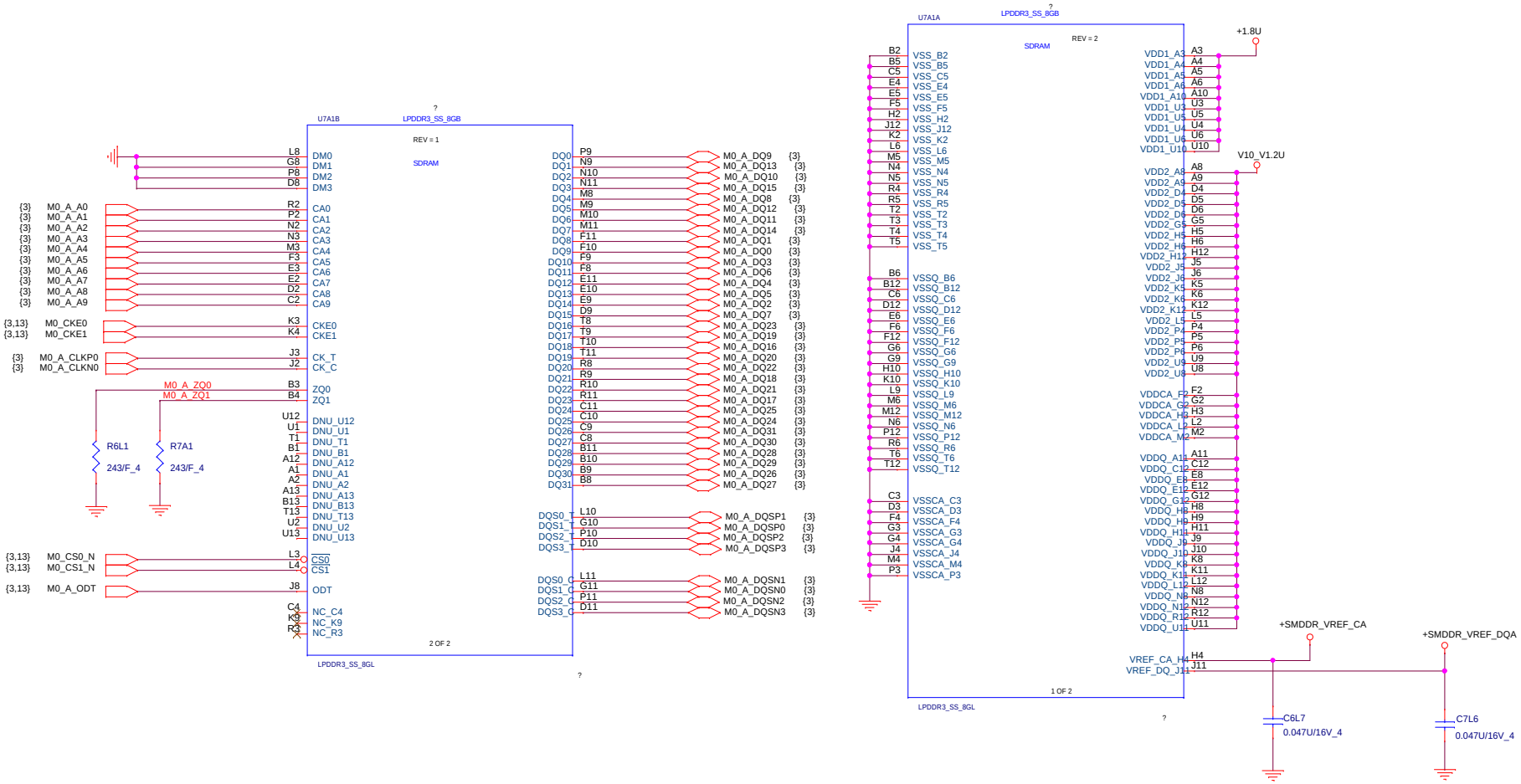
LPDDR3 MEMORY CHANNEL A-1



Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	LPDDR3 (CHA)	1A

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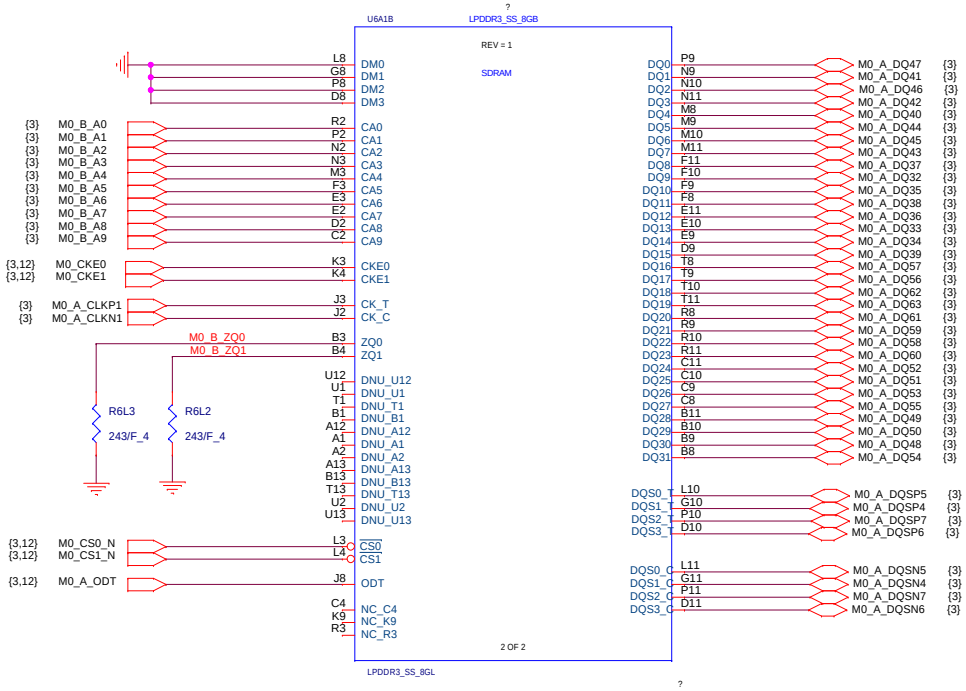
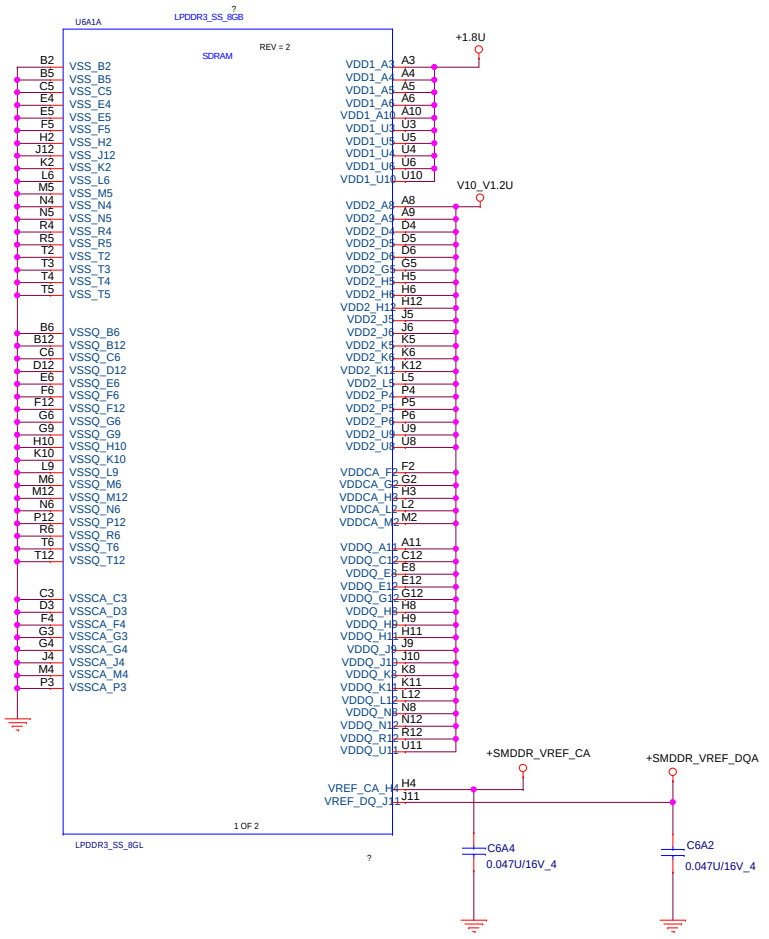
LPDDR3 MEMORY CHANNEL A-2





Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: LPDDR3 (CHA)-2	Rev: 1A
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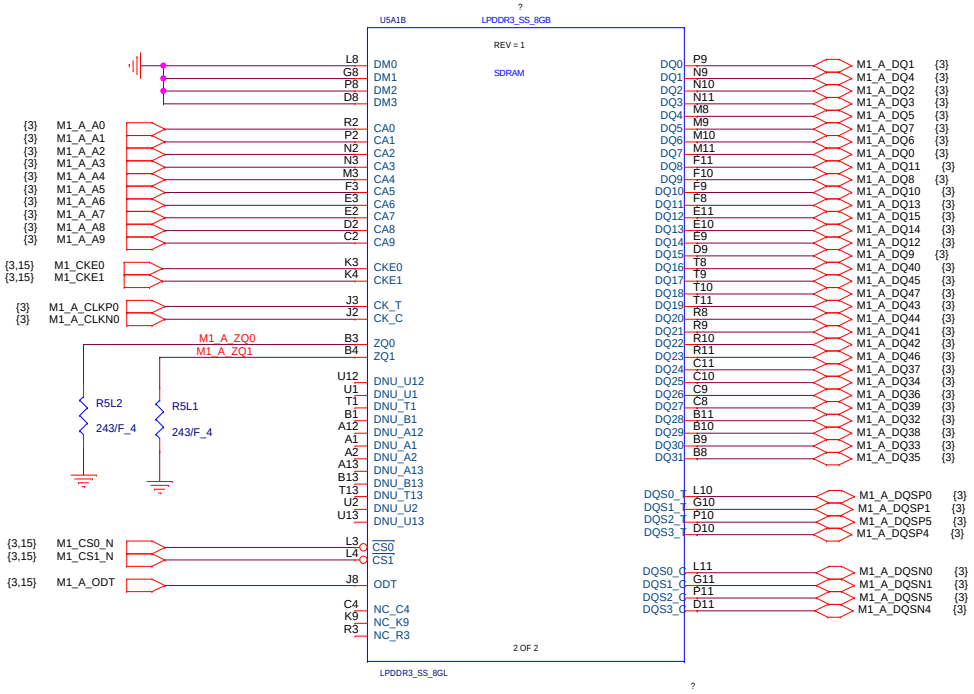
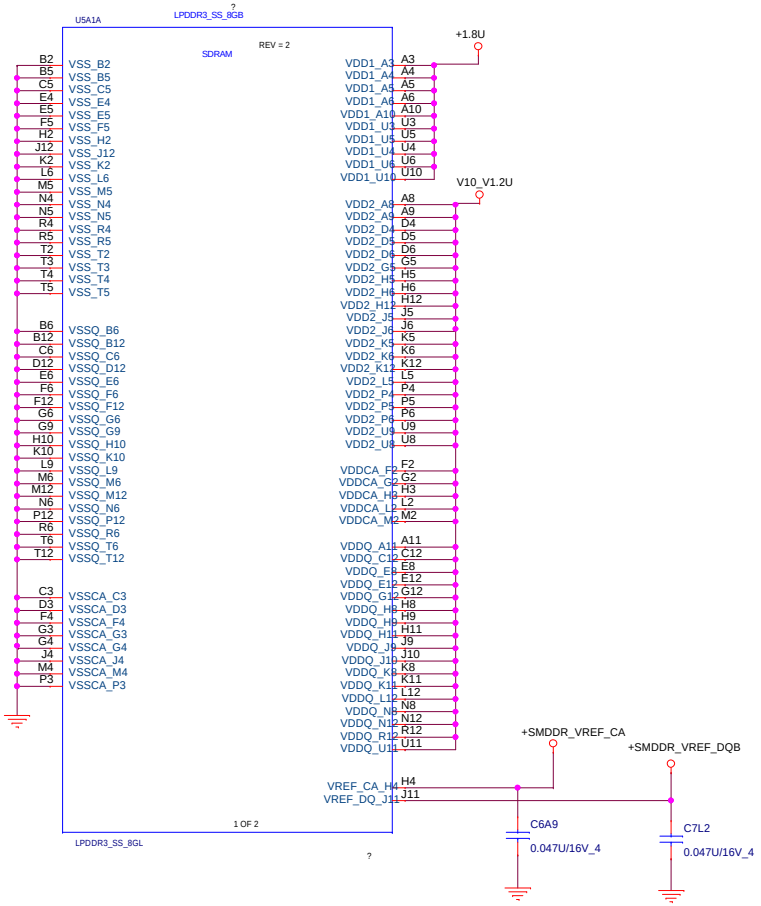
LPDDR3 MEMORY CHANNEL B-1





Quanta Computer Inc.		
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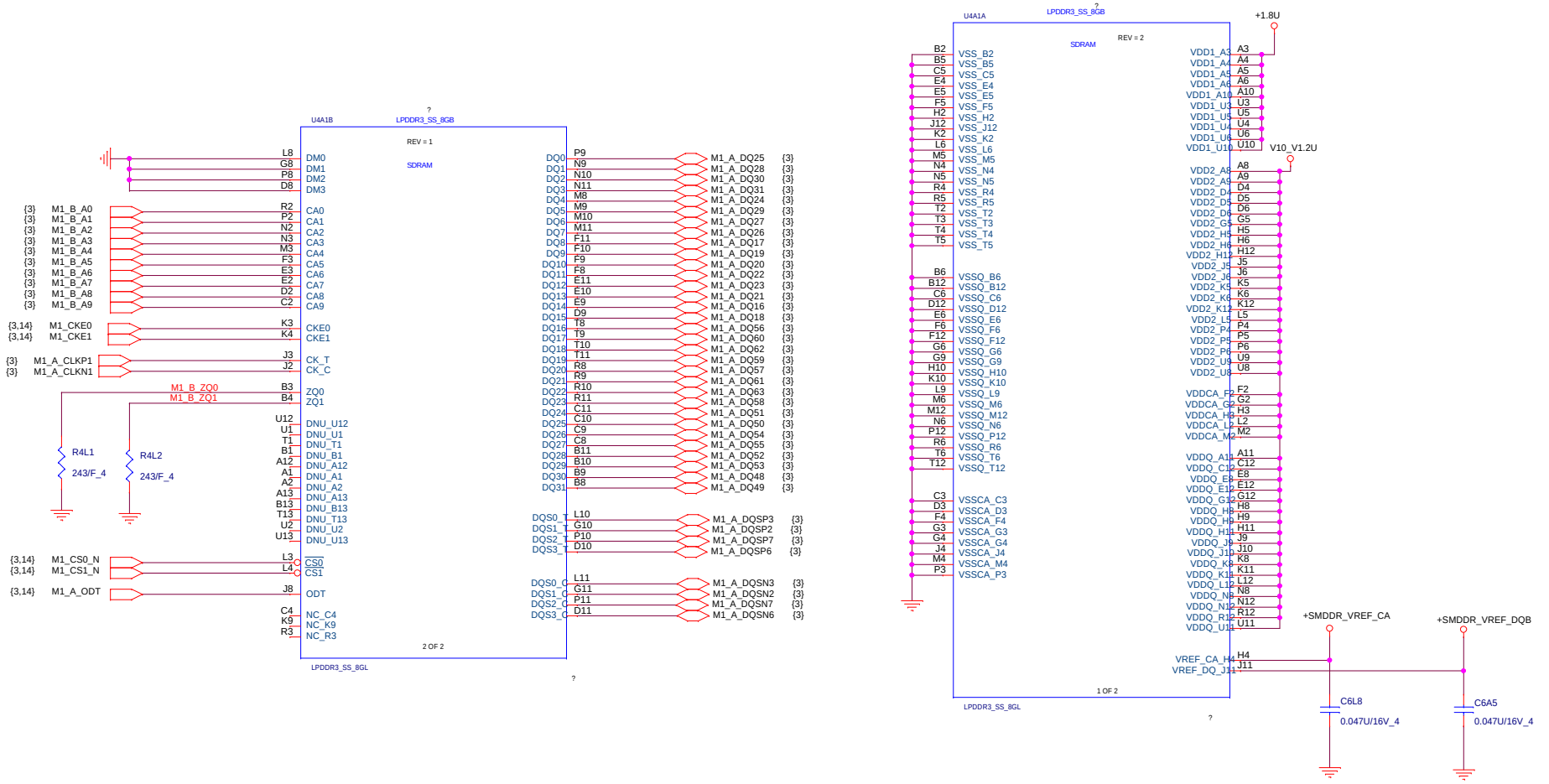
LPDDR3 MEMORY CHANNEL B-1



Quanta Computer Inc.	
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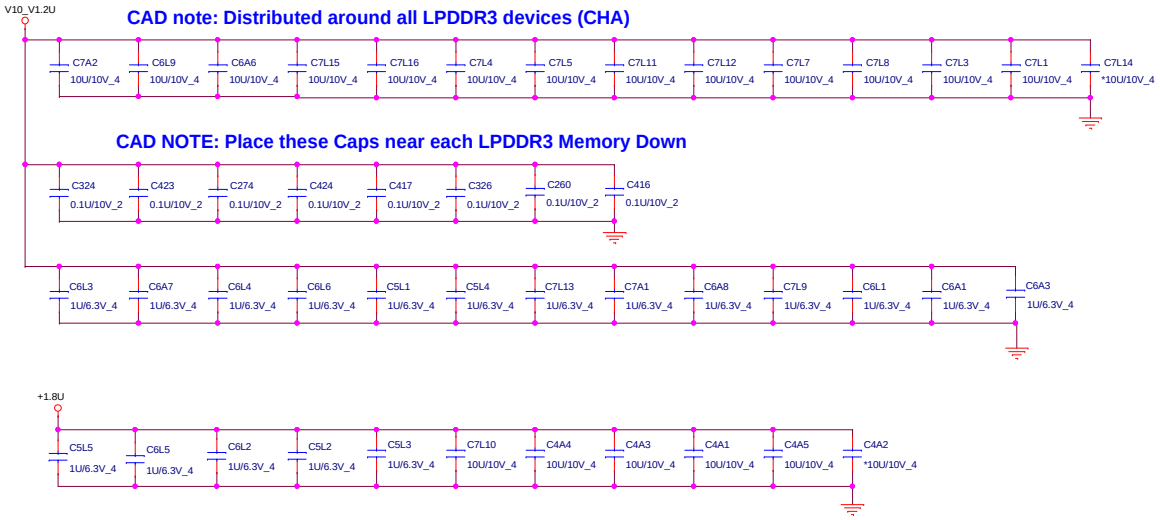
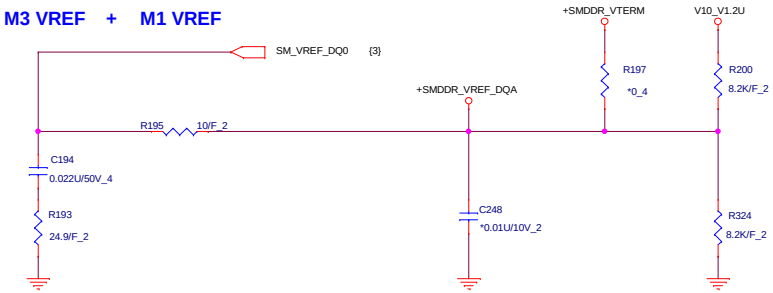
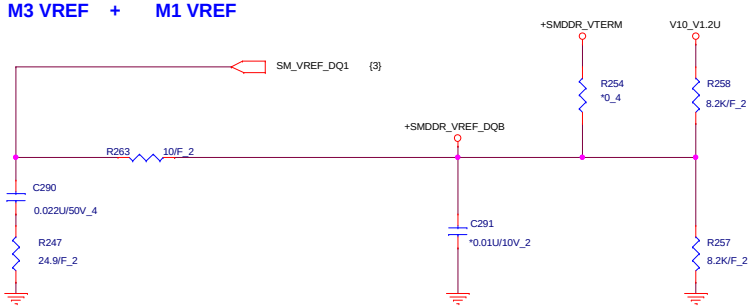
DE-CAPS FOR MEMORY CHANNEL A/B



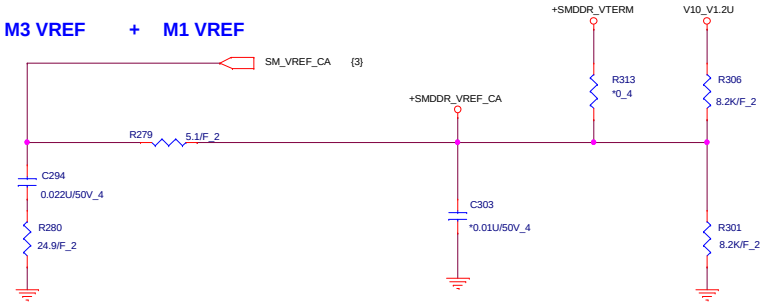
M3 VREF + M1 VREF



M3 VREF + M1 VREF



M3 VREF + M1 VREF



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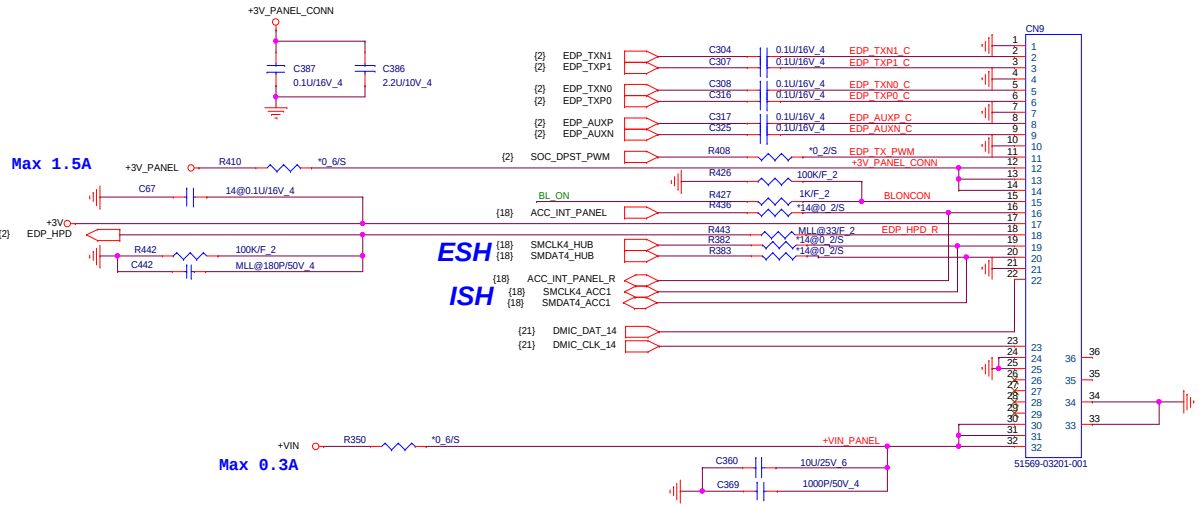
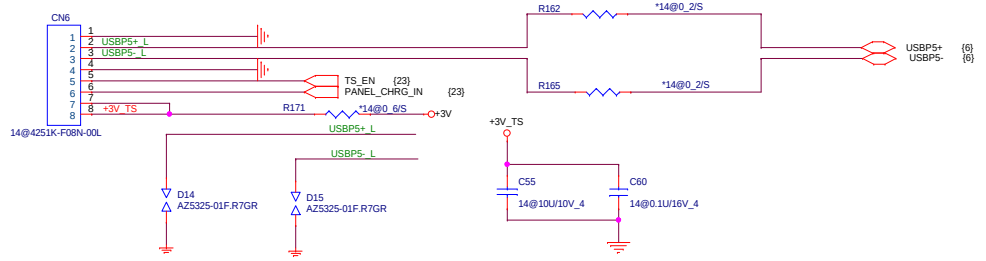
eDP/Camera Panel Conn.

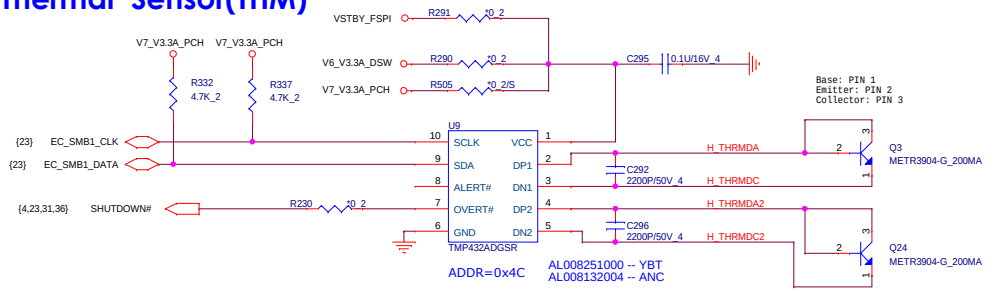


Touch Screen on DB

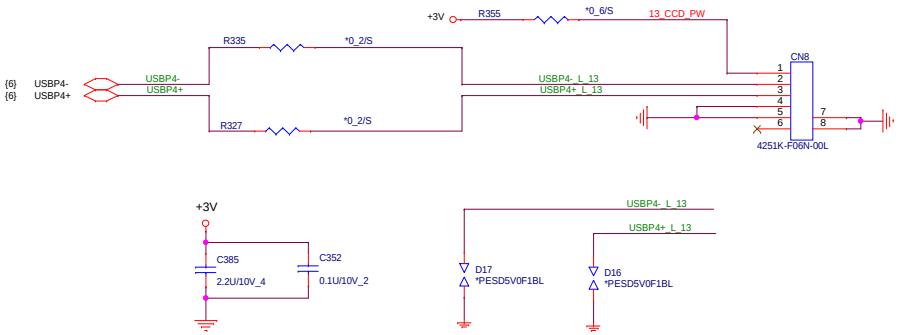


17

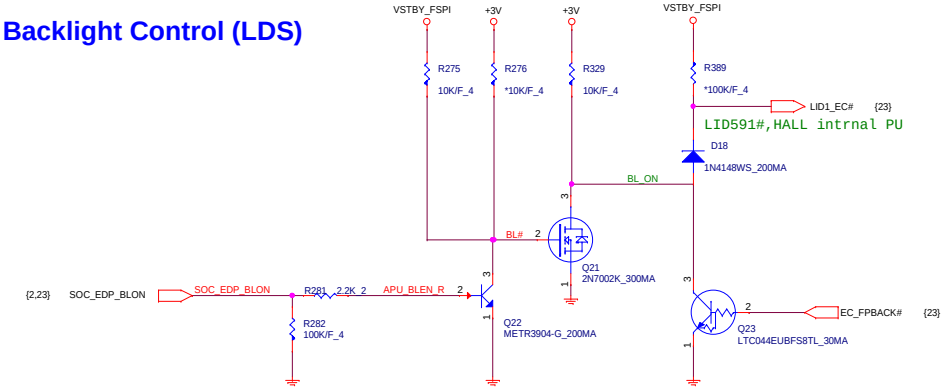
Thermal Sensor(THM)



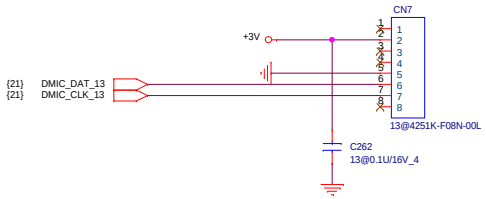
CCD



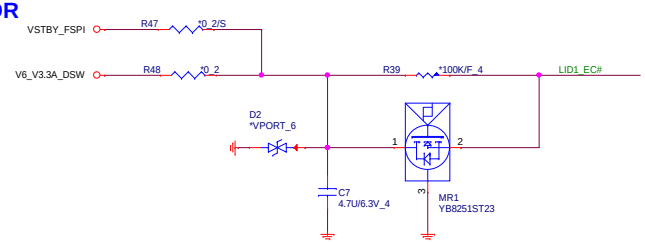
Backlight Control (LDS)



13" Daul MIC



LID1 SENSOR



Quanta Computer Inc.
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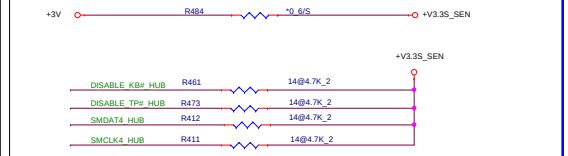
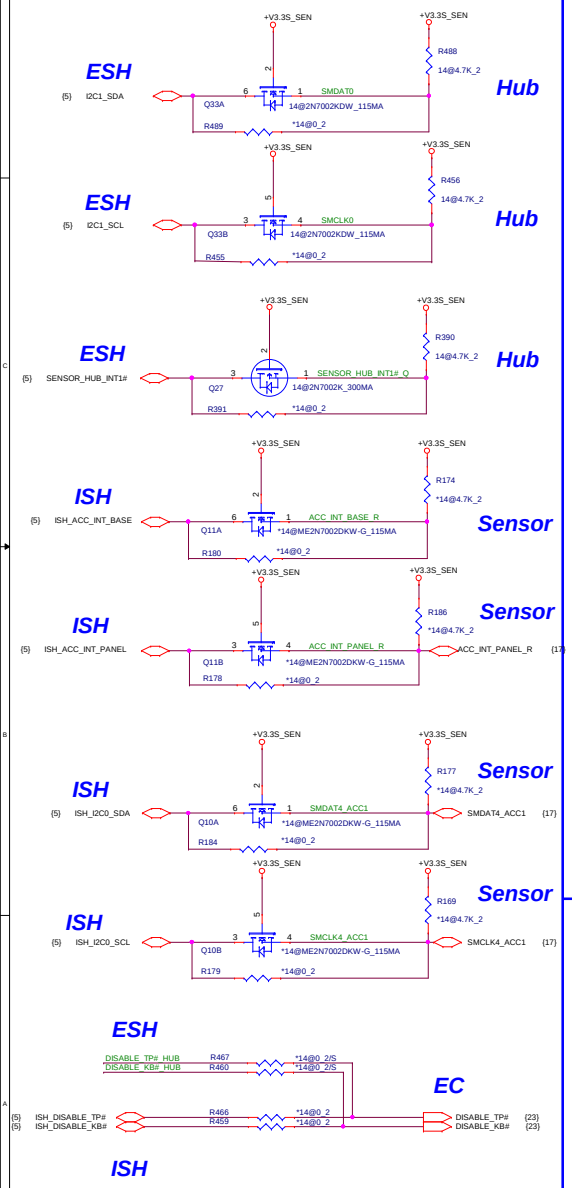
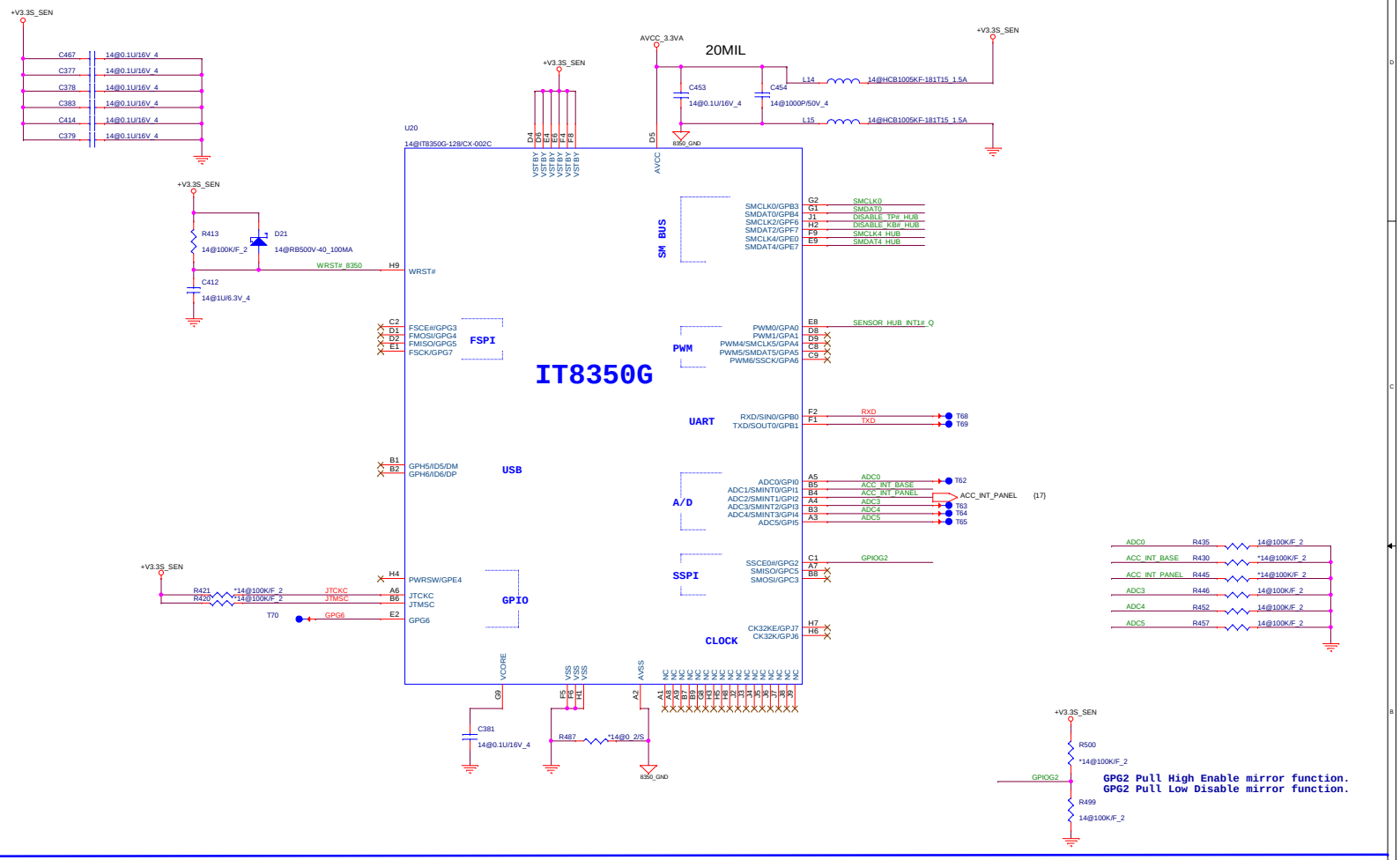
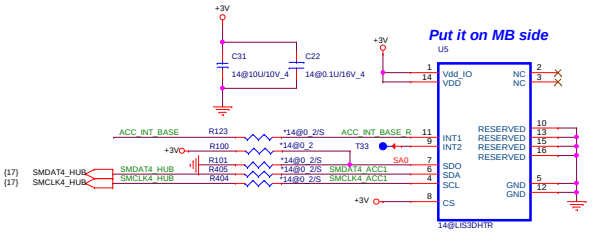
Sensor Hub







Accelerometer Sensor on MB



Quanta Computer Inc.
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	Button	1A
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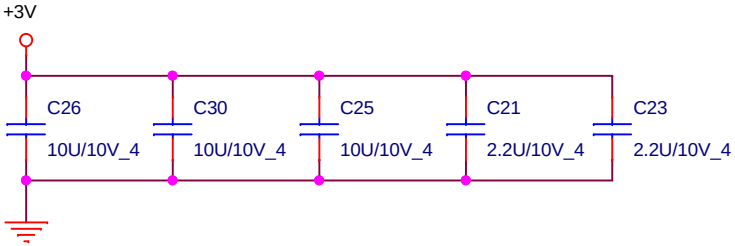
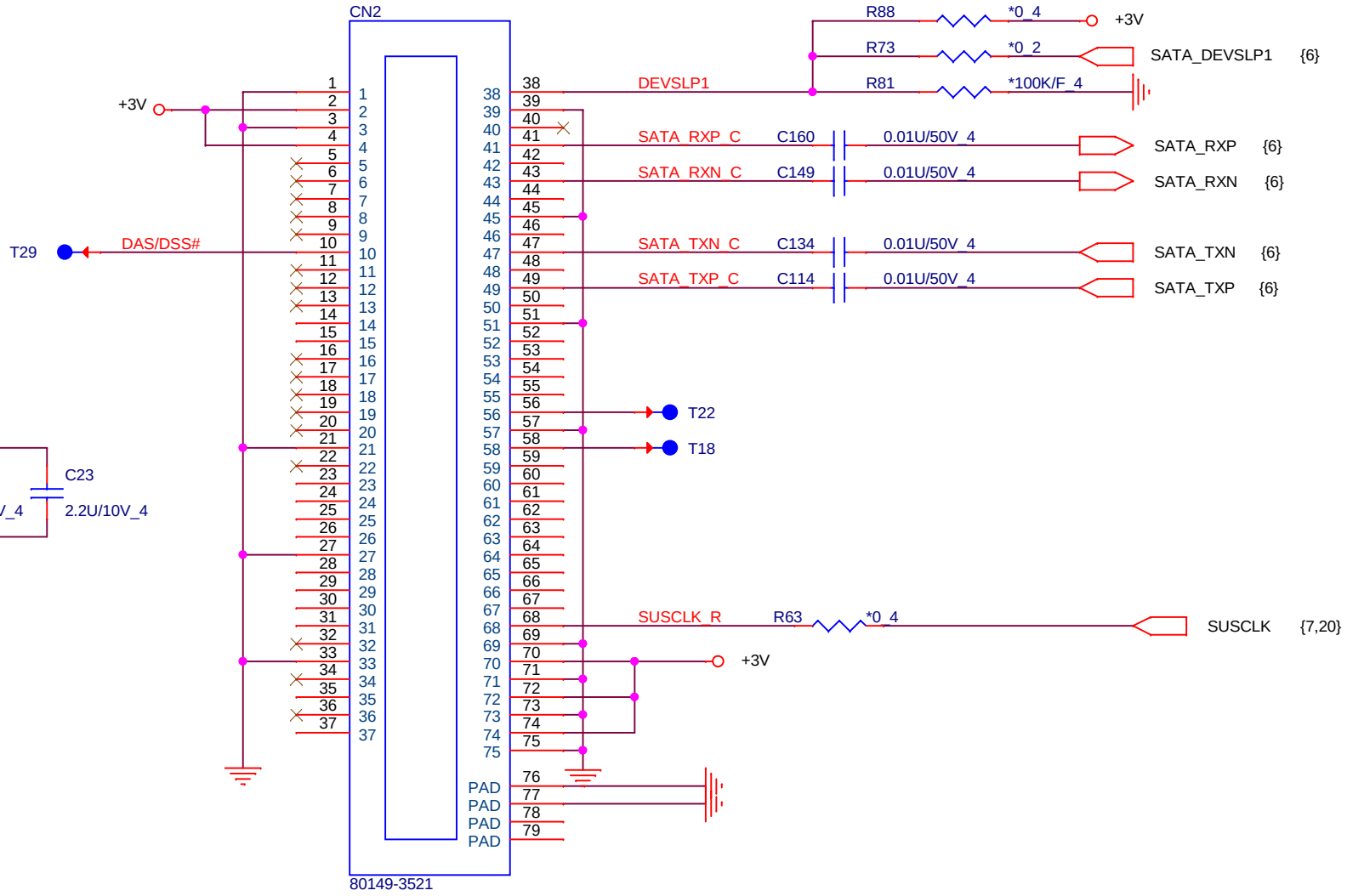
CONN: B KEY
MODULE: B KEY

Func.	CPU TX	CPU RX
PCIE2	0.1uF	0ohm
PCIE3	0.22uF	0ohm
SATA	0.01uF	0.01uF
PCIE2/SATA	0.1uF	0ohm
PCIE3/SATA	0.22uF	0ohm

2.8A(Max)





Quanta Computer Inc.		
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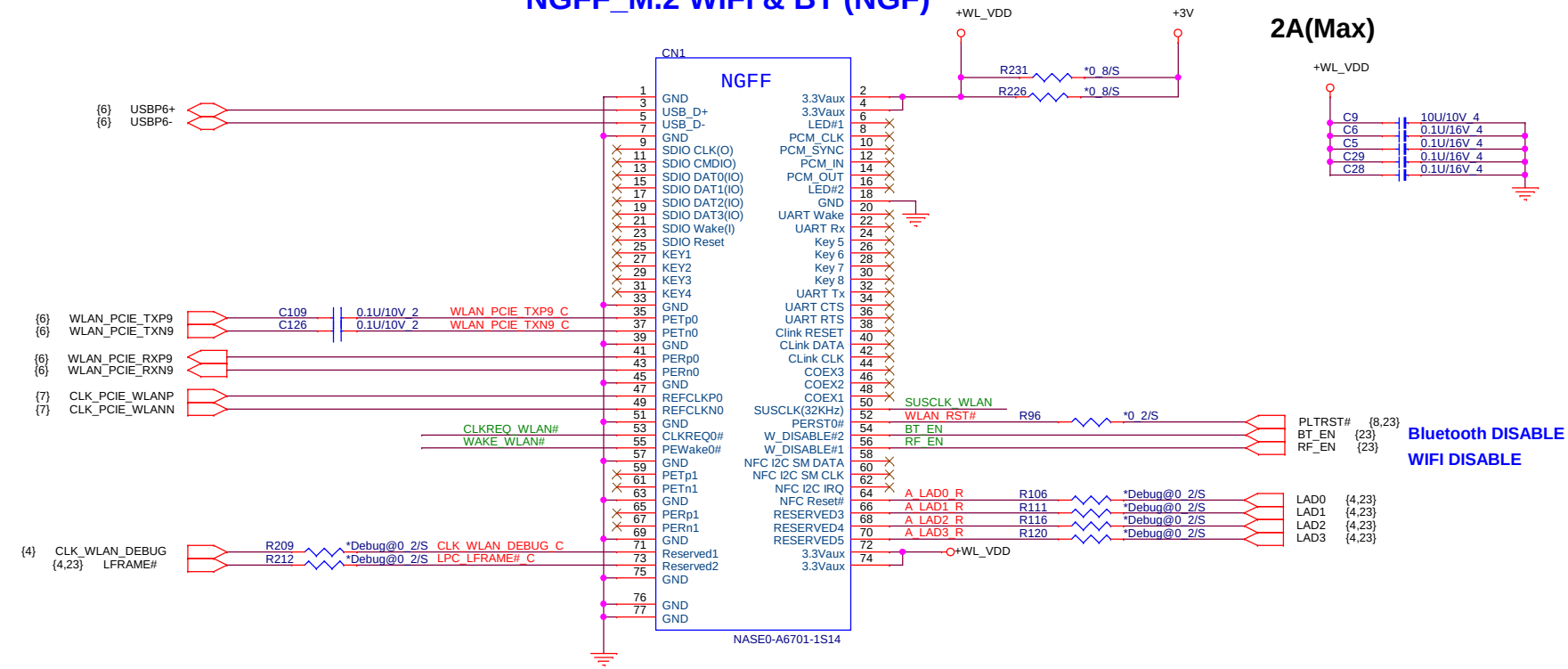
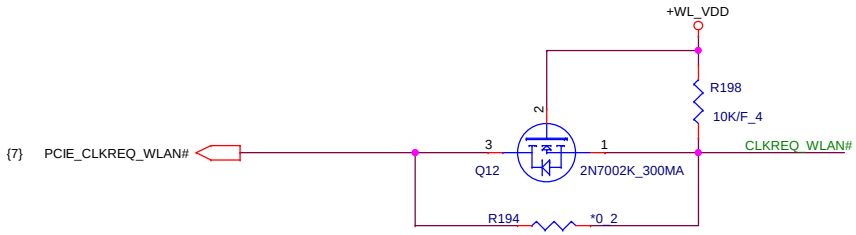
NGFF_M.2 WiFi & BT (NGF)

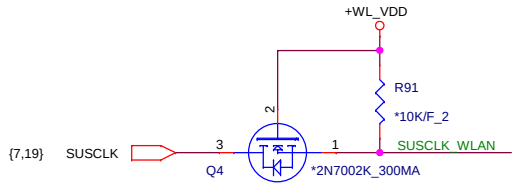
2A(Max)

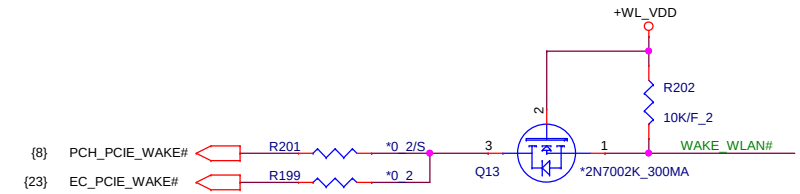


Bluetooth DISABLE

WIFI DISABLE







Quanta Computer Inc.

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	WIFI/BT	1A

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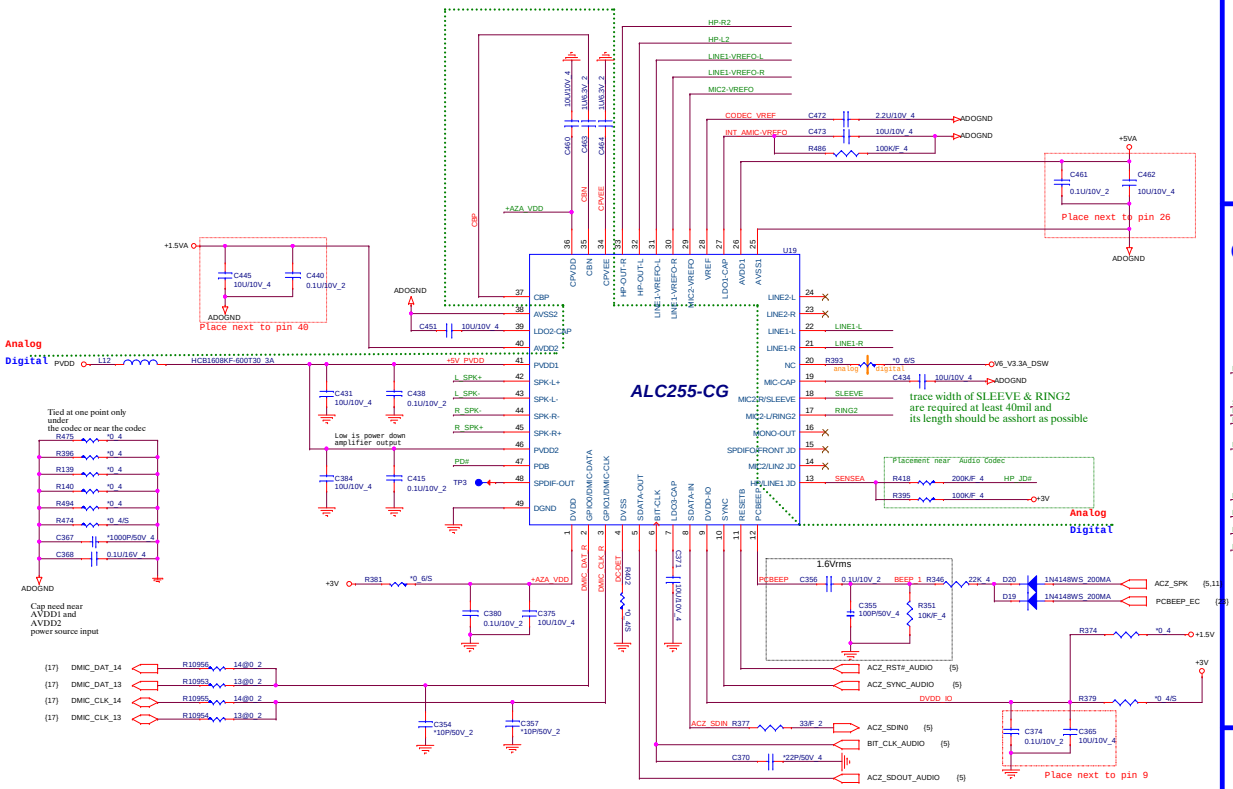
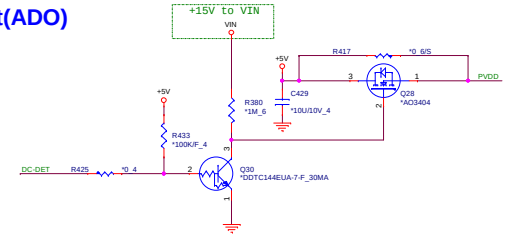
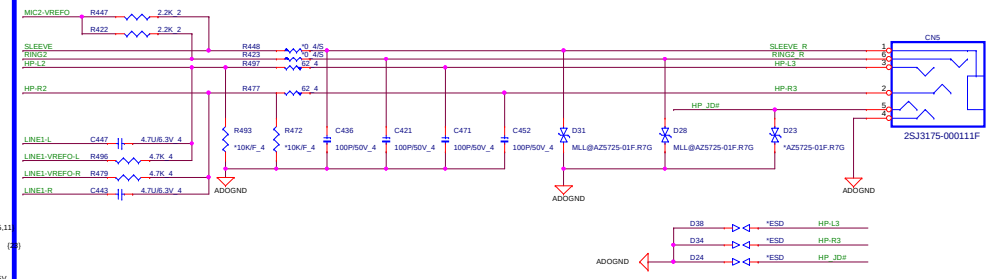
Codec(ADO)



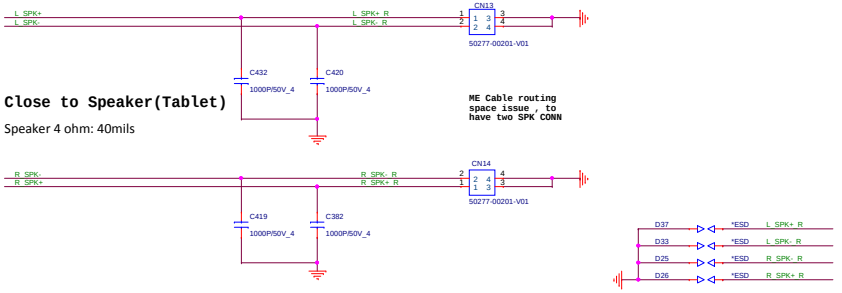
DC-DET circuit(ADO)



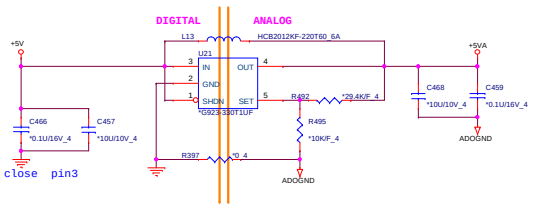
CTIA JACK (ADO)



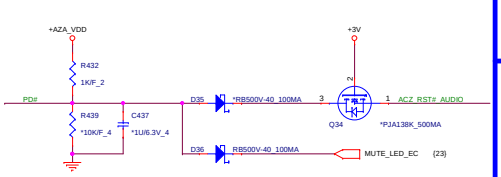
Internal Speaker(ADO)



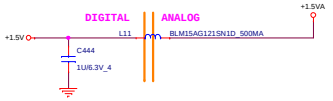
Codec PWR 5V(ADO)



Mute(ADO)



Codec PWR 1.5V(ADO)



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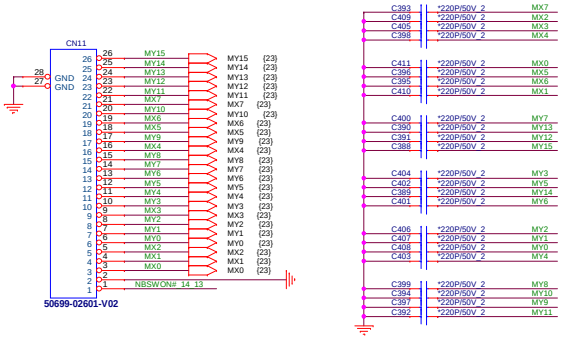
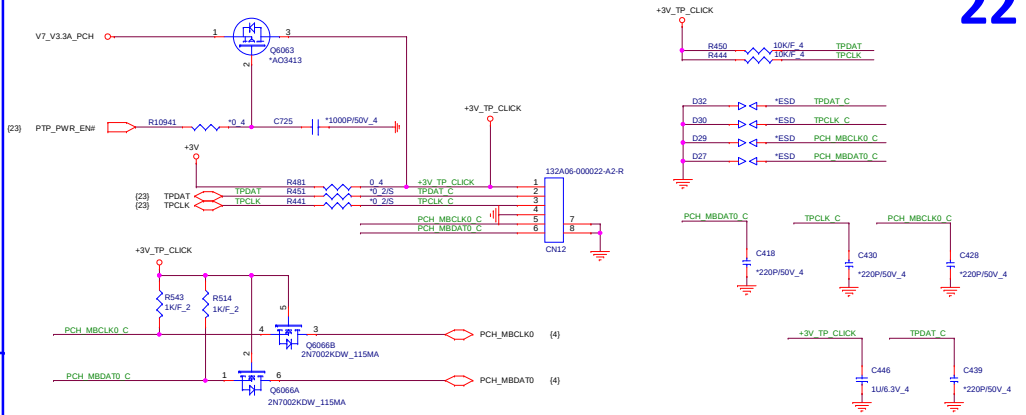
Keyboard Connector

Close to KB conn



Touch Pad Connector



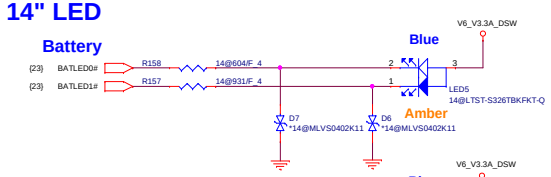
13" LED

Battery

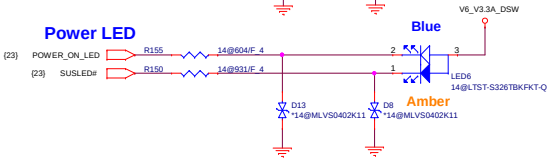
Power LED

14" LED

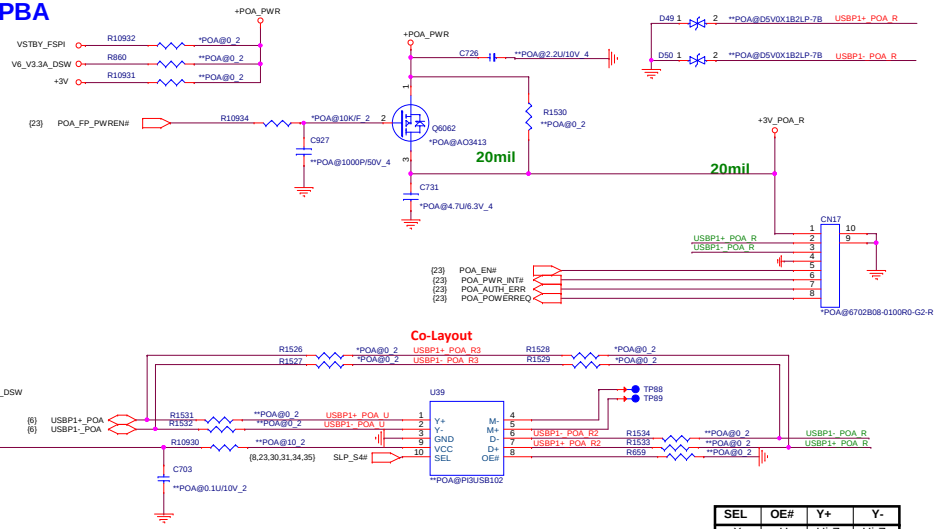
Battery



Power LED



POA,PBA

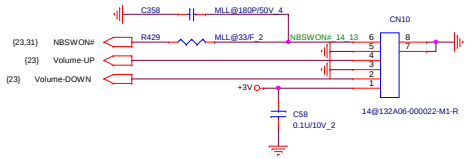


Co-Layout

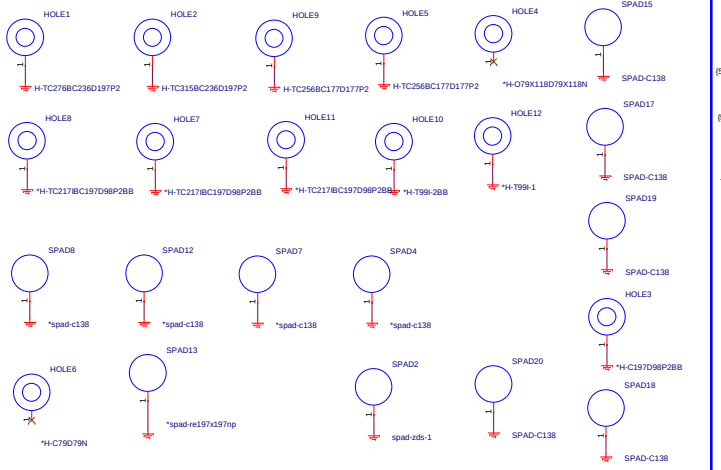
SEL	OE#	Y+	Y-
X	H	Hi-Z	Hi-Z
L	L	M+	M-
H	L	D+	D-

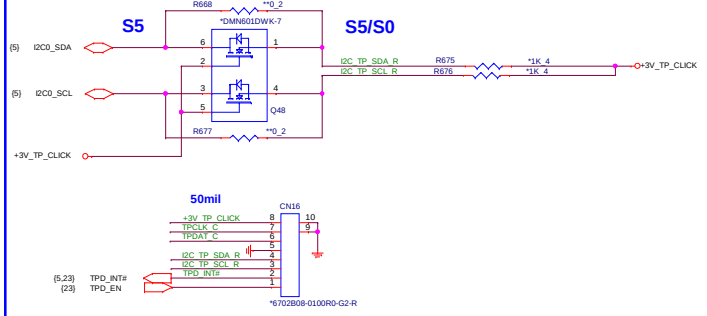
Spec define: High Active

Switch Board Connector



HOLE





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PROJECT : ZDS/ZSV			
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	KB/TP/THERMAL/LID		1A
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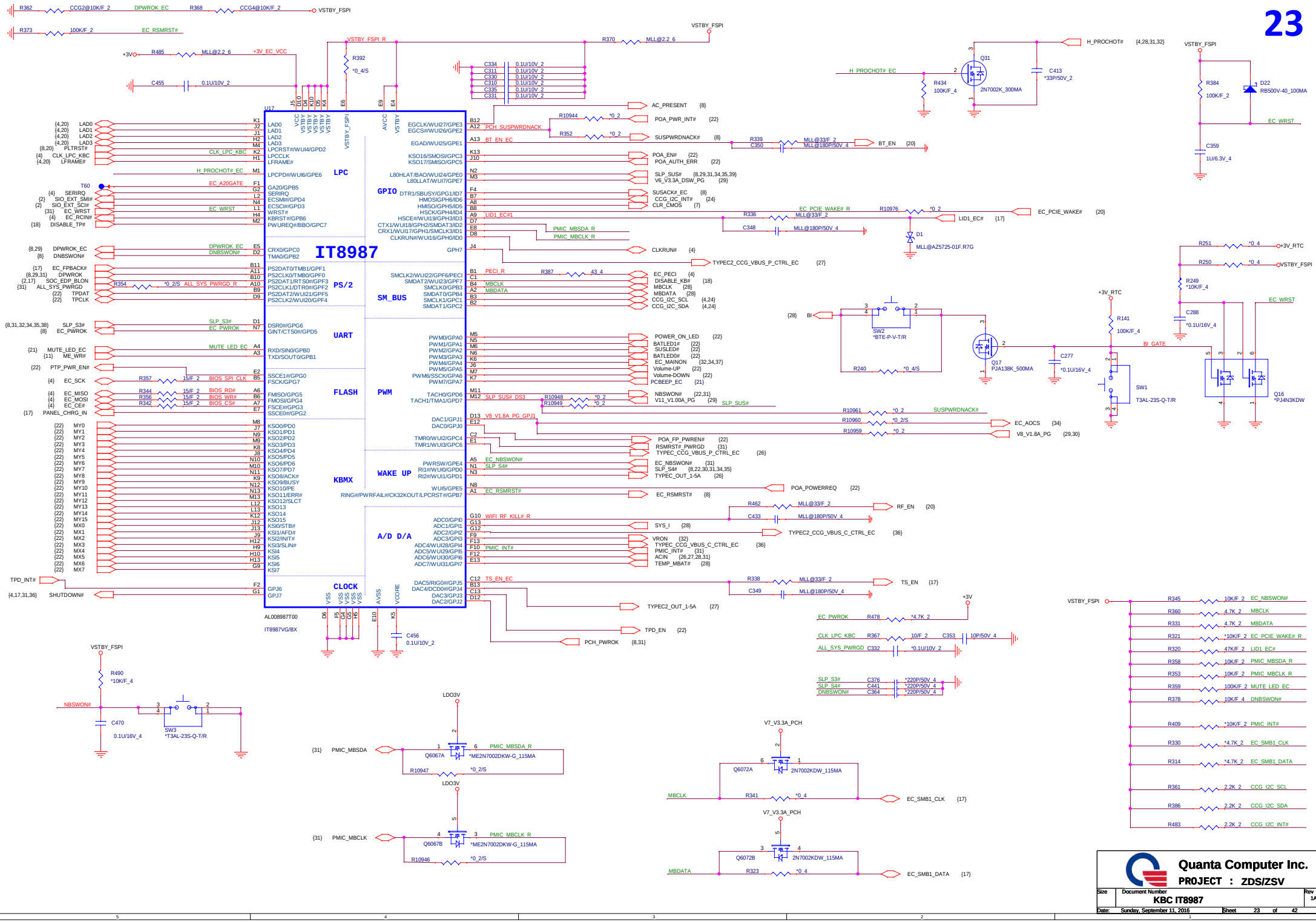

Quanta Computer Inc.			
PROJECT : ZDS/ZSV			
Size	Document Number		Rev
	KBC IT8987		1A
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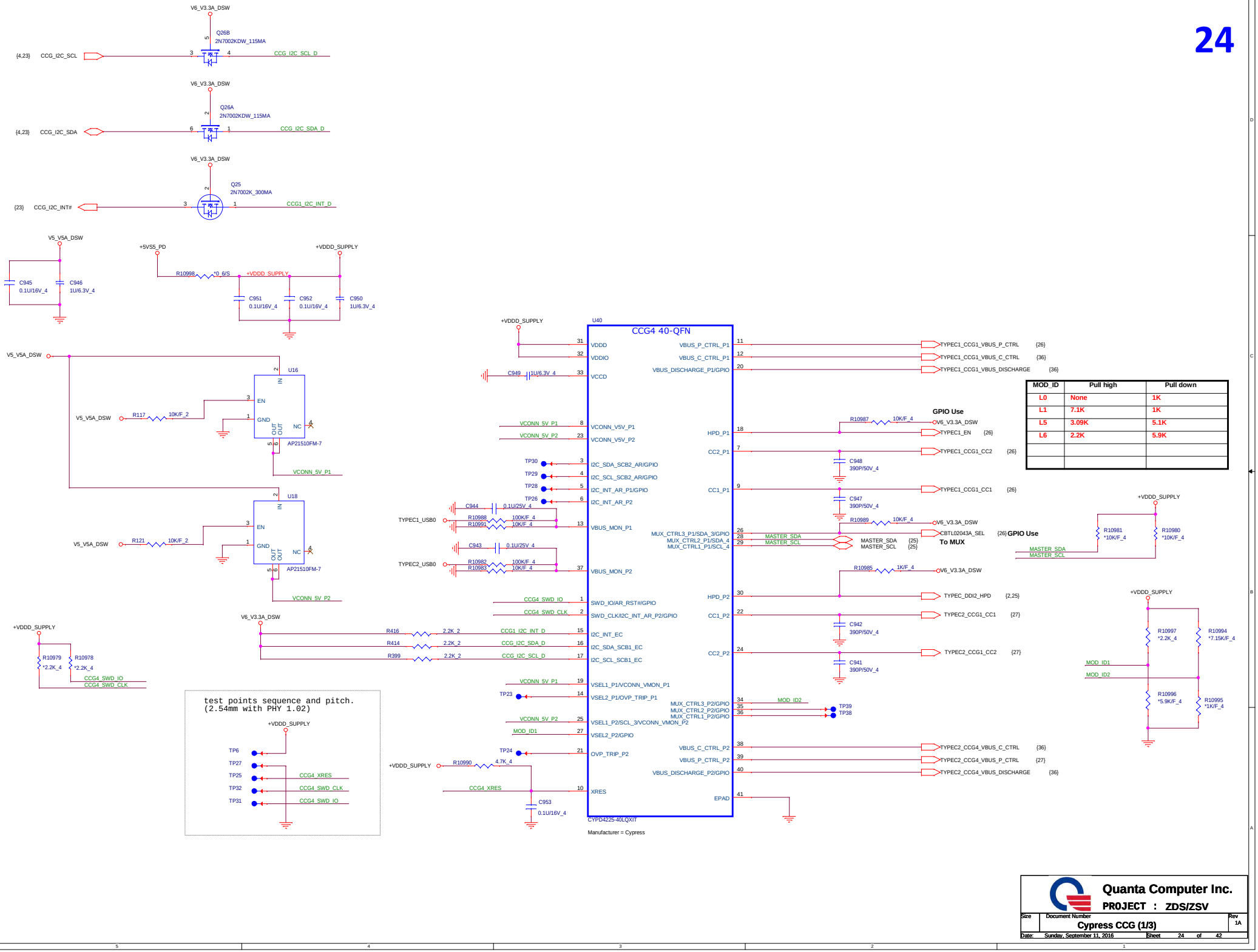

MOD_ID	Pull high	Pull down
L0	None	1K
L1	7.1K	1K
L5	3.09K	5.1K
L6	2.2K	5.9K

Quanta Computer Inc.
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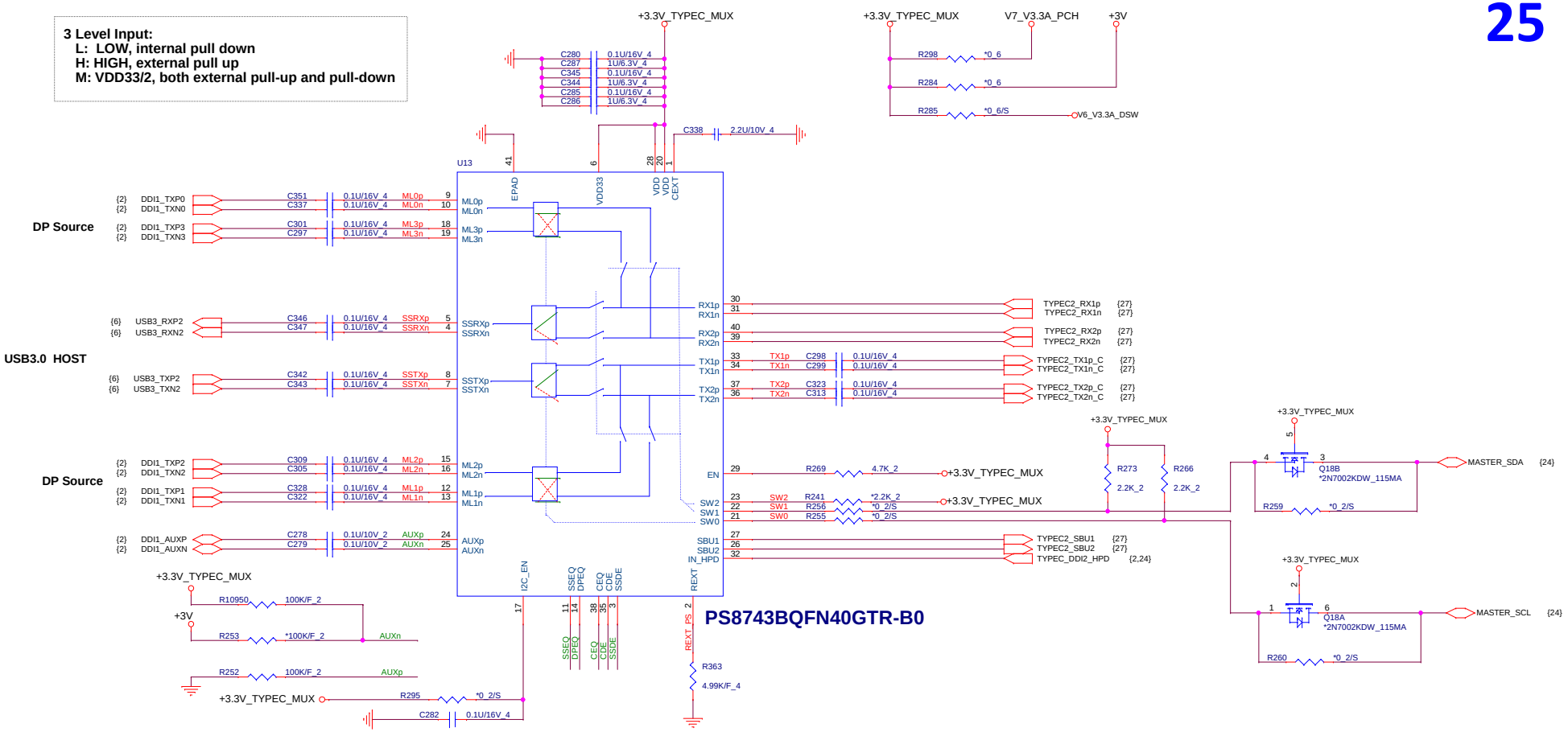
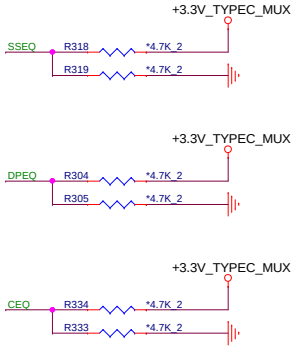
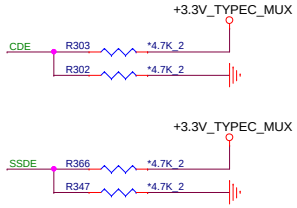
3 Level Input:
L: LOW, internal pull down
H: HIGH, external pull up
M: VDD33/2, both external pull-up and pull-down







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PROJECT : ZDS/ZSV		
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	PS8740B Pin Control (2/3)	1A
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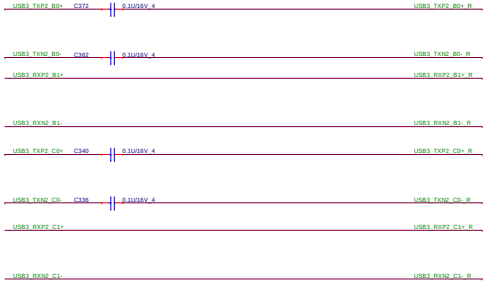
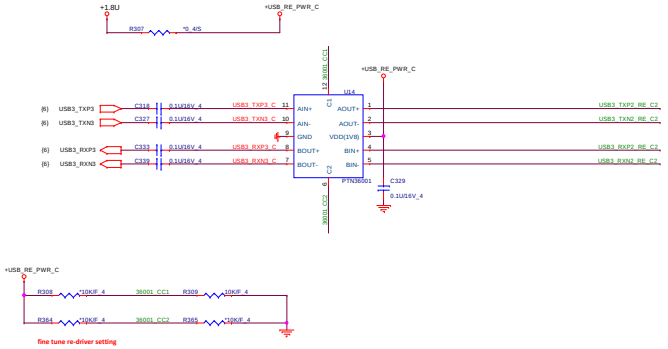
USB2.0 ESD

Type C1_HSIO_ ESD

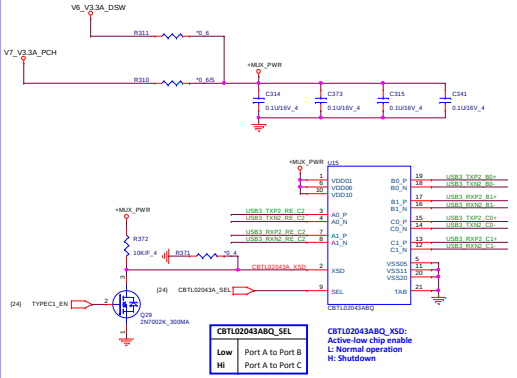


USB Type C (UTC)

USB 3.0 redriver

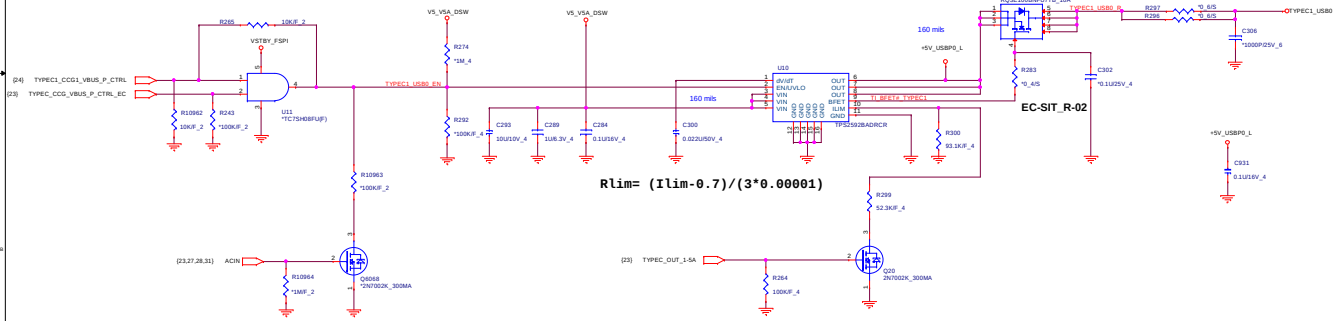


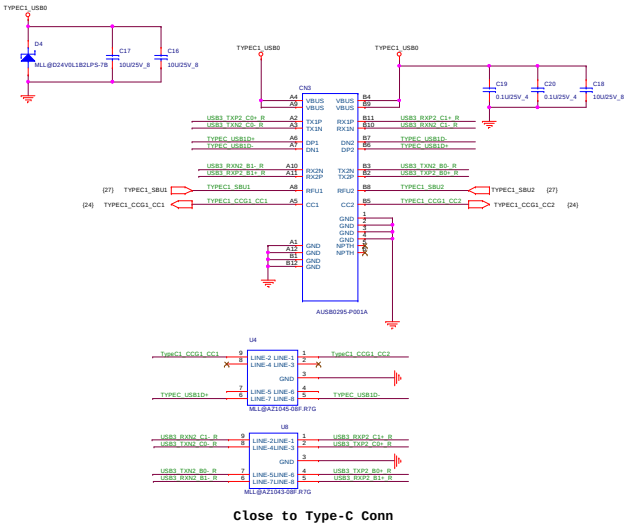
Type-C MUX



CBTL02043ABQ_SEL	
Low	Port A to Port B
Hi	Port A to Port C

CBTL02043ABQ_XSD:
Active-low chip enable
L: Normal operation
H: Shutdown





Close to Type-C Conn

USB2.0 ESD

{6} USBP2+ → TYPEC_USB2D+
{6} USBP2- → TYPEC_USB2D-

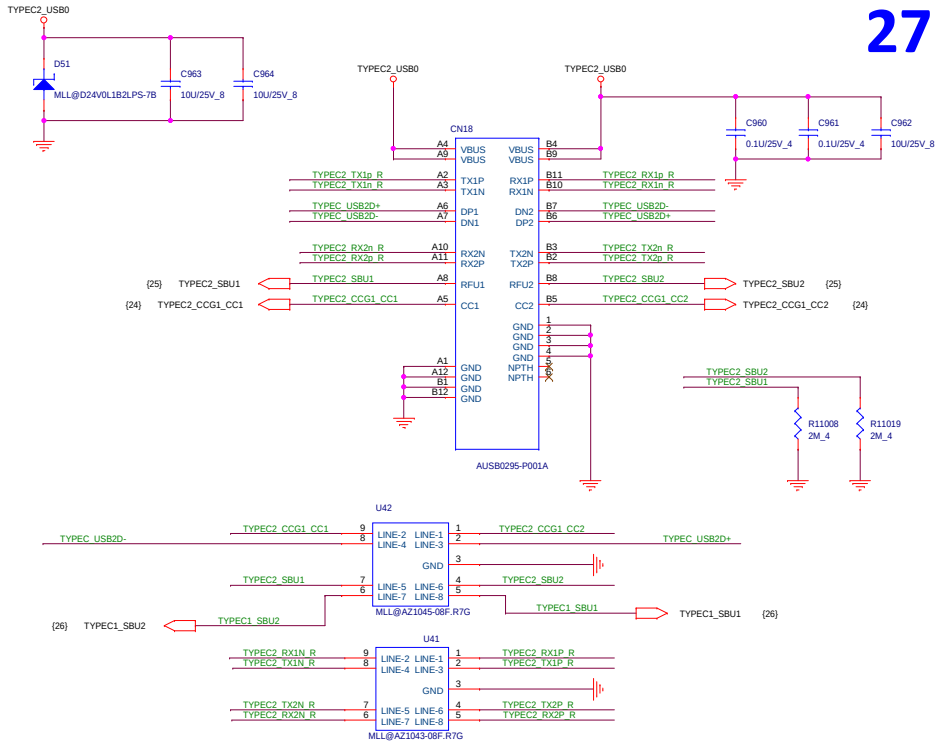
TYPE-C_HSIO_ ESD

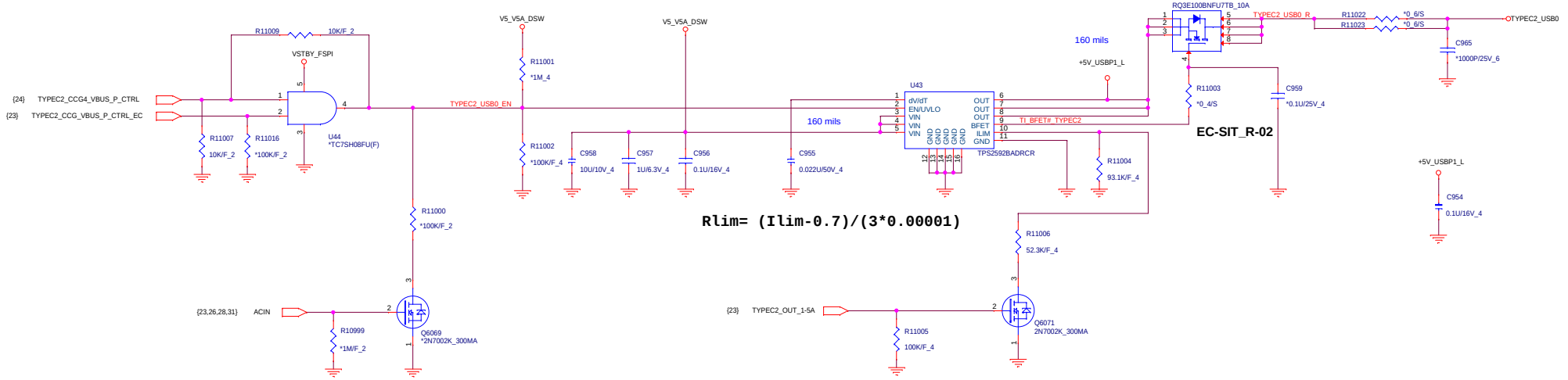
{25} TYPEC2_TX1p_C → TYPEC2_TX1p_R
{25} TYPEC2_TX1n_C → TYPEC2_TX1n_R

{25} TYPEC2_RX2n → TYPEC2_RX2n_R
{25} TYPEC2_RX2p → TYPEC2_RX2p_R

{25} TYPEC2_TX2p_C → TYPEC2_TX2p_R
{25} TYPEC2_TX2n_C → TYPEC2_TX2n_R

{25} TYPEC2_RX1n → TYPEC2_RX1n_R
{25} TYPEC2_RX1p → TYPEC2_RX1p_R





Quanta Computer Inc.			
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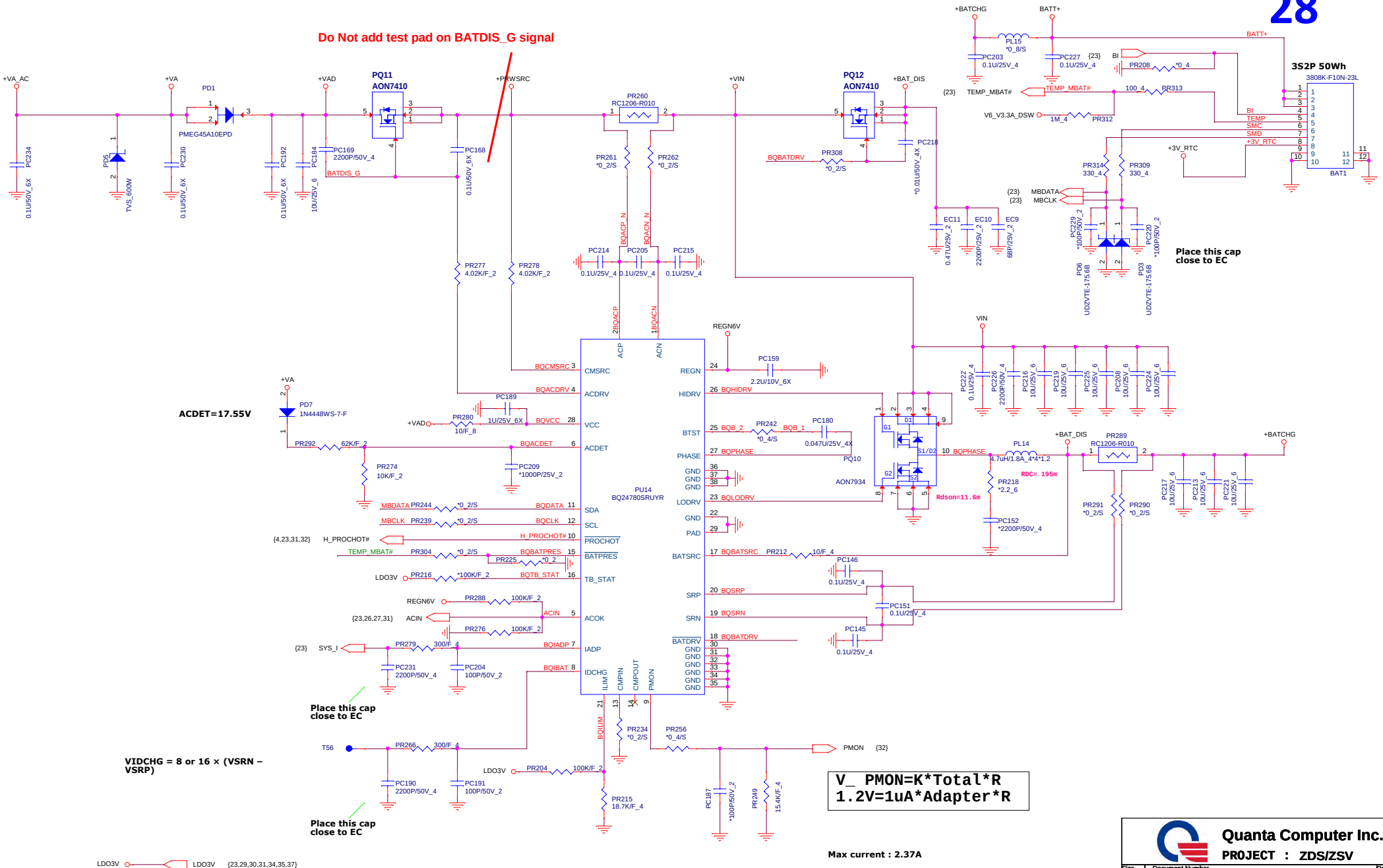
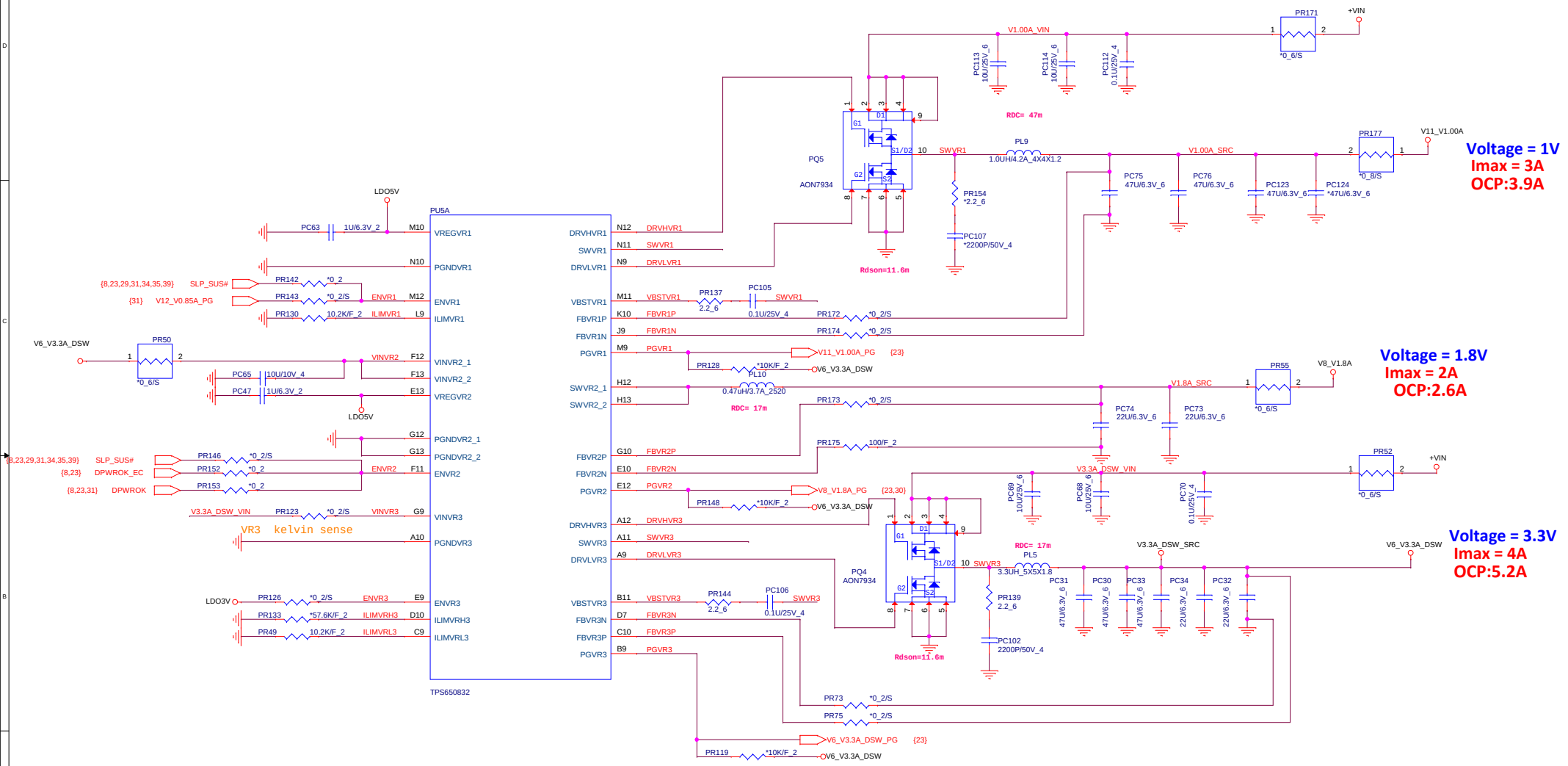
Do Not add test pad on BATDIS_G signal



Quanta Computer Inc.			
PROJECT : ZDS/ZSV			
Size	Document Number		Rev
	Charger (BQ24780S)		1A
Date: Sunday, September 11, 2016	Sheet 28	of 42	


Voltage = 1V
Imax = 3A
OCP:3.9A
Voltage = 1.8V
Imax = 2A
OCP:2.6A
Voltage = 3.3V
Imax = 4A
OCP:5.2A
TPS650832
PU5A
PQ5
AON7934
PQ4
AON7934
VR3 kelvin sense
RDC= 47m
RDC= 17m
RDC= 17m
Rdson=11.6m
Rdson=11.6m
V1.00A_VIN
V1.00A_SRC
V1.8A_SRC
V3.3A_DSW_VIN
V3.3A_DSW_SRC
LDO5V
LDO3V
+VIN
V11_V1.00A
V8_V1.8A
V6_V3.3A_DSW
PL9 1.0UH/4.2A_4X4X1.2
PL10 0.47uH/3.7A_2520
PL5 3.3UH_5X5X1.8
V6_V3.3A_DSW {4,8,10,17,21,22,24,25,26,28,30,31,34,35,37,38,39}
V11_V1.00A {10,11,31,34,35}
V8_V1.8A {10,31,34}
Quanta Computer Inc.
PROJECT : ZDS/ZSV
System PMIC (TPS650832_1)
Size
Document Number
Rev 1A
Date: Sunday, September 11, 2016
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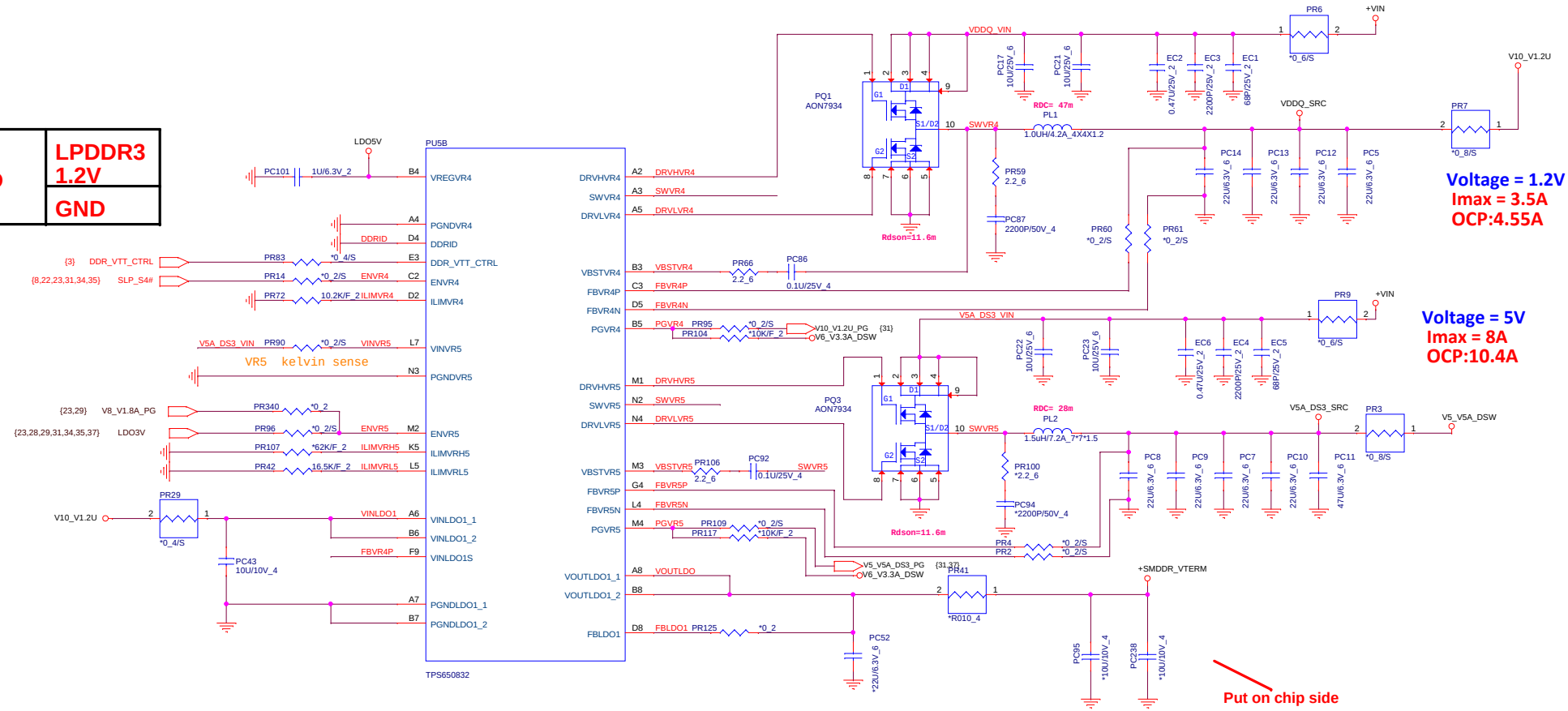
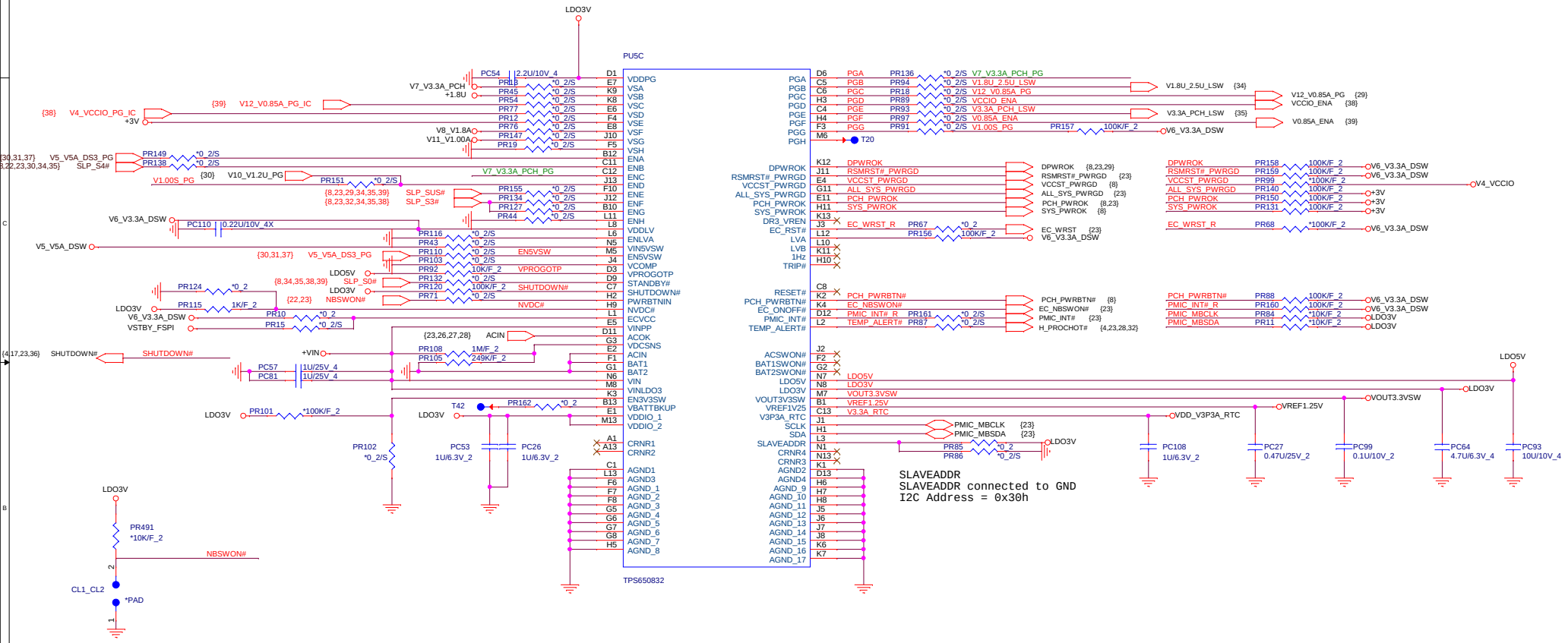
DDRID	LPDDR3 1.2V
	GND



V10_V1.2U {10,12,13,14,15,16,34}

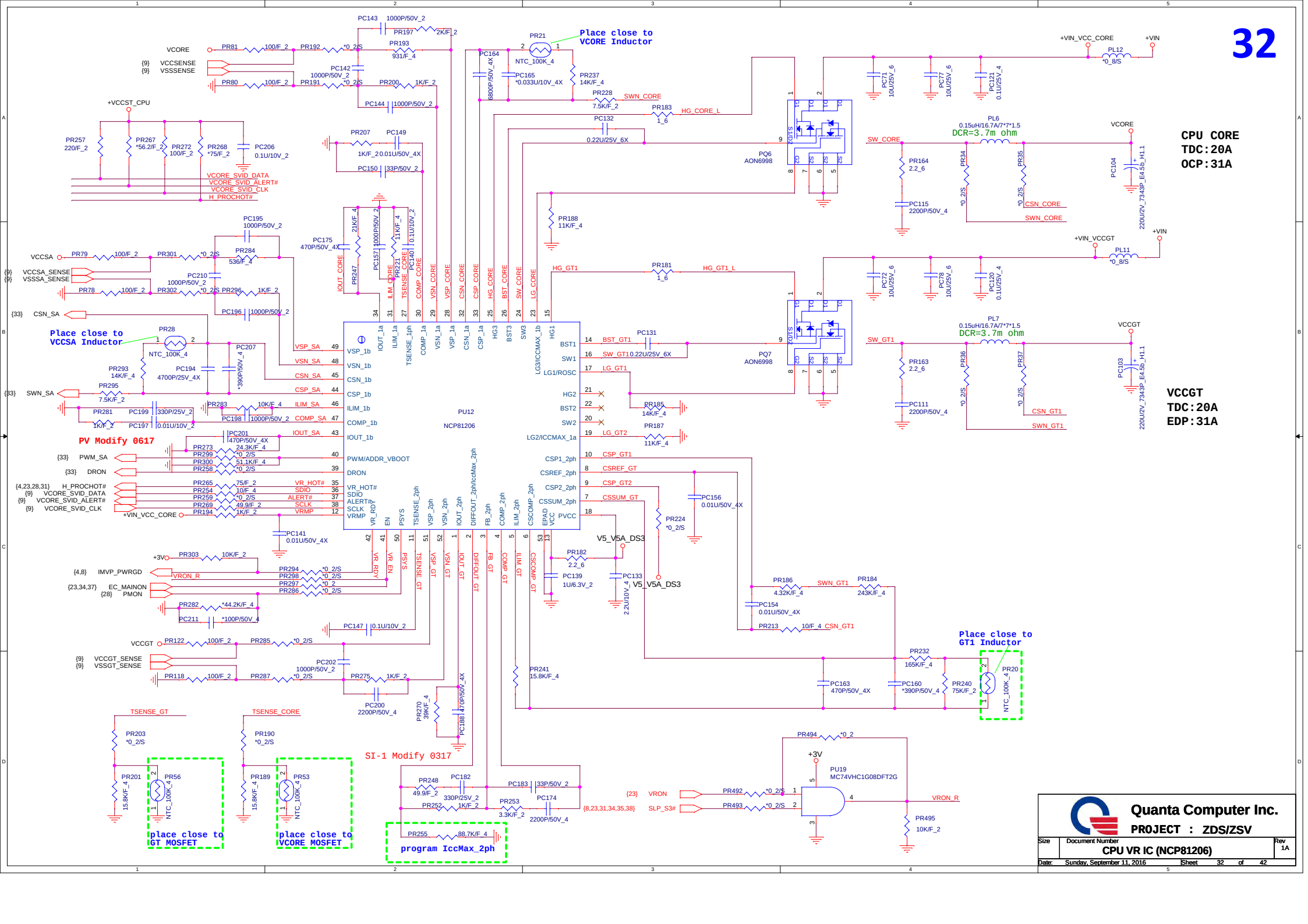
+SMDDR_VTERM {16}

Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: System PMIC (TPS650832_2)	Rev 1A
Date: Sunday, September 11, 2016	Sheet 30 of 42	

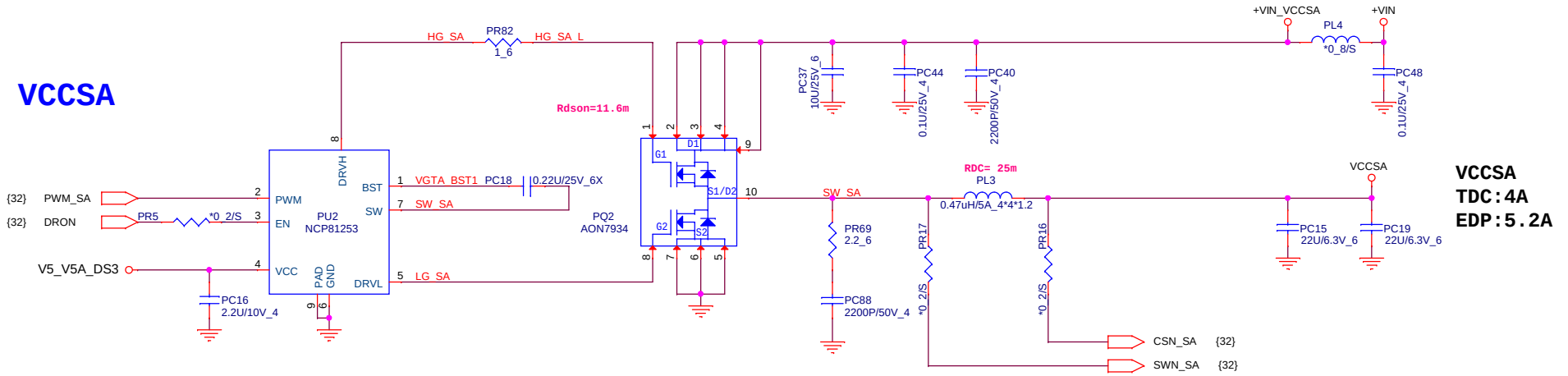


LDO3V ⟶ LDO3V {23,28,29,30,34,35,37}

Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: System PMIC (TPS650832_3)	Rev 1A
Date: Sunday, September 11, 2016	Sheet 31 of 42	

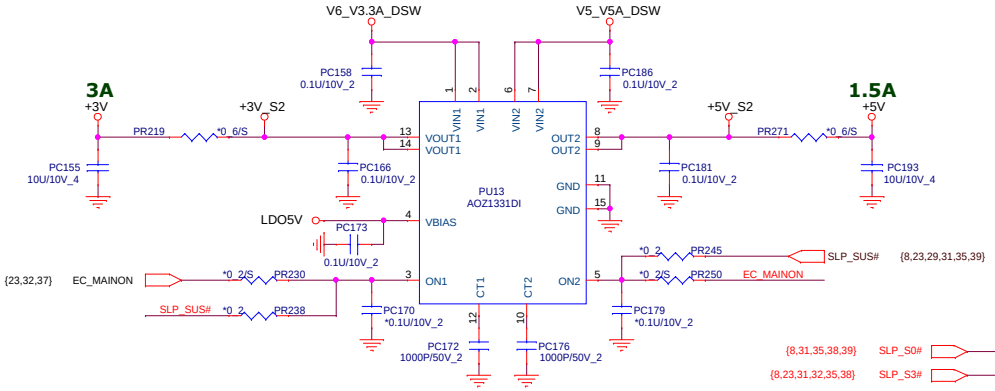

32
CPU CORE
TDC:20A
OCP:31A
VCCGT
TDC:20A
EDP:31A
PU12
NCP81206
PU19
MC74VHC1G08DFT2G
PV Modify 0617
SI-1 Modify 0317
Place close to VCORE Inductor
Place close to VCCSA Inductor
Place close to GT1 Inductor
place close to GT MOSFET
place close to VCORE MOSFET
program IccMax_2ph
DCR=3.7m ohm
Quanta Computer Inc.
PROJECT : ZDS/ZSV
CPU VR IC (NCP81206)
Sunday, September 11, 2016

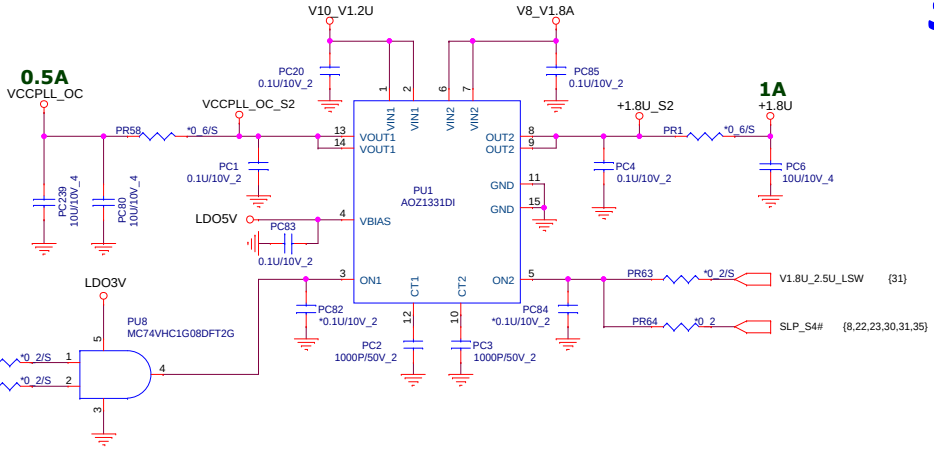
VCCSA

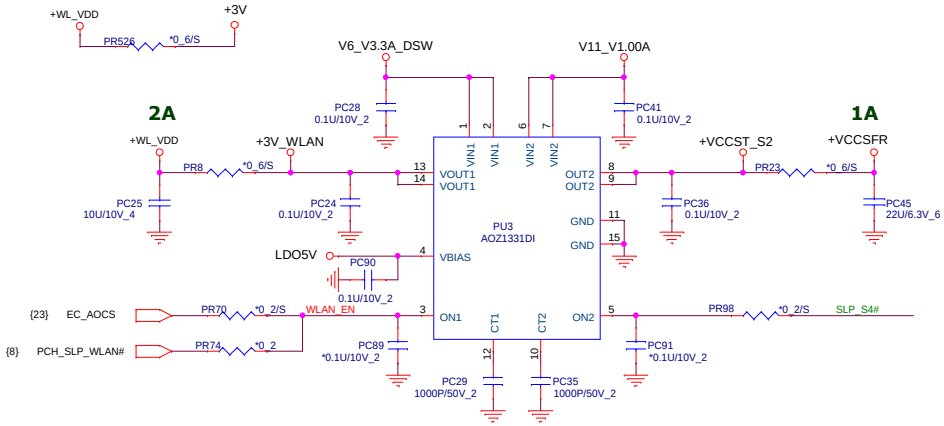


VCCSA
TDC : 4A
EDP : 5 . 2A

Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: +VCCSA (NCP81253)	Rev: 1A
Date: Sunday, September 11, 2016	Sheet 33 of 42	





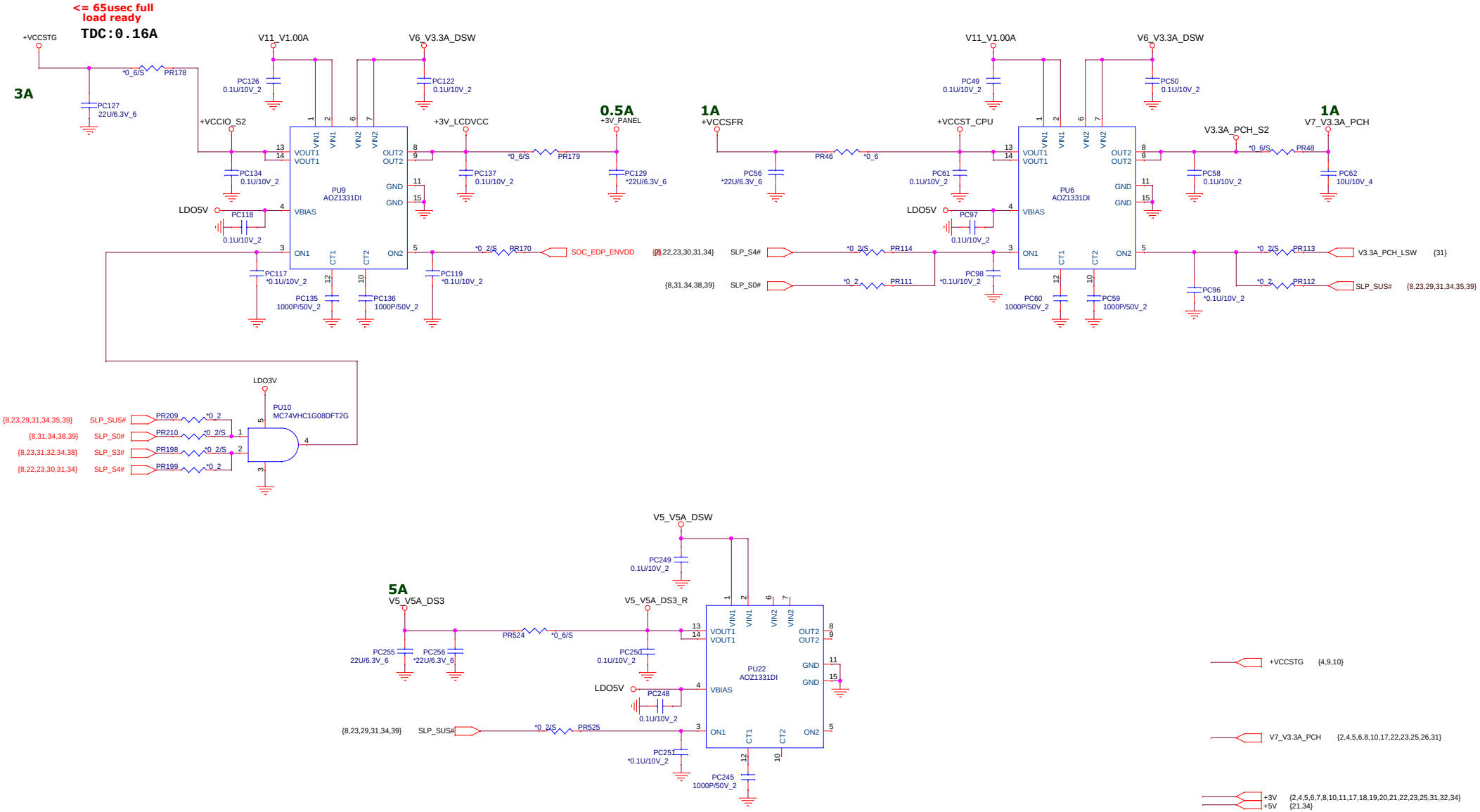


+WL_VDD {20}

+3V {2,4,5,6,7,8,10,11,17,18,19,20,21,22,23,25,31,32}

+5V {21}

Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: Load switch IC (APL3523A)	Rev: 1A
Date: Sunday, September 11, 2016	Sheet 34 of 42	

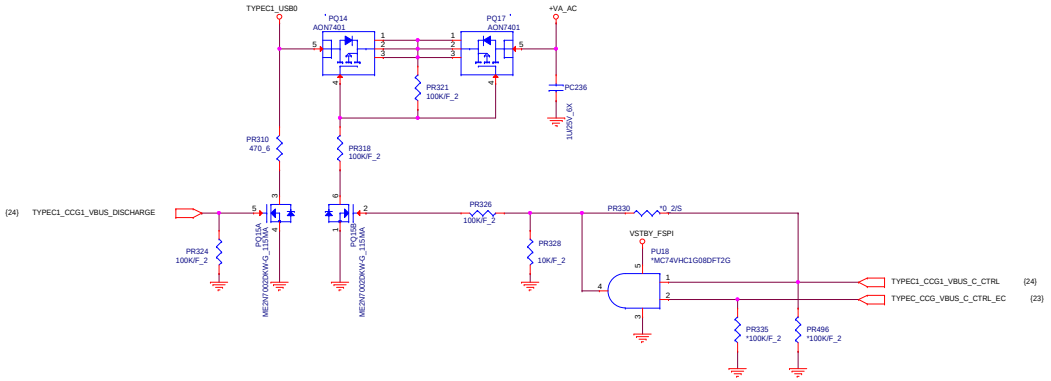


Quanta Computer Inc.

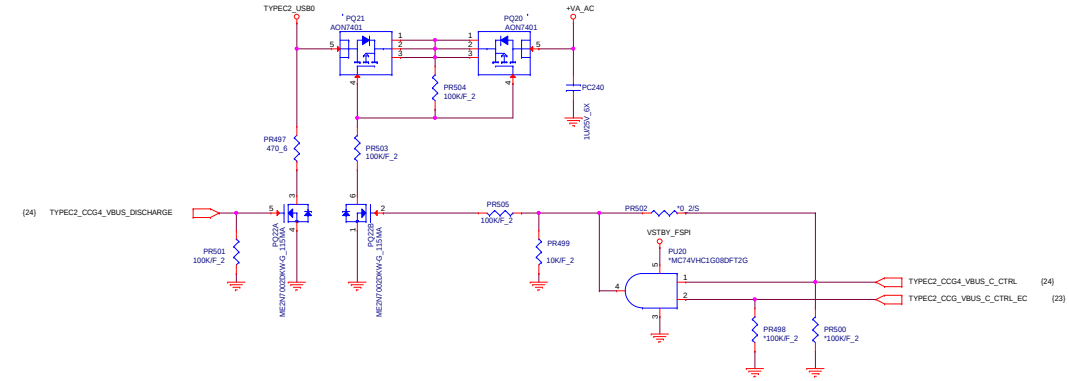
PROJECT : ZDS/ZSV

Size	Document Number	Rev
	Load switch IC (APL3523A)	1A
Date: Sunday, September 11, 2016	Sheet 35 of 42	

For TypeC PORT1



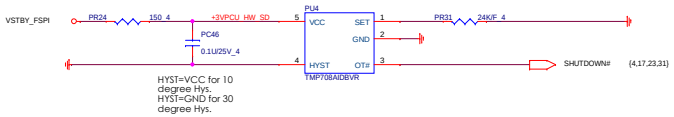
For TypeC PORT2



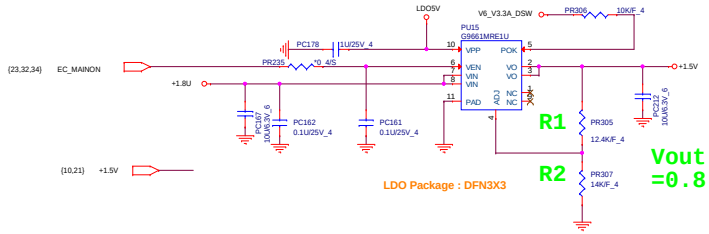
Thermal protection

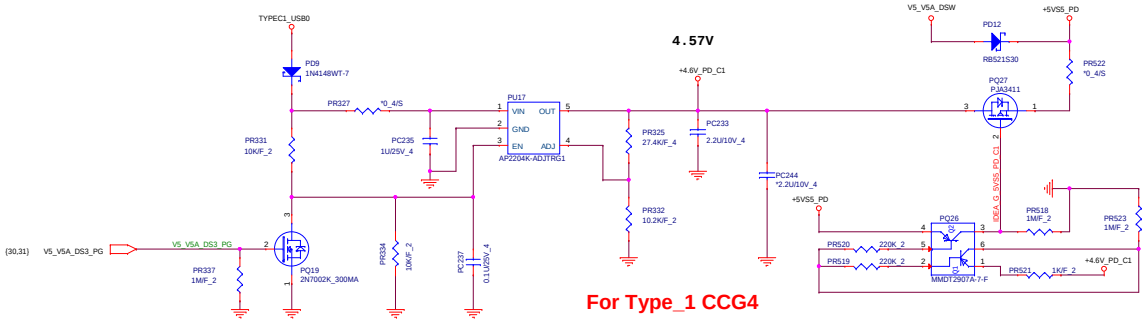
For thermal protect point
Note placement position
TEMP=85C

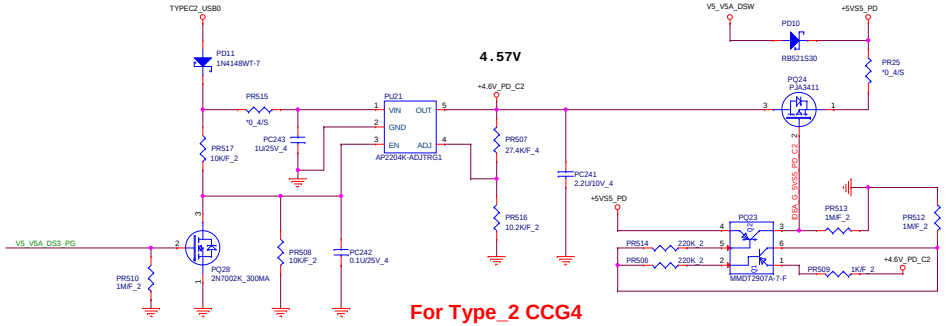
Rset(Kohm)=0.0012T*T-0.9308T+96.147

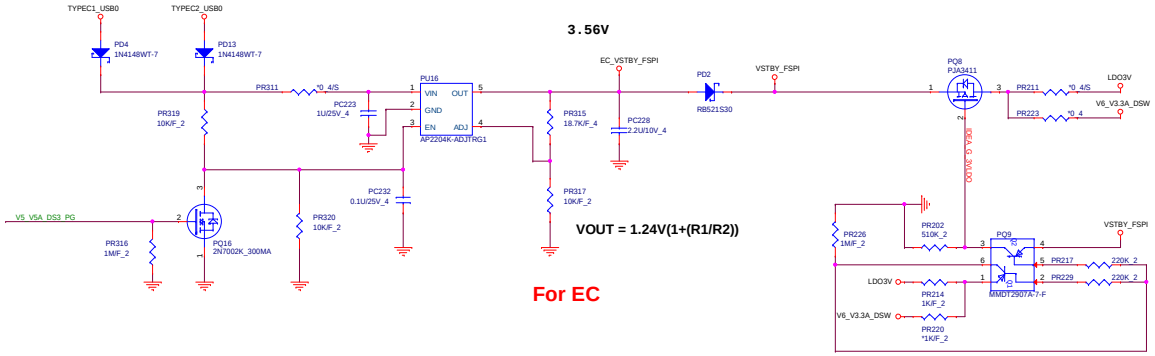


Quanta Computer Inc.		
PROJECT : ZDS/ZSV		
Size	Document Number: USB TYPE-C Plug	Rev: 1A
Date: Sunday, September 11, 2016	Sheet 36 of 42	





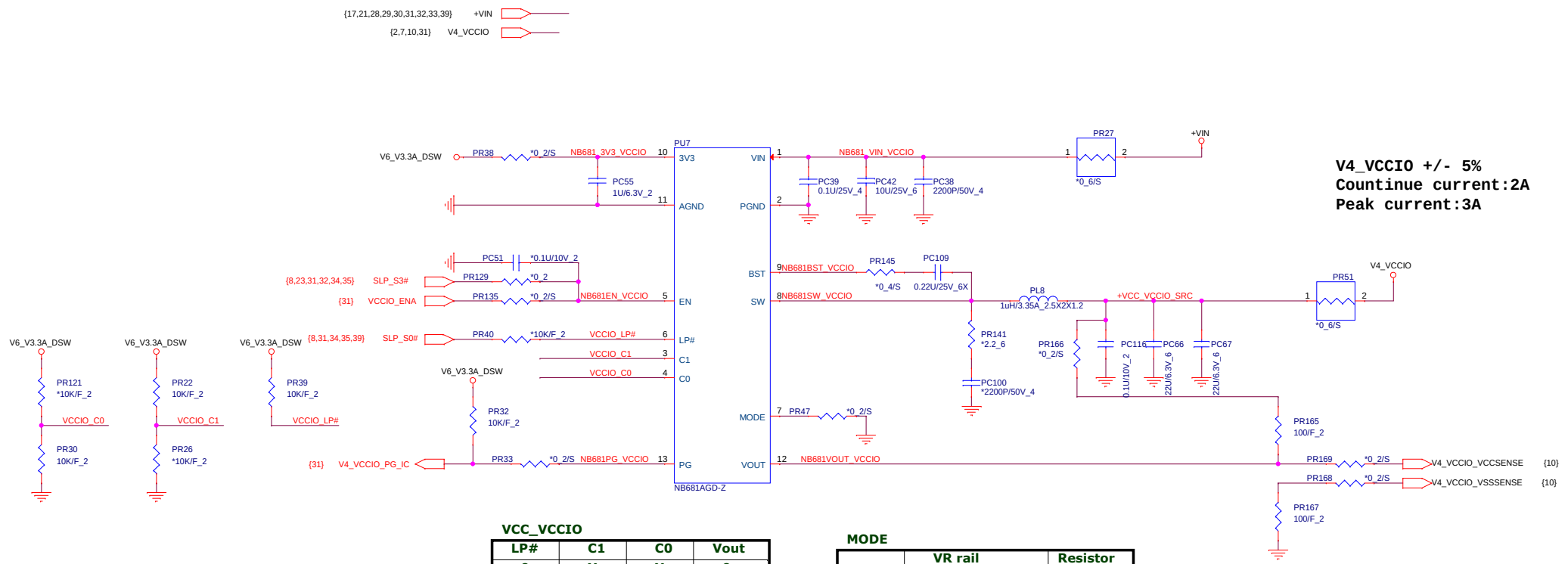




Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	Dead Battery	1A
Date: Sunday, September 11, 2016	Sheet 37 of 42	



V4_VCCIO +/- 5%
Countinue current:2A
Peak current:3A

VCC_VCCIO

LP#	C1	C0	Vout
0	X	X	0
1	0	0	0.85
1	0	1	0.875
1	1	0	0.95
1	1	1	0.975

MODE

	VR rail	Resistor
M1	VCCIO	0
M2	PRIMCORE	Float
M3	EDRAM/EOPIO	100K
M4	other	150K

Quanta Computer Inc.
PROJECT : ZDS/ZSV

Size	Document Number	Rev
	+VCC_VCCIO (NB681)	1A
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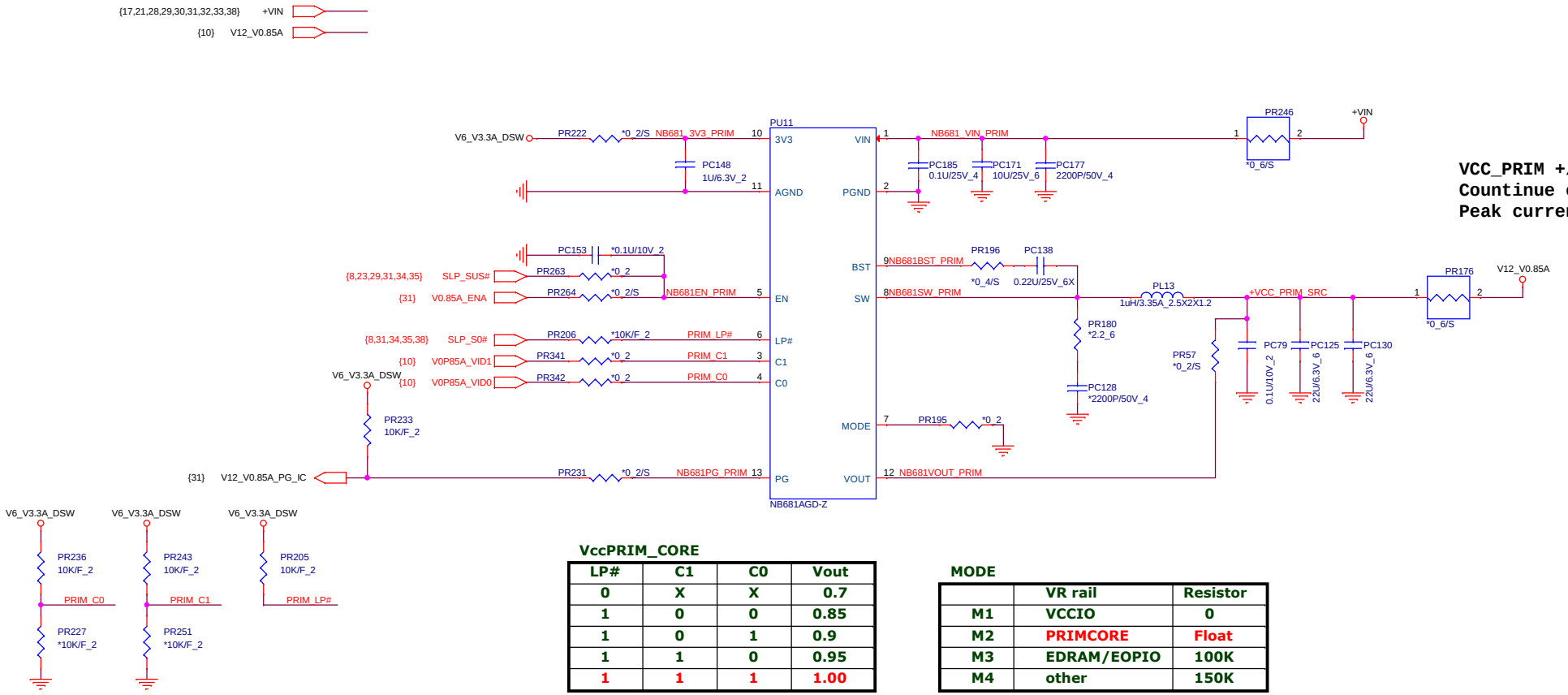

VCC_PRIM +/- 5%
Countinue current:1A
Peak current:1.5A

VccPRIM_CORE

LP#	C1	C0	Vout
0	X	X	0.7
1	0	0	0.85
1	0	1	0.9
1	1	0	0.95
1	1	1	1.00

MODE

	VR rail	Resistor
M1	VCCIO	0
M2	PRIMCORE	Float
M3	EDRAM/EOPIO	100K
M4	other	150K

Quanta Computer Inc.
PROJECT : ZDS/ZSV

Size	Document Number	Rev
	+VCC_PRIM (NB681)	1A
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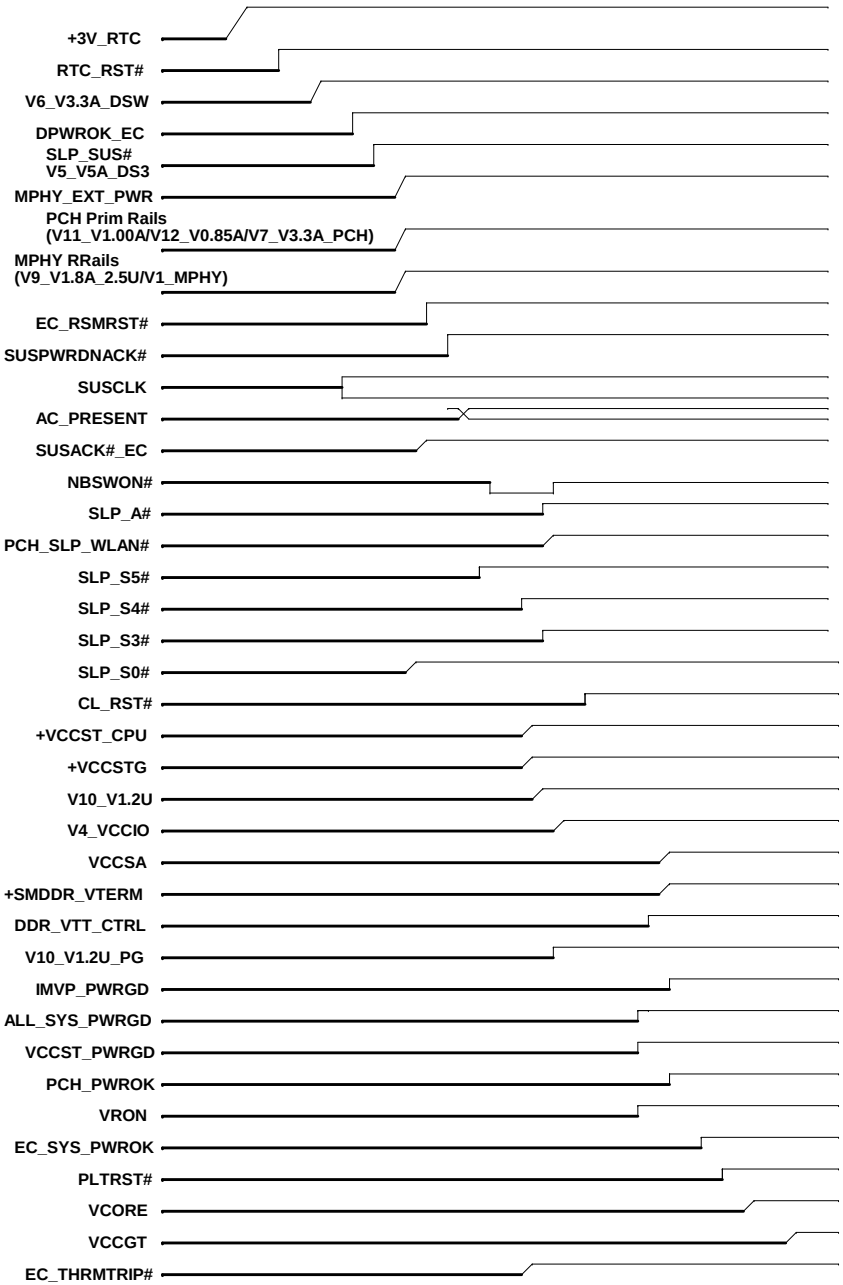
Power up sequence

40



Quanta Computer Inc.

PROJECT : ZDS/ZSV

Size	Document Number	Rev
	Power Sequence	1A

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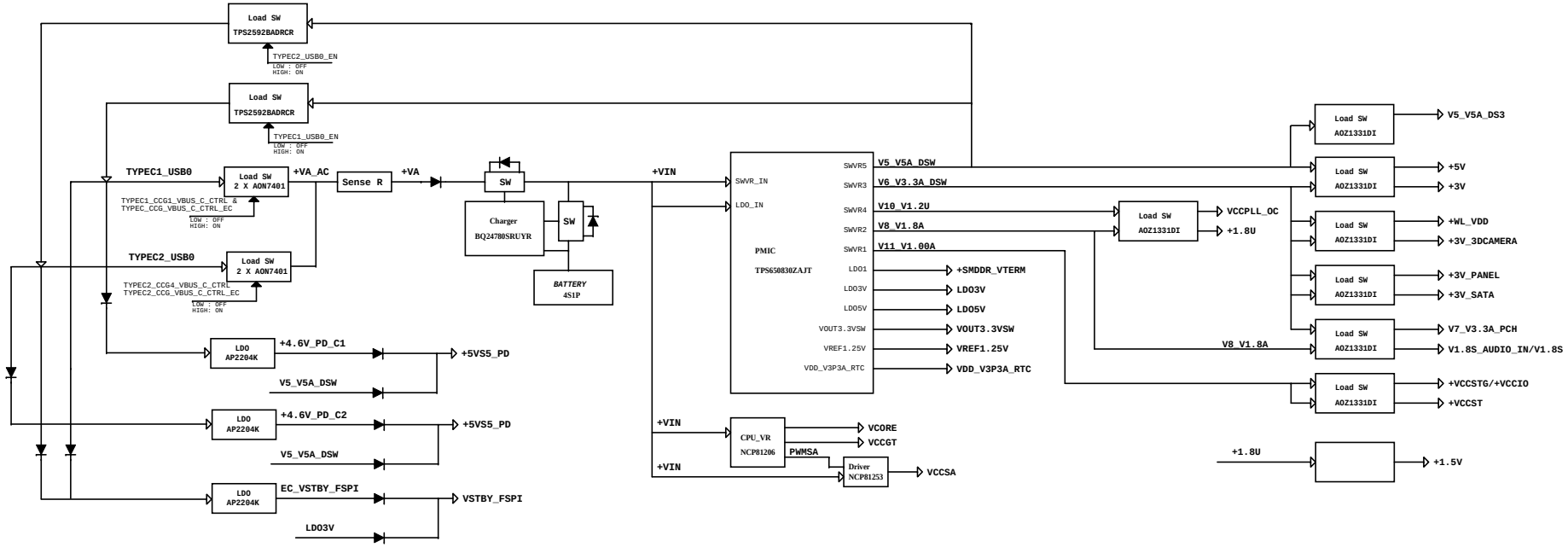

Quanta Computer Inc.

PROJECT : ZDS/ZSV

Power Tree

Rev 1A

Date: Sunday, September 11, 2016 Sheet 41 of 60

Model	Date	CHANGE LIST
ZDS/ZDV REV:A	4/27	1. FIRST RELEASED
ZDS/ZDV REV:B	6/1	1. Change PR284/PR193/PR247/PR283/PR273/PR187/PR240 Value for CPU transient and load line 2. Add PR492/PR493/PU19/PR495 for sequence 3. Add Q6064 for EC read PCH temperature 4. Change R52/R54 Value from 4.7K to 1K ohm for Touch pad 5. Add R10592/R10591/D39 for RTC voltage 6. Change R291/R47 PU netname from VSTBY_FSP to VSTBY_FSPI for netname wrong 7. Add R10953/R10954/R10955/R10956 for separate Boxster and Dino MIC signals 8. Stuff R309 for USB RX
ZDS/ZDV REV:C1	7/10	1. Remove R28 / Add R10971 to link V7_V3.3A_PCH for leakage current issue 2. Remove R133,R130,R128,R124,R67,R61 and Add R10967,R10966,R10968 to link V7_V3.3A_PCH for leakage current issue 3. Reverse POA/PBA circuit for customer requirement 4. Remove R32,R113 and Add Add R10970 to link V7_V3.3A_PCH for leakage current issue 5. Change C81/C80/C83//C96/C228/C484/C485/C483/C487/C488 Value t0 47U/4V_8H095 for ID height-Z limitation 6. Add C51/C52/C53/C121/C185/C199/C225/C64//C65/C66/C235/C161/C233 for VCCGT voltage stable 7. Add C259/C267 for V4_VCCIO voltage stable 8. Add C940/C939/C938/C937/C936/C935/C934/C312/C425/C426 for V10_V1.2U voltage stable 9. Change 13" ZDS Dual MIC connector from 4pin to 8 pin for FA issue 10. Add D14/D15 for ESD fail 11. Remove R391/R455/R489 and mount Q33/Q27/R390/R456/R488 for leakage current issue 12 Change Audio Jack CN5 from 5 pin to 7 pin for noise issue 13. Change R151/R152/R153/R154/R150/R155/R157/R158 Value for LED brightness issue 14. Change PR292/PR274 Value for ACDet 15. Change PL5 from 1UH to 3.3UH for Noise issue
ZDS/ZDV REV:C2	7/27	1. Change PD circuit from CCG2 to CCG4 for support PD*2
ZDS/ZDV REV:C3	8/10	1. Del R330,R314,R323,R341,R291 and add Q6072,R332,R337,R505 to V7_V3.3A_PCH for S5 leakage current issue 2. Change R150,R151,R152,R153,R154,R157 PN to fine tune LED brightness for ME ID Len change Lens color issue 3. Del R10980,R10981,R298,Q18,R311 and add R285,R273,R266,R259,R260,R295,R310,C282 and change Change U16,U18,R117,R121 to V5_V5A_DSW for S4 resume TYPE C device detect detect issue
ZDS/ZDV REV: Ramp	8/22	1. Change 0 ohm to shortpad

Quanta Computer Inc.

PROJECT : ZDS/ZDV

Size	Document Number	Rev
	Change list	1A

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