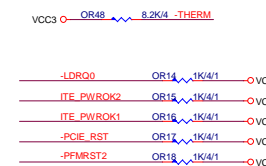
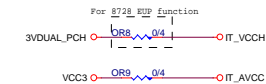
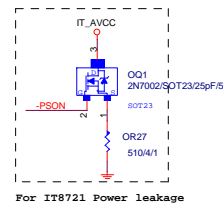
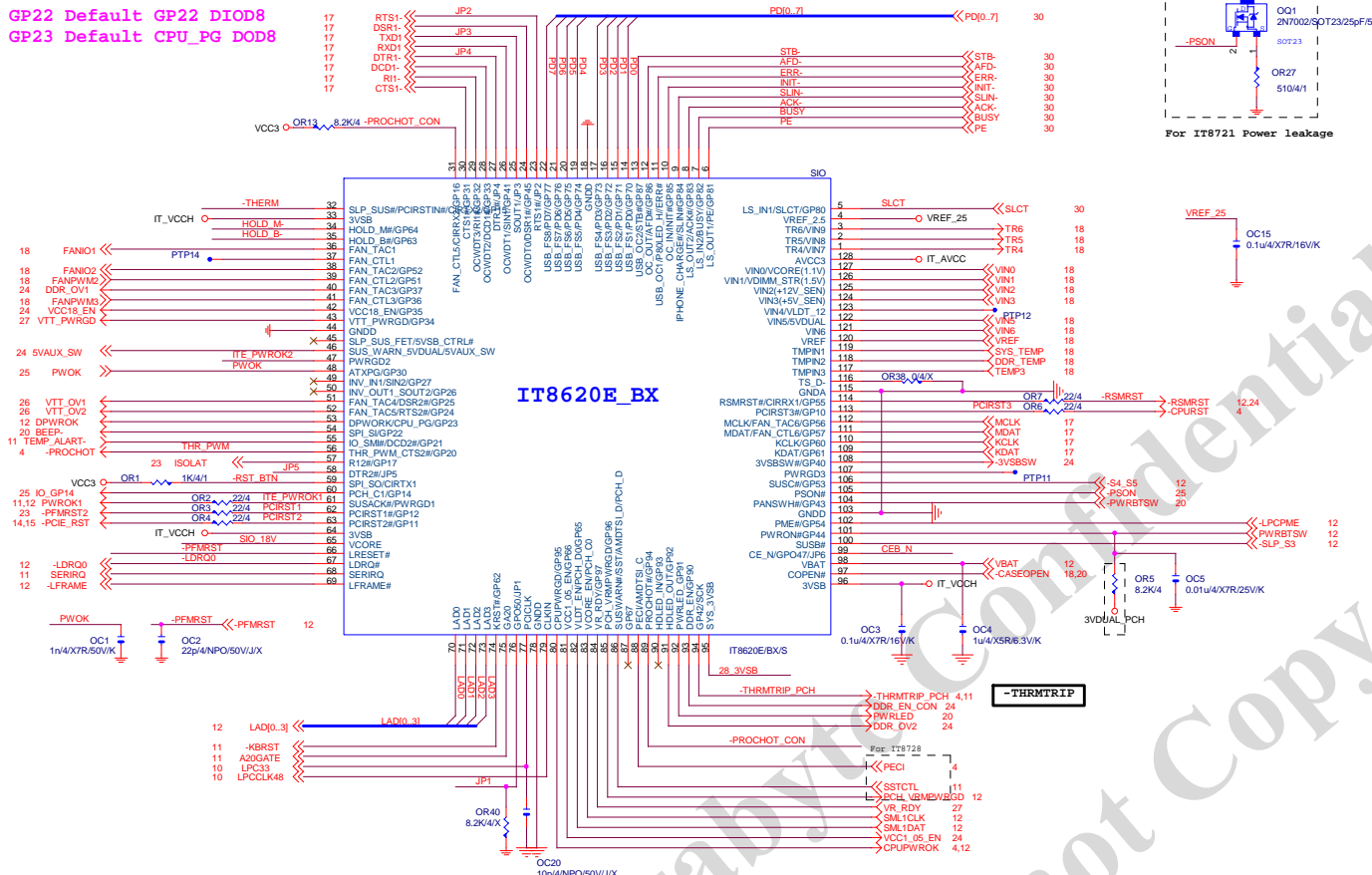
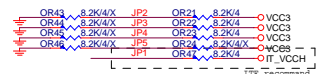


```
GP22 Default GP22 DIOD8
GP23 Default CPU_PG DOD8
```



H61M-S2 1.1 JP6 stuff
pull down



```

| JP3--- High SPI-Flash Disable
|         Low SPI-Flash Enable

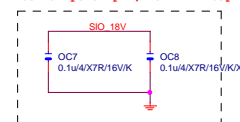
```

EUP from PCH

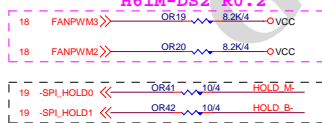
EUP control by PCH



internal power pin, max 22nF cap



H61M-DS2 R0.2



-CASEOPEN



	IT8728
PIH121	VCORE_EN/PCH_C0
PIH120	VLDT_EN/PCH_D0
PIH19	ATXP0
PIH31	PCH_C1
PIH53	SST/AMTS1_D/WTB3B/PCH_D1
PIH55	PECI/AMTS1_C/DRVBS#
PIH66	SYS_3VSB
PIH70	GP47
PIH95	VIN2 (VCC5)
PIH96	VIN1 (VCC12)
PIH97	VIN1/VDIMM_STR1(1.5V)
PIH98	VIN0/VCORE(1.1V)/NC

