GeForce6100PM-M2

REV: 2.0 (Support AM2+)

PCB:15-V09-011010
BOM:89-206-V09210

MCP61S Real S3
Components :693PCS
Add R26.R28.R29.R30
Del R48

Page Index

	Signature	Date
Designer	Eli	07/11/2007
Layout	Susan/Angela	05/29/2007
Check		
Approval		

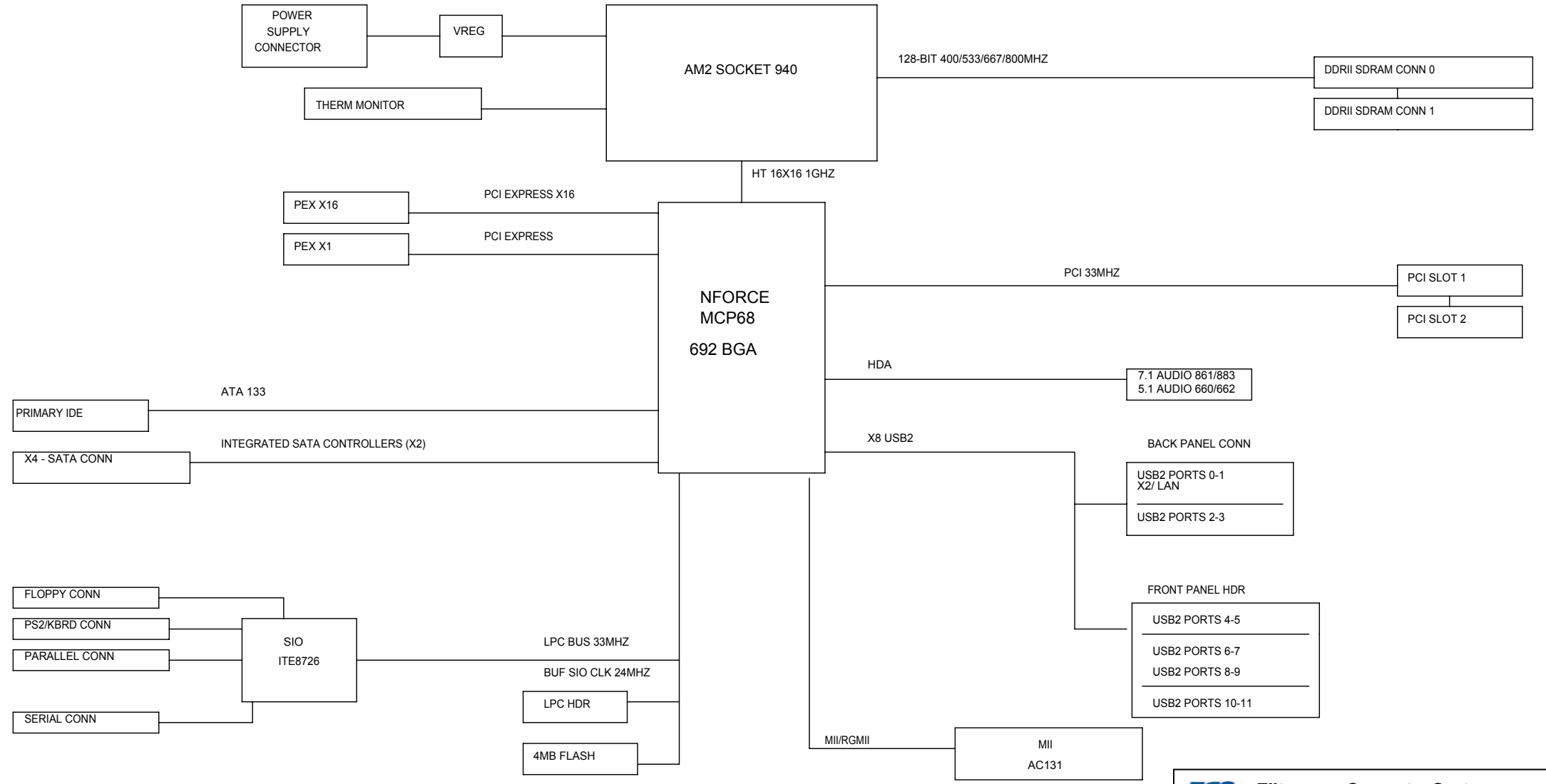
Elitegroup Computer Systems

Title: GeForce6100PM-M2

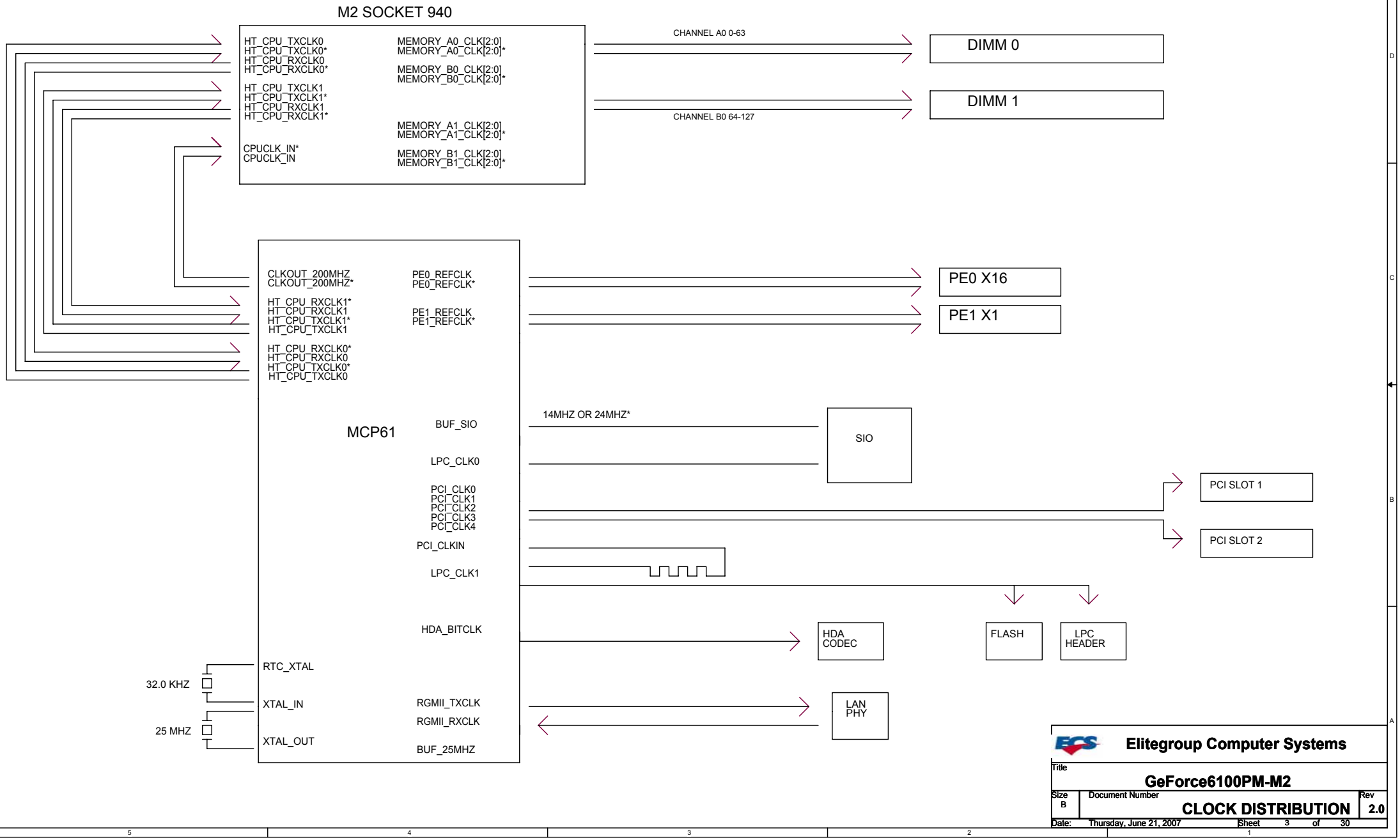
Size: B | Document Number: COVER PAGE | Rev: 2.0

Date: Monday, November 19, 2007 | Sheet 1 of 30

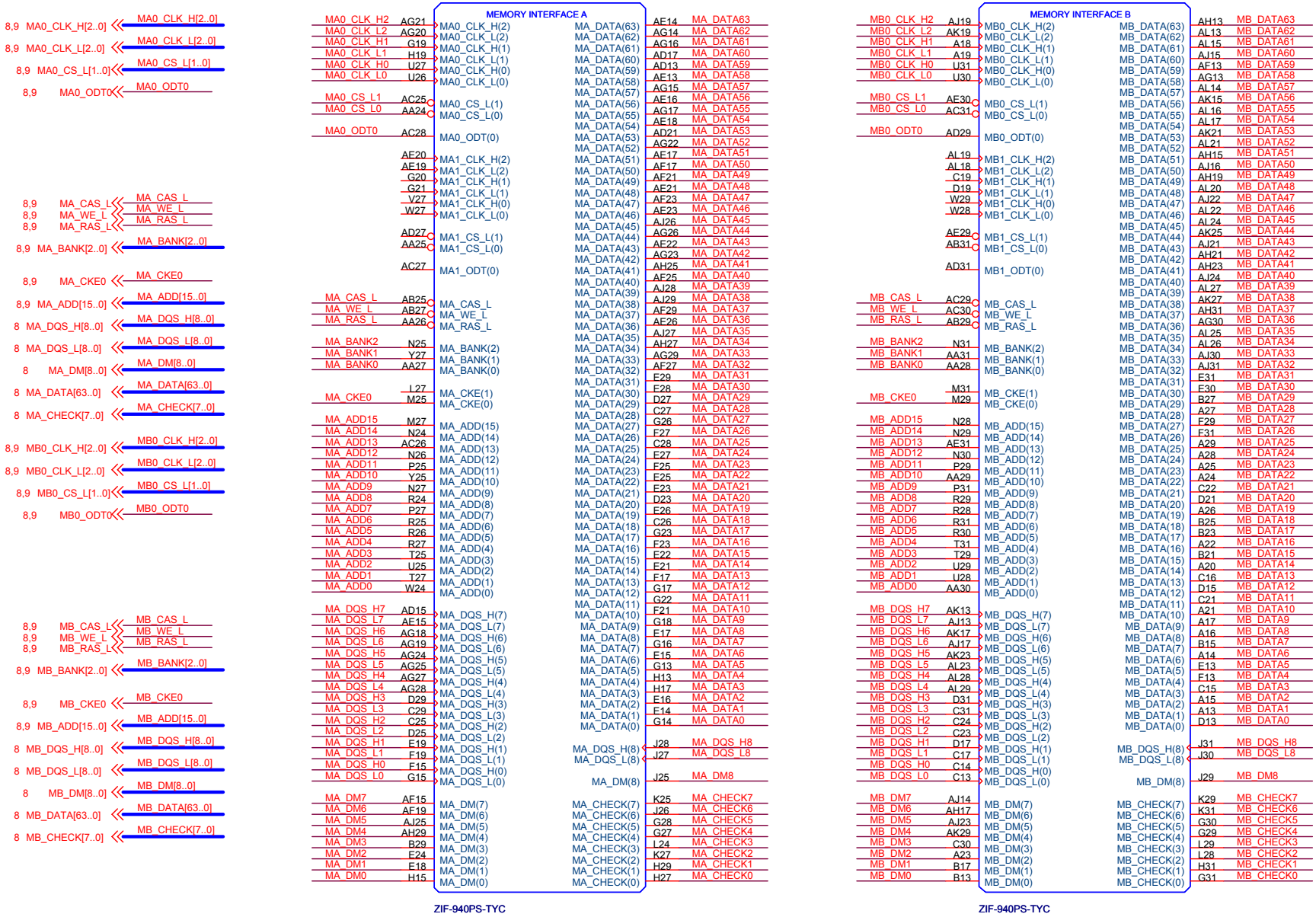
BLOCK DIAGRAM



Elitegroup Computer Systems		
Title: GeForce6100PM-M		
Size: B	Document Number: BLOCK DIAGRAM	Rev: 2.0
Date: Monday, November 19, 2007	Sheet 2 of 30	

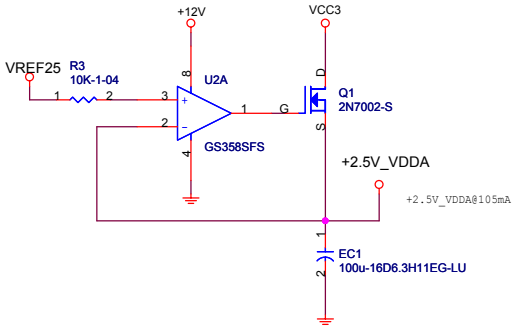


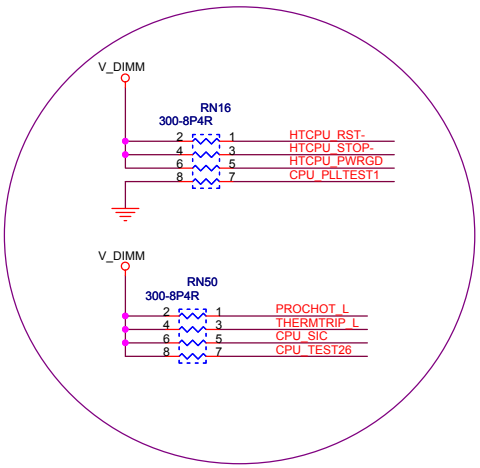
<OrgAddr1>	Elitegroup Computer Systems
Title	GeForce6100PM-M2
Size B	Document Number CPU M2-1 HyperTransport Rev 1.0
Date: Thursday, June 21, 2007	Sheet 4 of 30

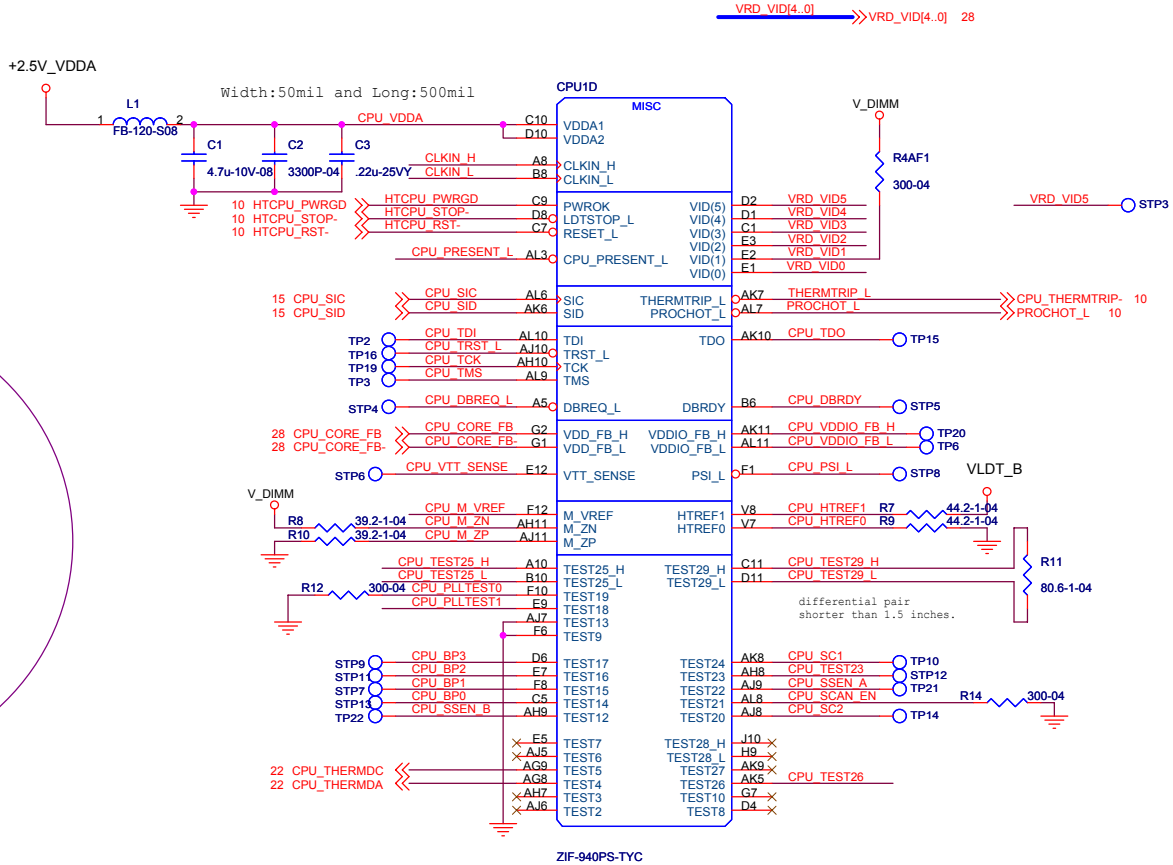


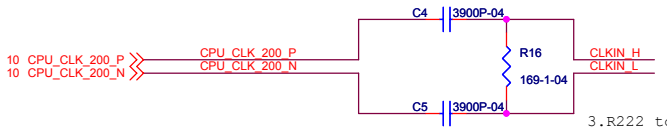
Elitegroup Computer Systems	
Title	GeForce6100PM-M2
Size: B	Document Number: CPU M2-2 DDR2
Rev	2.0
Date: Thursday, June 21, 2007	Sheet 5 of 30

+2.5V_VDDA for CPU PLL



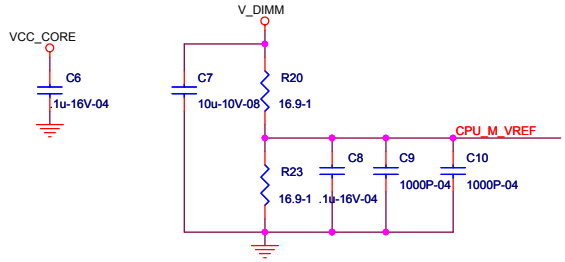


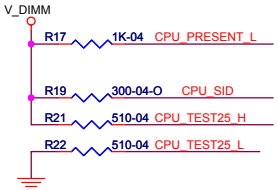




3.R222 to CPU <600mil

4.C211,C218 to CPU <1250mil





Elitegroup Computer Systems

Title: GeForce6100PM-M2

Size: B | Document Number: CPU M2-3 Miscellany | Rev: 2.0

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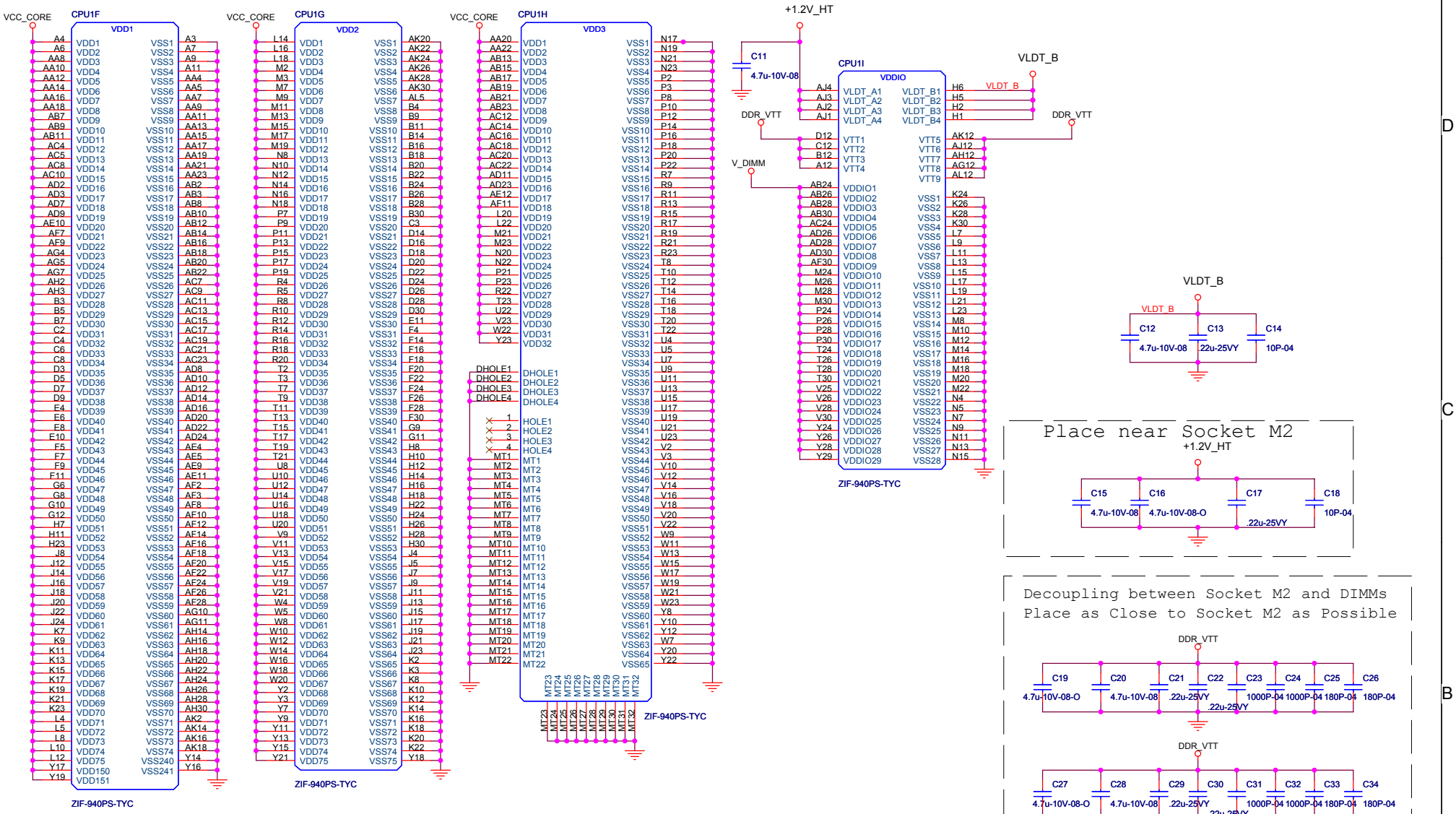

Elitegroup Computer Systems

Title: GeForce6100PM-M2

Size: B | Document Number: CPU M2-4 Power and Ground | Rev: 2.0

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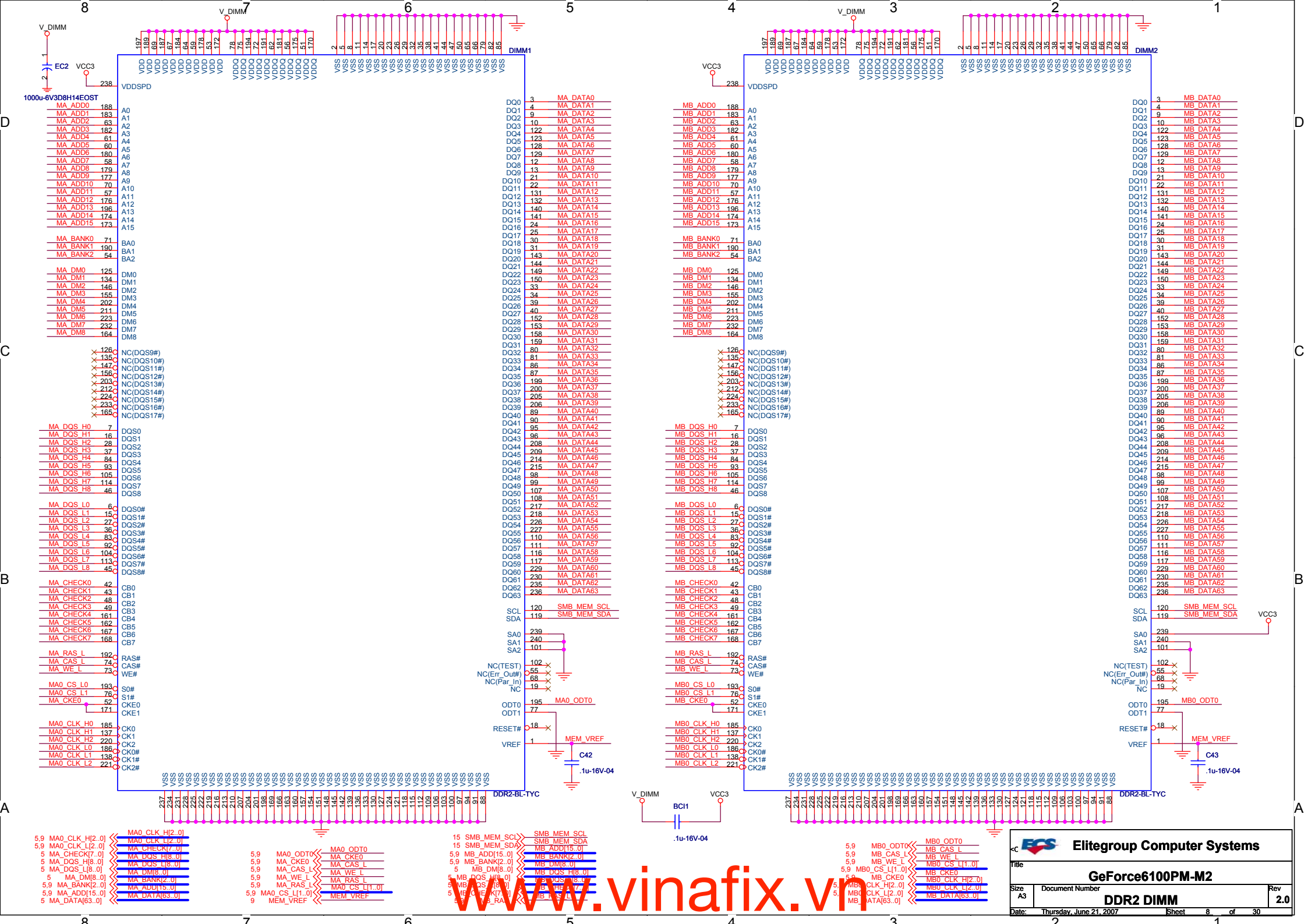

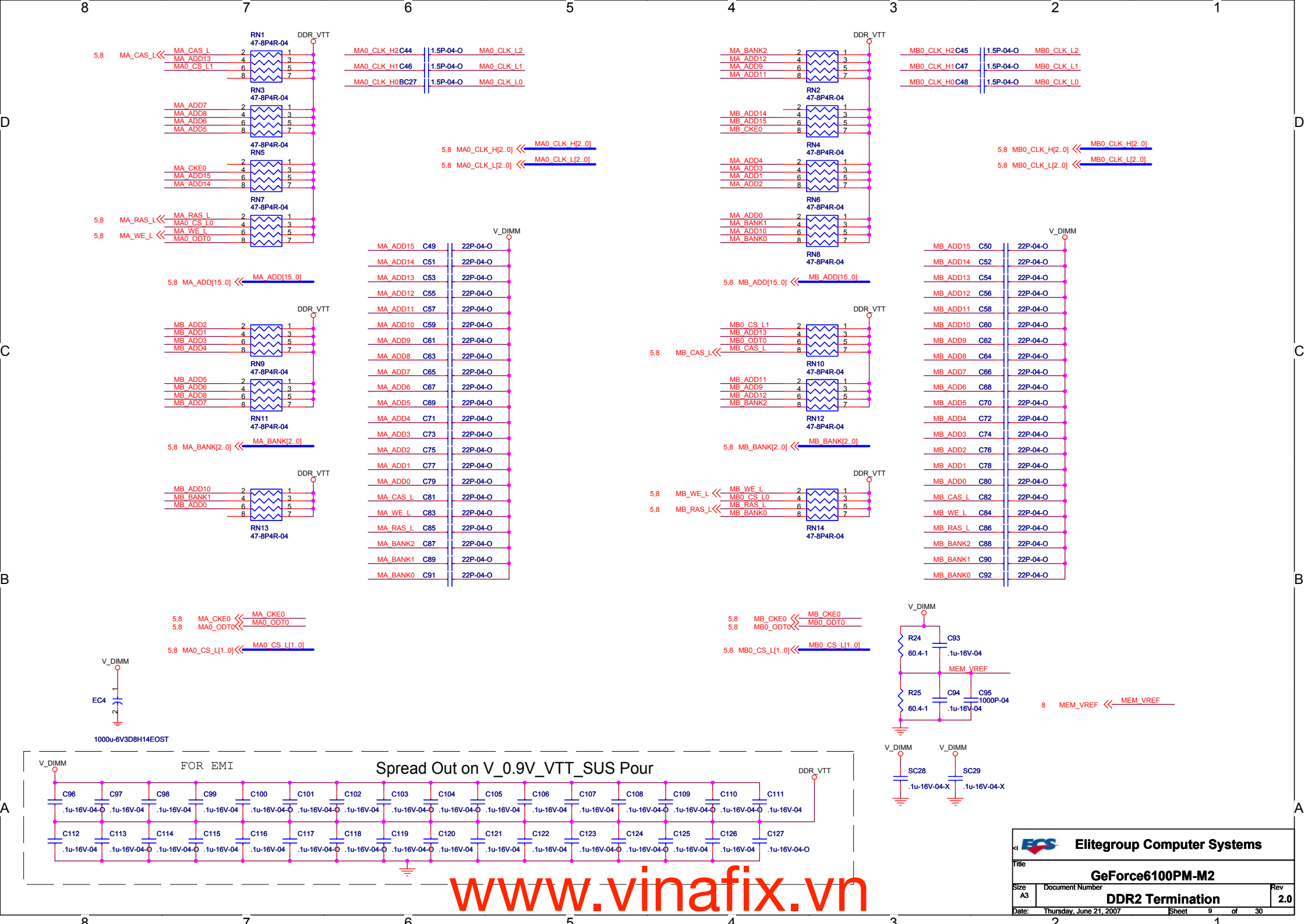

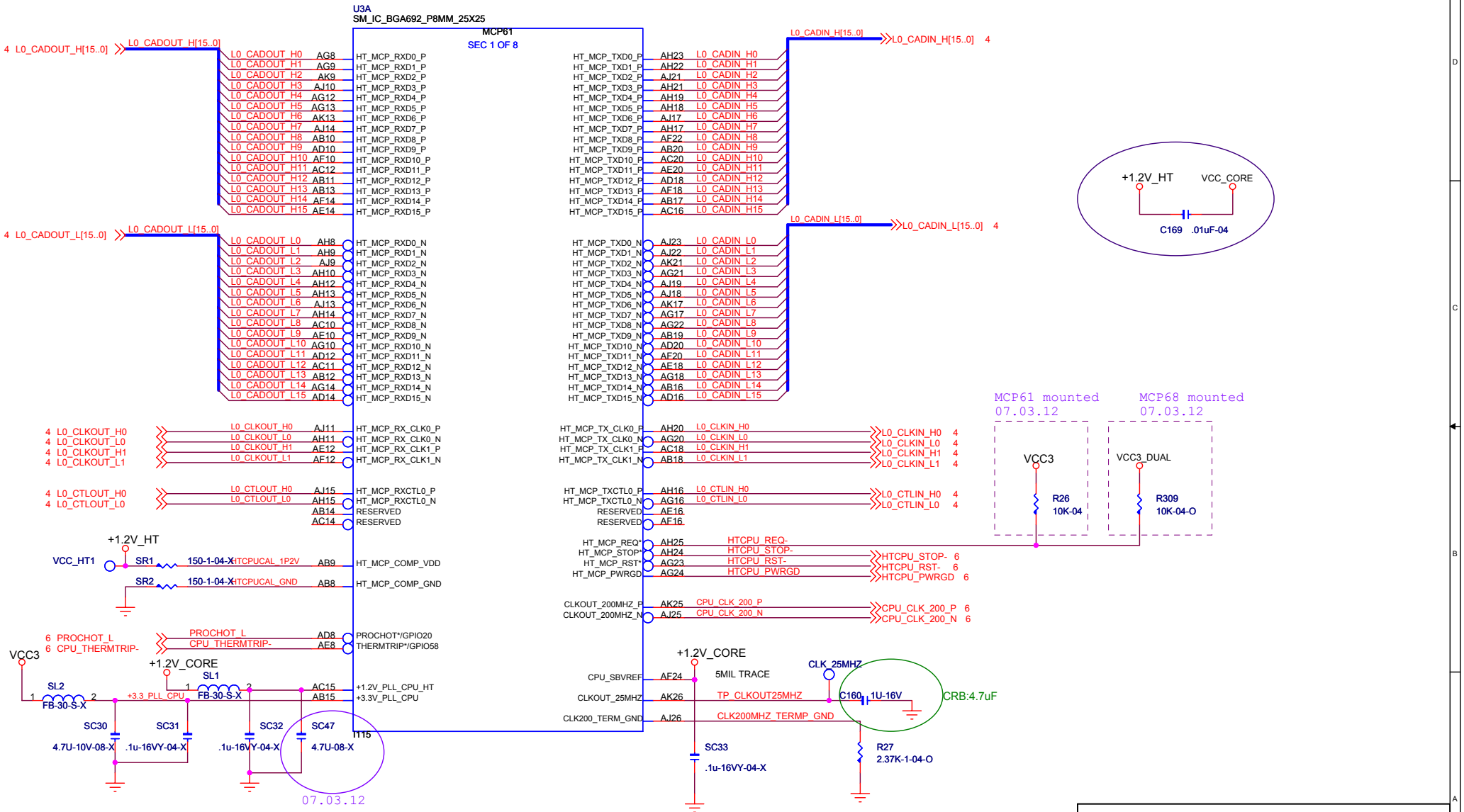

Elitegroup Computer Systems		
Title	GeForce6100PM-M2	
Size B	Document Number MCP61 HT	Rev 2.0
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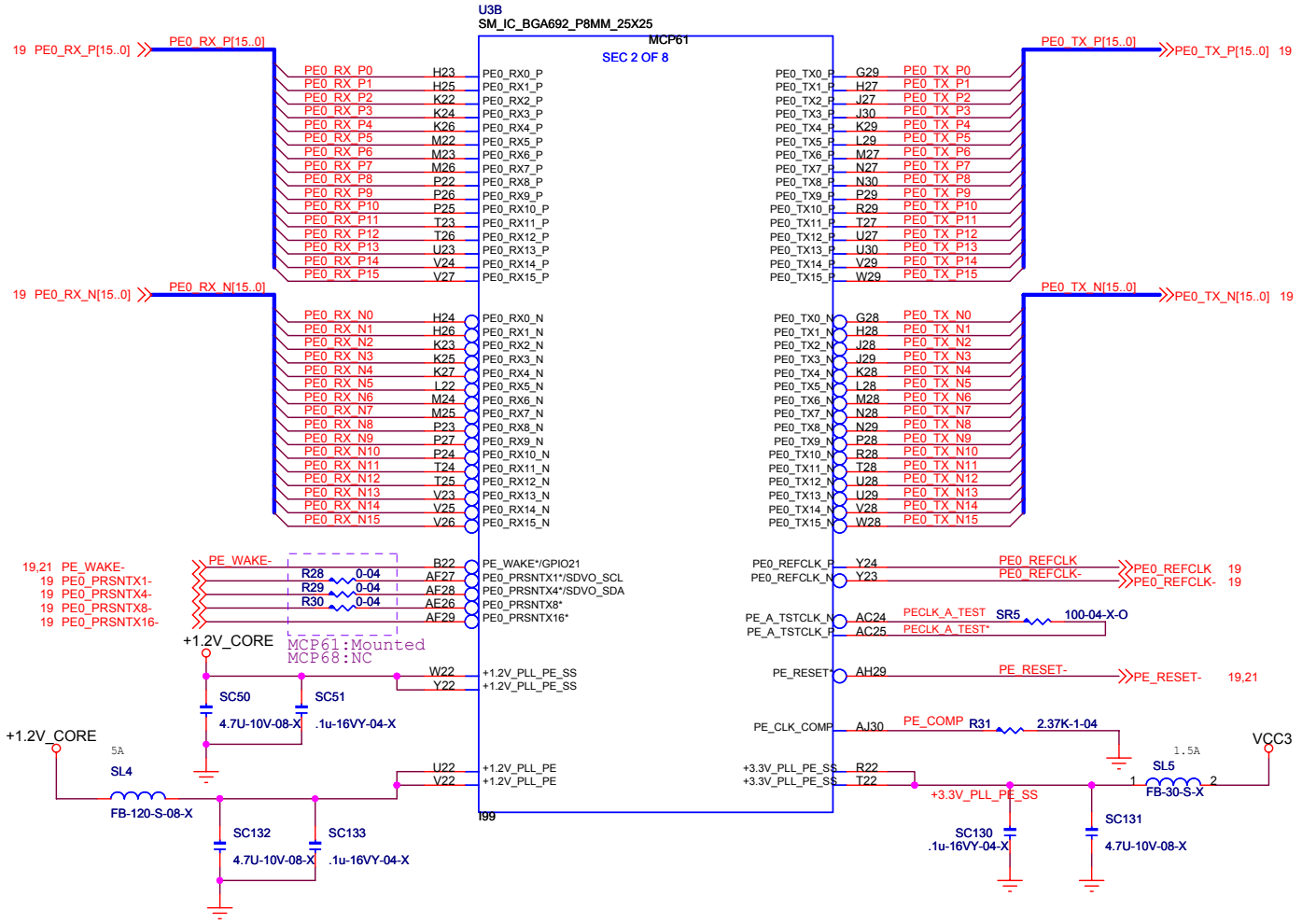


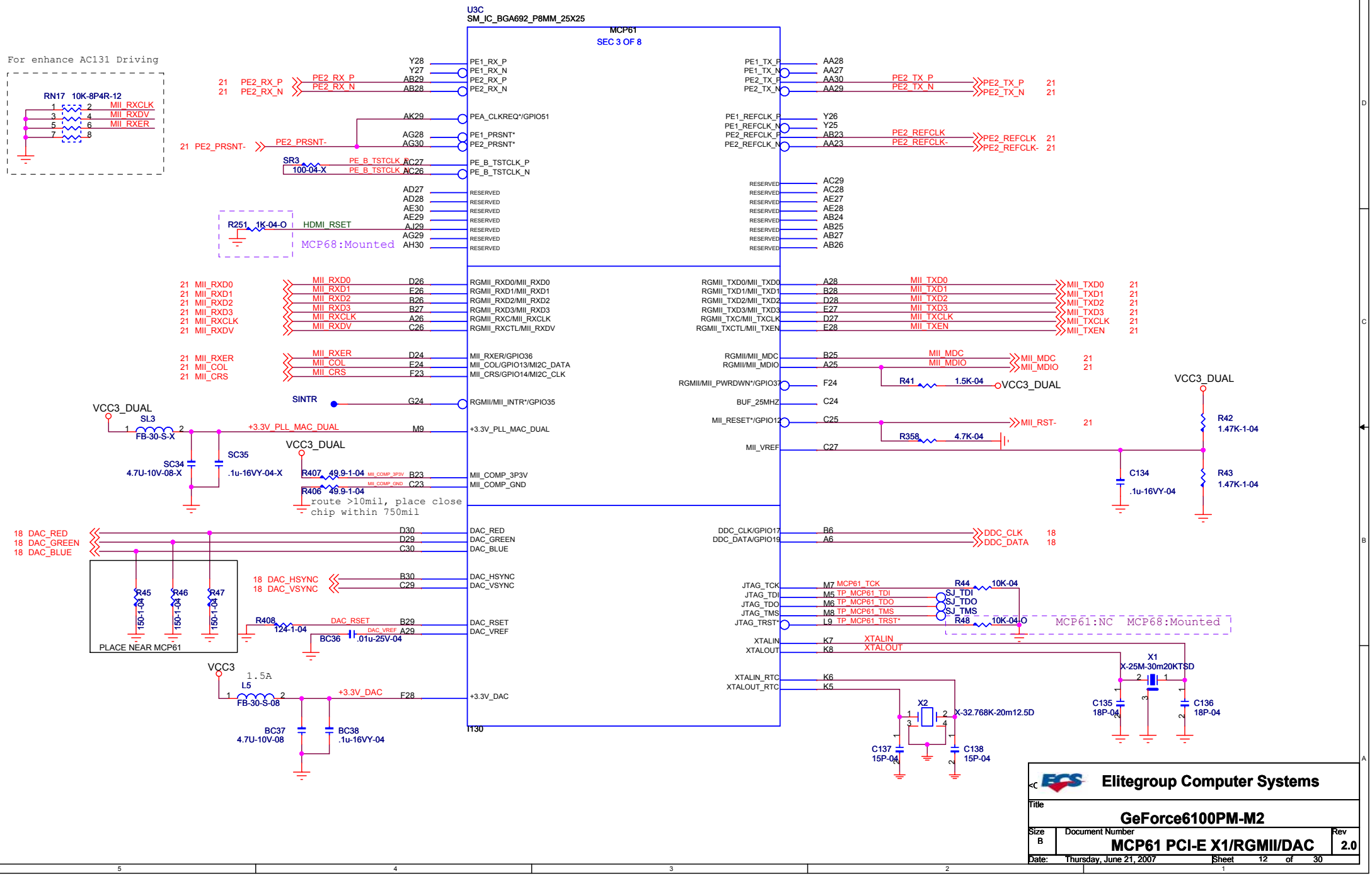
MCP61 Pin Define	MCP68 Pin Define
PRSNTX1(AF27)	+3.3V_HDMI_PLL_HVDD
PRSNTX4(AF28)	HDCP_ROM_SCLK
PRSNTX8(AE26)	+3.3V_HDMI
GND(AF30)	HDCP_ROM_SDATA

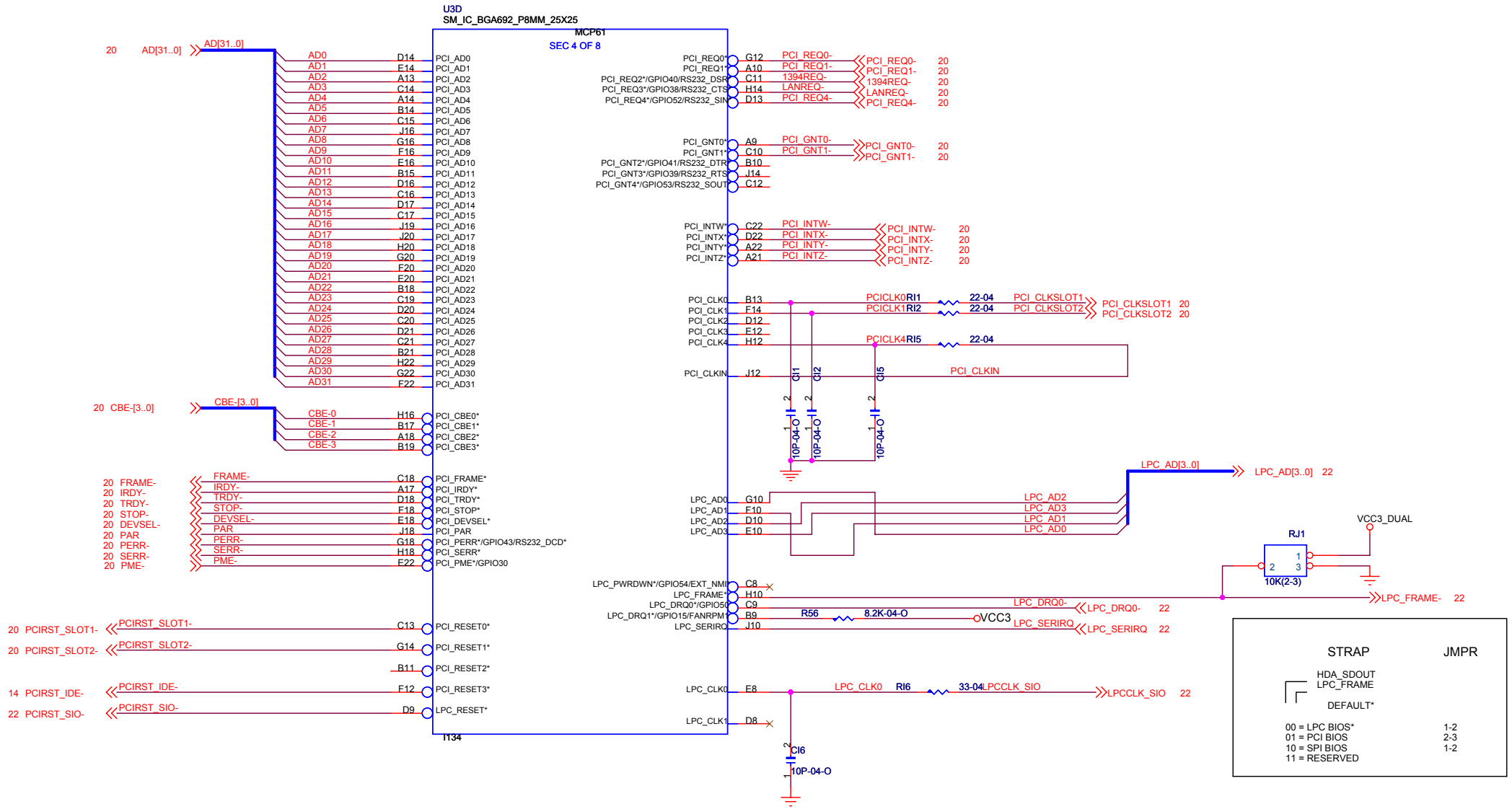
Elitegroup Computer Systems

Title: GeForce6100PM-M2

Size: B | Document Number: MCP61 PCI-E X16 | Rev: 2.0

Date: Thursday, June 21, 2007 | Sheet 11 of 30





Elitegroup Computer Systems

Title: GeForce6100PM-M2

Size: Custom | Document Number: MCP61 PCI | Rev: 2.0

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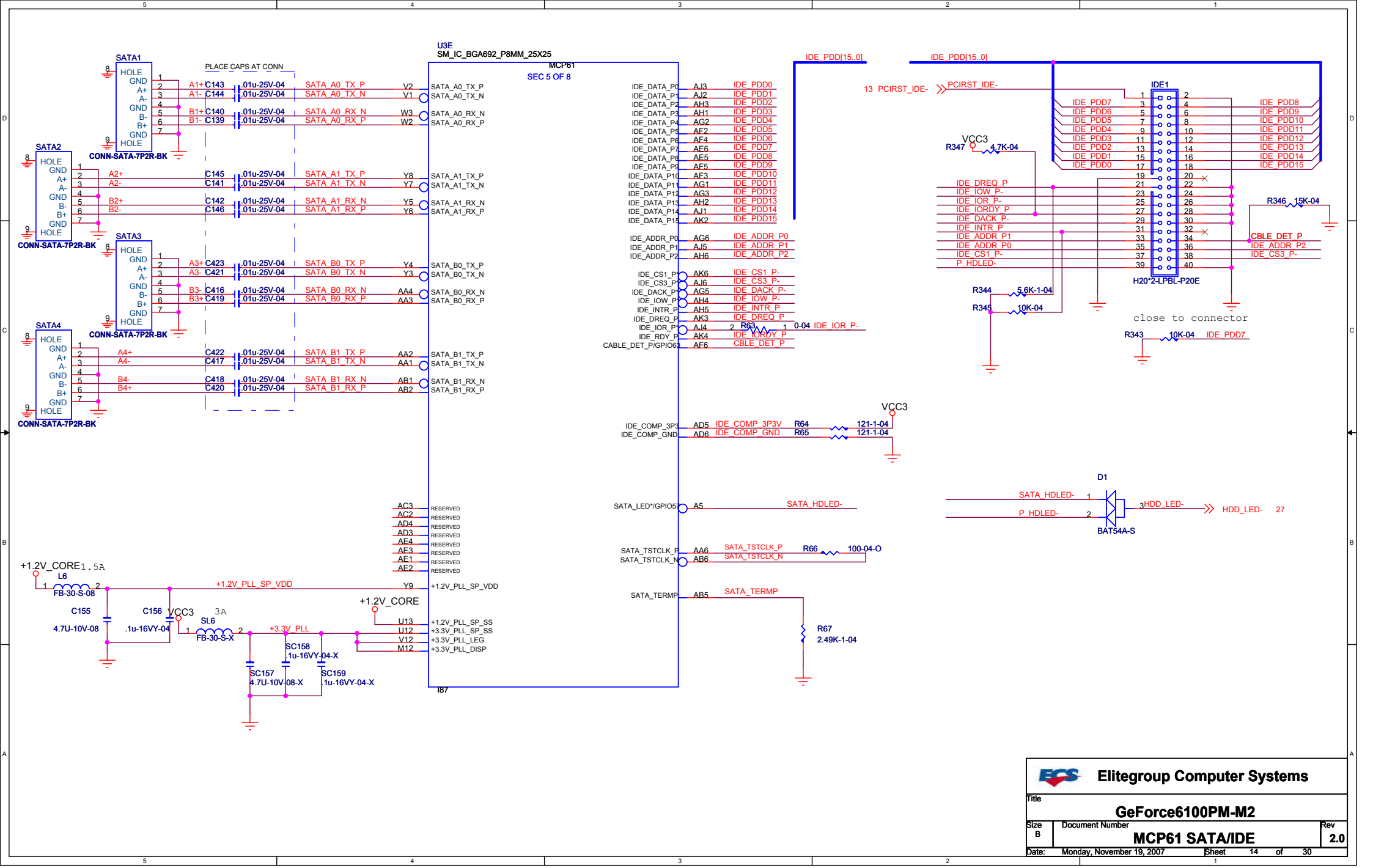

Elitegroup Computer Systems		
Title	GeForce6100PM-M2	
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REMOVE FOR PRODUCTION

USB0~3 --> BACK CONNECTORS
USB4~11 --> ON-BOARD HEADER

Location Unimportant.

U3F
SM_IC_BGA692_P8MM_25X25
MCP61
SEC 6 OF 8

AC_RST*
1 = *RGMII
0 = MII

AC_SYNC
(SIO CLK)
1 = *24MHZ
0 = 14.318MHZ

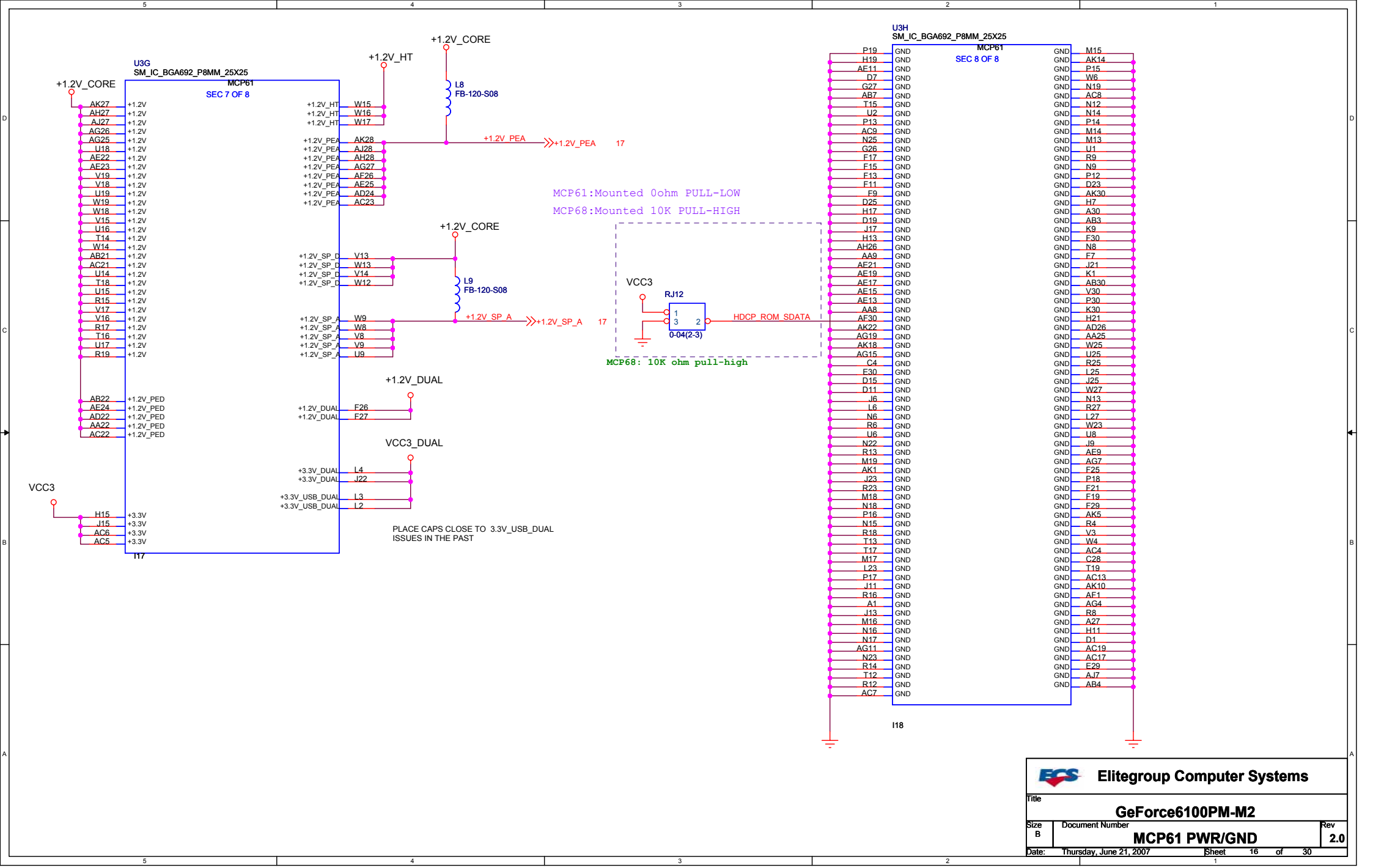
* = DEFAULT

MCP61 SPI CLK STRAP	MCP68 SPI CLK STRAP
SPI_DO | SPI_CLK	SPI_DO | SPI_CLK
00 = 500KHZ 01 = 1.8MHZ 10 = 2.5MHZ *11 = 25MHZ	00 = 31MHZ 01 = 42MHZ *10 = 25MHZ 11 = 1MHZ
*DEFAULT	*DEFAULT

GPIO6 : HI--Pseudo S3
GPIO6 : LOW--Real S3

Elitegroup Computer Systems

Title	GeForce6100PM-M2	
Size B	Document Number MCP61 AUDIO/USB/MISC	Rev 2.0
Date: Wednesday, July 11, 2007	Sheet 15 of 30	



Elitegroup Computer Systems	
Title	GeForce6100PM-M2
Size: B	Document Number: MCP61 PWR/GND
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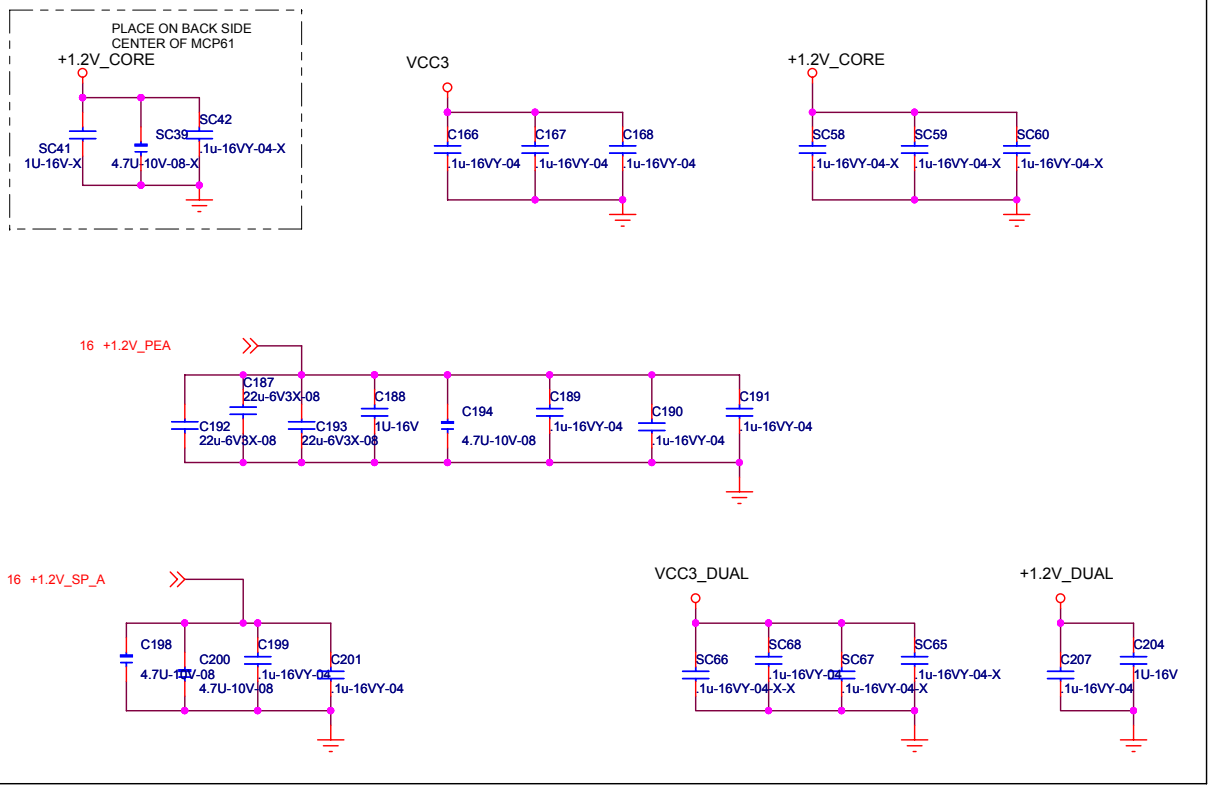
MCP61/8 DECOUPLING/EMI



MCP61 INTERNAL PULL-UP/DWN'S

PE0_PRSNTX16*	10K PU TO 3.3V
PE0_PRSNTX8*	10K PU TO 3.3V
PE1_PRSNT*	10K PU TO 3.3V
PE2_PRSNT*	10K PU TO 3.3V
PE1_CLKREQ*	10K PU TO 3.3V
PCI_PME*/GPIO_30	8.2K PU TO 3.3V_DUAL
LPC_AD0	8.2K PU TO 3.3V
LPC_AD1	8.2K PU TO 3.3V
LPC_AD2	8.2K PU TO 3.3V
LPC_AD3	8.2K PU TO 3.3V
LPC_DRQ1/LPC_CS*	8.2K PU TO 3.3V
LPC_DRQ0*	8.2K PU TO 3.3V
LPC_SERIRQ	10K PU TO 3.3V
HDA_SDATA_IN1/GPIO_23/MGPIO_0	10K PD TO GND
HDA_SDATA_IN0/GPIO_22	10K PD TO GND
JTAG_TMS	10K PU TO 3.3V
JTAG_TRST*	10K PD TO GND
JTAG_TDI	10K PU TO 3.3V
A20GATE	10K PU TO 3.3V
PE_WAKE*	10K PU TO 3.3V_DUAL
EXT_SMI*/GPIO32	10K PU TO 3.3V_DUAL
THERM*/GPIO_59	10K PU TO 3.3V
KBRDRSTIN*/GPIO_58	10K PU TO 3.3V
RI*/GPIO_33	10K PU TO 3.3V_DUAL
SIO_PME*/GPIO_31/MGPIO_2	10K PU TO 3.3V_DUAL
PWRBTN*	10K PU TO 3.3V_DUAL
RSTBTN*	10K PU TO 3.3V_DUAL

MCP61 SPI CLK STRAP	MCP68 SPI CLK STRAP
SPI_DO | SPI_CLK	SPI_DO | SPI_CLK
00 = 500KHZ	00 = 31MHZ
01 = 1.8MHZ	01 = 42MHZ
10 = 2.5MHZ	*10 = 25MHZ
*11 = 25MHZ	11 = 1MHZ
*DEFAULT	*DEFAULT

Elitegroup Computer Systems

Title: GeForce6100PM-M2

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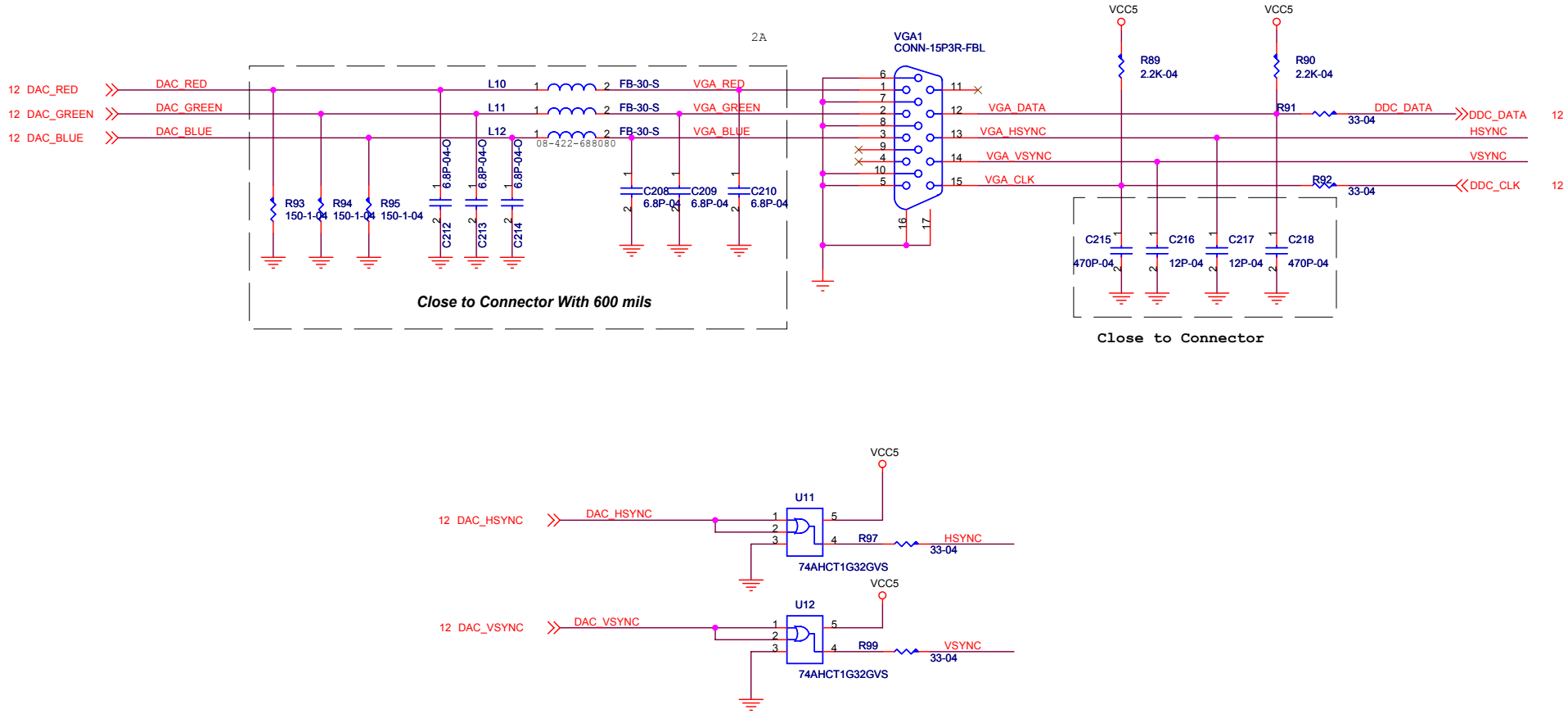

Elitegroup Computer Systems		
Title: GeForce6100PM-M2		
Size: B	Document Number: VGA	Rev: 2.0
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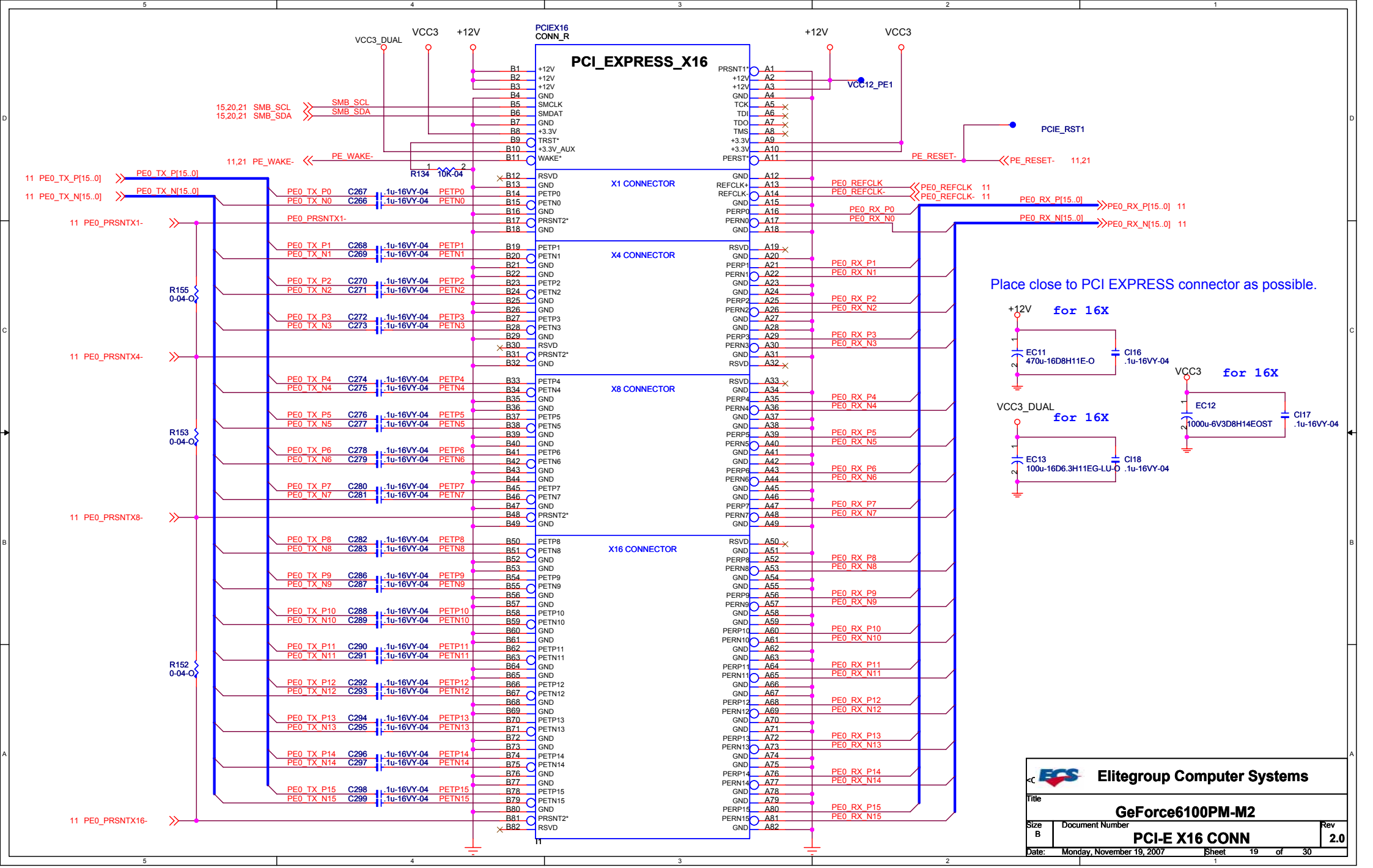

Elitegroup Computer Systems

Title: GeForce6100PM-M2

Size: B | Document Number: PCI-E X16 CONN | Rev: 2.0

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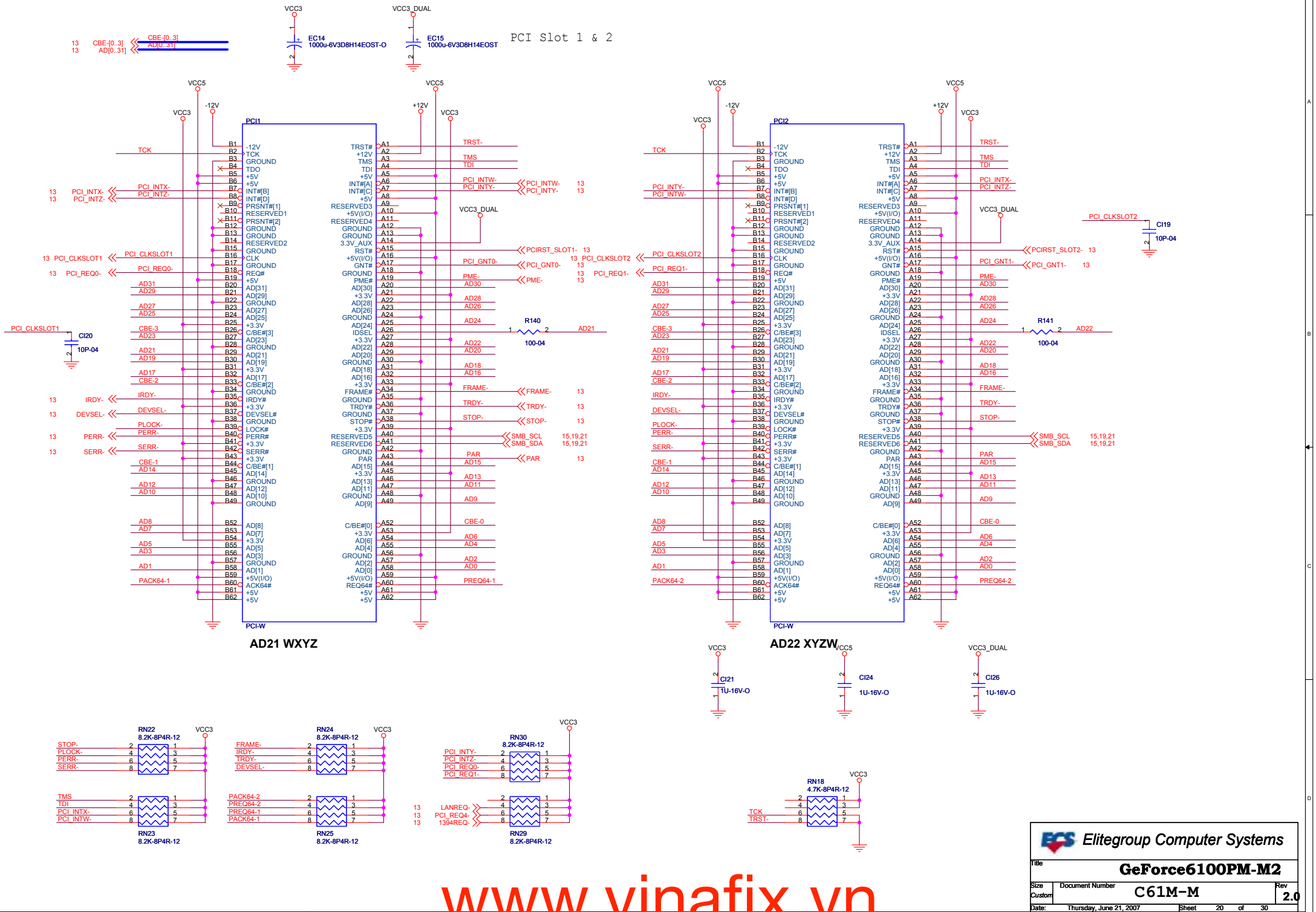
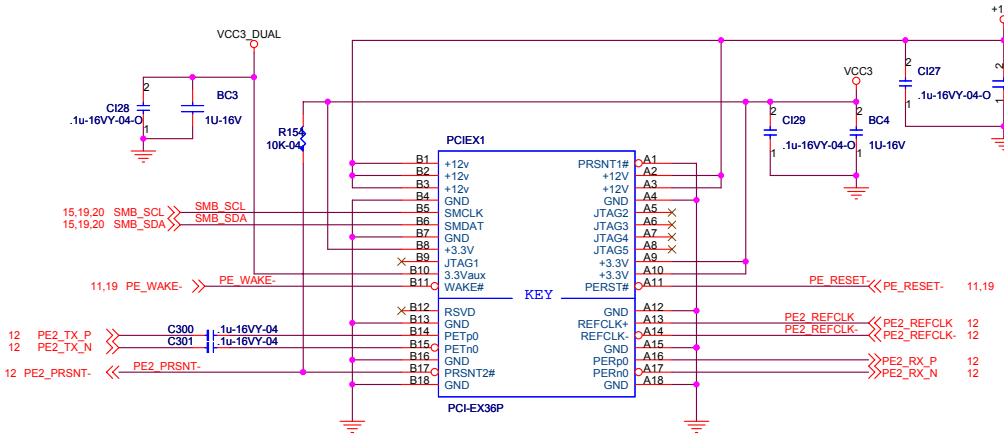
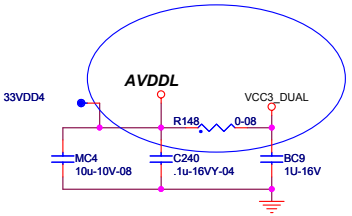
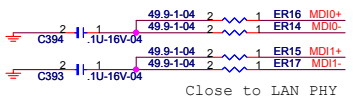
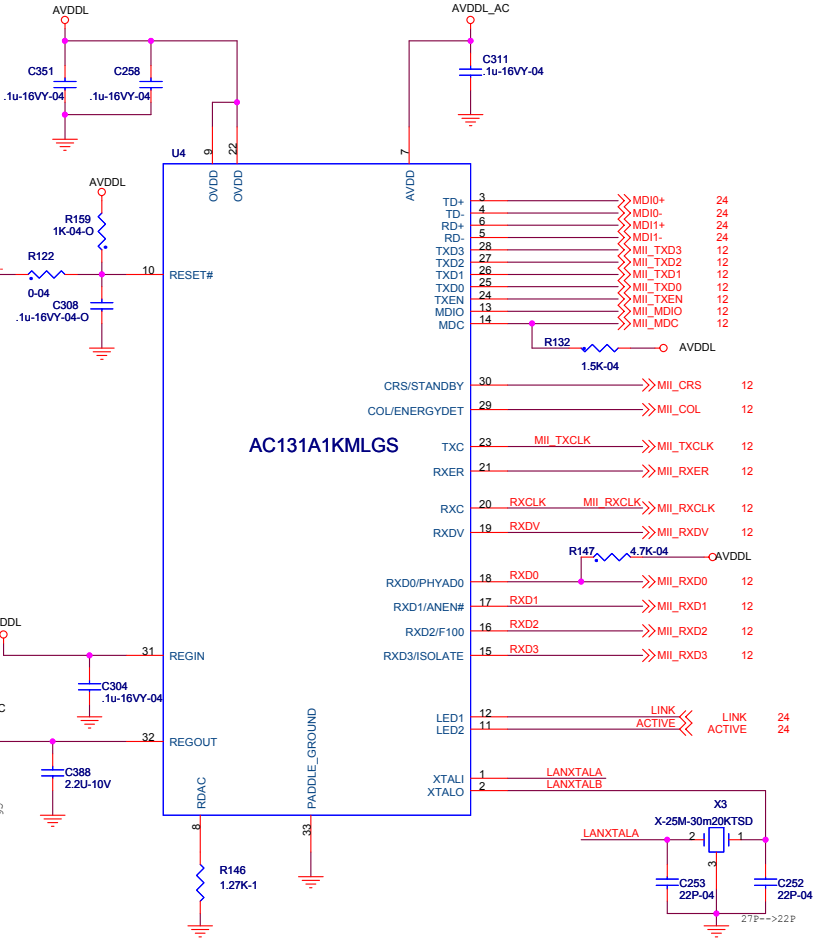


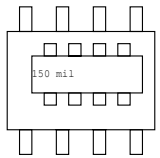


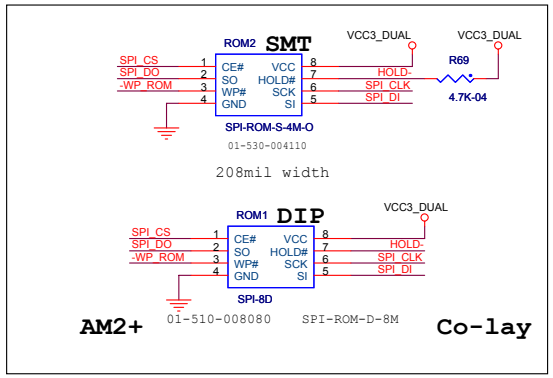




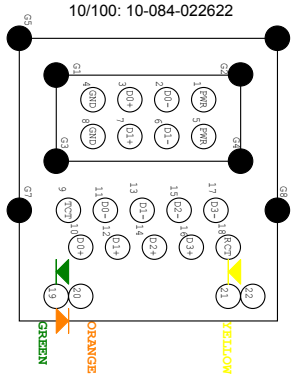


SO8 MN and MW co-layout layout draw





for SPI ROM



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Title	GeForce6100PM-M2	
Size: Custom	Document Number: PCI-E X1/AC131/SPI	Rev: 2.0
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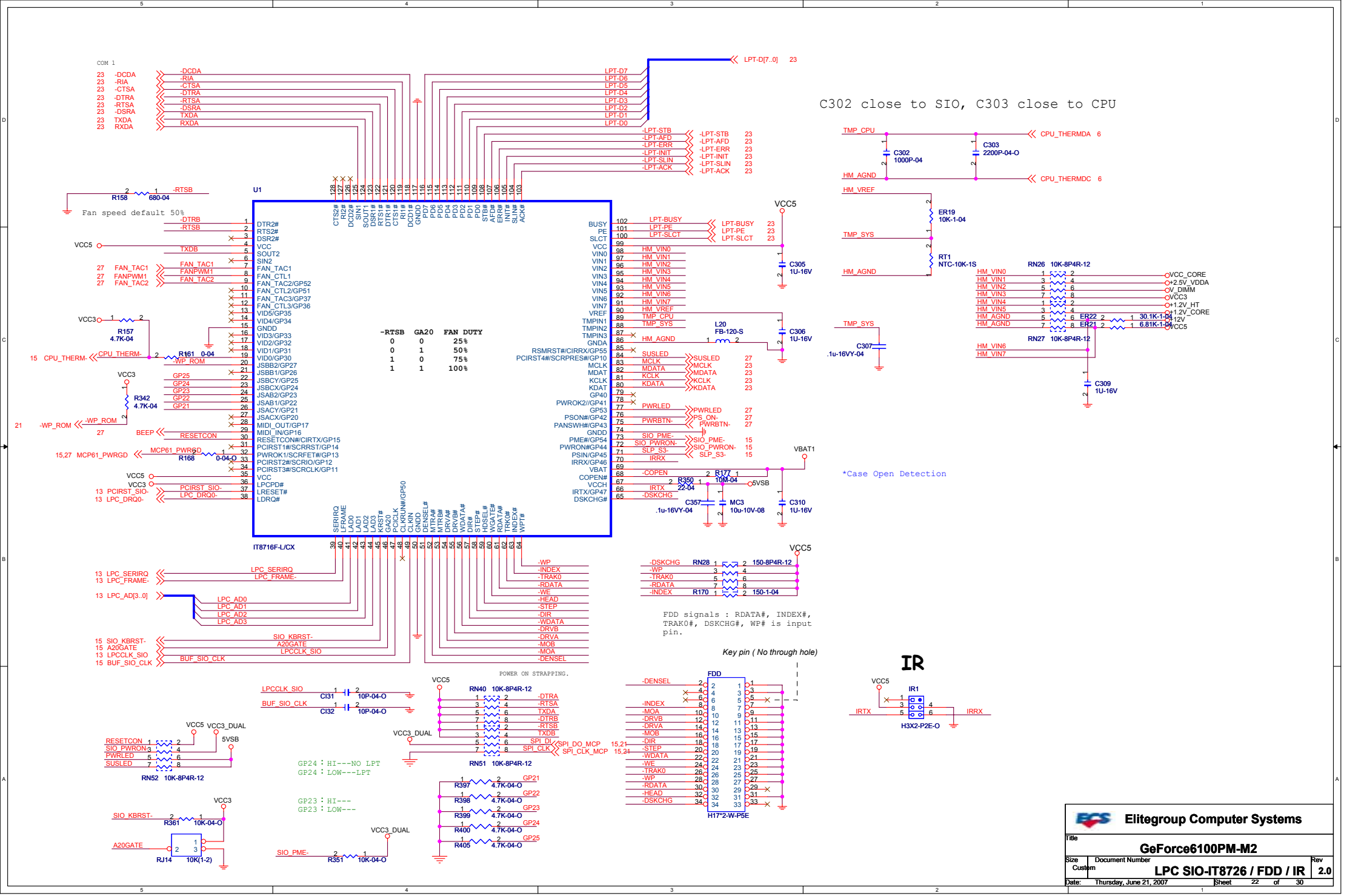

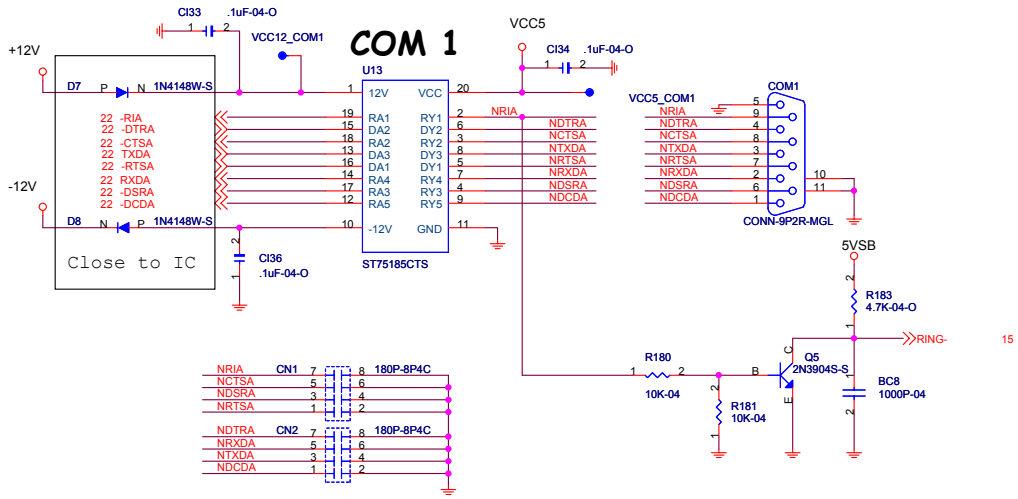

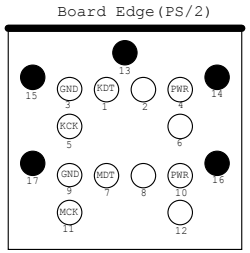

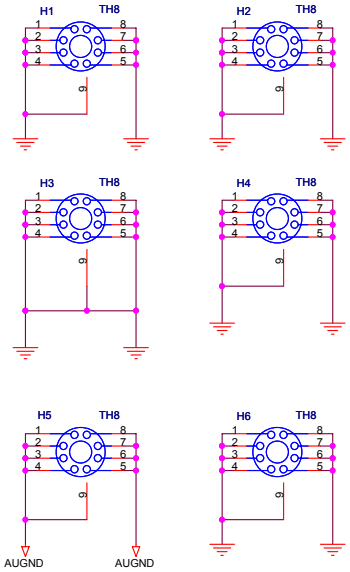

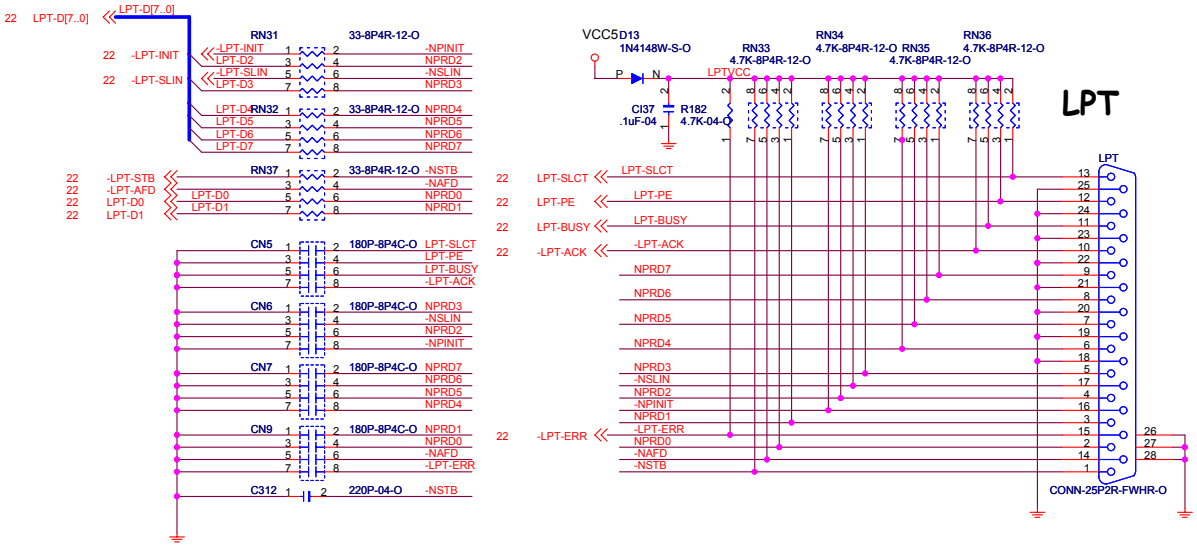

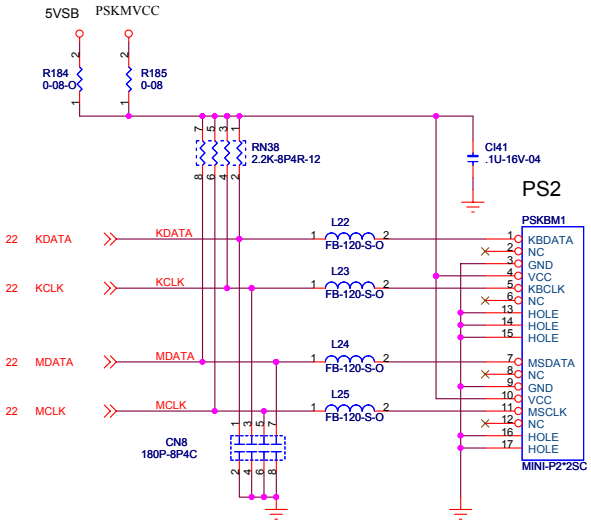

<Org	Elitegroup Computer Systems	
Title	GeForce6100PM-M2	
Size: Custom	Document Number: LPT / COM / PS2	Rev: 2.0
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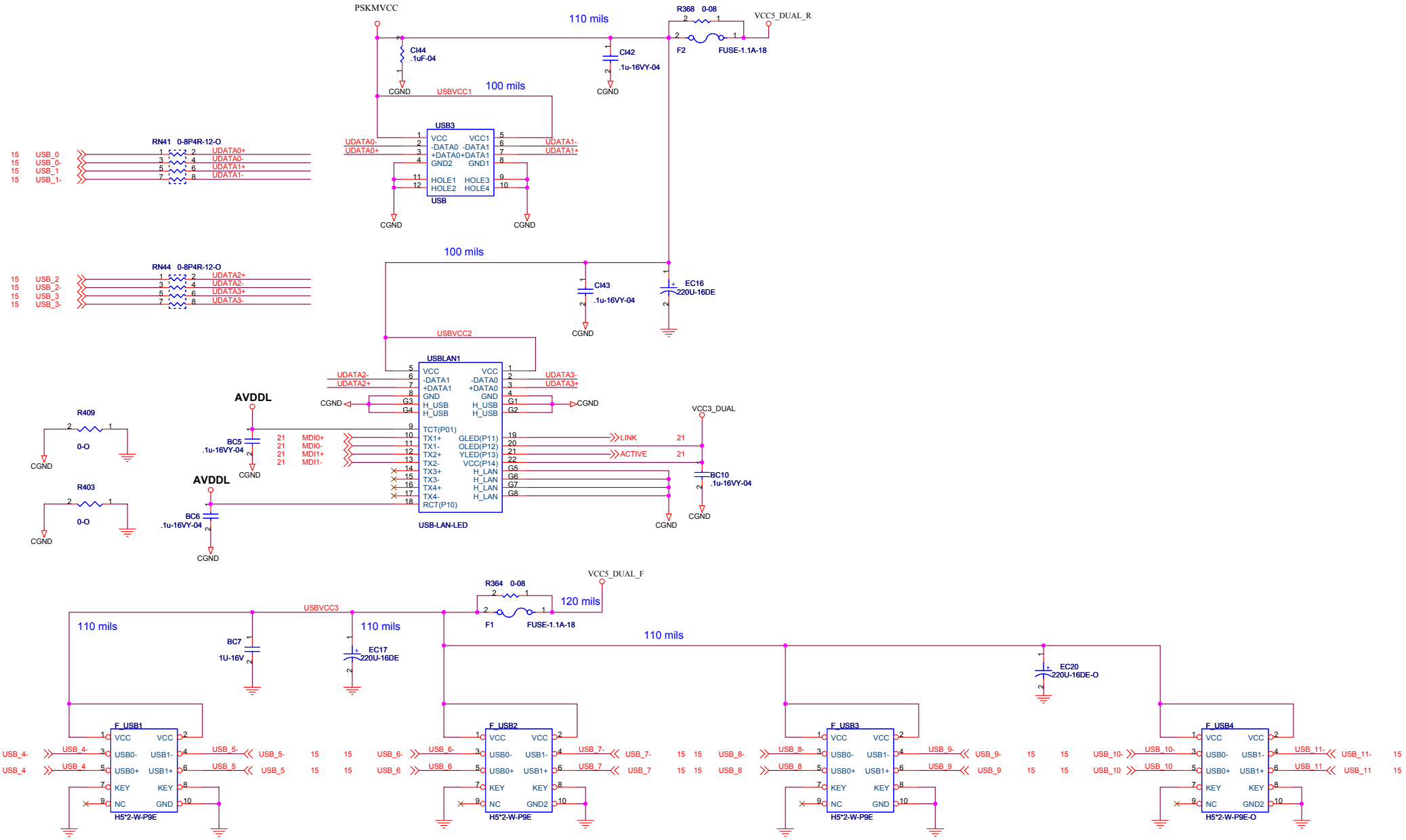

Elitegroup Computer Systems

Title: GeForce6100PM-M2

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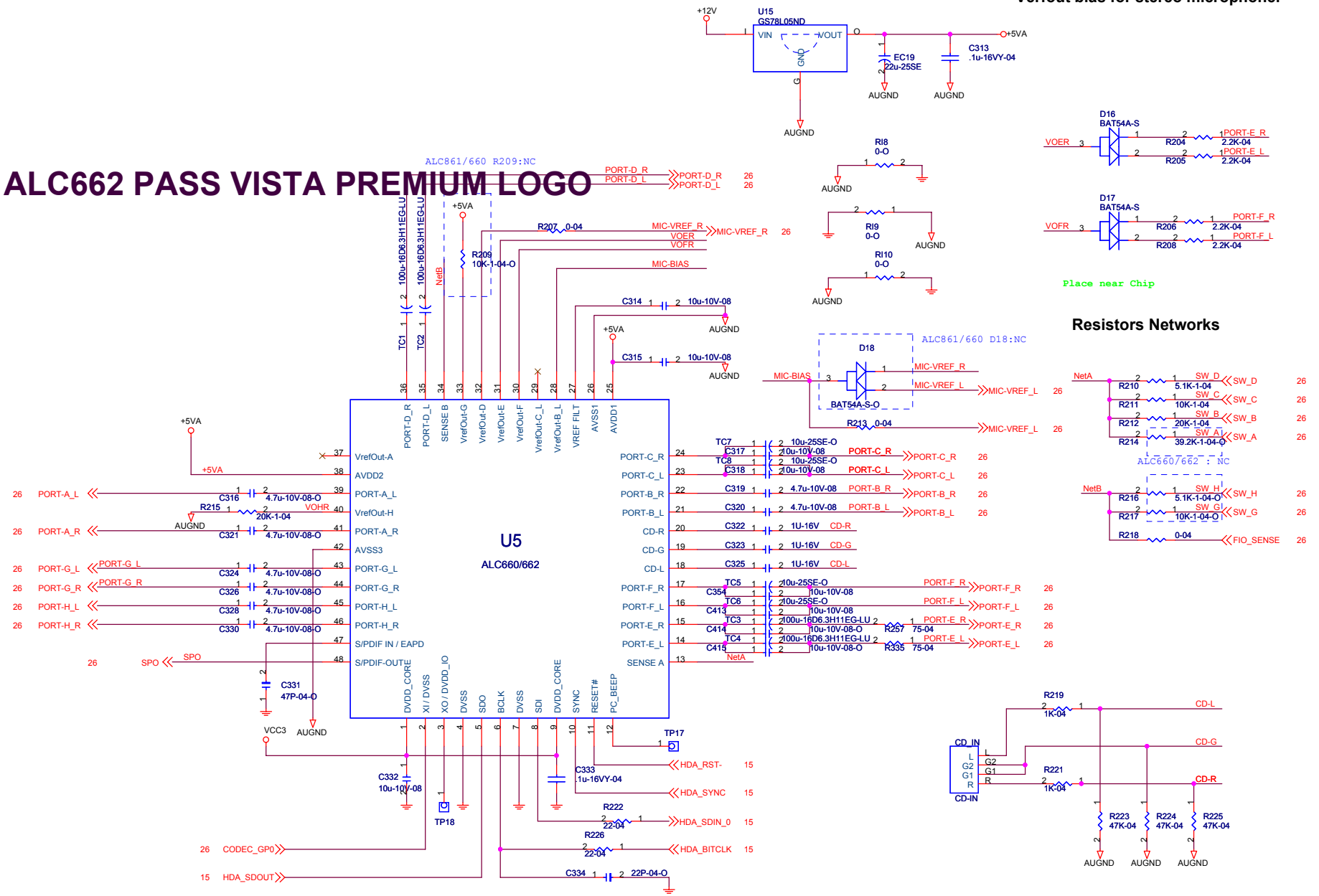
Improve the background noise of MIC boost

Verfout bias for stereo microphone.

ALC662 PASS VISTA PREMIUM LOGO



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Title	GeForce6100PM-M2	
Size: Custom	Document Number: AUDIO ALC883/861/660/662	Rev: 2.0
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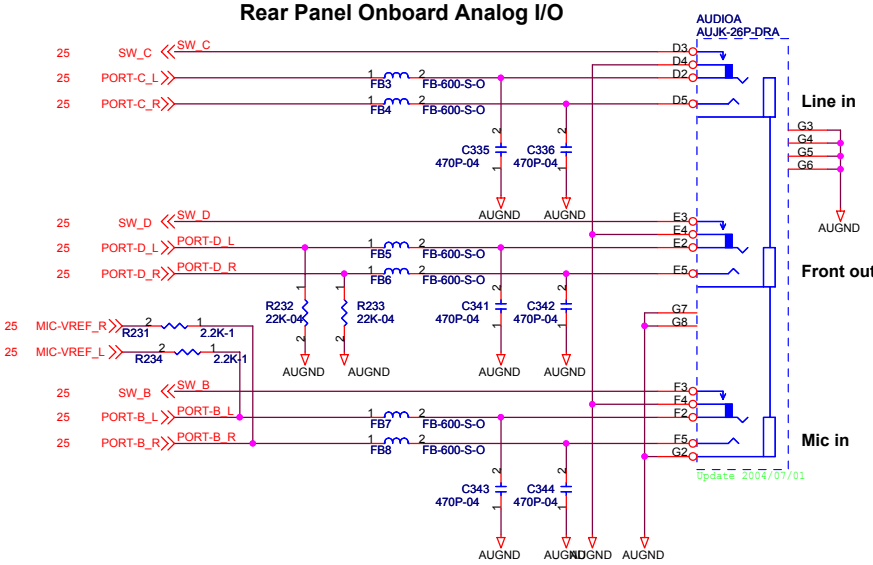
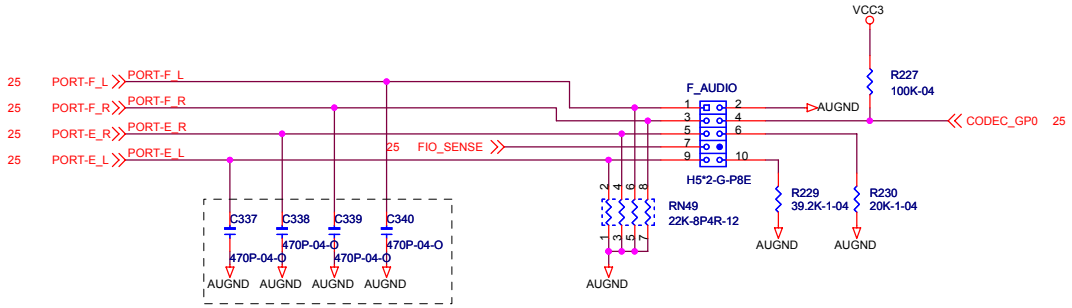
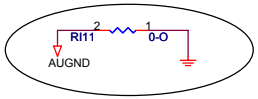
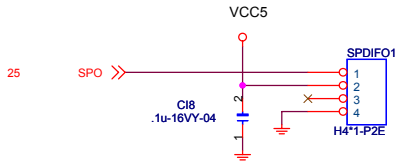
Rear Panel Onboard Analog I/O



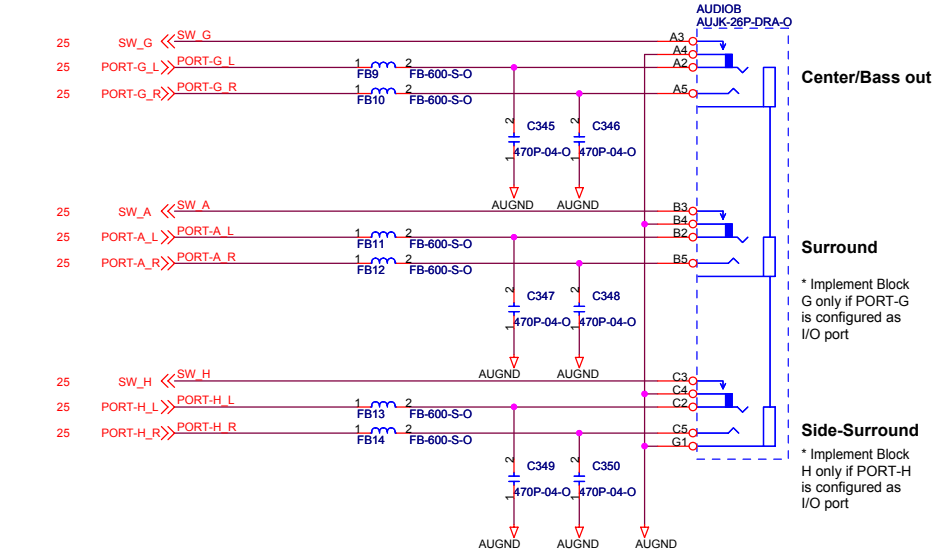




SPDIF Out



Rear Panel (Optional Rear Audio Panel)

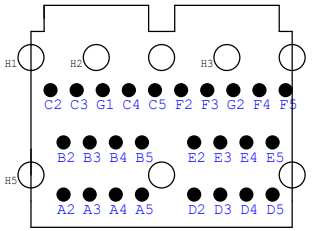


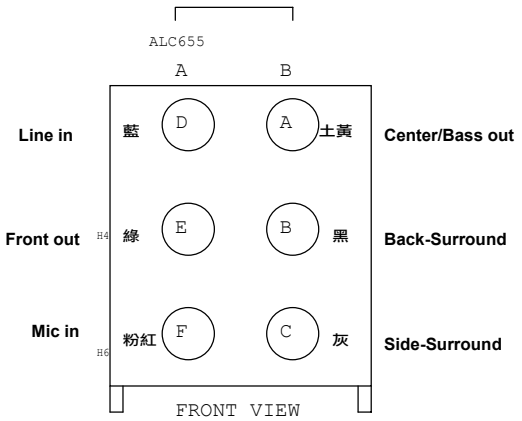
* Implement Block G only if PORT-G is configured as I/O port

* Implement Block H only if PORT-H is configured as I/O port

The schematic should consist with PINs define of I/O connecter.

TOP VIEW





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Title	GeForce6100PM-M2		
Size: Custom	Document Number: AUDIO ALC861/660/662(PANEL)		Rev: 2.0
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