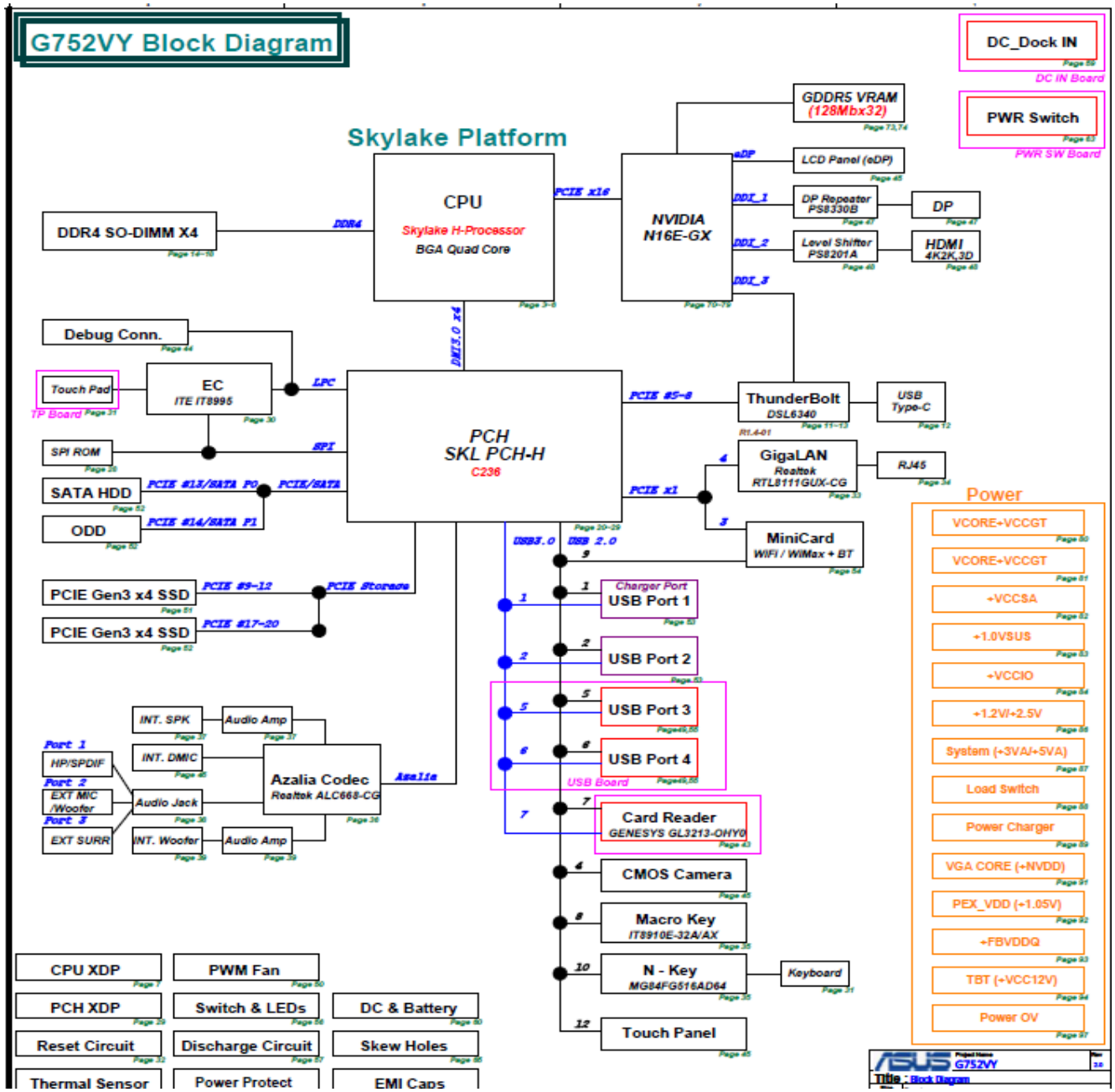
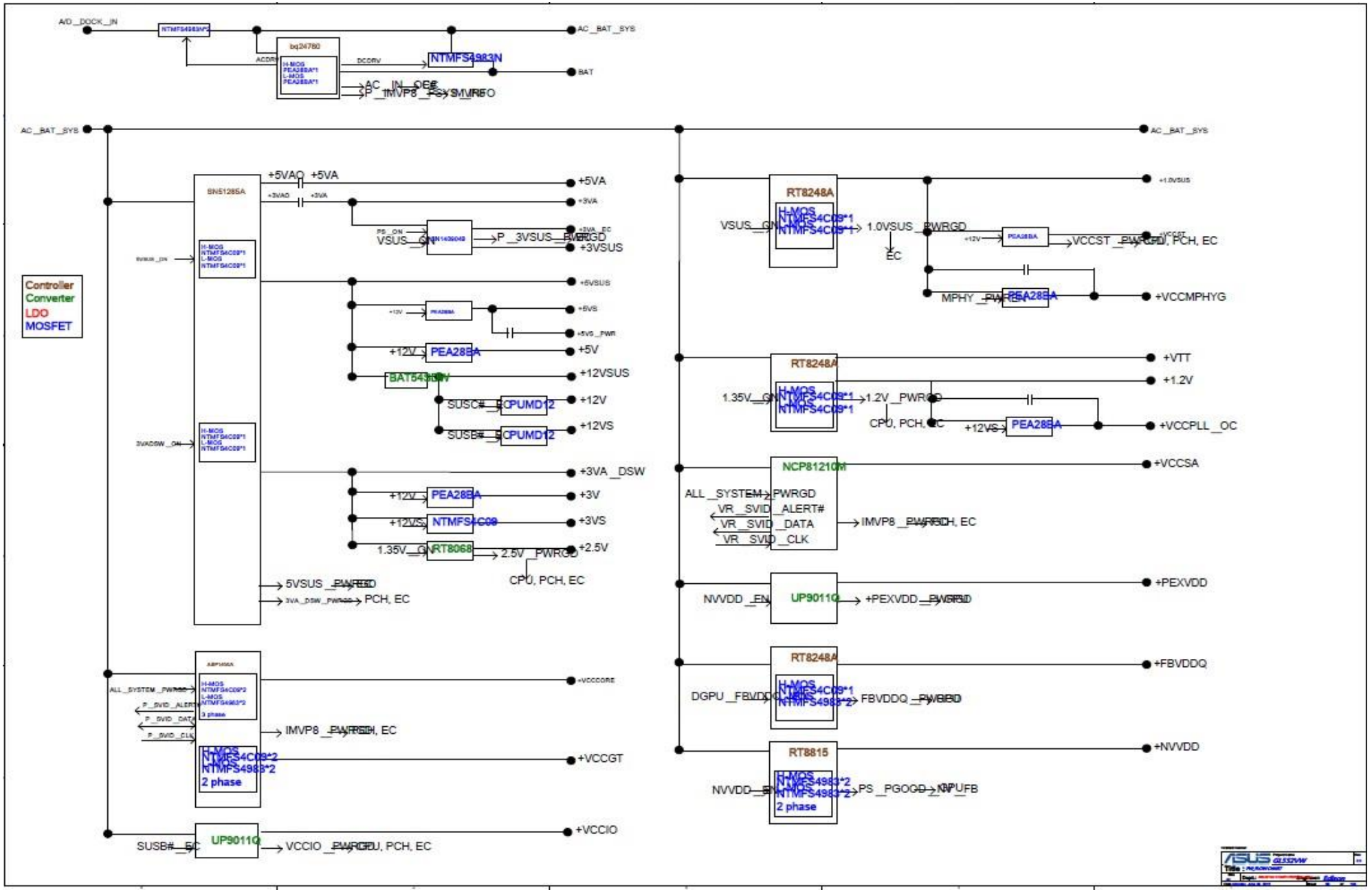
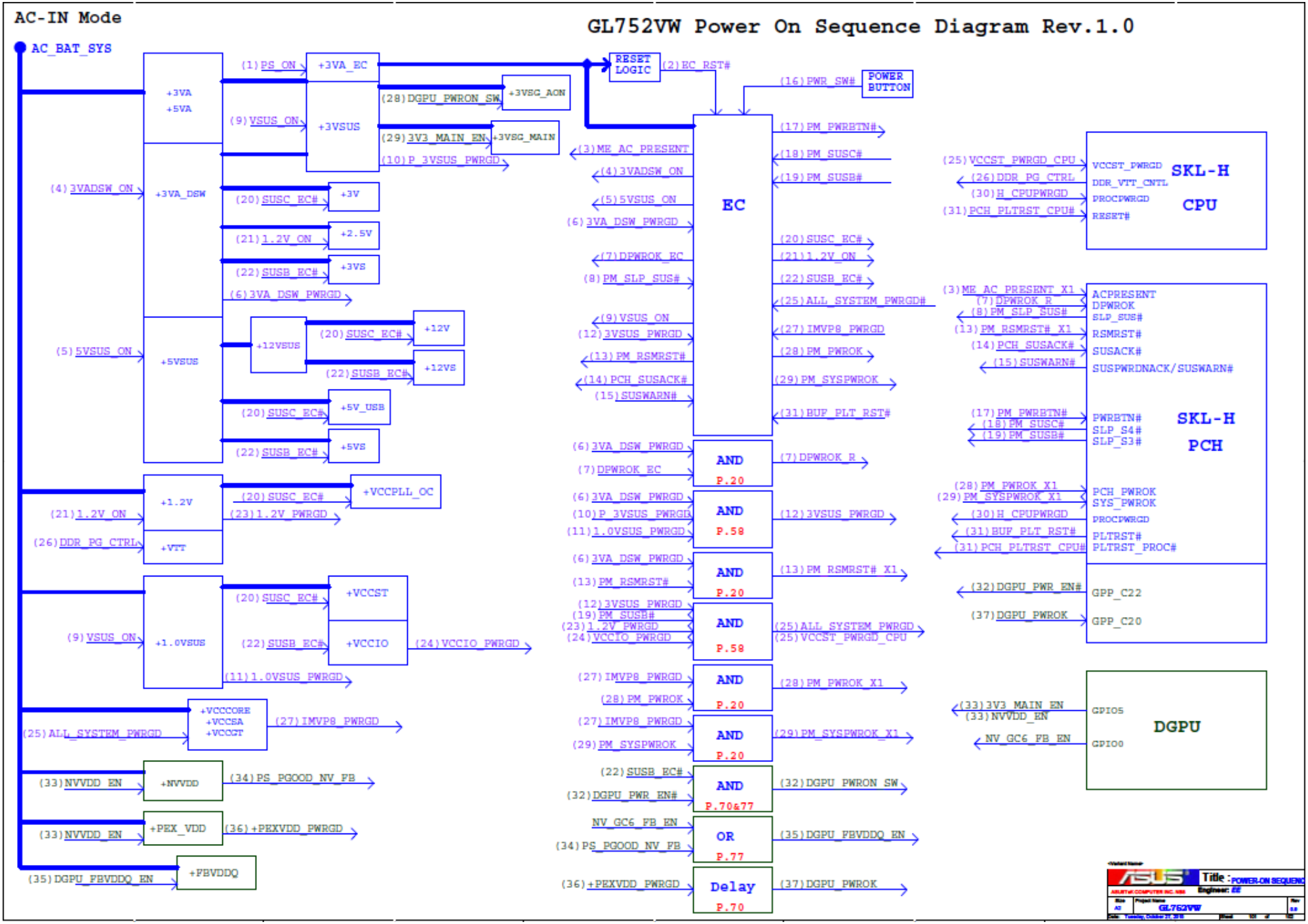
BLOCK DIAGRAM



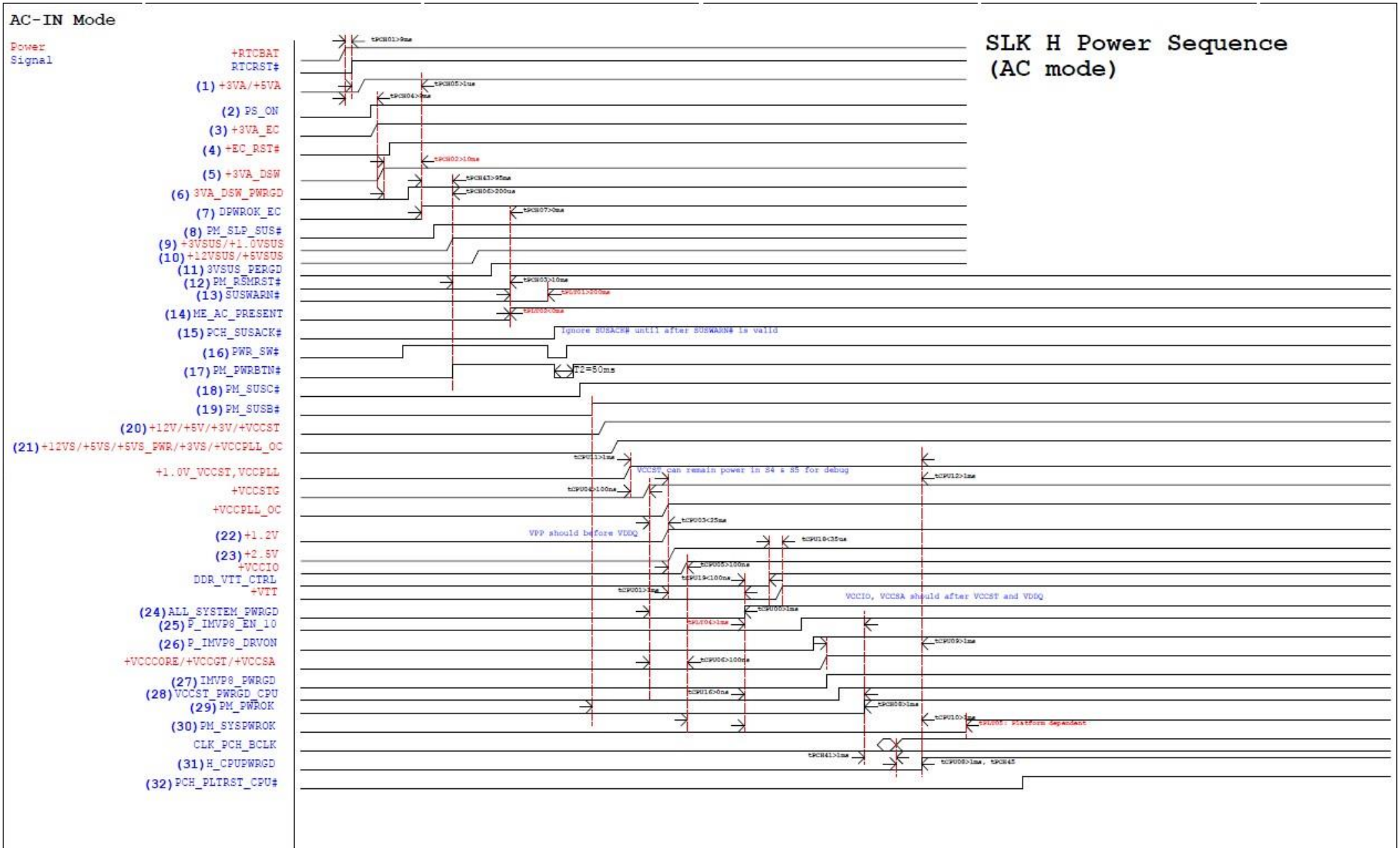
POWER FLOW


A/D_DOCK_IN
AC_BAT_SYS
BAT
bq24780
NTMFS4983N
Controller
Converter
LDO
MOSFET
SIS128SA
+5VAO
+5VA
+3VAO
+3VA
+3VSUS
+5VSUS
+5VS
+12V
PEA28SA
+5V
+12VSUS
SUSC#
CPUMD12
+12VS
SUSB#
+3VA_DSW
+3V
+3VS
NTMFS4C09
1.35V_GN
RT8068
2.5V_PWRGD
CPU, PCH, EC
5VSUS_PWRGD
3VA_DSW_PWRGD
PCH, EC
ALL_SYSTEM_PWRGD
P_SVID_ALERT#
P_SVID_DAT
P_SVID_CLK
IMVP8_PWRGD
+VCCGT
2 phase
SUSB#_EC
UP9011Q
VCCIO_PWRGD
RT8248A
VSUS_GN
1.0VSUS_PWRGD
EC
+1.0VSUS
VCCST_PWRGD
MPHY_PWREN
+VCCMPHYG
1.35V_GN
1.2V_PWRGD
+VTT
+1.2V
+VCCPLL_OC
NCP81210M
ALL_SYSTEM_PWRGD
VR_SVID_ALERT#
VR_SVID_DATA
VR_SVID_CLK
+VCCSA
NVVDD_EN
+PEXVDD
DGPU_FBVDDQ
FBVDDQ_PWRGD
+FBVDDQ
RT8815
NVVDD_EN
PS_PGOOD
+NVVDD

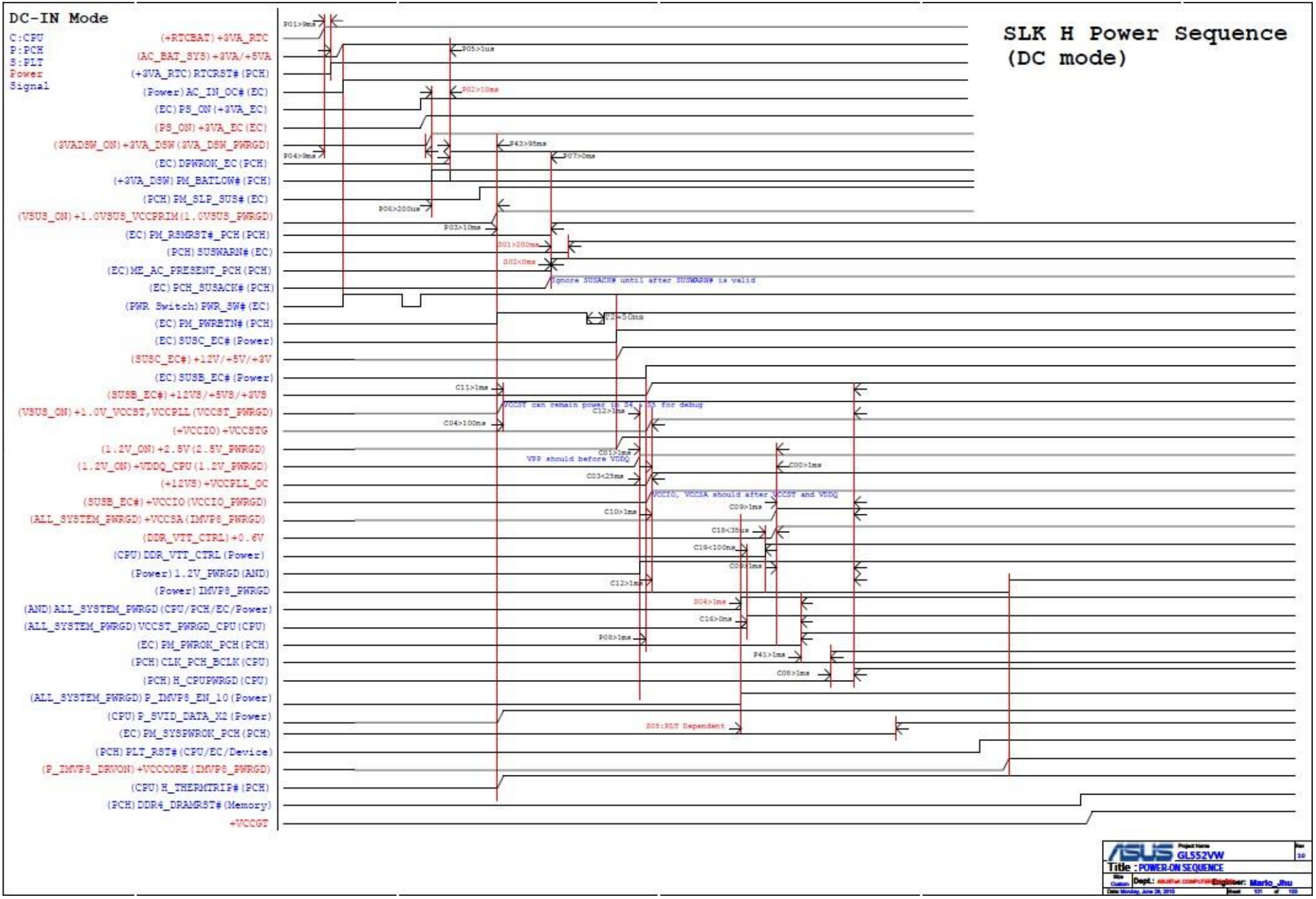
POWER ON SEQUENCE



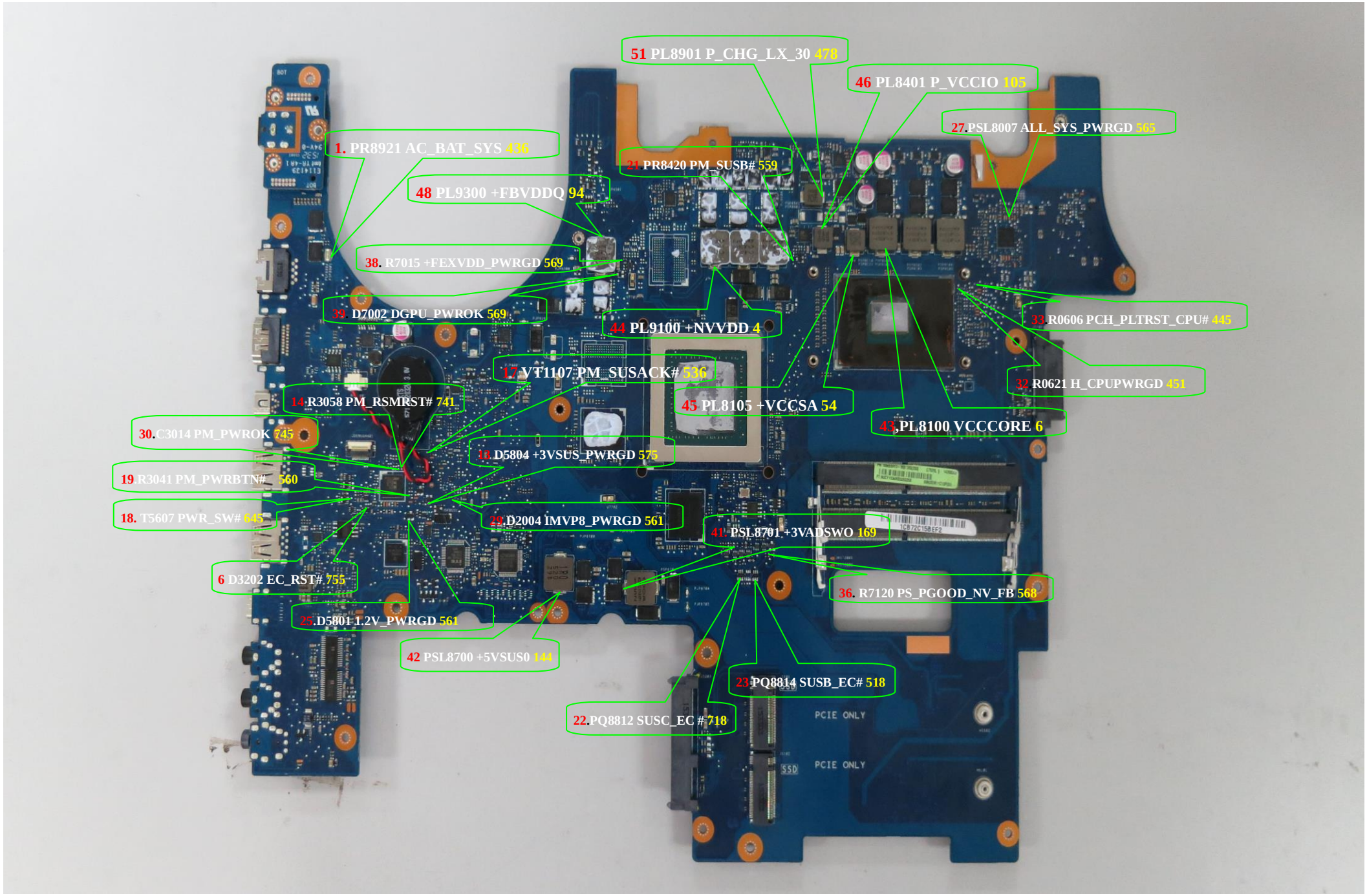
AC_IN POWER ON SEQUENCE



DC_IN POWER ON SEQUENCE



Signal Measure Point-Bottom



Signal Measure Point-Top