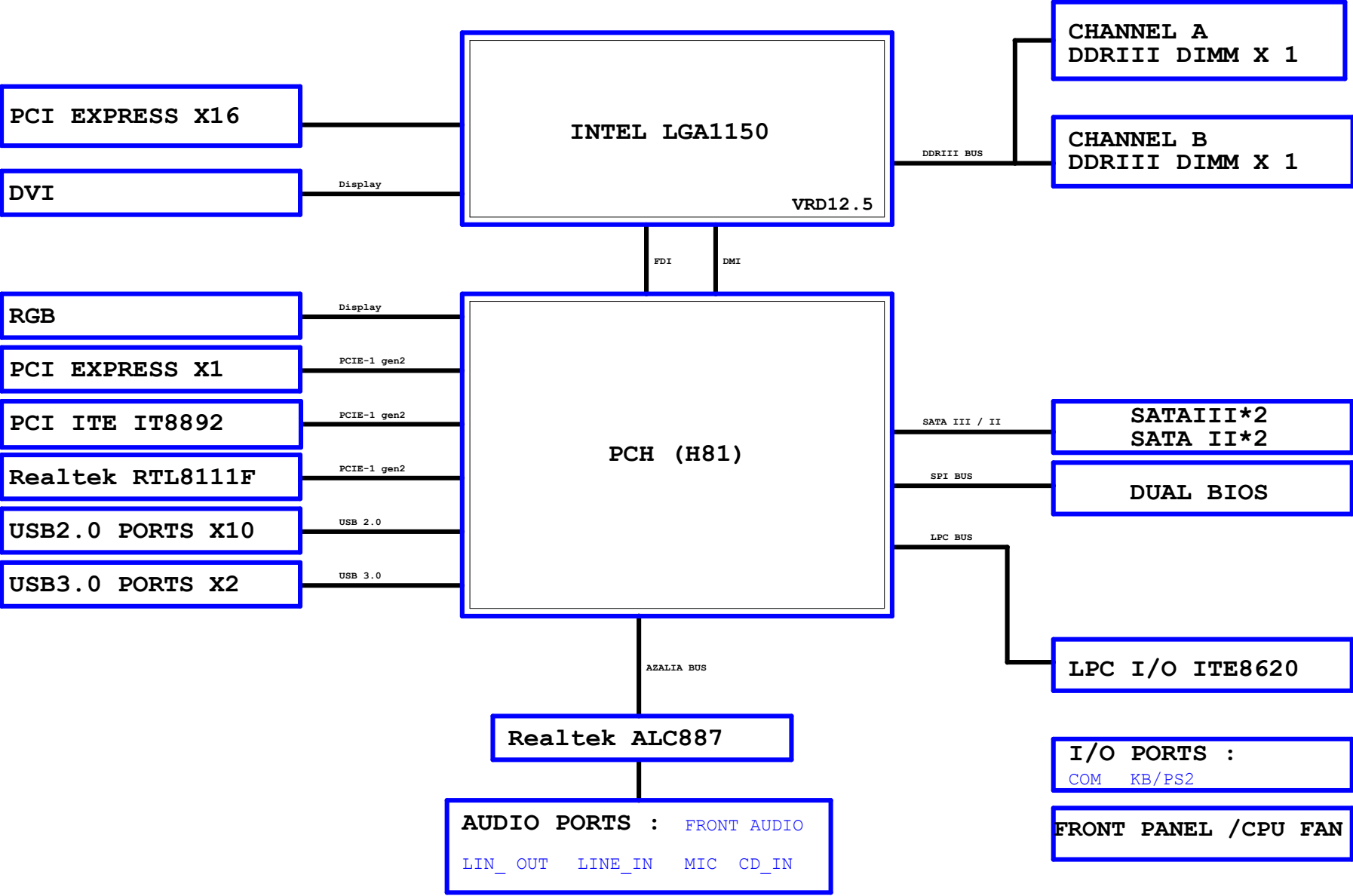


Revision 2.0

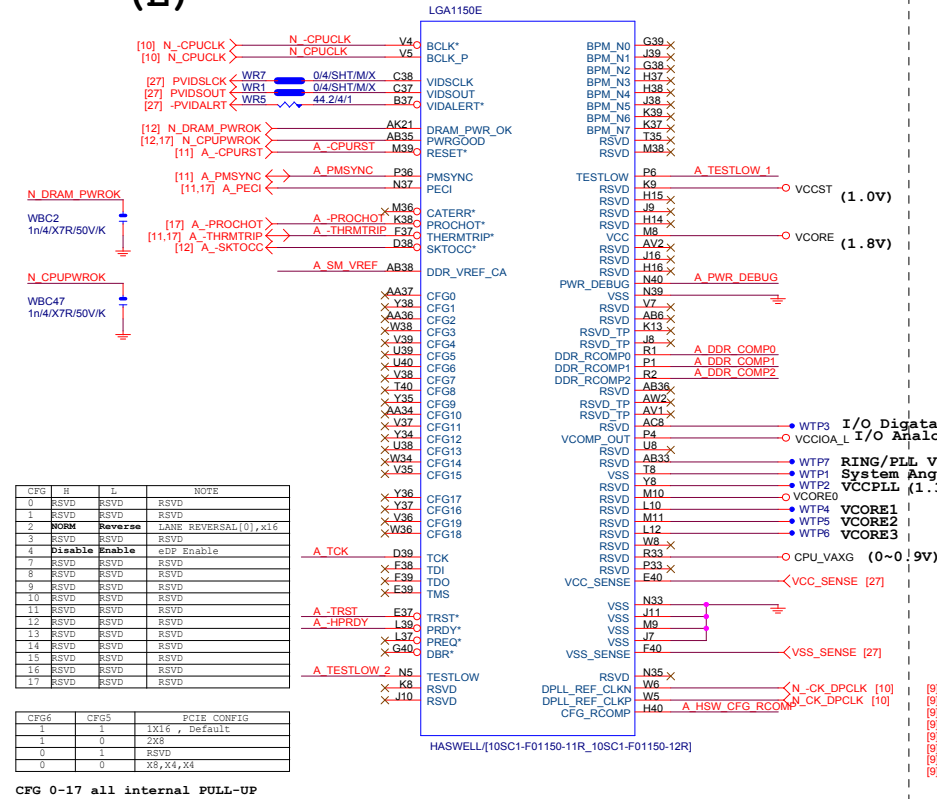
2014/04/02

[illegible]

BLOCK DIAGRAM



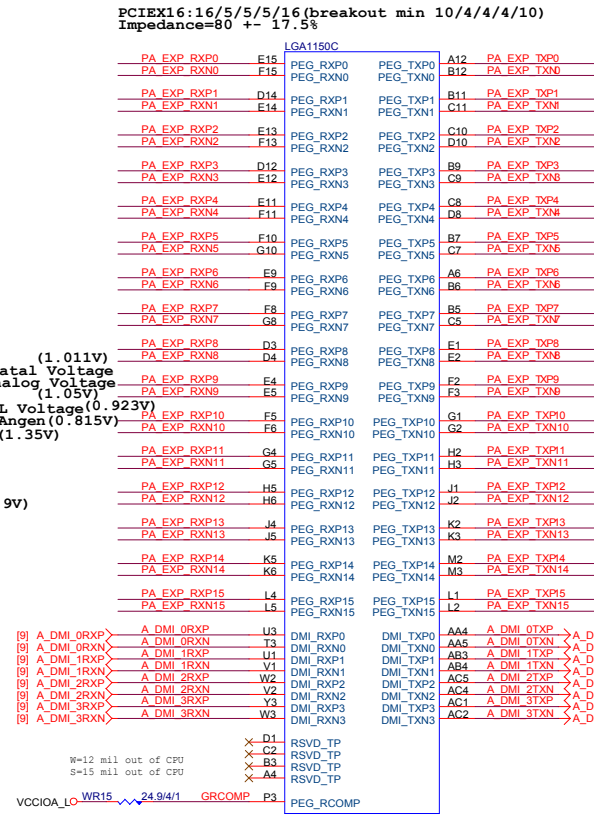
LGA1150 (E)



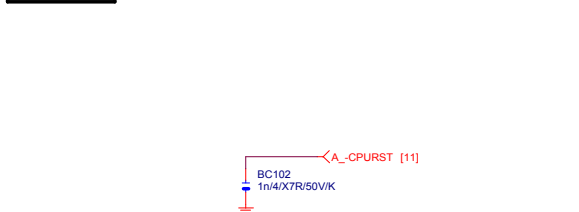
LGA1150 (D)



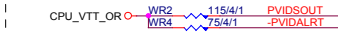
LGA1155 (C)



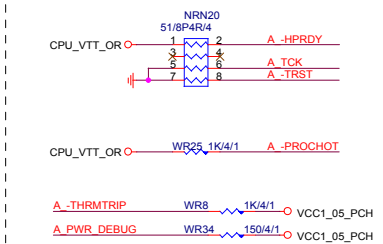
-CPURST



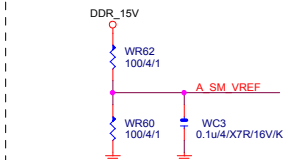
CPU SVID



CPU PU/PD



SM REF



Gigabyte Technology

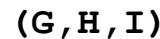
Title			CPU LGA1150-A
Size			Document Number
Custom			GA-H81M-S2PV
Date			Wednesday, April 09, 2014
Sheet			4 of 31
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LGA1150A									
MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0				
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1				
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2				
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3				
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4				
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5				
MAAA6	AV17	DDR0_MA6	DDR0_D06	AE37	MDA6				
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7				
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9				
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10				
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10				
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11				
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12				
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA13				
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14				
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15				
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17				
MODT_A1	AY3	DDR0_ODT1	DDR0_D17	AM39	MDA21				
AW9		DDR0_ODT2	DDR0_D18	AP39	MDA19				
AW8		DDR0_ODT3	DDR0_D19	AM37	MDA20				
AW33			DDR0_D21	AM38	MDA16				
AW33		DDR0_ECC0	DDR0_D22	AP37	MDA22				
AU31		DDR0_ECC1	DDR0_D23	AP40	MDA23				
AW31		DDR0_ECC2	DDR0_D24	AW37	MDA29				
AW33		DDR0_ECC3	DDR0_D25	AU35	MDA26				
AW33		DDR0_ECC4	DDR0_D26	AW35	MDA27				
AT31		DDR0_ECC5	DDR0_D27	AT37	MDA28				
AW31		DDR0_ECC6	DDR0_D28	AU37	MDA24				
AW31		DDR0_ECC7	DDR0_D29	AT35	MDA30				
			DDR0_D30	AW35	MDA31				
			DDR0_D31	AY6	MDA33				
[7] SBAA0	SBAA0	DDR0_BA0	DDR0_D32	AU6	MDA37				
[7] SBAA1	SBAA1	DDR0_BA1	DDR0_D33	AW6	MDA37				
[7] SBAA2	SBAA2	DDR0_BA2	DDR0_D34	AW4	MDA34				
			DDR0_D35	AU4	MDA35				
[7] CKEA0	CKEA0	DDR0_CKE0	DDR0_D36	AW6	MDA36				
[7] CKEA1	CKEA1	DDR0_CKE1	DDR0_D37	AW4	MDA32				
			DDR0_CKE2	AW4	MDA38				
			DDR0_CKE3	AW4	MDA39				
			DDR0_D40	AR1	MDA41				
[7] -CSA0	-CSA0	DDR0_CS_N0	DDR0_D41	AR4	MDA45				
[7] -CSA1	-CSA1	DDR0_CS_N1	DDR0_D42	AN3	MDA42				
			DDR0_CS_N2	AN4	MDA43				
			DDR0_CS_N3	AR2	MDA44				
			DDR0_CLK_P0	AR3	MDA40				
[7] DCLKA0	DCLKA0	DDR0_CLK_P0	DDR0_D45	AN2	MDA46				
[7] -DCLKA0	-DCLKA0	DDR0_CLK_N0	DDR0_D46	AN1	MDA47				
[7] DCLKA1	DCLKA1	DDR0_CLK_P1	DDR0_D47	AL1	MDA49				
[7] -DCLKA1	-DCLKA1	DDR0_CLK_N1	DDR0_D48	AL4	MDA53				
			DDR0_CLK_P2	AJ3	MDA50				
			DDR0_CLK_N2	AJ4	MDA51				
			DDR0_CLK_P3	AL2	MDA52				
			DDR0_CLK_N3	AJ2	MDA54				
			DDR0_D54	AJ1	MDA55				
		RSVD	DDR0_D55	AG1	MDA57				
			DDR0_D56	AG4	MDA61				
			DDR0_D57	AE3	MDA58				
			DDR0_D58	AE4	MDA59				
			DDR0_D59	AG2	MDA60				
			DDR0_D60	AG3	MDA56				
[7] -SRASA	-SRASA	DDR0_RAS*	DDR0_D61	AE2	MDA62				
[7] -SWEA	-SWEA	DDR0_WE*	DDR0_D62	AE1	MDA63				
			DDR0_D63	AE1	MDA63				
			DDR0_DOS_P0	AE39	DQSA0				
			DDR0_DOS_P1	AJ39	DQSA1				
			DDR0_DOS_P2	AN39	DQSA2				
			DDR0_DOS_P3	AJ36	DQSA3				
			DDR0_DOS_P4	AV5	DQSA4				
			DDR0_DOS_P5	AP3	DQSA5				
			DDR0_DOS_P6	AK3	DQSA6				
			DDR0_DOS_P7	AF3	DQSA7				
			DDR0_DOS_P8	AV32	-DQSA0				
			DDR0_DOS_N0	AE38	-DQSA1				
			DDR0_DOS_N1	AJ38	-DQSA2				
			DDR0_DOS_N2	AN38	-DQSA3				
			DDR0_DOS_N3	AJ36	-DQSA4				
			DDR0_DOS_N4	AW5	-DQSA5				
			DDR0_DOS_N5	AP2	-DQSA6				
			DDR0_DOS_N6	AK2	-DQSA7				
			DDR0_DOS_N7	AF2	-DQSA7				
			DDR0_DOS_N8	AJ32	-DQSA7				

HASWELL-[10SC1-F01150-11R_10SC1-F01150-12R]

LGA1150B									
MAAB0	AL19	DDR1_MA0	DDR1_MA0	AE34	MDB0				
MAAB1	AK23	DDR1_MA1	DDR1_MA1	AE35	MDB1				
MAAB2	AM22	DDR1_MA2	DDR1_MA2	AG35	MDB2				
MAAB3	AM23	DDR1_MA3	DDR1_MA3	AH35	MDB3				
MAAB4	AP23	DDR1_MA4	DDR1_MA4	AD34	MDB4				
MAAB5	AL23	DDR1_MA5	DDR1_MA5	AG35	MDB5				
MAAB6	AY24	DDR1_MA6	DDR1_MA6	AG34	MDB6				
MAAB7	AV25	DDR1_MA7	DDR1_MA7	AH34	MDB7				
MAAB8	AU26	DDR1_MA8	DDR1_MA8	AL34	MDB8				
MAAB9	AW25	DDR1_MA9	DDR1_MA9	AL35	MDB9				
MAAB10	AP18	DDR1_MA10	DDR1_MA10	AK31	MDB10				
MAAB11	AY25	DDR1_MA11	DDR1_MA11	AL31	MDB11				
MAAB12	AV26	DDR1_MA12	DDR1_MA12	AK34	MDB12				
MAAB13	AR15	DDR1_MA13	DDR1_MA13	AK35	MDB13				
MAAB14	AV27	DDR1_MA14	DDR1_MA14	AK32	MDB14				
MAAB15	AY28	DDR1_MA15	DDR1_MA15	AL32	MDB15				
MODT_B0	AM17	DDR1_ODT0	DDR1_ODT0	AP34	MDB21				
MODT_B1	AL18	DDR1_ODT1	DDR1_ODT1	AK31	MDB19				
AM16		DDR1_ODT2	DDR1_ODT2	AP31	MDB23				
AK15		DDR1_ODT3	DDR1_ODT3	AP35	MDB20				
AM26		DDR1_ECC0	DDR1_ECC0	AP35	MDB16				
AM25		DDR1_ECC1	DDR1_ECC1	AN32	MDB18				
AP25		DDR1_ECC2	DDR1_ECC2	AP32	MDB22				
AP26		DDR1_ECC3	DDR1_ECC3	AM29	MDB25				
AL26		DDR1_ECC4	DDR1_ECC4	AM28	MDB28				
AL25		DDR1_ECC5	DDR1_ECC5	AR29	MDB27				
AR26		DDR1_ECC6	DDR1_ECC6	AR28	MDB30				
AR25		DDR1_ECC7	DDR1_ECC7	AL28	MDB24				
			DDR1_ECC7	AL28	MDB29				
			DDR1_ECC7	AP29	MDB26				
			DDR1_ECC7	AP28	MDB31				
[8] SBAB0	SBAB0	DDR1_BA0	DDR1_BA0	AR12	MDB32				
[8] SBAB1	SBAB1	DDR1_BA1	DDR1_BA1	AL13	MDB33				
[8] SBAB2	SBAB2	DDR1_BA2	DDR1_BA2	AL12	MDB35				
			DDR1_CKE0	AR13	MDB36				
[8] CKEB0	CKEB0	DDR1_CKE0	DDR1_CKE0	AP13	MDB37				
[8] CKEB1	CKEB1	DDR1_CKE1	DDR1_CKE1	AM13	MDB38				
			DDR1_CKE2	AM12	MDB39				
			DDR1_CKE3	AR9	MDB45				
[8] -CSB0	-CSB0	DDR1_CS_N0	DDR1_CS_N0	AP9	MDB41				
[8] -CSB1	-CSB1	DDR1_CS_N1	DDR1_CS_N1	AR6	MDB47				
			DDR1_CS_N2	AP6	MDB43				
			DDR1_CS_N3	AR10	MDB44				
			DDR1_CLK_P0	AP10	MDB40				
			DDR1_CLK_N0	AR7	MDB46				
			DDR1_CLK_P1	AP7	MDB42				
[8] DCLKB0	DCLKB0	DDR1_CLK_P0	DDR1_CLK_P0	AM9	MDB52				
[8] -DCLKB0	-DCLKB0	DDR1_CLK_N0	DDR1_CLK_N0	AL9	MDB53				
[8] DCLKB1	DCLKB1	DDR1_CLK_P1	DDR1_CLK_P1	AL6	MDB50				
[8] -DCLKB1	-DCLKB1	DDR1_CLK_N1	DDR1_CLK_N1	AL7	MDB55				
			DDR1_CLK_P2	AM10	MDB48				
			DDR1_CLK_N2	AL10	MDB49				
			DDR1_CLK_P3	AM6	MDB54				
			DDR1_CLK_N3	AM7	MDB51				
			DDR1_CAS*	AH6	MDB61				
			DDR1_RAS*	AE6	MDB59				
			DDR1_WE*	AE7	MDB63				
			DDR1_WE*	AJ6	MDB56				
			DDR1_WE*	AJ7	MDB57				
			DDR1_WE*	AF6	MDB58				
			DDR1_WE*	AF7	MDB62				
			DDR1_WE*	AF36	DQSB0				
			DDR1_WE*	AL33	DQSB1				
			DDR1_WE*	AN28	DQSB2				
			DDR1_WE*	AN28	DQSB3				
			DDR1_WE*	AN29	DQSB4				
			DDR1_WE*	AN29	DQSB5				
			DDR1_WE*	AN29	DQSB6				
			DDR1_WE*	AN29	DQSB7				
			DDR1_WE*	AN29	DQSB8				
			DDR1_WE*	AN29	DQSB9				
			DDR1_WE*	AN29	DQSB10				
			DDR1_WE*	AN29	DQSB11				
			DDR1_WE*	AN29	DQSB12				
			DDR1_WE*	AN29	DQSB13				
			DDR1_WE*	AN29	DQSB14				
			DDR1_WE*	AN29	DQSB15				
			DDR1_WE*	AN29	DQSB16				
			DDR1_WE*	AN29	DQSB17				
			DDR1_WE*	AN29	DQSB18				
			DDR1_WE*	AN29	DQSB19				
			DDR1_WE*	AN29	DQSB20				
			DDR1_WE*	AN29	DQSB21				
			DDR1_WE*	AN29	DQSB22				
			DDR1_WE*	AN29	DQSB23				
			DDR1_WE*	AN29	DQSB24				
			DDR1_WE*	AN29	DQSB25				
			DDR1_WE*	AN29	DQSB26				
			DDR1_WE*	AN29	DQSB27				
			DDR1_WE*	AN29	DQSB28				
			DDR1_WE*	AN29	DQSB29				
			DDR1_WE*	AN29	DQSB30				
			DDR1_WE*	AN29	DQSB31				
			DDR1_WE*	AN29	DQSB32				
			DDR1_WE*	AN29	DQSB33				
			DDR1_WE*	AN29	DQSB34				
			DDR1_WE*	AN29	DQSB35				
			DDR1_WE*	AN29	DQSB36				
			DDR1_WE*	AN29	DQSB37				
			DDR1_WE*	AN29	DQSB38				
			DDR1_WE*	AN29	DQSB39				
			DDR1_WE*	AN29	DQSB40				
			DDR1_WE*	AN29	DQSB41				
			DDR1_WE*	AN29	DQSB42				
			DDR1_WE*	AN29	DQSB43				
			DDR1_WE*	AN29	DQSB44		</		

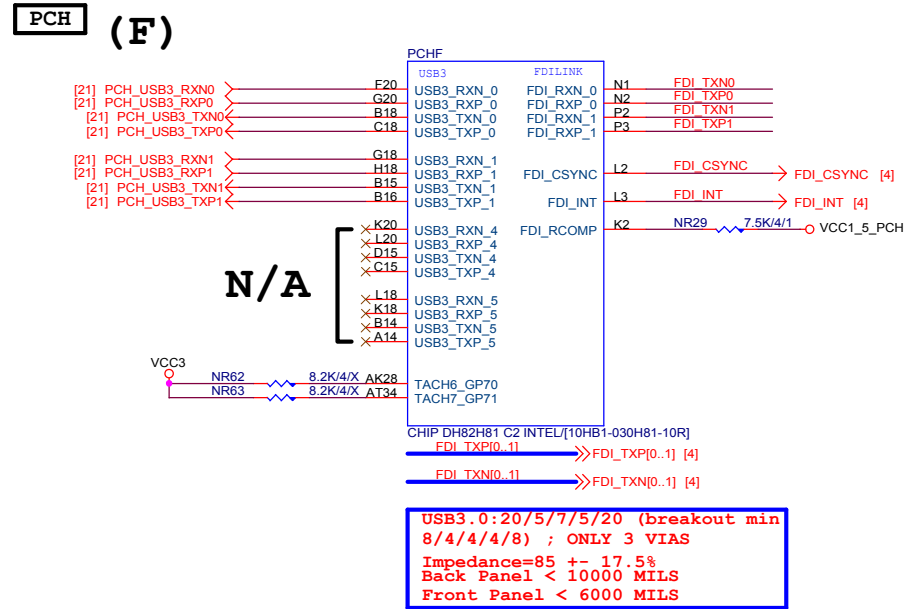
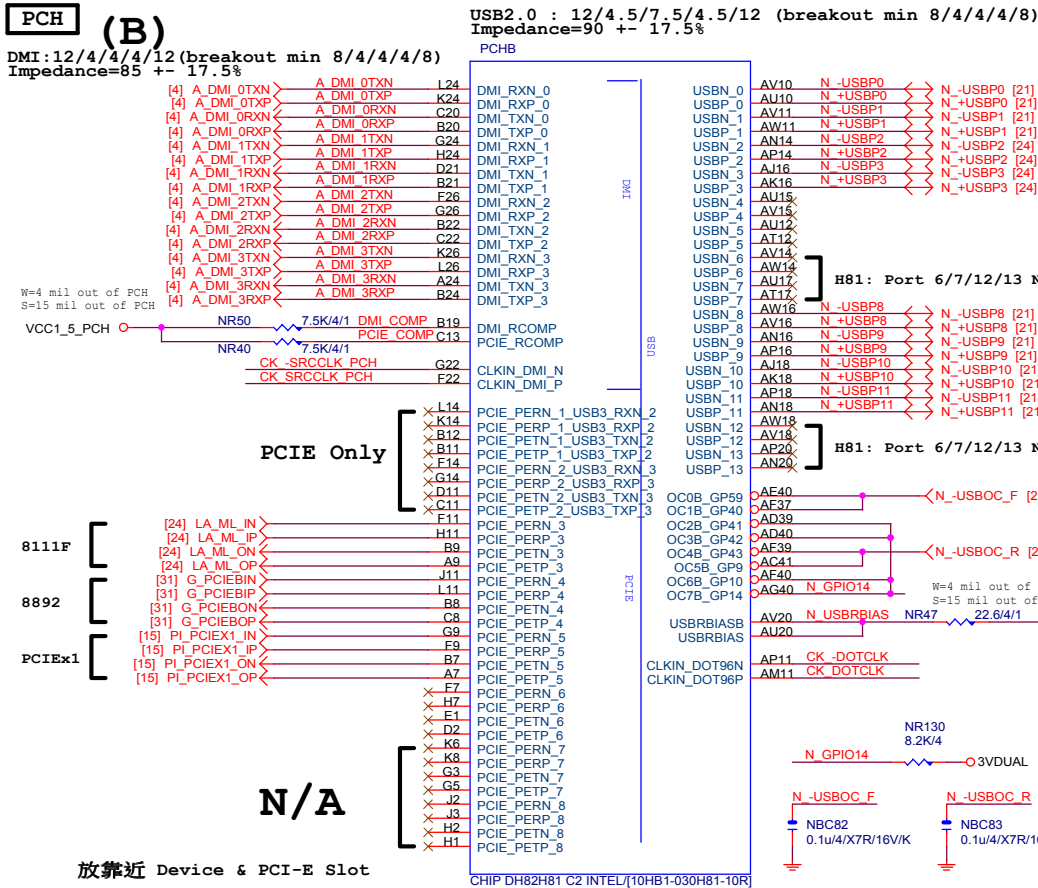
(F, J)



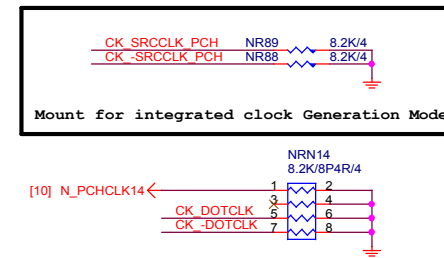
(A)



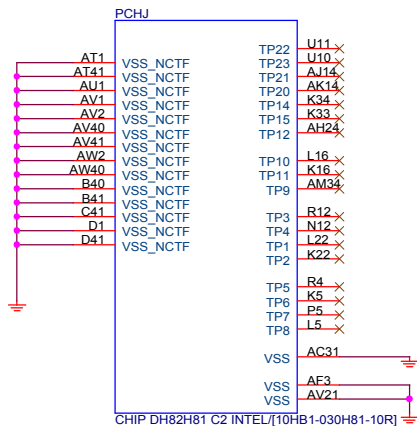
Title			
DDRIII CHANNEL A			
Size	Document Number		Rev
Custom	GA-H81M-S2PV		2.0
Date:	Sheet	7	of 31



PCH CLK PD

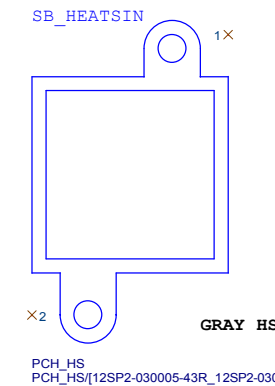


PCH (J)



PCH H/S

LOW COST ICH7 HEATSINK



USB TABLE

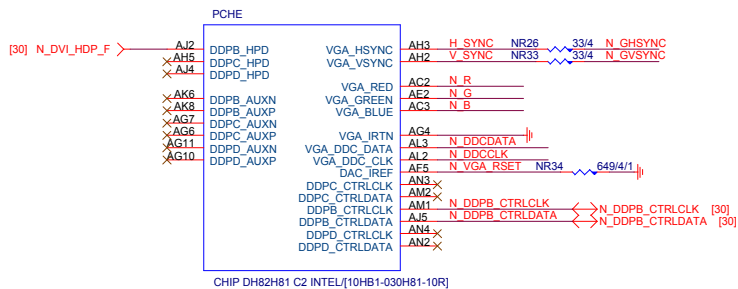
OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

USB OC#	Configure
OC0#	R_USB30
OC1#	USB_LAN
OC2#	N/A
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	N/A
OC7#	Not Use

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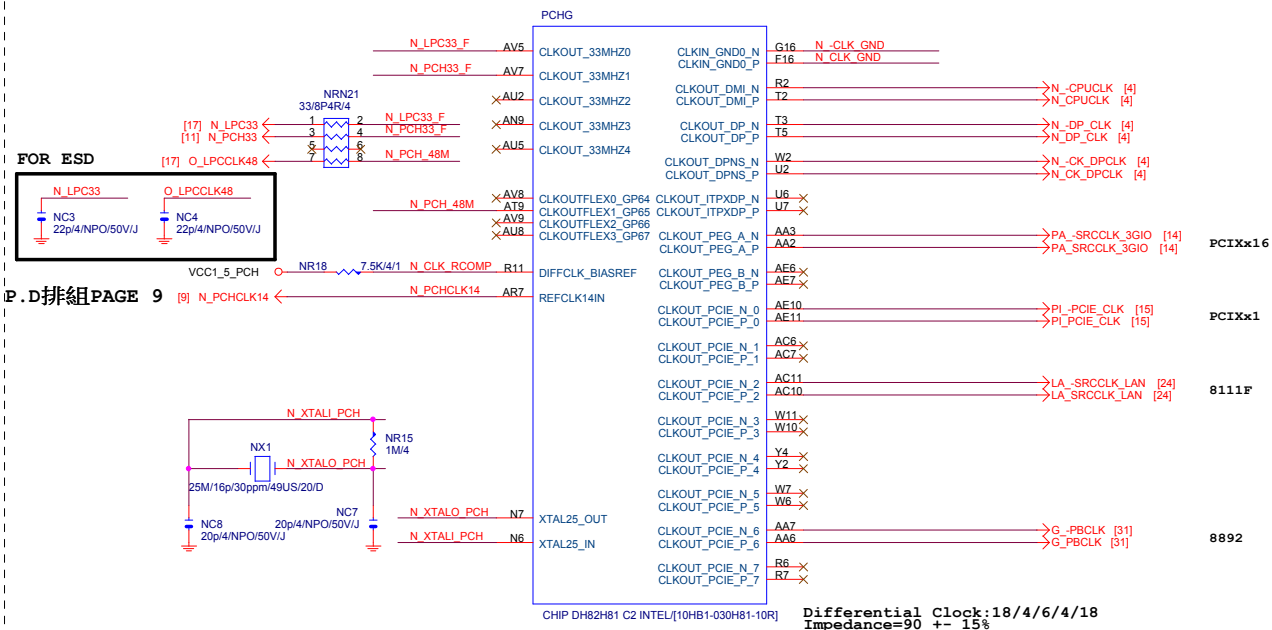
Title			PCH FDI,DMI,USB,PCIE,NVRAM		
Size Custom			GA-H81M-S2PV		
Date:			Wednesday, April 09, 2014		
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Rev			2.0		

(E)



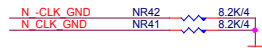
CHIP DH82H81 C2 INTEL/I10HB1-030H81-10R1

(G)

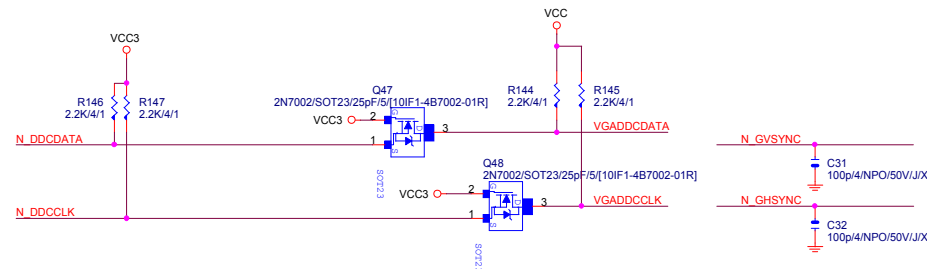


Differential Clock:18/4/6/4/18
Impedance=90 +- 15%

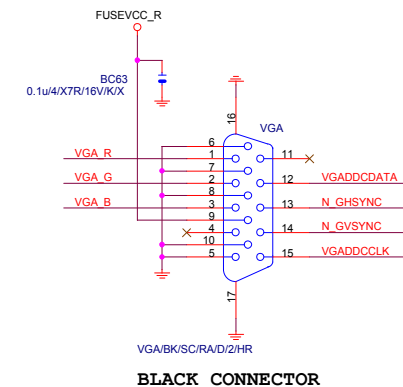
PCH	CLK	PD
-----	-----	----



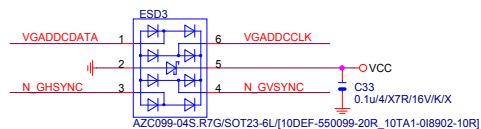
VGA DDC



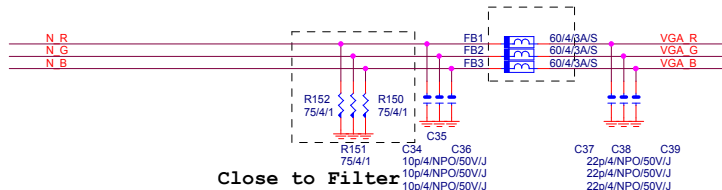
VGA CONNECTOR



VGA ESD



VGA DDC



Close to Filter

Gigabyte Technology

Title			
PCH DISPLAY ,CLK BUFFER			
Size	Document Number		Rev
Custom	GA-H81M-S2PV		2.0
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(C)



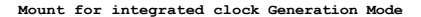
SATA CONNECTOR



(A)



PCH CLK PD



PCH	PU/PD
-----	-------

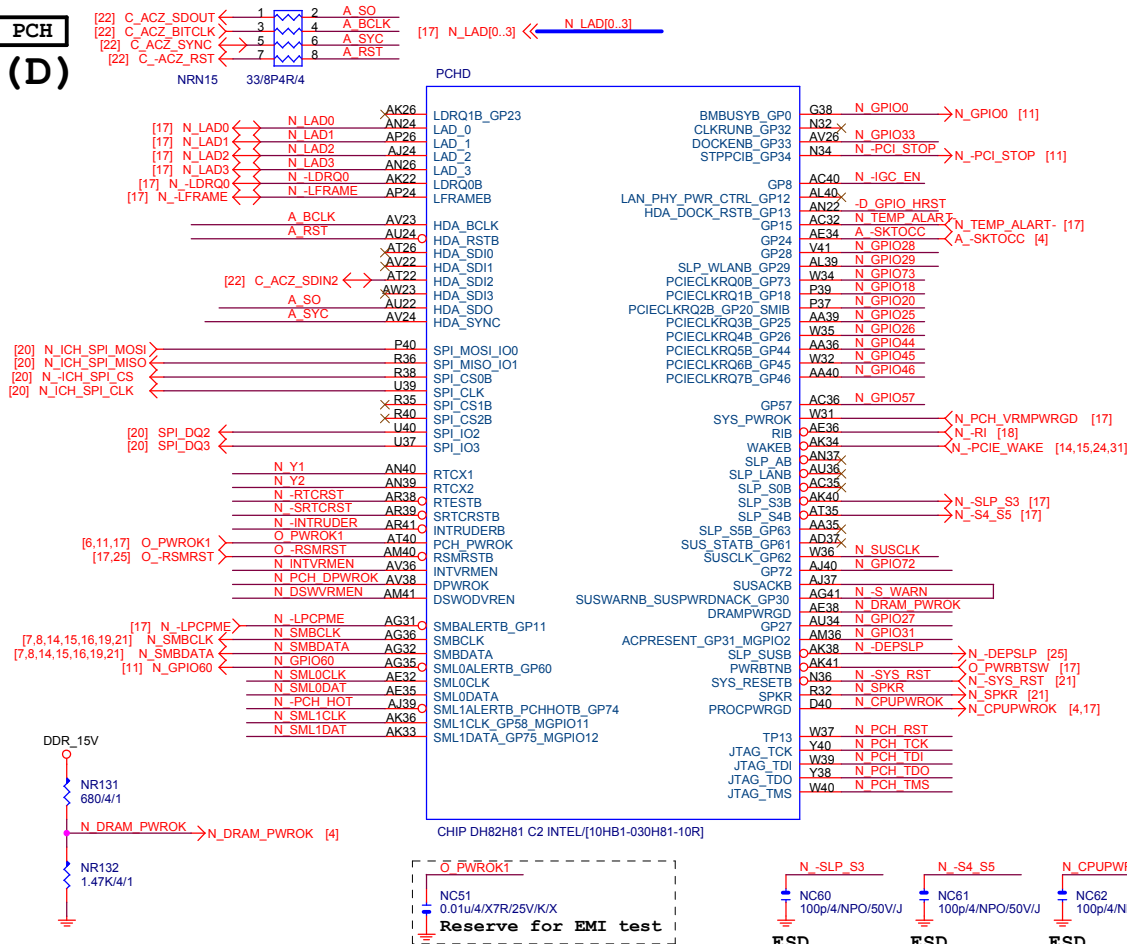
GPIO38 Ctrl

N/A

Gigabyte Technology

Title			
PCH HOST , SATA, PCI			
Size	Document Number		Rev
Custom	GA-H81M-S2PV		2.0
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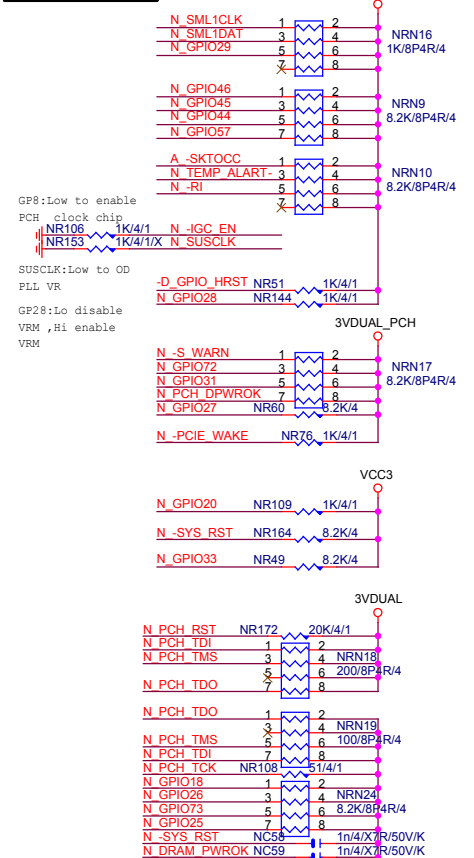
PCH (D)



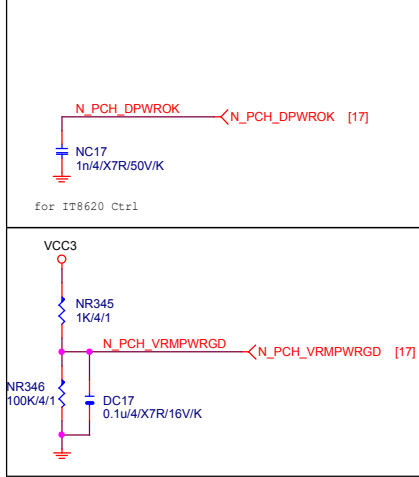
ACZ_SDOUT

N/A

PCH PU/PD



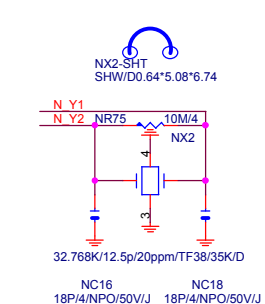
PCH DPWROK



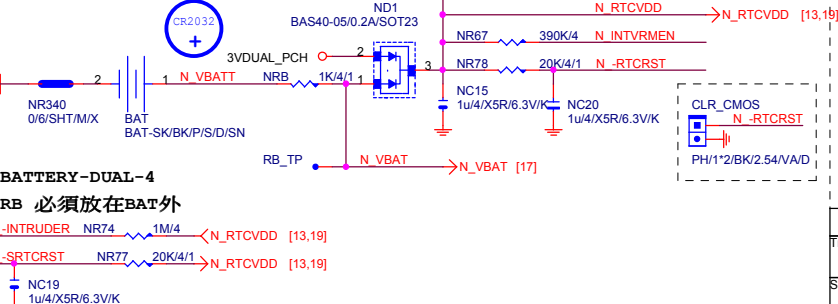
HSW_STRAP13

N/A

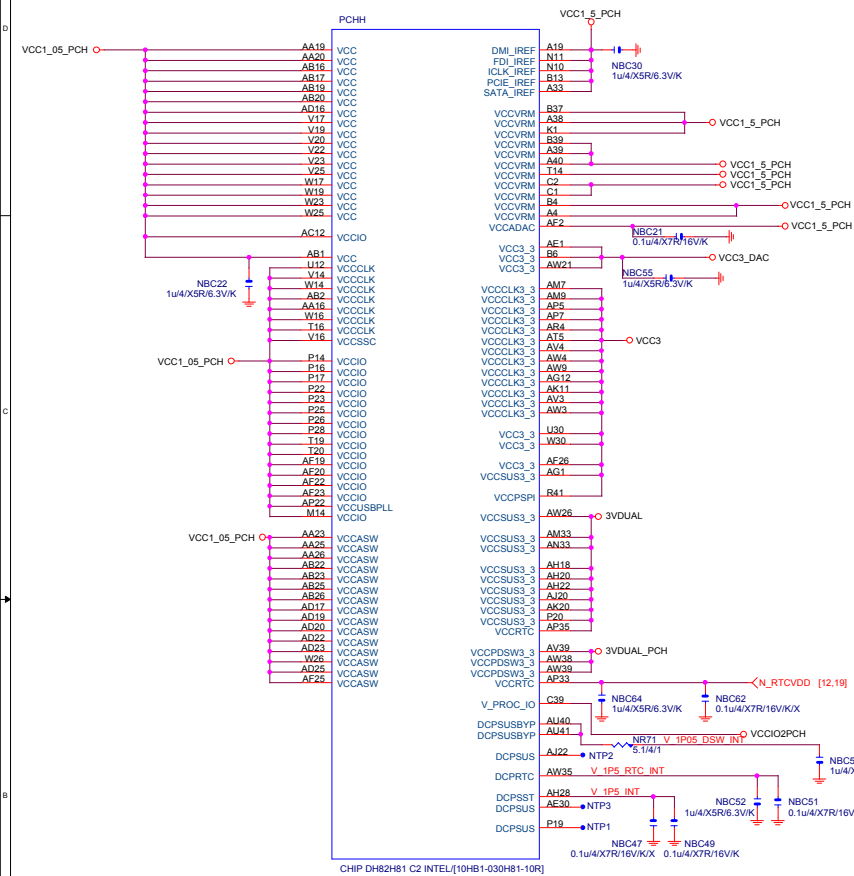
32.768KHZ



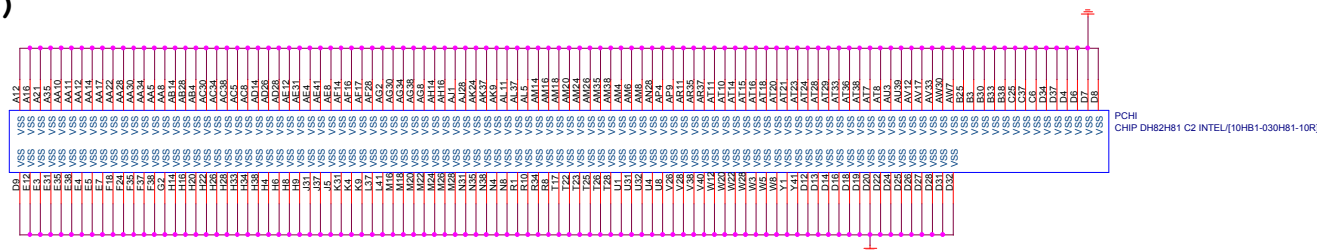
CLR_CMOS



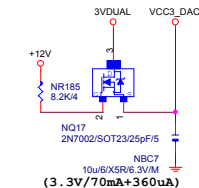
PCH (H)



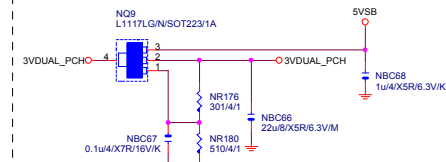
PCH (I)



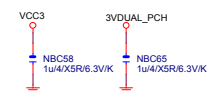
VCC3	DAC
------	-----



3VDUAL PCH

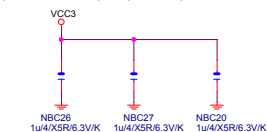


SHT PWR

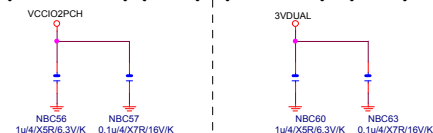


CAP

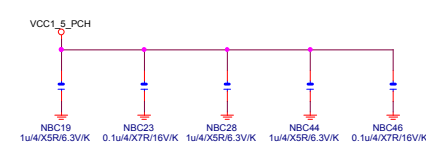
(3.3V) (x6)



(1.05V)(x2) (3.3V) (x2)

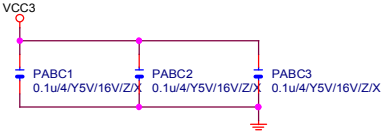


(1.5V) (x5)

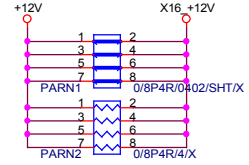


PCIEX16 CAP

N/A



PCIEX16 PROTECT SHT



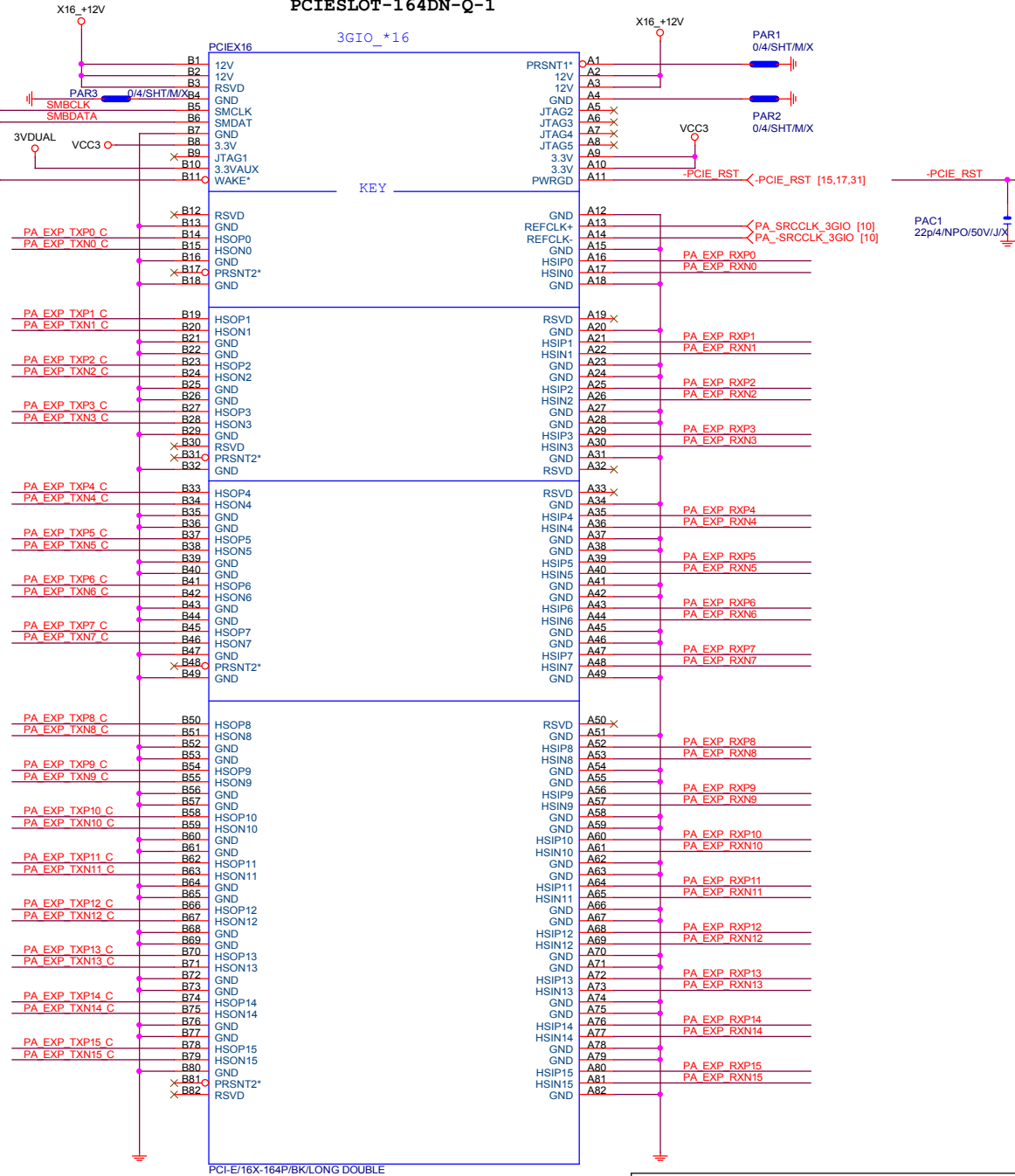
PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0_15] >>>PA_EXP_RXP[0..15] [4]
 PA EXP RXN0_15] >>>PA_EXP_RXN[0..15] [4]
 PA EXP TXP0_15] >>>PA_EXP_TXP[0..15] [4]
 PA EXP TXN0_15] >>>PA_EXP_TXN[0..15] [4]

PCIEX16 SLOT

PCIESLOT-164DN-Q-1



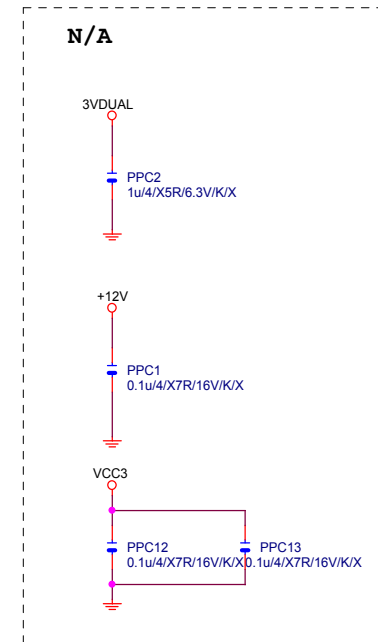
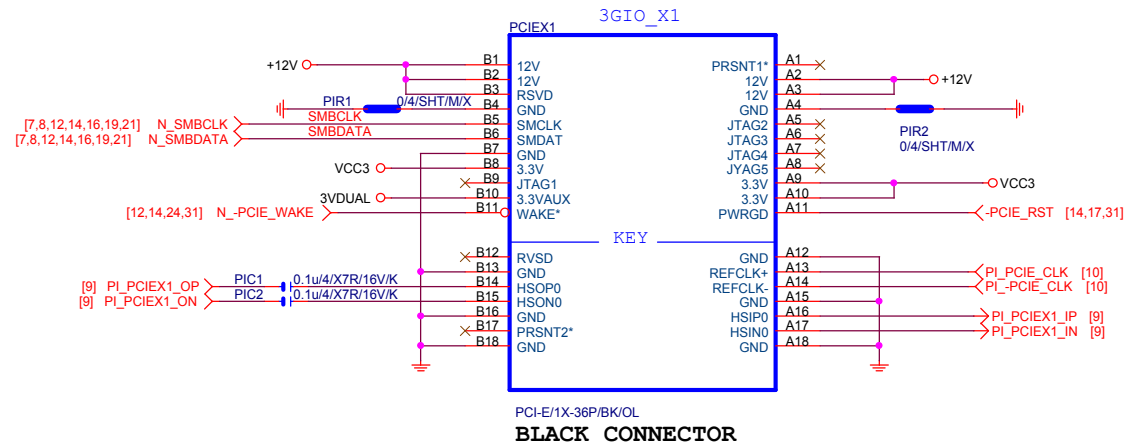
PCI-E/16X-164P/BK/LONG DOUBLE

BLACK CONNECTOR

Gigabyte Technology

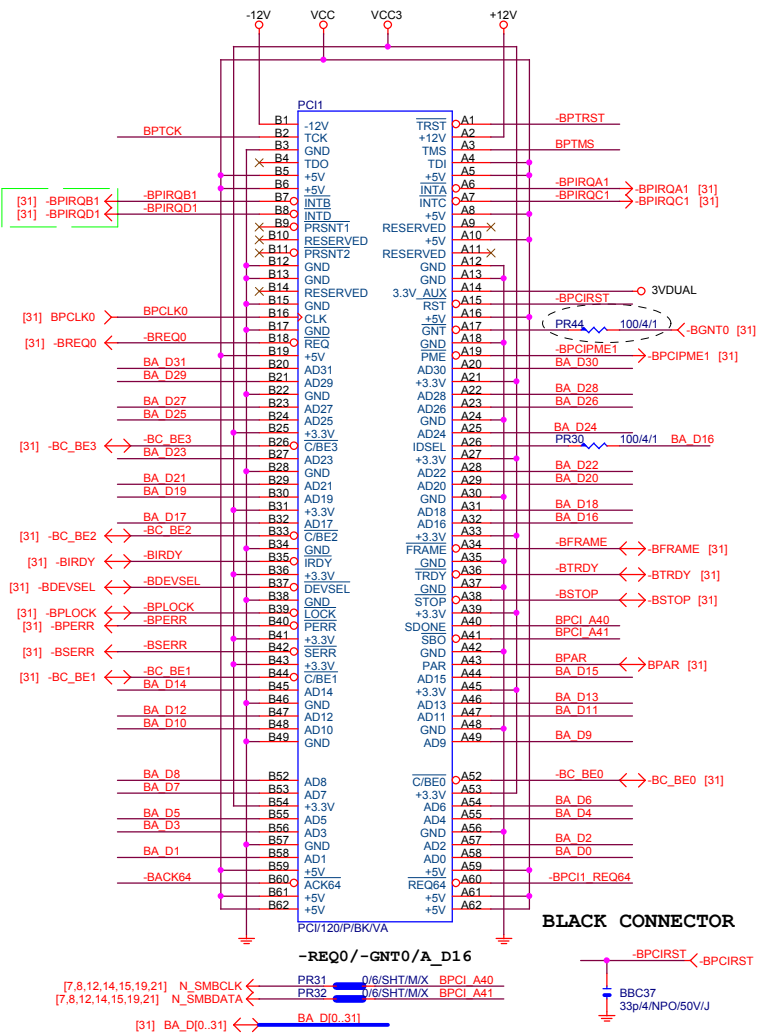
Title			PCI EXPRESS * 16		
Size			GA-H81M-S2PV		
Custom			Rev 2.0		
Date:			Wednesday, April 09, 2014		
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PCIE_X1_SLOT



Gigabyte Technology			
Title			
PCI EXPRESS X 1 PORT			
Size	Document Number		Rev
Custom	GA-H81M-S2PV		2.0
Date:	Wednesday, April 09, 2014	Sheet	15 of 31

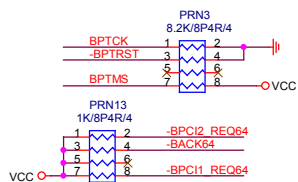
PCI SLOT 1



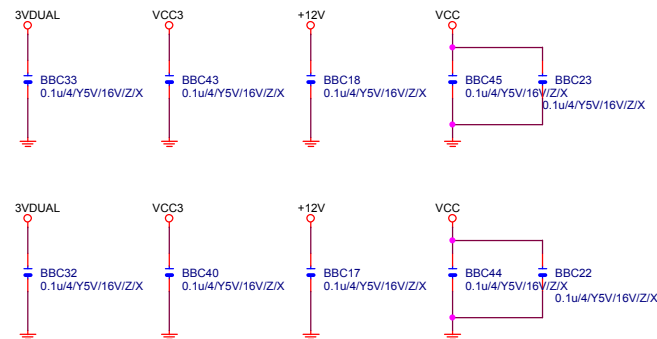
PCI SLOT 2



PCI	PU
-----	----



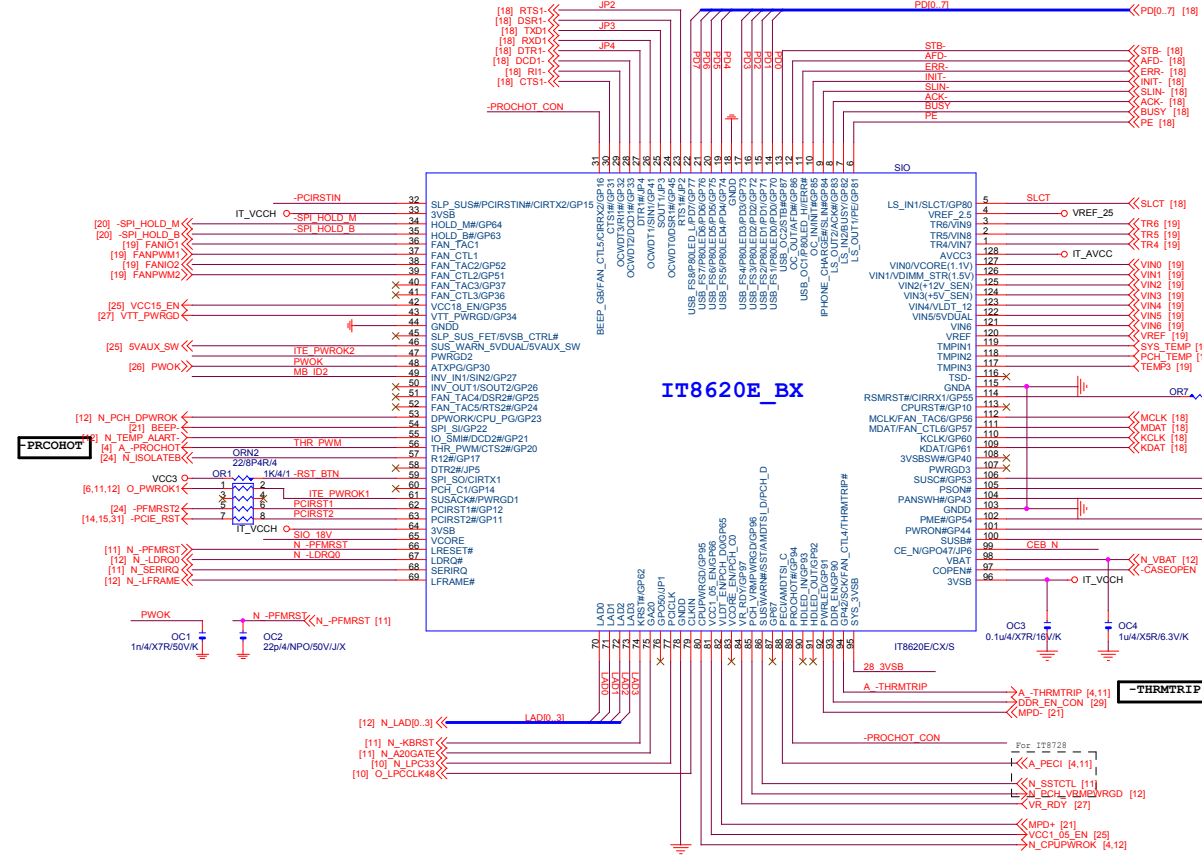
PCI CAP	N/A
---------	-----



Gigabyte Technology

Title			
PCI SLOT 1&2			
Size	Document Number	Rev	
Custom	GA-H81M-S2PV	2.0	
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SIO IT8620



PROCHOT

PCIRSTIN

PCIRSTIN

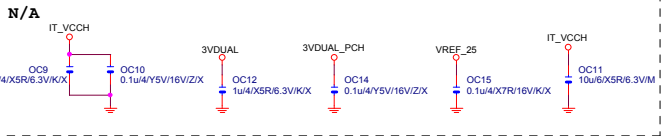
PCIRSTIN

IT8620E_BX

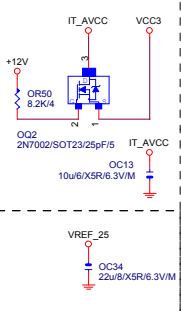
DUAL BIOS OPT STRAP

SIO 18V

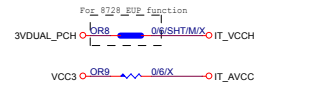
SIO CAP



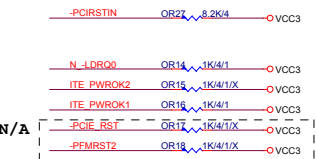
FIX ATX 插拔漏電



PWR SHT

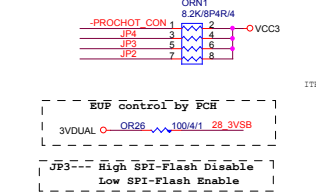


SIO PU



DO8:N/A

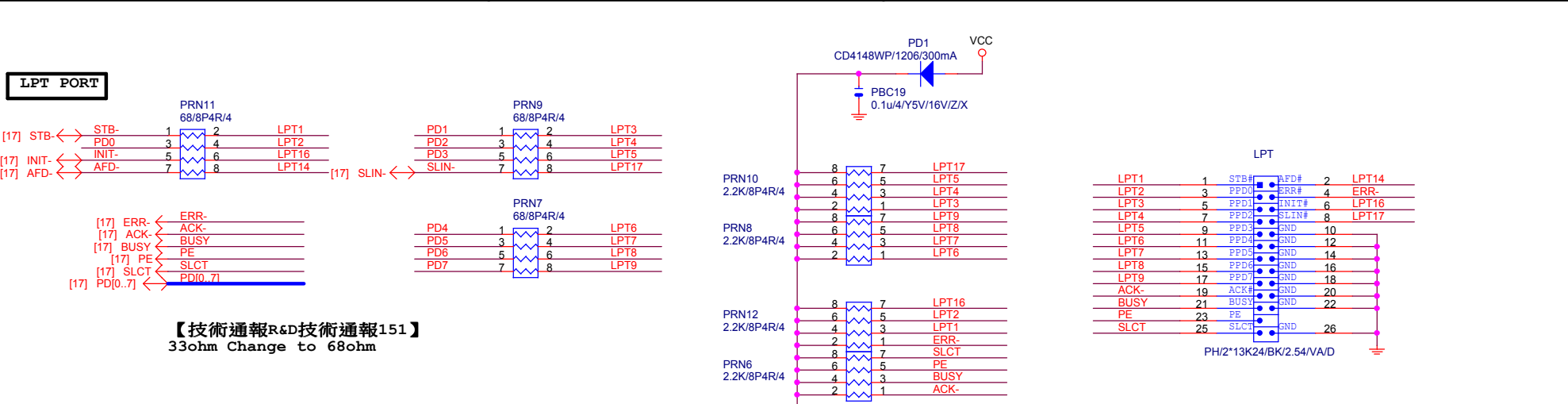
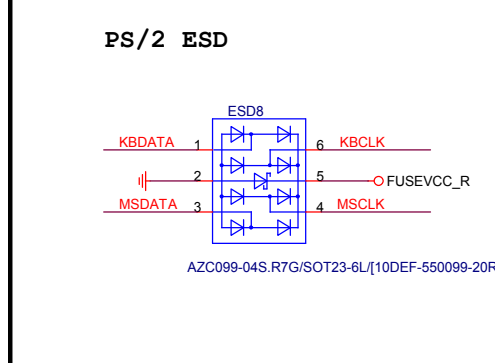
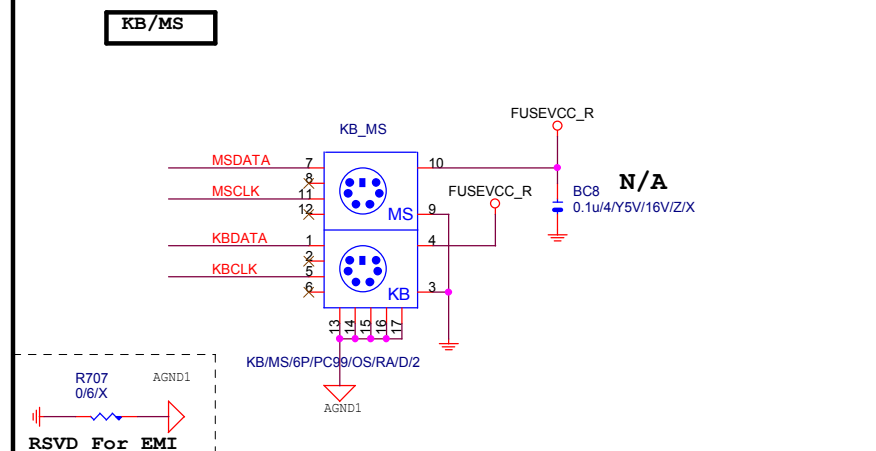
SIO STRAP



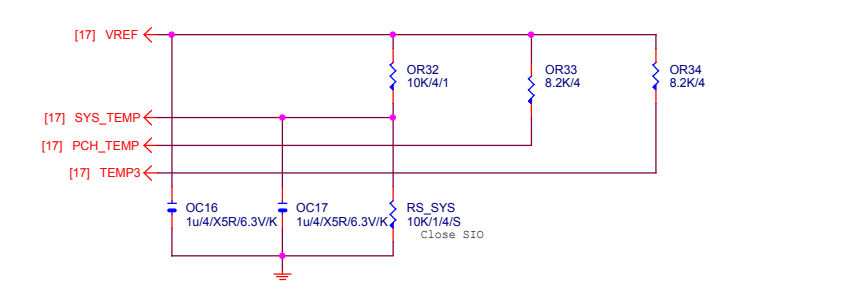
Power leakage

MB ID

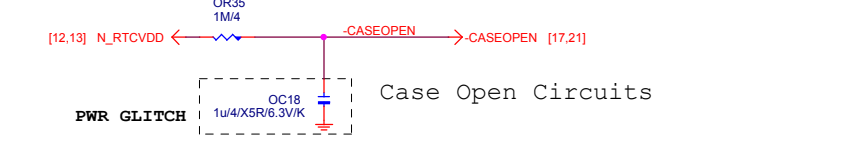




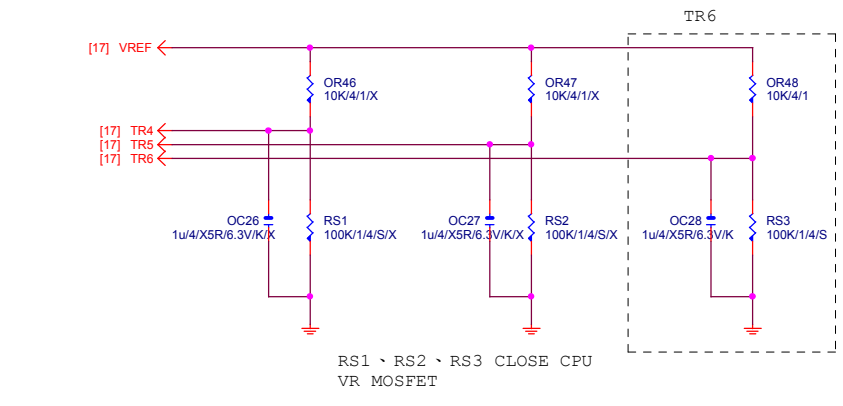
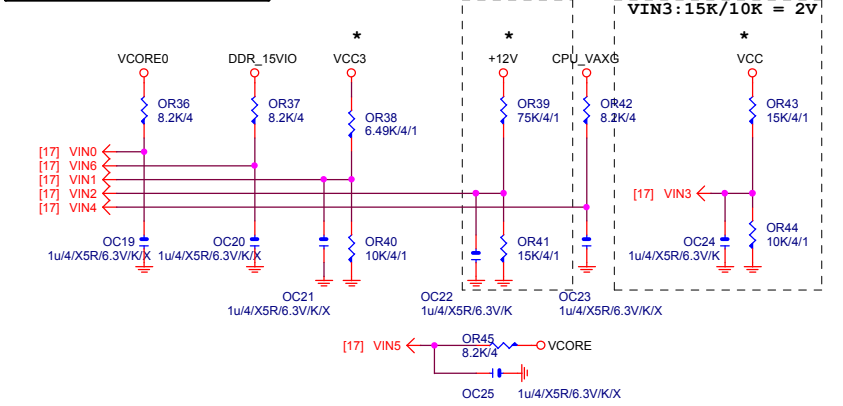
TEMP H/W MONITOR



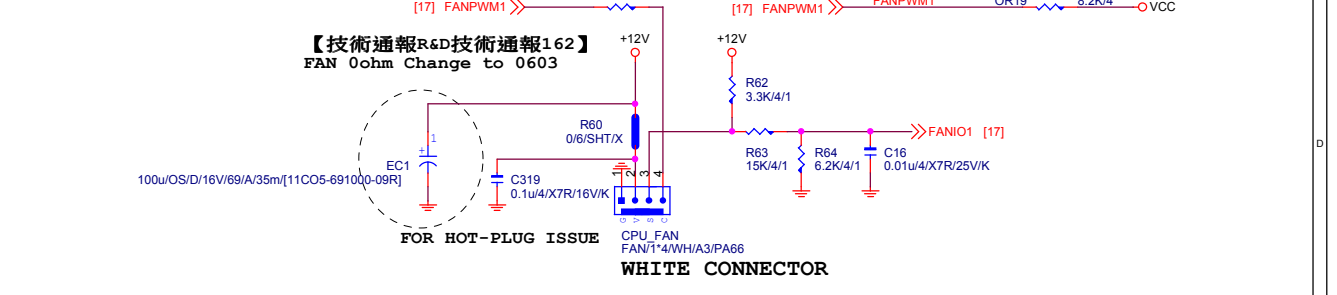
CASE OPEN



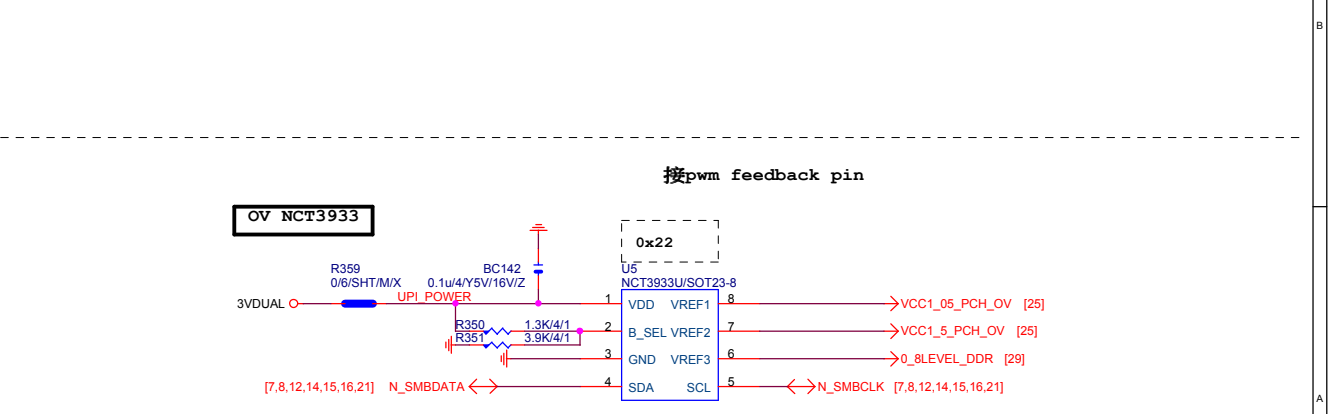
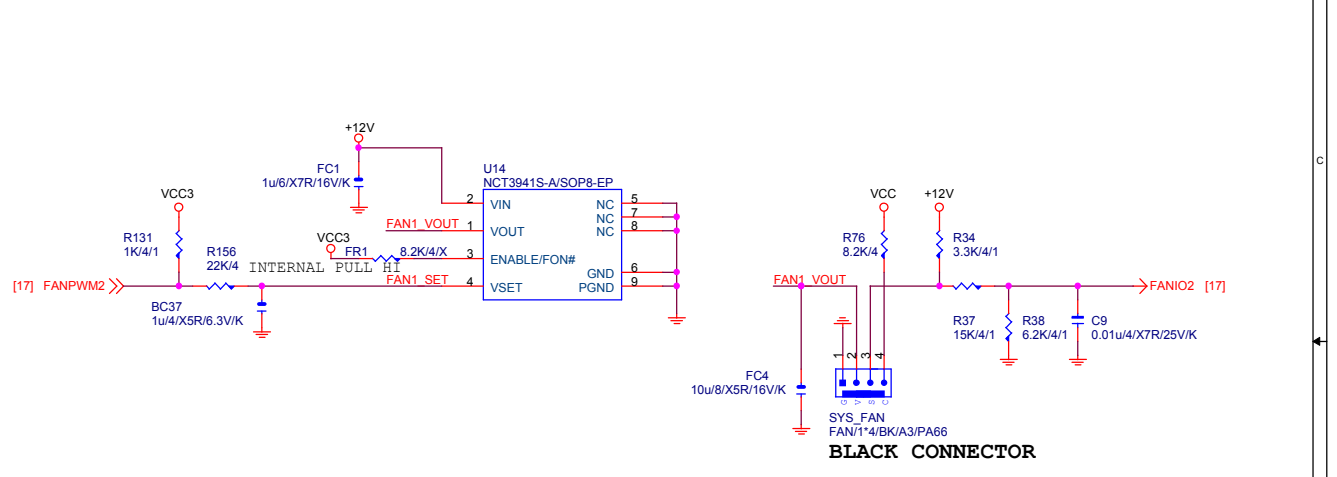
VOLTAGE-- H/W MONITOR

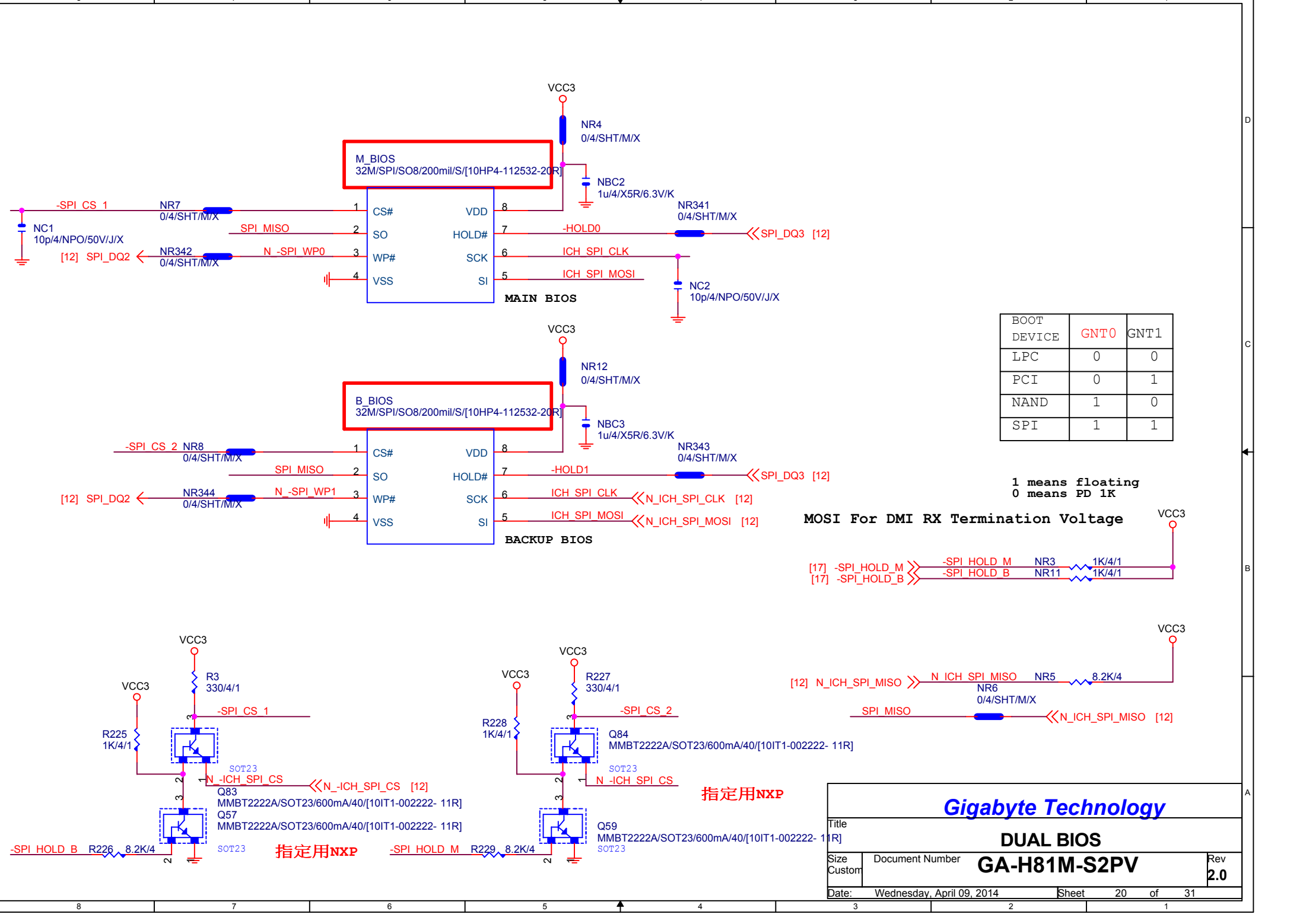


CPU SMART FAN



SYS SMART FAN





BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

MOSI For DMI RX Termination Voltage

[17] -SPI_HOLD_M >> -SPI_HOLD M NR3 1K/4/1
[17] -SPI_HOLD_B >> -SPI_HOLD B NR11 1K/4/1

[12] N_ICH_SPI_MISO >> N ICH SPI_MISO NR5 8.2K/4
SPI_MISO >> N_ICH_SPI_MISO [12] NR6 0/4/SHT/M/X

指定用NXP

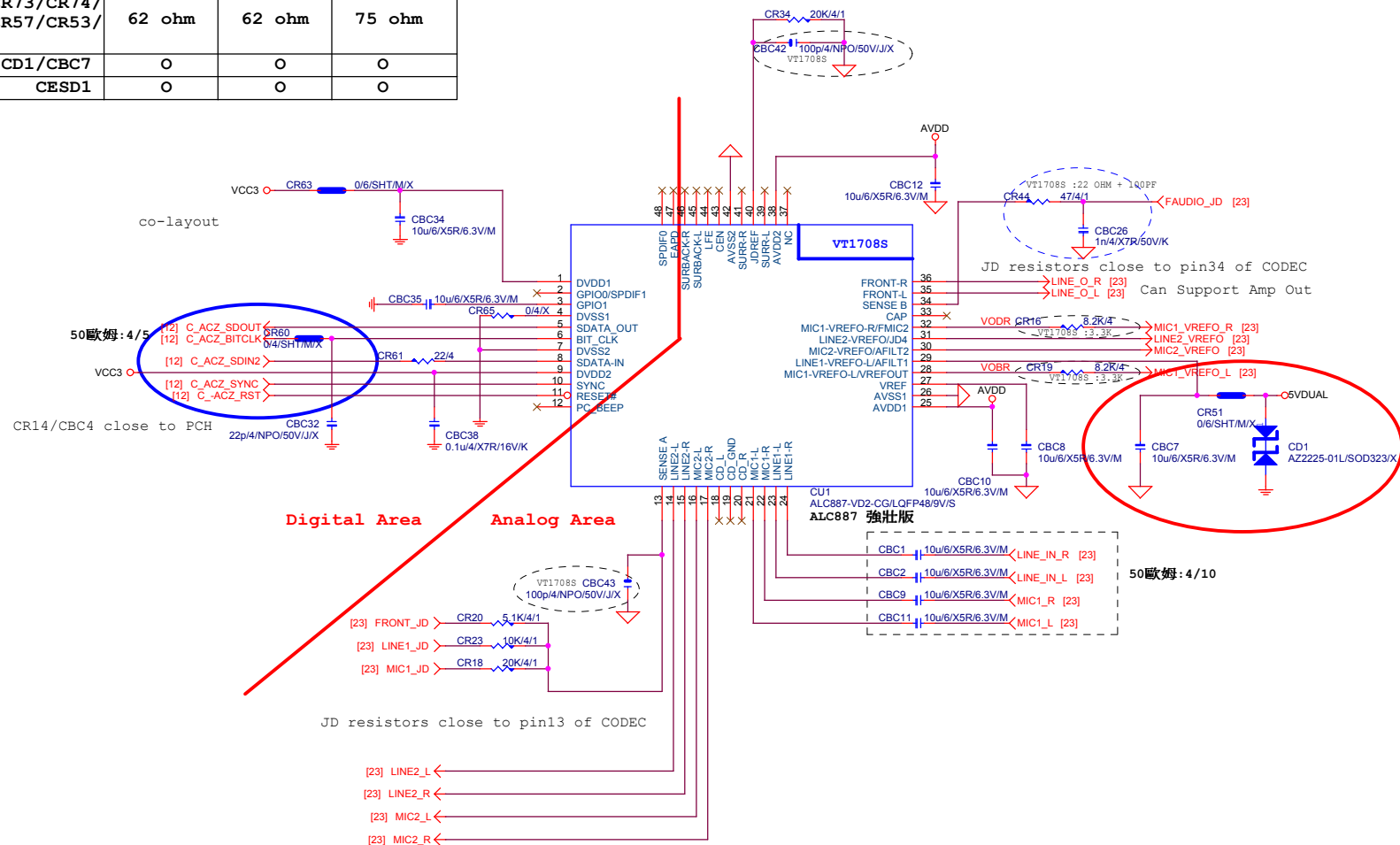
Gigabyte Technology

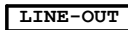
DUAL BIOS

GA-H81M-S2PV

Rev
2.0

	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O





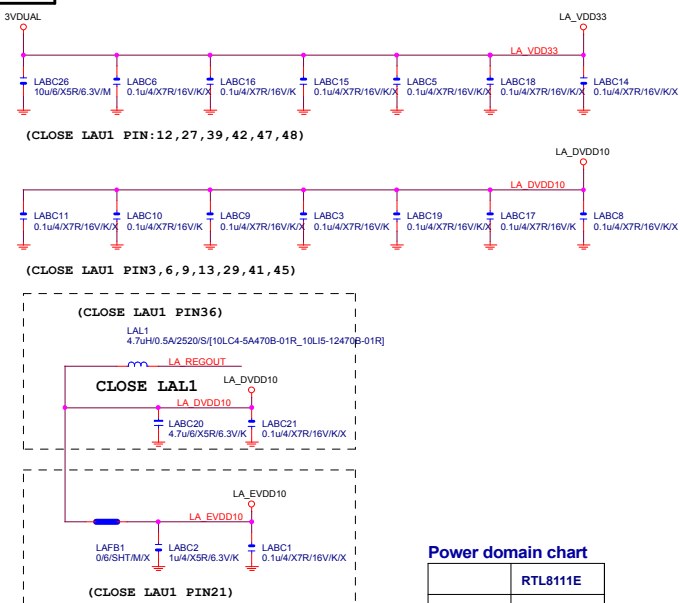
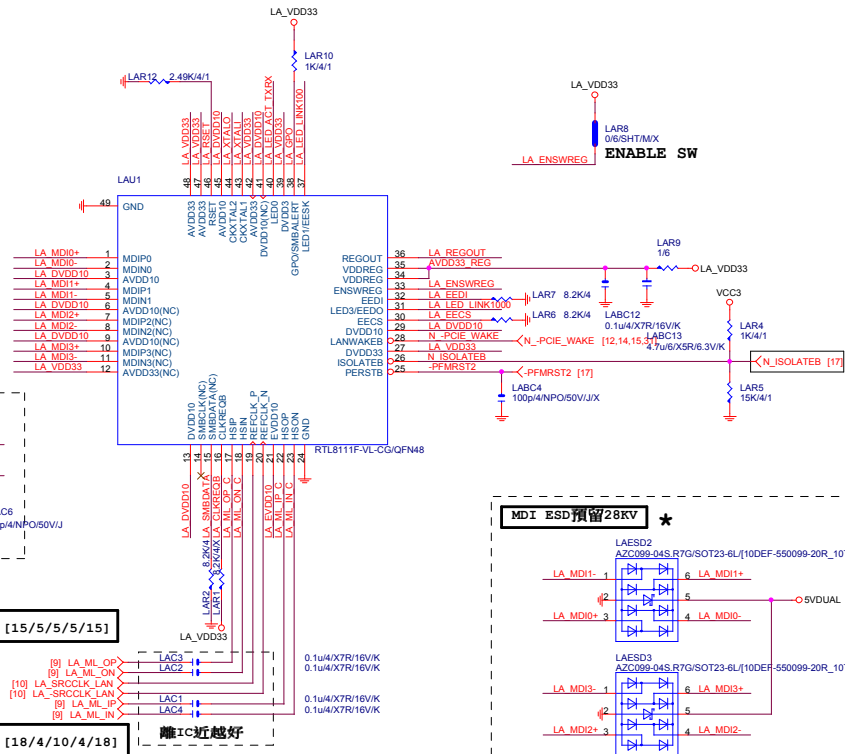
For 889A/888



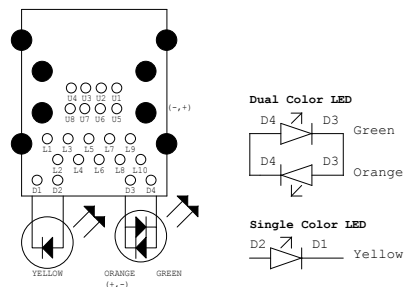
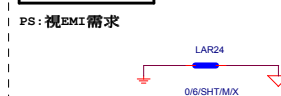
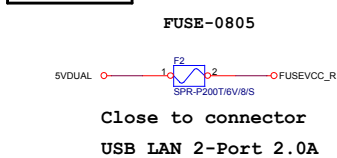
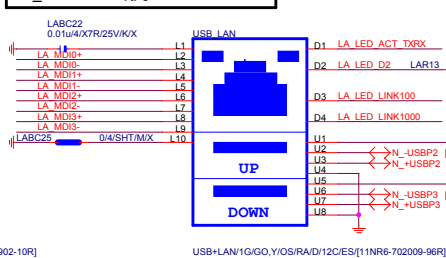
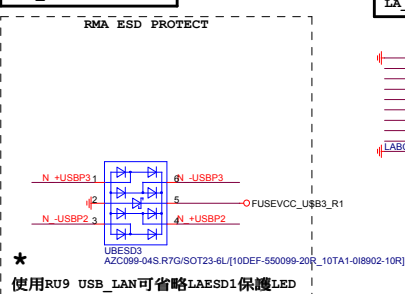
AZALIA FRONT PANEL



5



	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



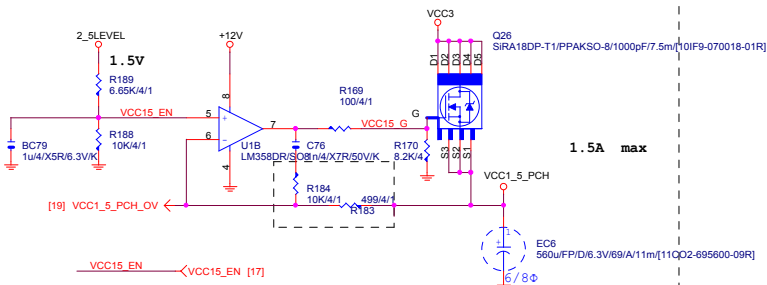
料號	規格	廠商
11NR6-702009-96R	1G LAN (12core)	UDE (RU9 ESD+)
[LED獨立走線,可省略外加AZC099料件LAESD1]		

1. 9KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R

2. 28KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R
LAESD2, LAESD3: 上件AZC398-04S

Gigabyte Technology			
Title			
Realtek RTL8111G			
Size	Document Number	GA-H81M-S2PV	Rev
Custom			2.0
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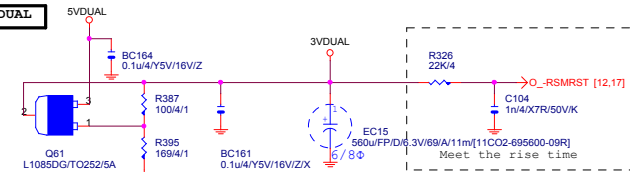
VCC1_5_PCH



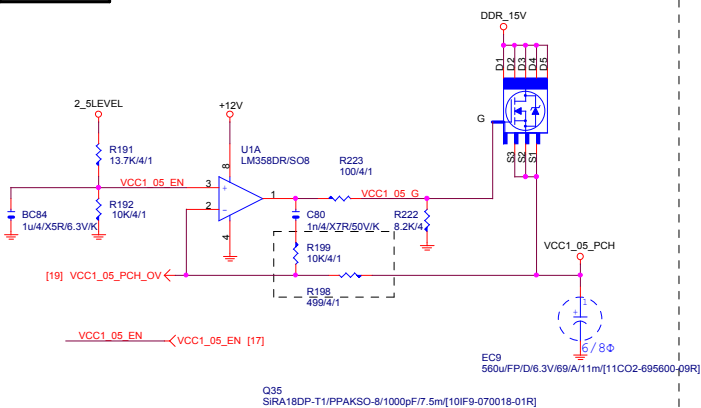
2_5LEVEL



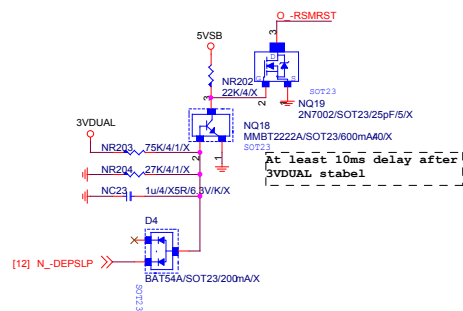
3VDUAL



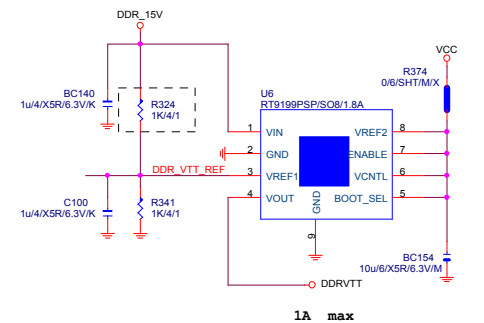
VCC1_05_PCH



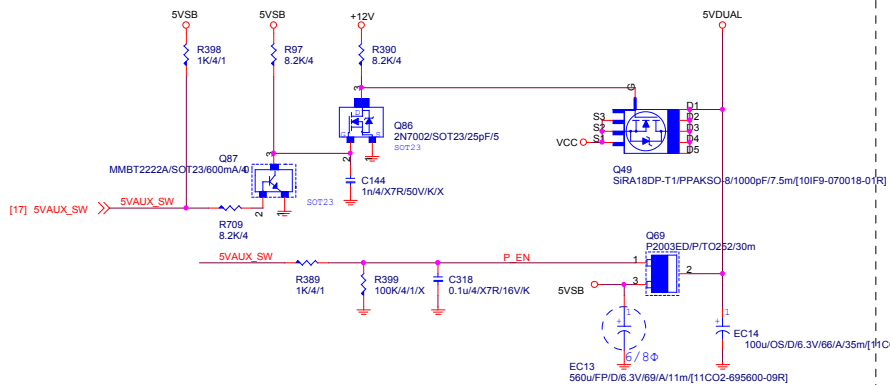
N/A



DDRVTT

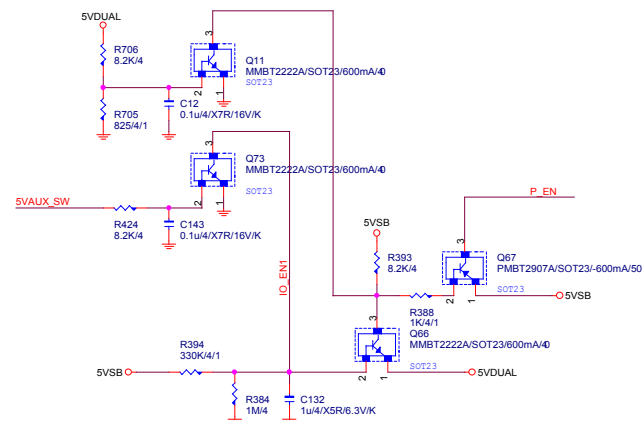


5VDUAL



5VDUAL SHORT PROTECT

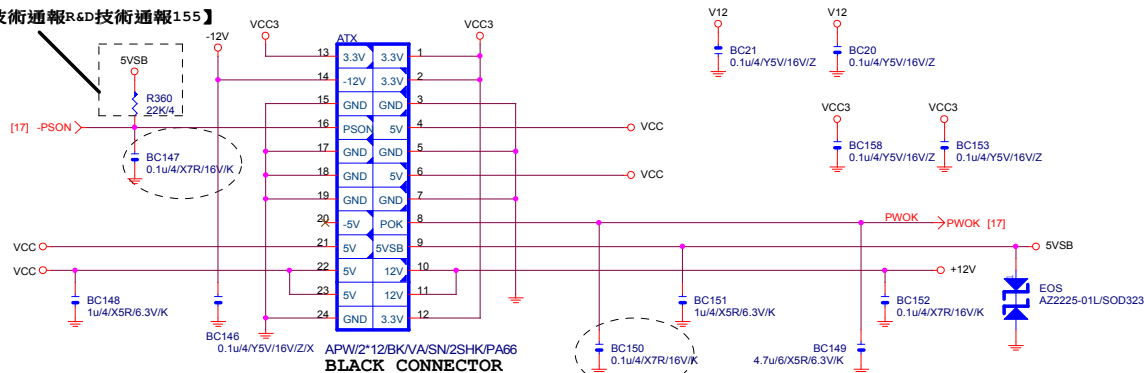
5VSB OVP:7.5V protection



Gigabyte Technology			
Title			
DISCRETE POWER			
Size	Document Number	GA-H81M-S2PV	Rev
Custom			2.
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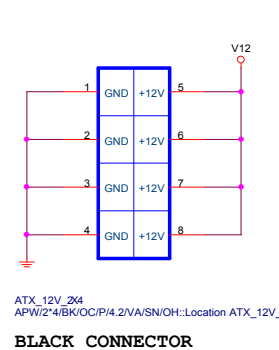
ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】

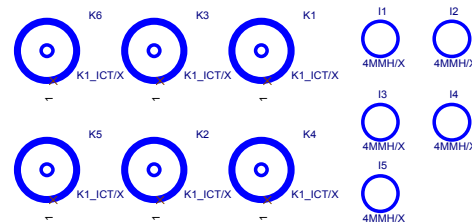
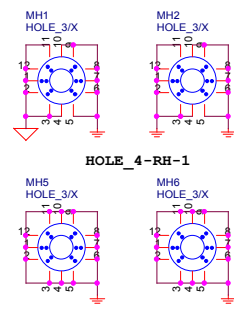


BLACK CONNECTOR

ATXX4 POWER CONNECTOR



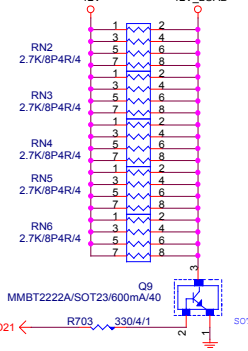
ATX_12V_24
APW/2'4/BK/OC/P/4.2VA/SN/OH::Location ATX_12V_2X4
BLACK CONNECTOR



To prevent the 5VSB under loading when boot

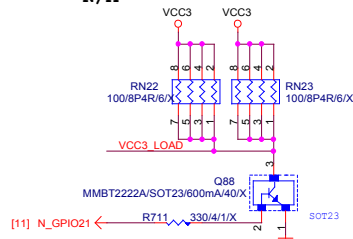
【技術通報R&D技術通報153】

To fix 12V light load abnormal issue



FIX PWR MINMUN LOAD

N/A



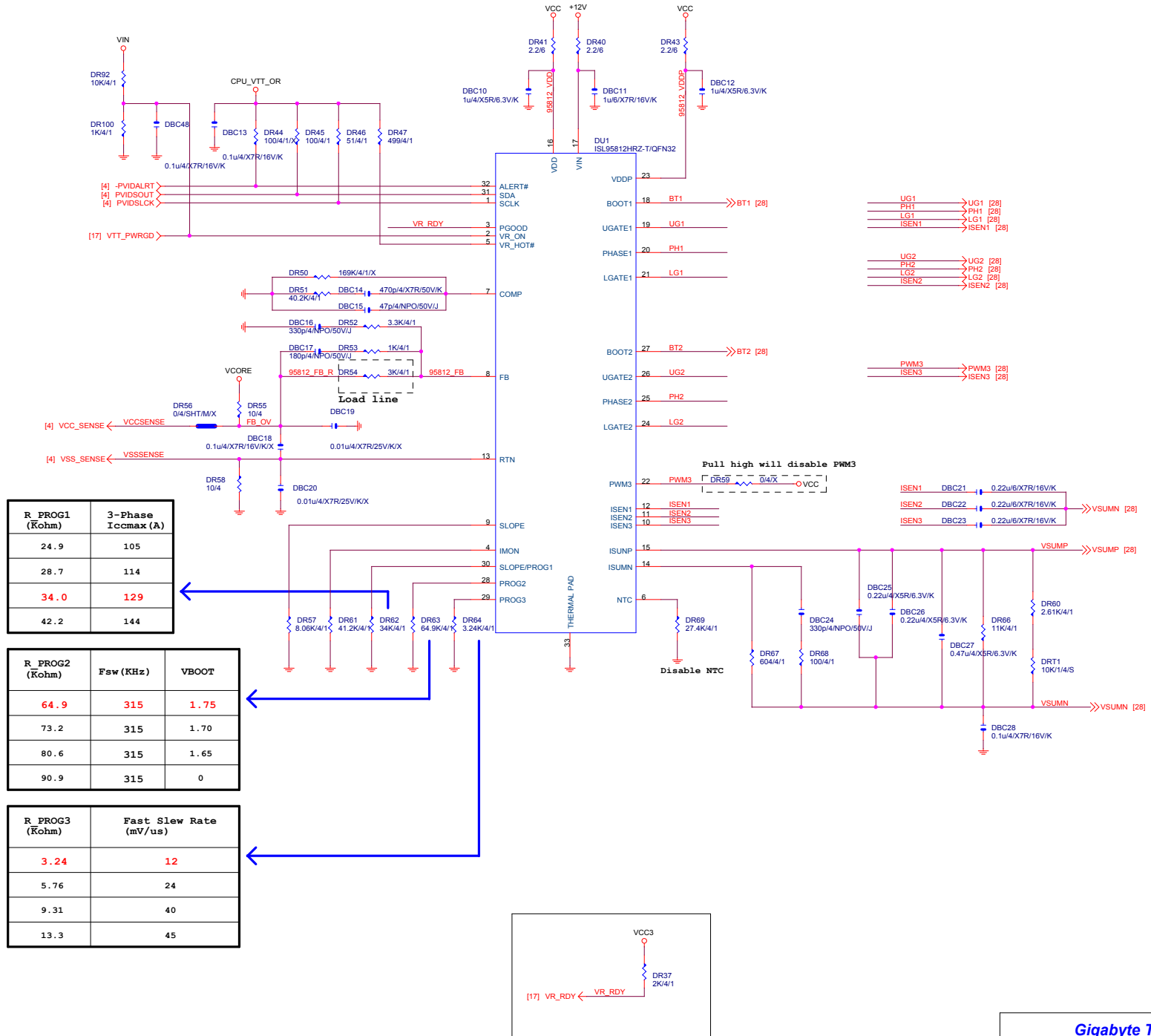
Gigabyte Technology

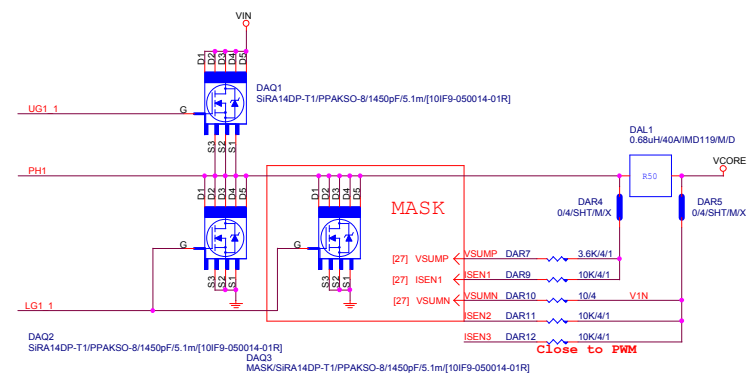
ATX CONNECTOR

GA-H81M-S2PV

Rev 2.0

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PWM3 → PWM3 [27]

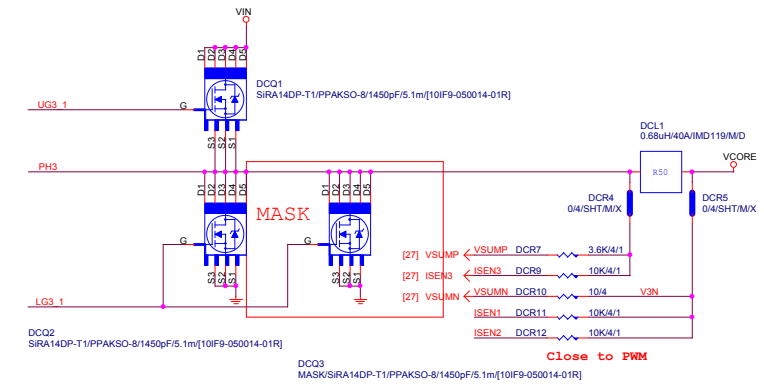
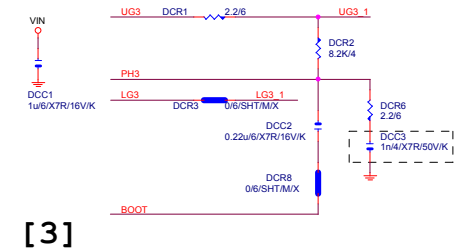
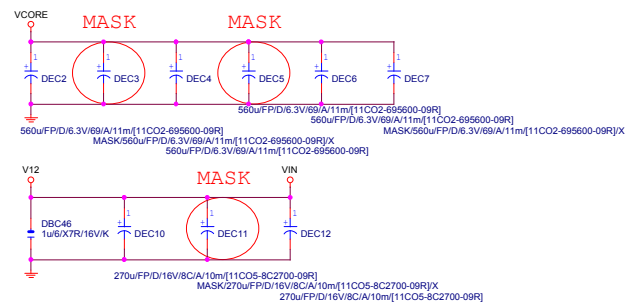
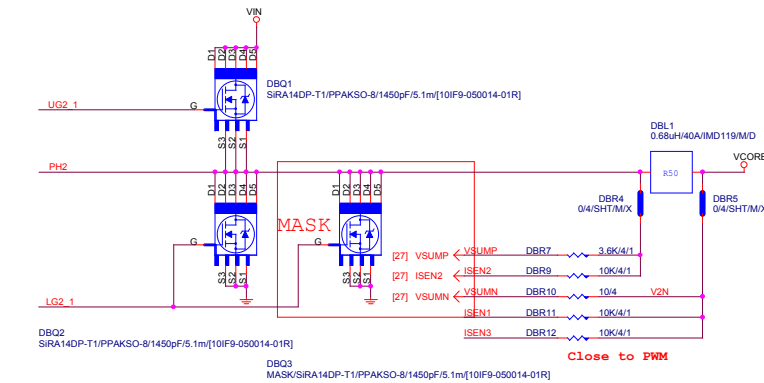


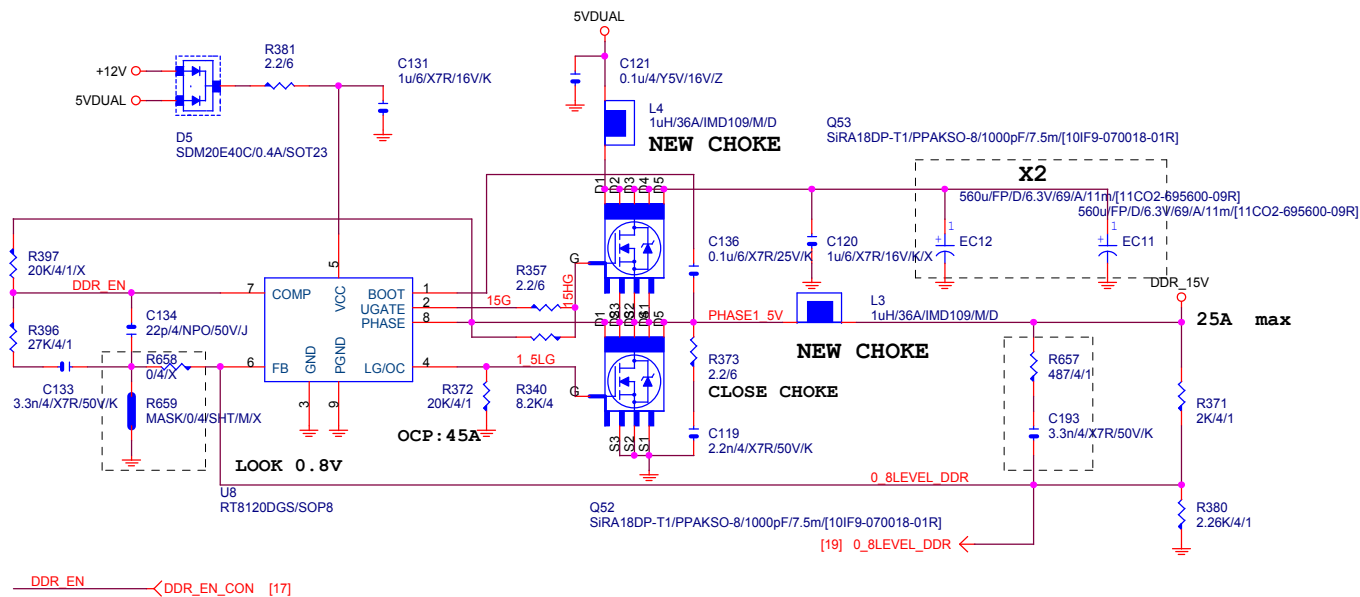
Diagram illustrating the mapping of variables to their corresponding values in the context of the 27th element:

- UG2 maps to UG2 [27]
- PH2 maps to PH2 [27]
- LG2 maps to LG2 [27]

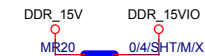


Gigabyte Technology			
Title CPU CORE VR-2			
Size Custom	Document Number GA-H81M-S2PV	Rev 2.0	
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DDR15V



From DDR_15V source
10 mils trace to SIO



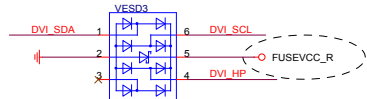
PWR SEQ

VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7x1.7=7.99A(85°C)
-->故固態電容須2x7.99=15.98>11.45A

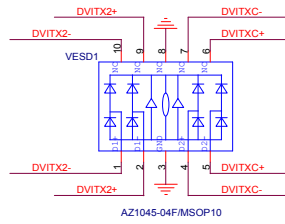
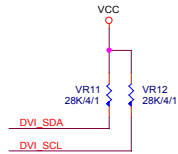
```
Rocset=(Iocp*Lgate,rdson)/Iocset
Rocset=(45A*6.7mOhm)/10uA = 30K
Iocset=10uA
```

<i>Gigabyte Technology</i>			
Title			
DDR POWER			
Size	Document Number	GA-H81M-S2PV	Rev
Custom			2.0
Date:	Wednesday, April 09, 2014	Sheet	29 of 31

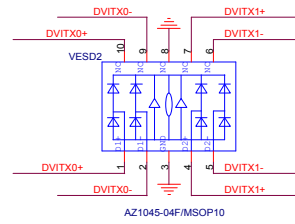
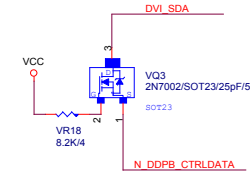
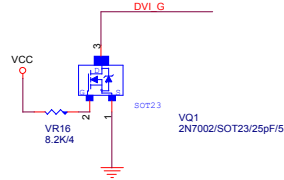
DVI



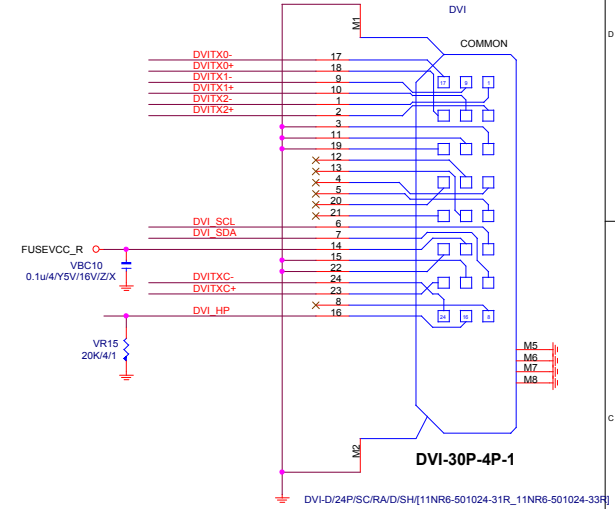
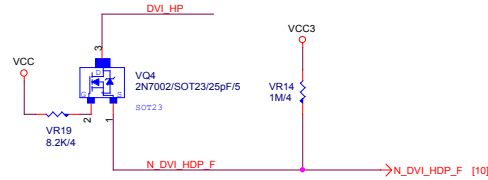
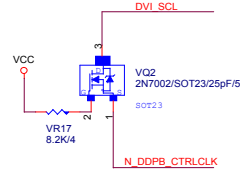
Close to connector



Close to connector

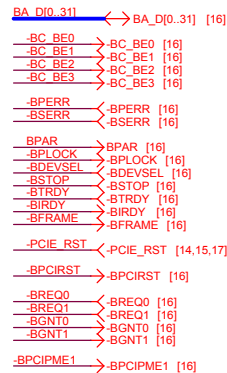


Close to connector



PCIE TO PCI

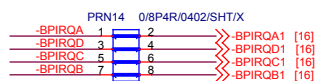
PCI:5/4/5 Impedance=50 +- 15%



High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz



High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip



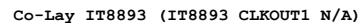
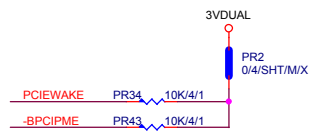
PCI slot



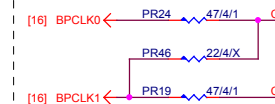
PCI slot

-BPCIPME1 PR27 0/4/SHT/M/X>>N -PCIE WAKE [12.14.15.24]

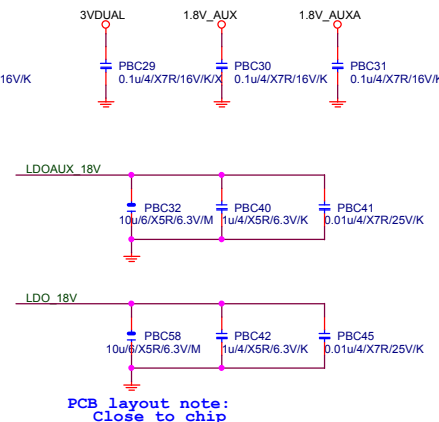
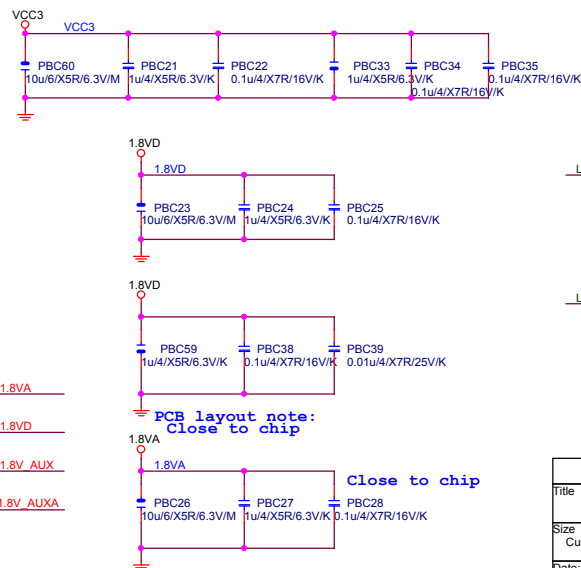
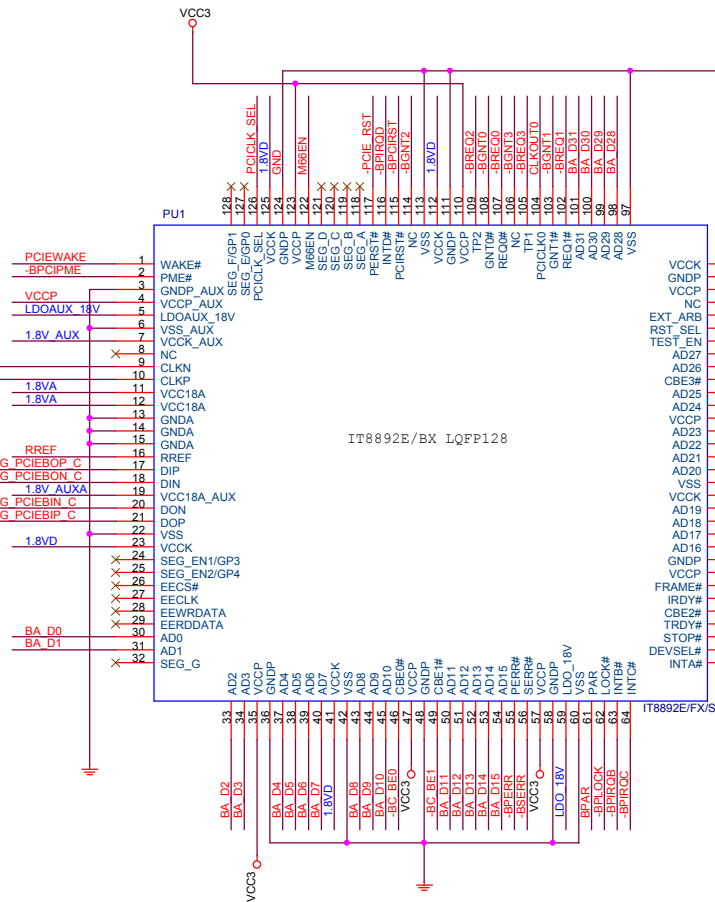
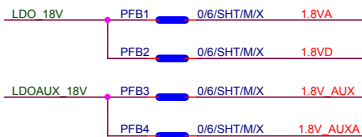
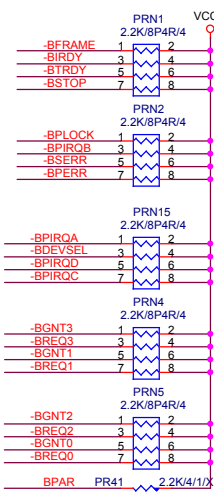
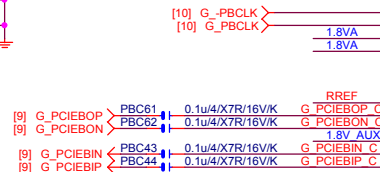
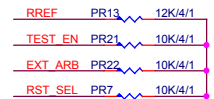
chipset side



```
IT8892: PR24 -> 47ohm
IT8893: PR24 -> 22ohm
```



IT8892: PR19
IT8893: PR19



PCB layout note:
Close to chip

Gigabyte Technology

ITE IT8892E
GA-H81M-S2PV

2.0