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HSF Property:ROHS or Halogen-Free

DAKAR10F/FG  
(45W CPU)  
MP(A02) BUILD

2012.03.23

21-04-01-2002		
DATE	CHANGE NO.	REV

INVENTEC			
DRAWER	EE	DATE	POWER
DESIGN	LOREN	2012.03.23	EDISON
CHECK	LOREN	2012.03.23	EDISON
RESPONSIBLE	BREYAN	2012.03.23	BY
SIZE	VER:		
FILE TIME	PIN		
SIZE	CODE	DOC NUMBER	REV
C	CS	13100000000000000000	X01
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www.laptopfix.vn

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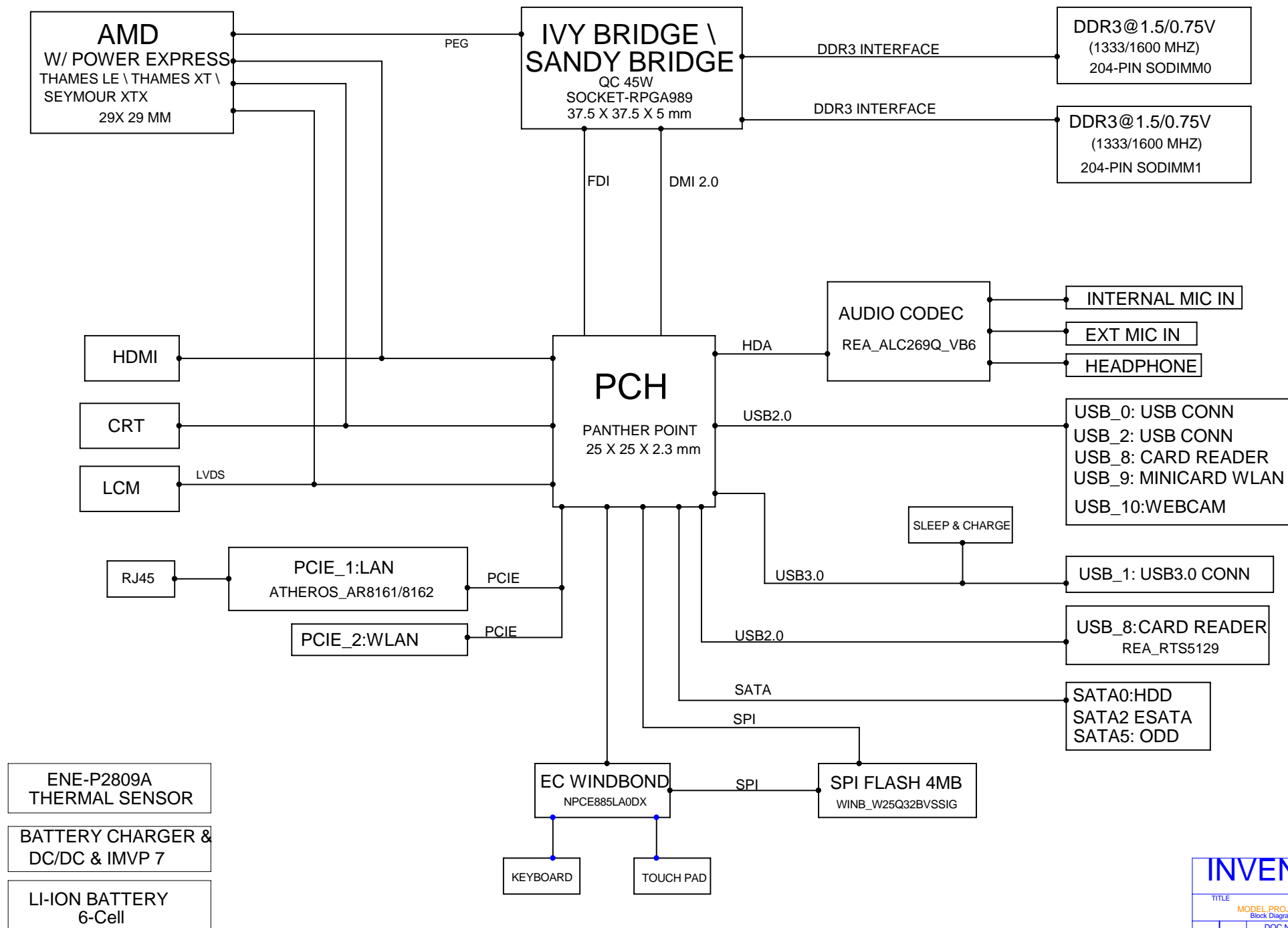
## INVENTEC

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MODEL PROJECT FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01

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**INVENTEC**

TITLE

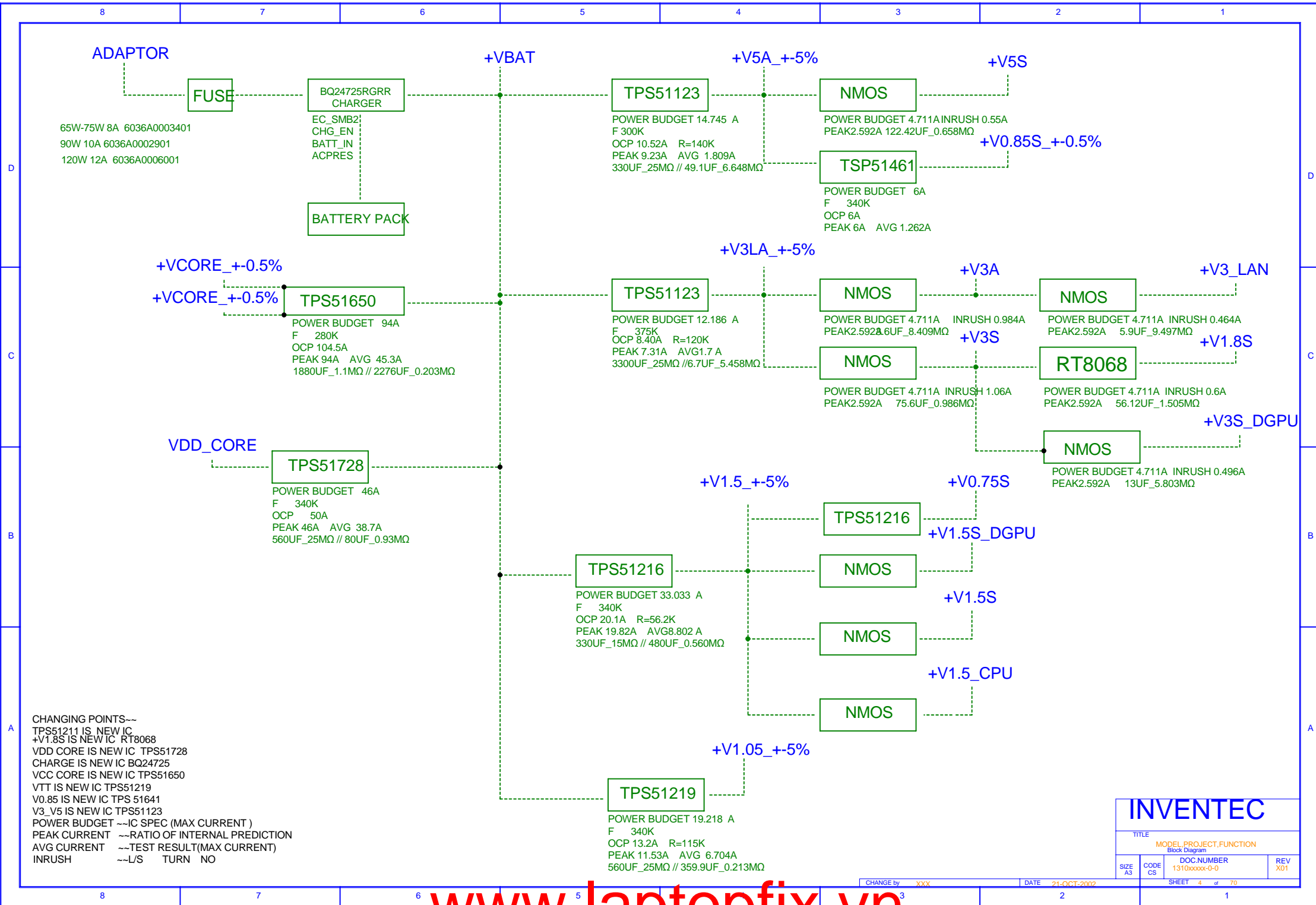
MODEL,PROJECT,FUNCTION  
Block Diagram

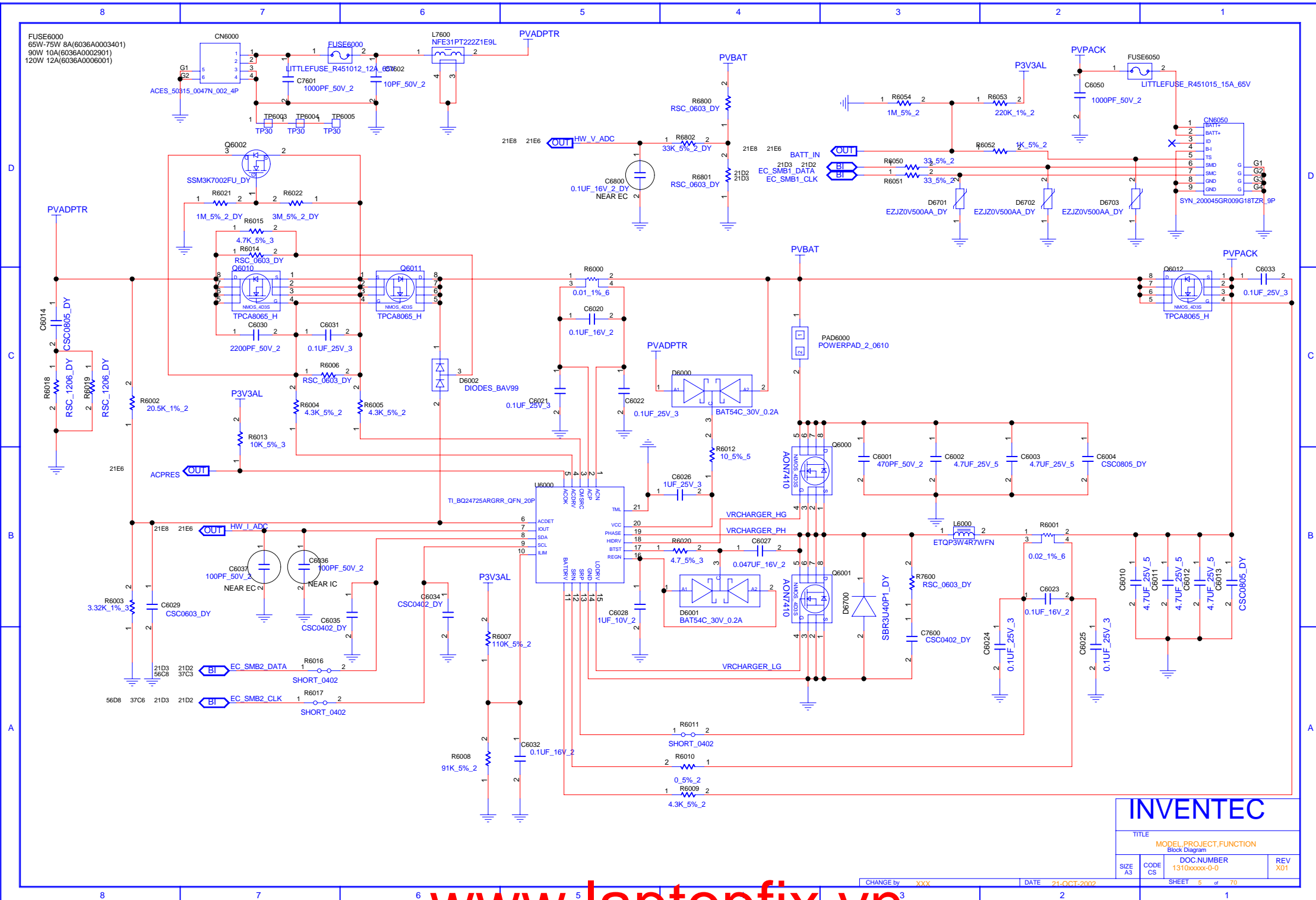
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A3	CS	1310xxxx-0-0	X01

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D

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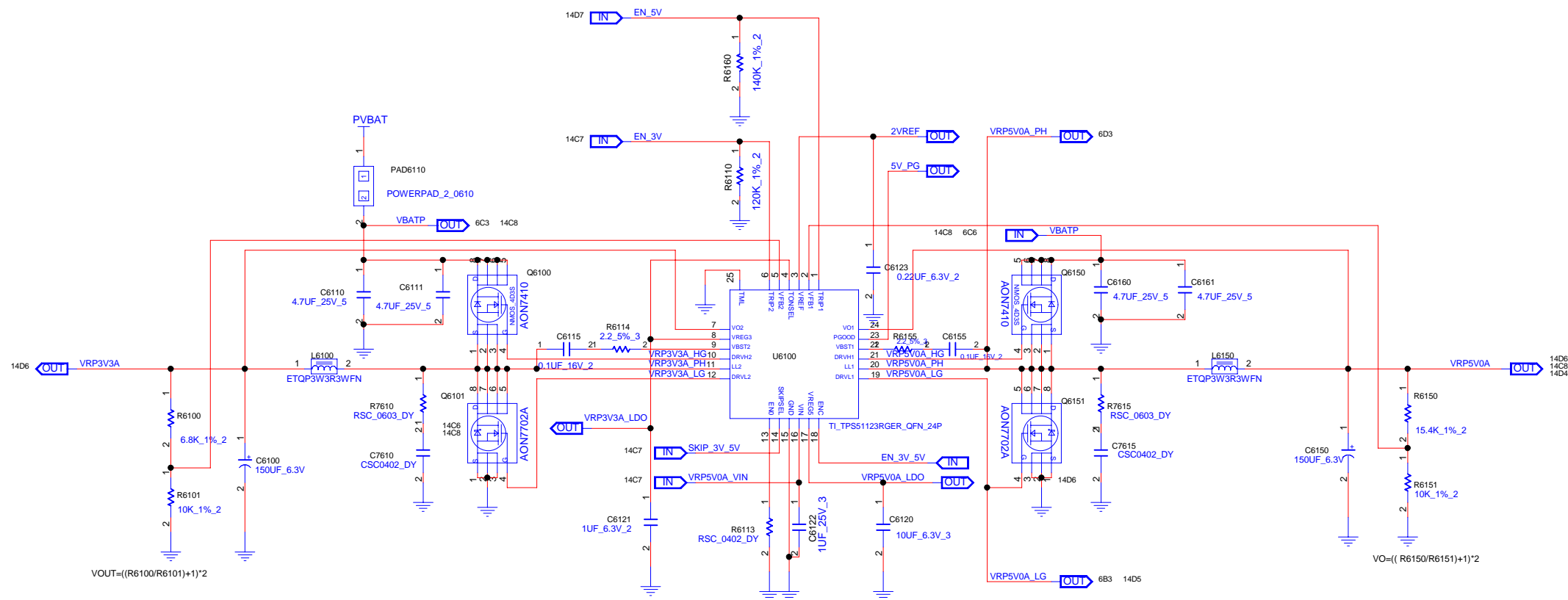
A

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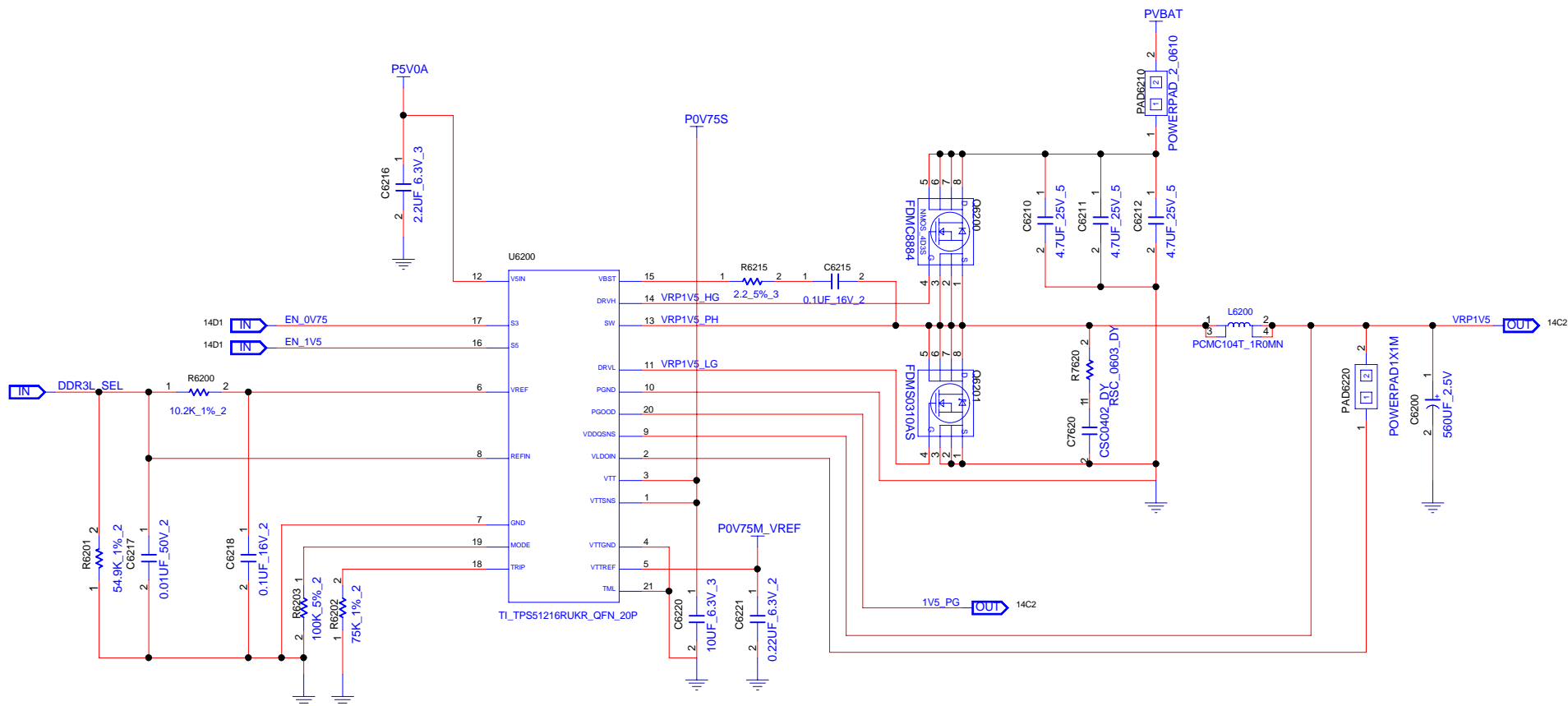
INVENTEC

TITLE

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$$VOUT=REFIN=1.8 \cdot (R6201 / (R6200 + R6201))$$

MODE=100KOHM:TRACKING DISCHARGE

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

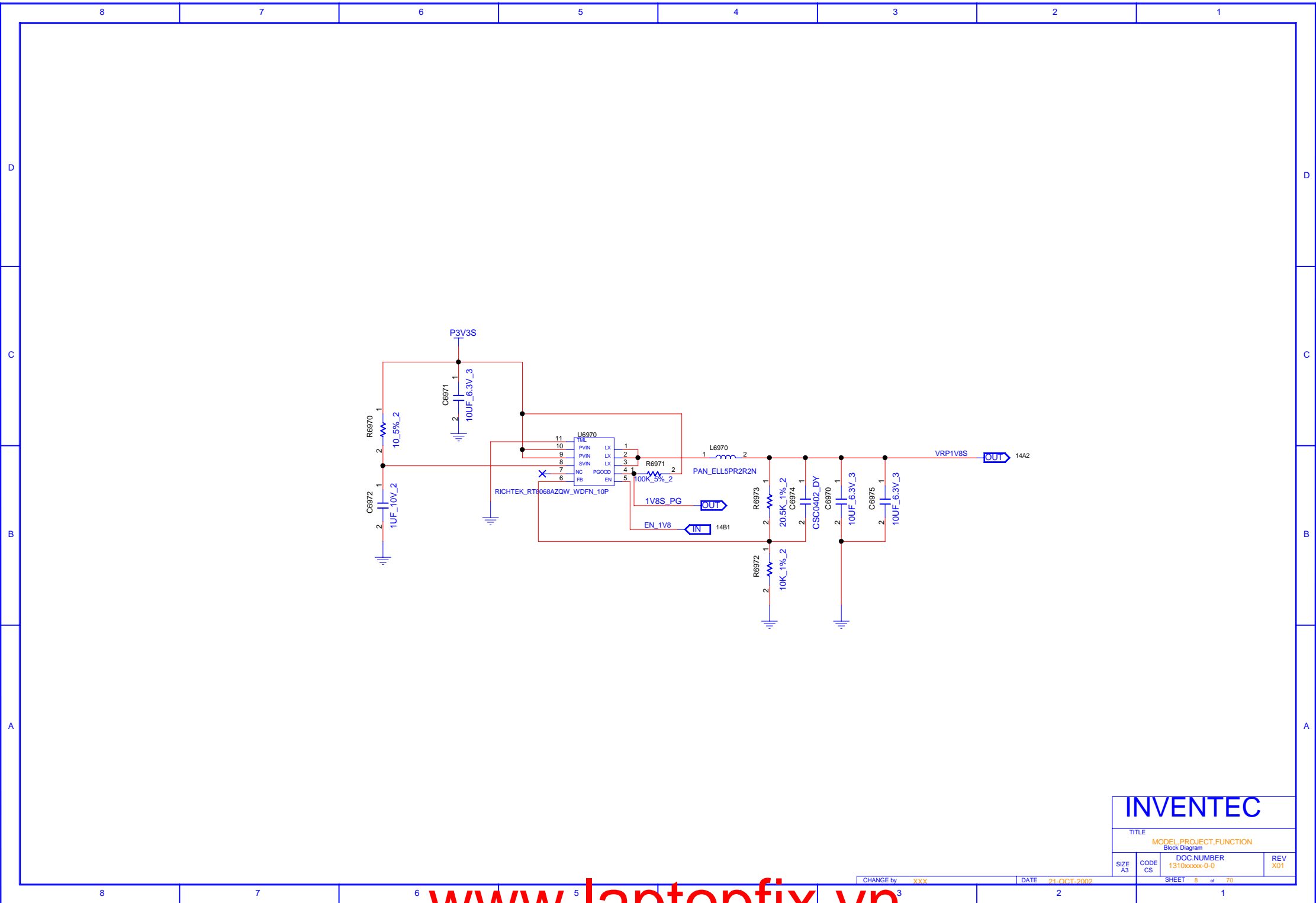
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D

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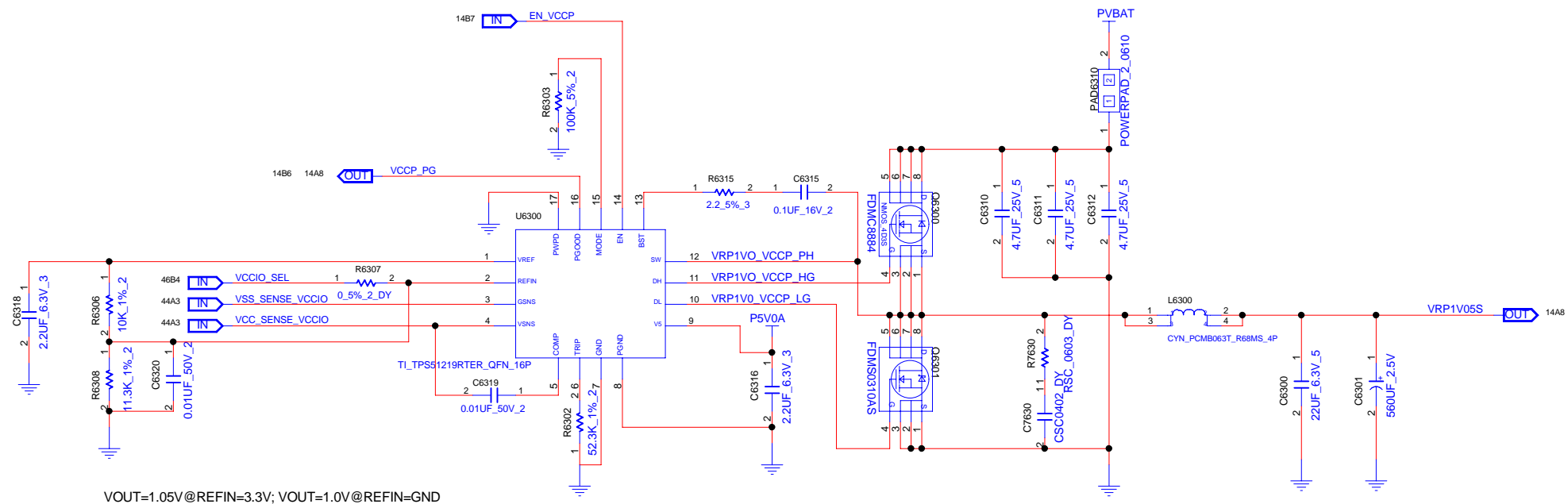
A

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B

A

**INVENTEC**TITLE  
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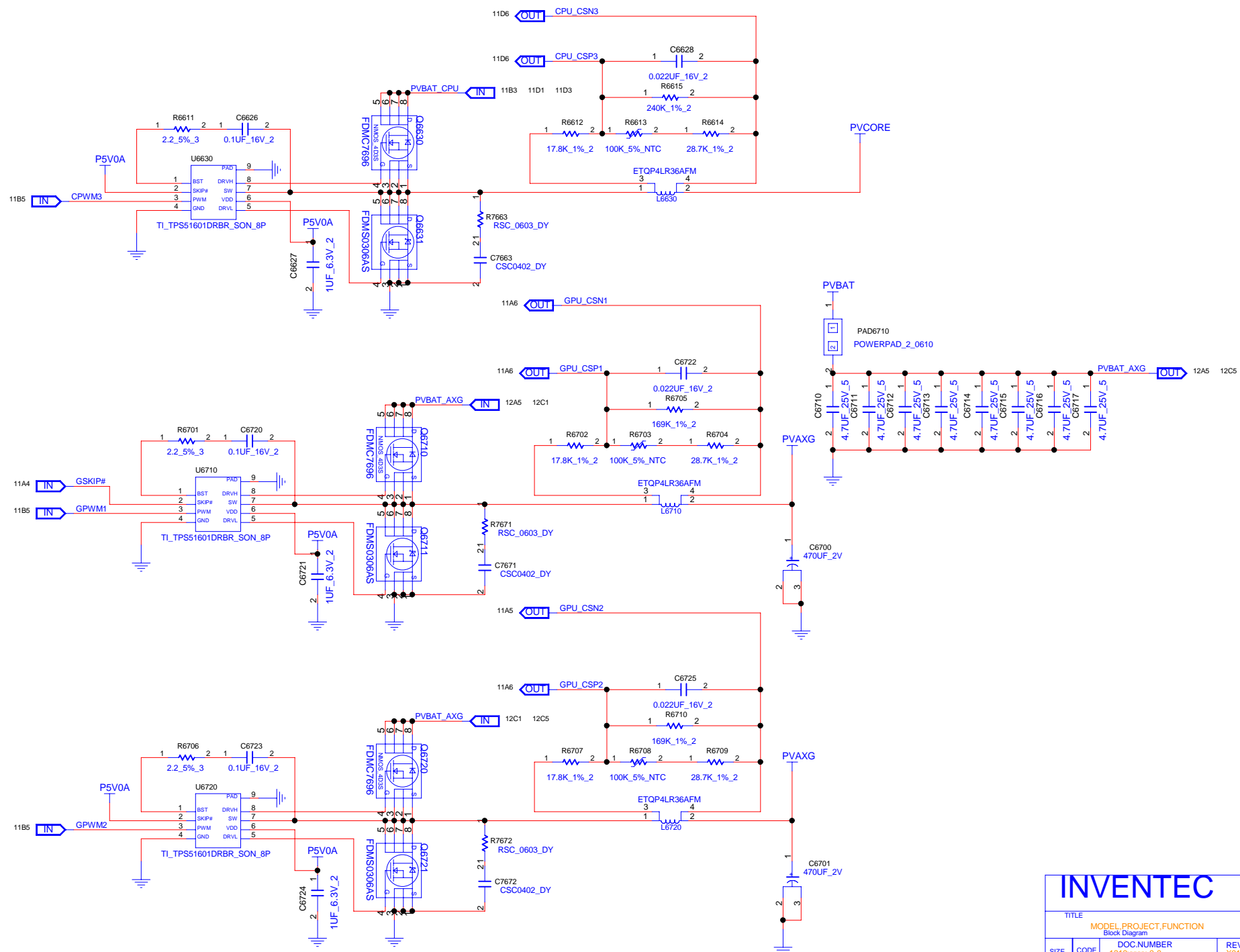


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INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

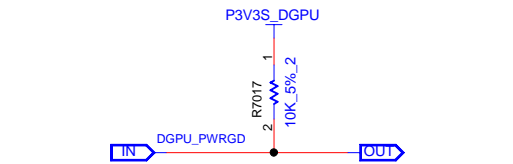
1310xxxx-0-0

REV

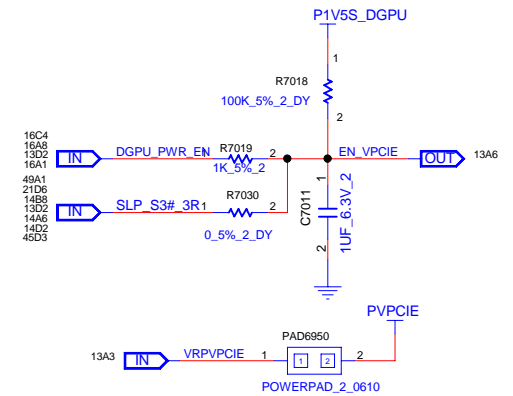
X01

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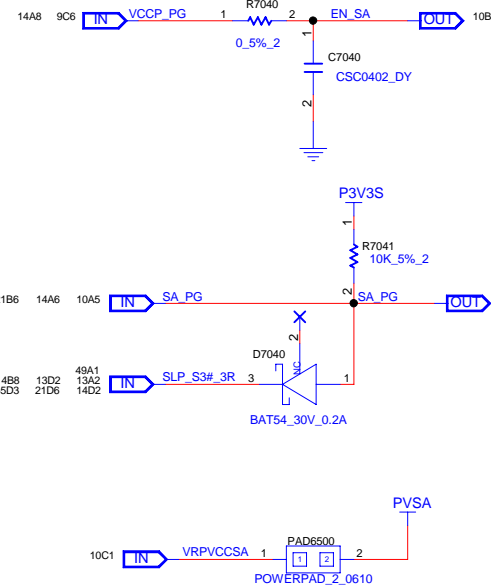
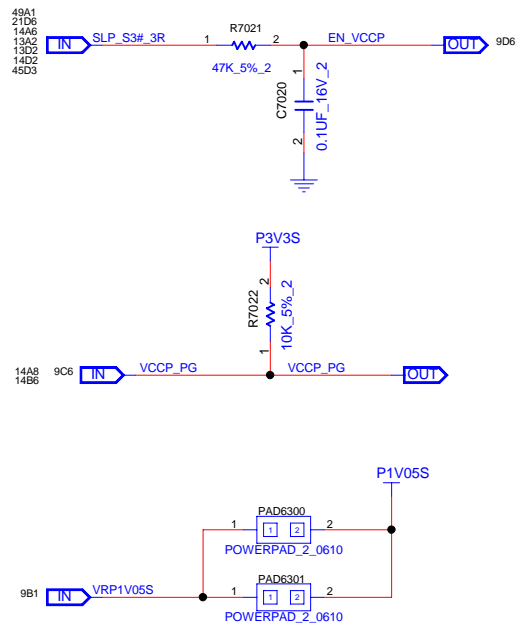
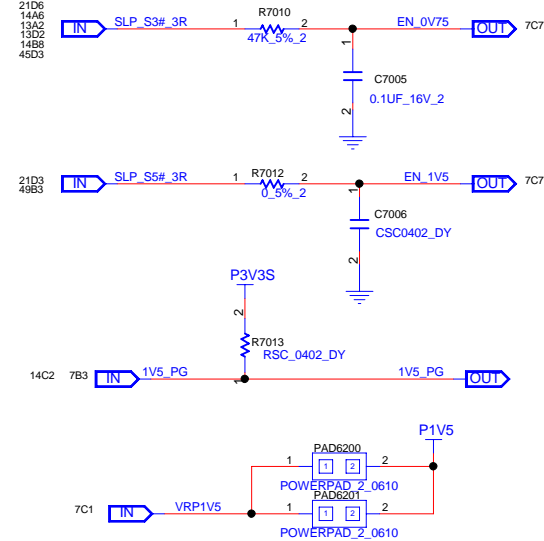
CHANGE by XXX DATE 21-OCT-2002



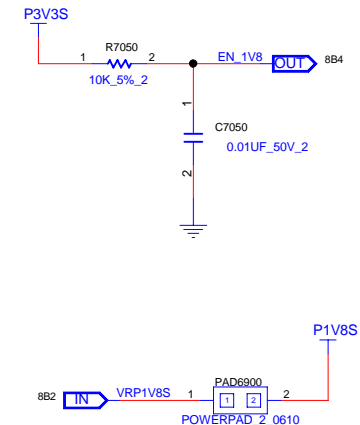
P.S. R6750(R1)R6751(R2)R6753(R3)R6754(R4)



TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0



DGPU



# INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. <b>Project Overview</b>	1. <b>Project Overview</b>
2. <b>Project Objectives</b>	2. <b>Project Objectives</b>
3. <b>Project Scope</b>	3. <b>Project Scope</b>
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5. <b>Project Management</b>	5. <b>Project Management</b>
6. <b>Project Communication</b>	6. <b>Project Communication</b>
7. <b>Project Risk Management</b>	7. <b>Project Risk Management</b>
8. <b>Project Quality Management</b>	8. <b>Project Quality Management</b>
9. <b>Project Procurement Management</b>	9. <b>Project Procurement Management</b>
10. <b>Project Stakeholder Management</b>	10. <b>Project Stakeholder Management</b>
11. <b>Project Performance Management</b>	11. <b>Project Performance Management</b>
12. <b>Project Change Management</b>	12. <b>Project Change Management</b>
13. <b>Project Closure</b>	13. <b>Project Closure</b>

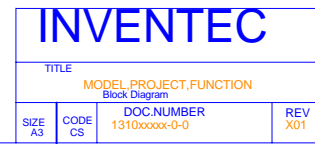
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R



D

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B

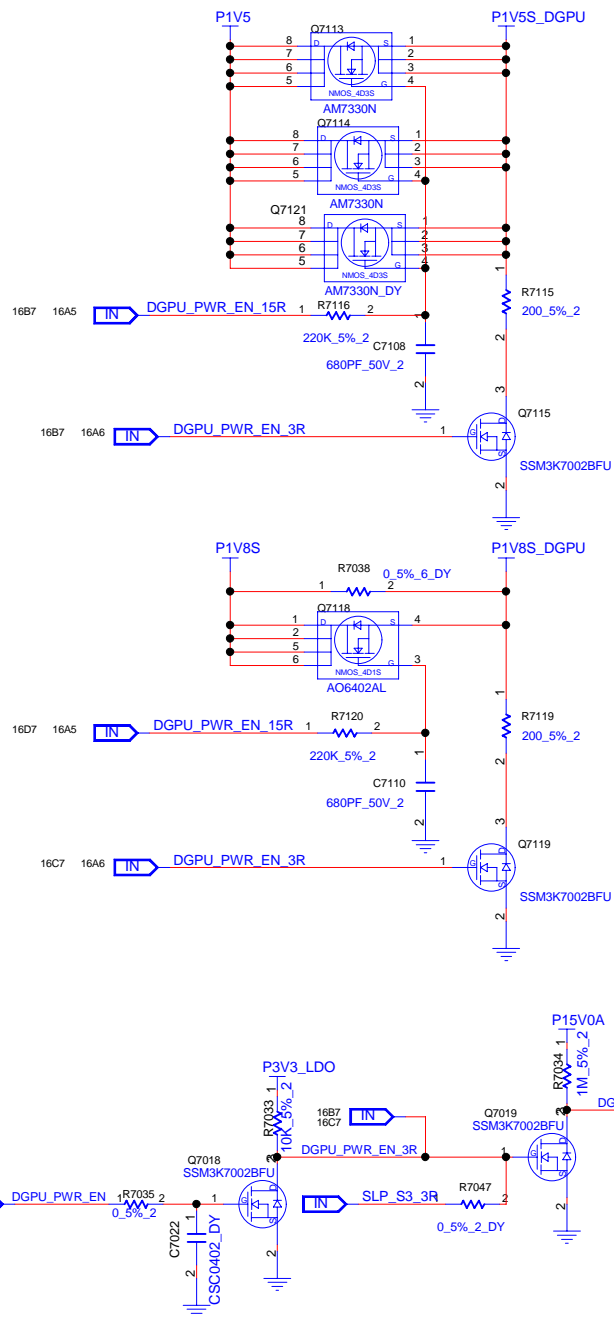
A

D

C

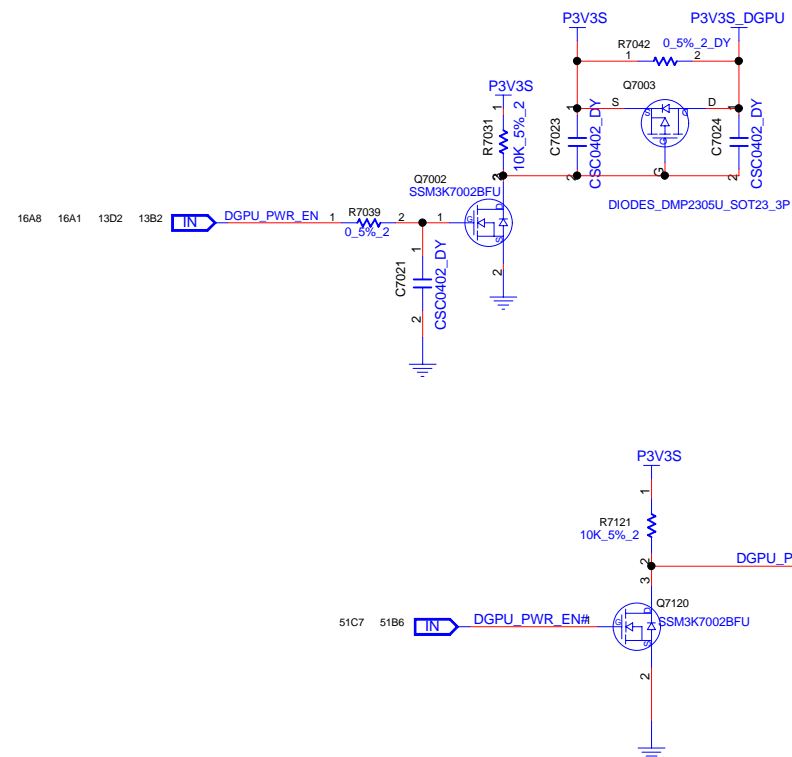
B

A



## POWER EXPRESS

	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PWRGD			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED



INVENTEC

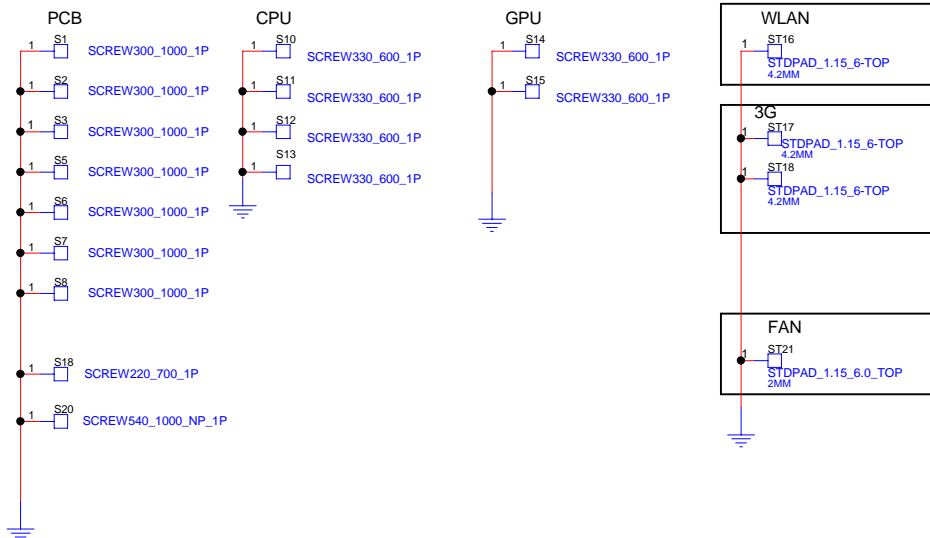
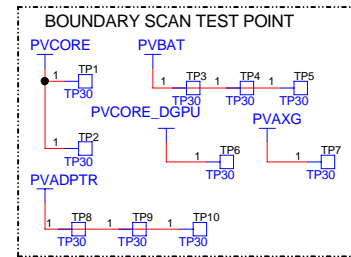
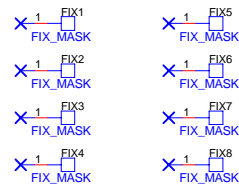
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REFERENCE 0~49(PCB SCREW)



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A3	CS	1310xxxx-0-0	X01

REFERENCE 50-99(HALL SENSOR)

D

D

C

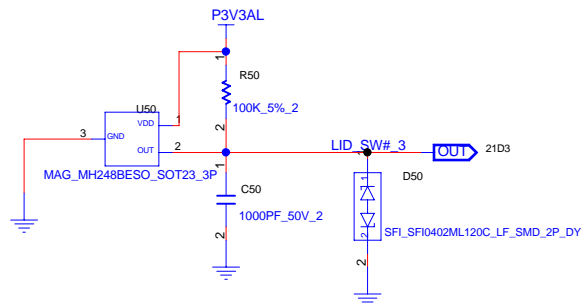
C

B

B

A

A



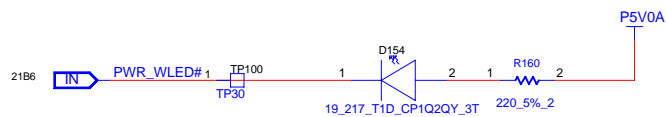
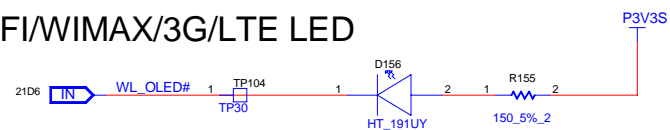
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Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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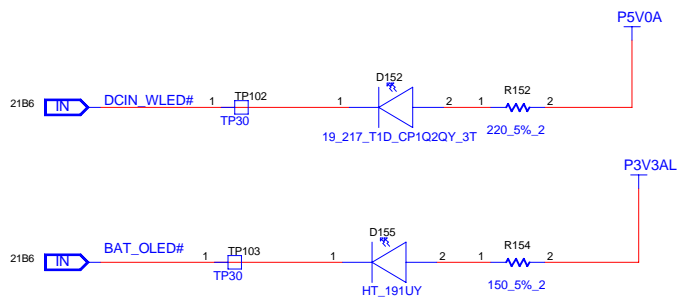
REFERENCE 100~199(LED)

**POWER ON LED**

D154 BRIGHT WHEN SYSTEM IS POWER ON  
D154 BLINK WHEN SYSTEM GO TO SLEEP

**WIFI/WIMAX/3G/LTE LED****DC IN / BATTERY CHARGE LED**

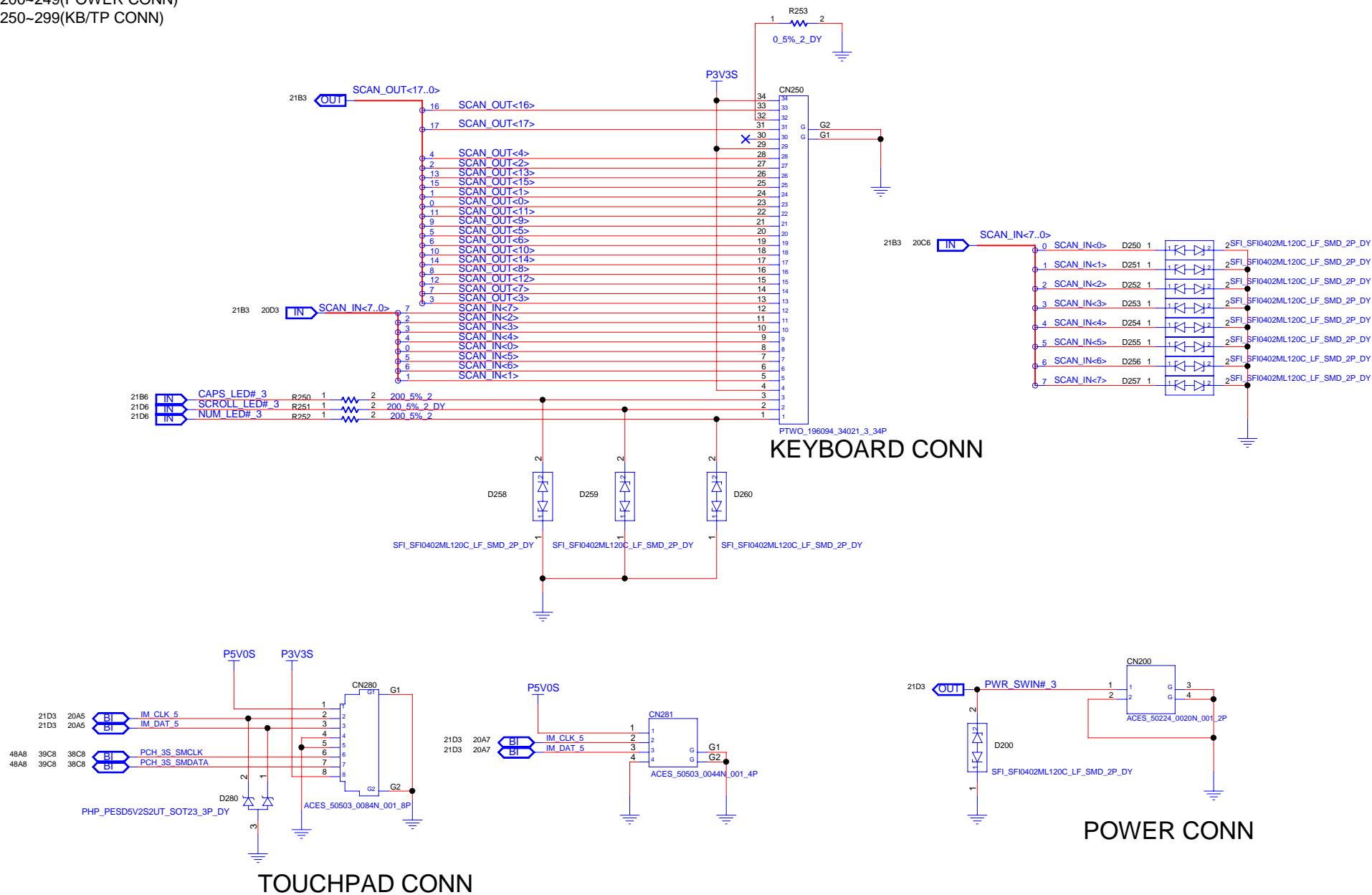
D152 BRIGHT: BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED  
D155 BRIGHT: WHILE CHARGING BATTERY FROM AC-ADAPTER  
BLINK: LOW BATTERY

**INVENTEC**TITLE  
MODEL, PROJECT, FUNCTION  
Block DiagramSIZE A3 CODE CS DOC NUMBER  
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X01

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REFERENCE 200~249(POWER CONN)  
REFERENCE 250~299(KB/TP CONN)



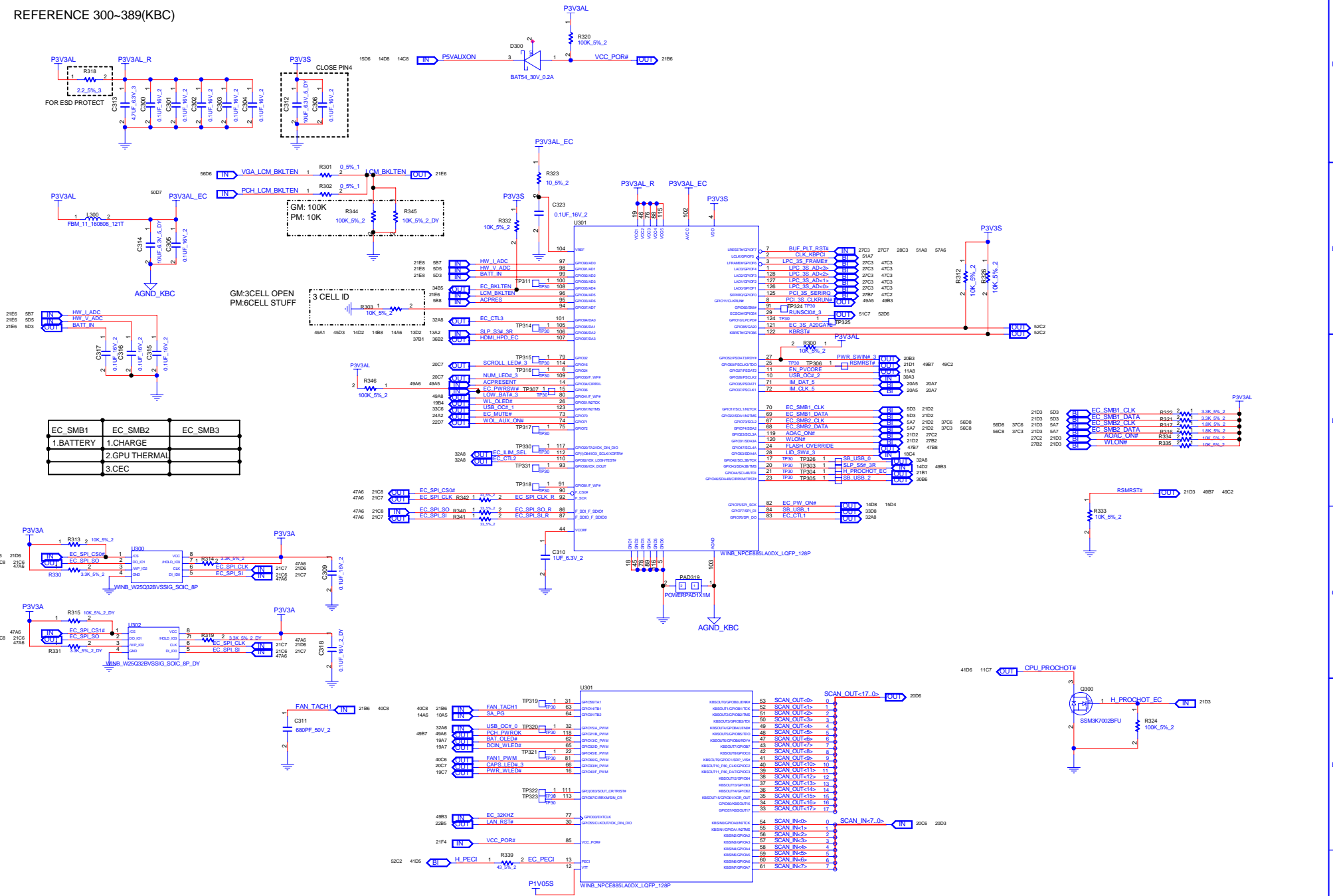
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TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

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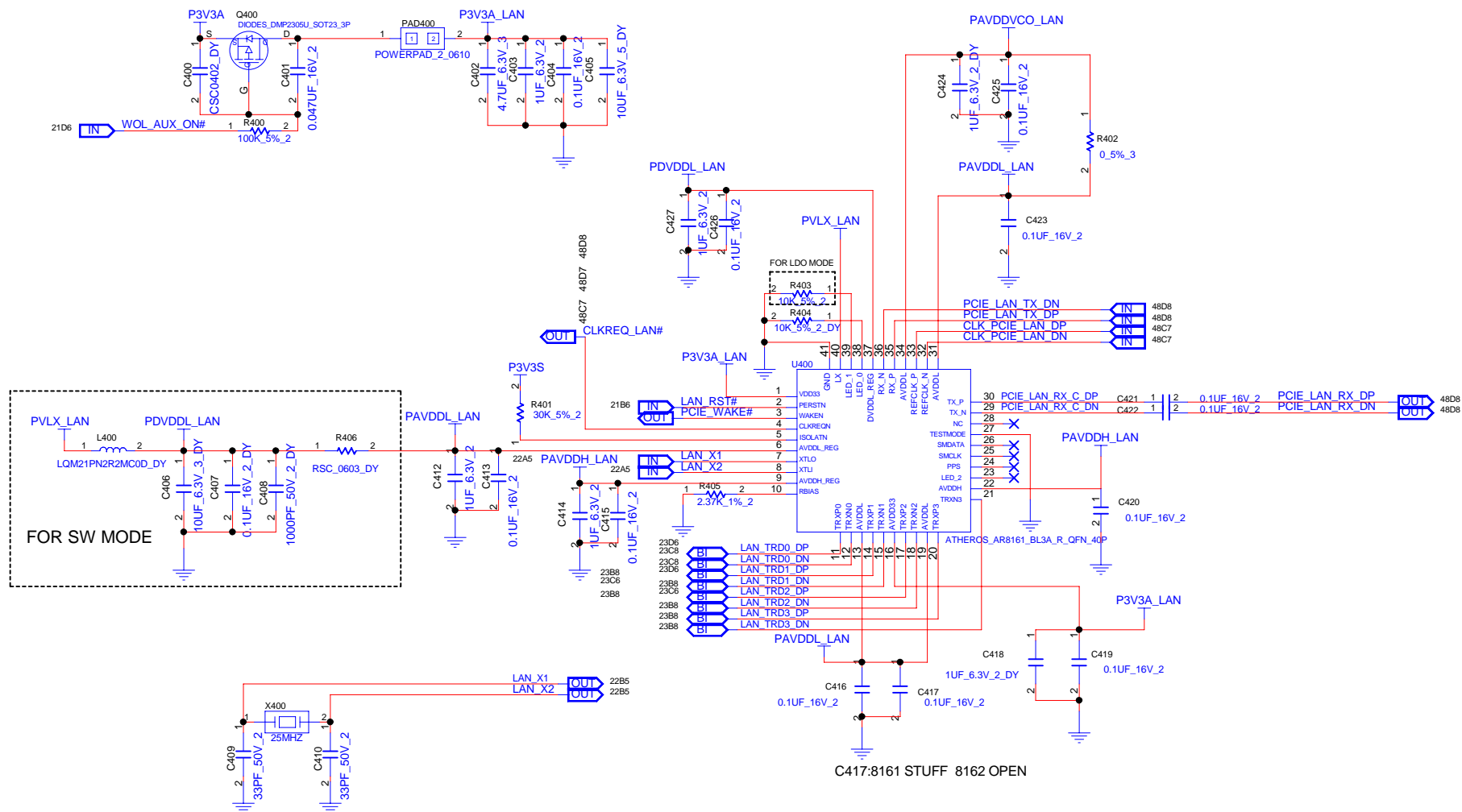
# REFERENCE 300-389(KBC)



INVENTEC

Title			
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C	CS	131000000-05	001
SHEET		4	4

REFERENCE 400~499(LAN)



INVENTEC

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X01

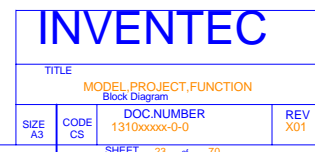
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Diagram illustrating the Ethernet PHY connection for the Panta module. The module is connected to a network via an RJ45 connector (JACK470). The connections are as follows:

Pin	Signal
1	LAN_TD_DP
2	LAN_TD_DN
3	LAN_RD_DP
4	LAN_RD_DN
5	LAN_CD_DP
6	LAN_CD_DN
7	LAN_D_DP
8	LAN_D_DN

The module is labeled PANTA\_130456\_051\_8P-.





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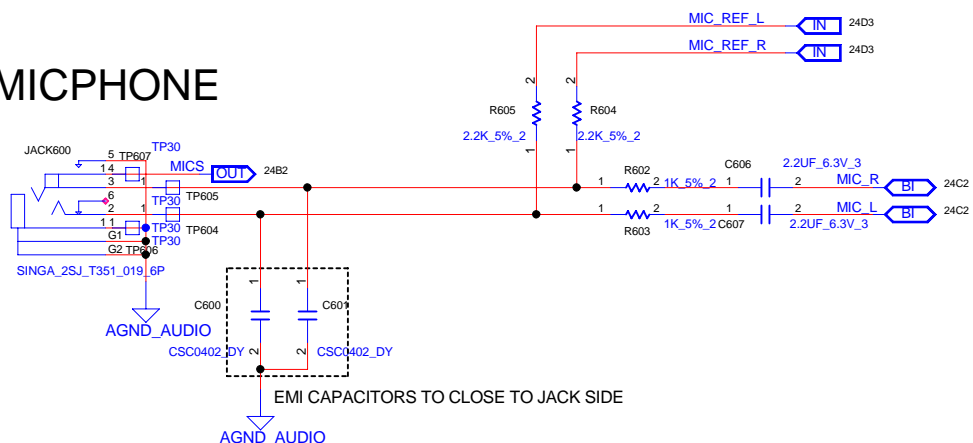
1



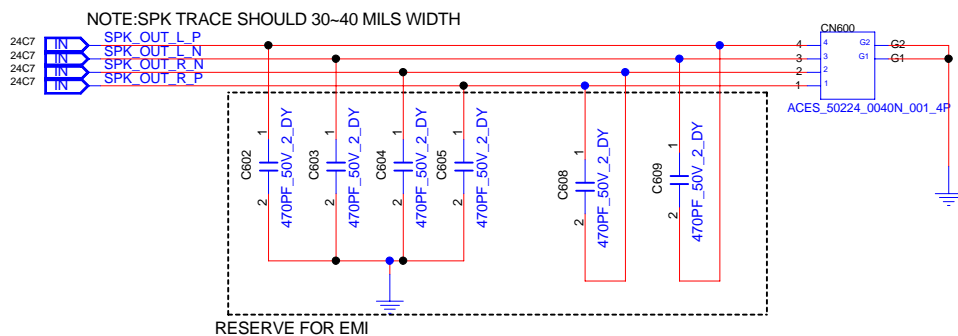
REFERCE 600-649(JACK/MIC/SPEAKER)

# AUDIO JACKS

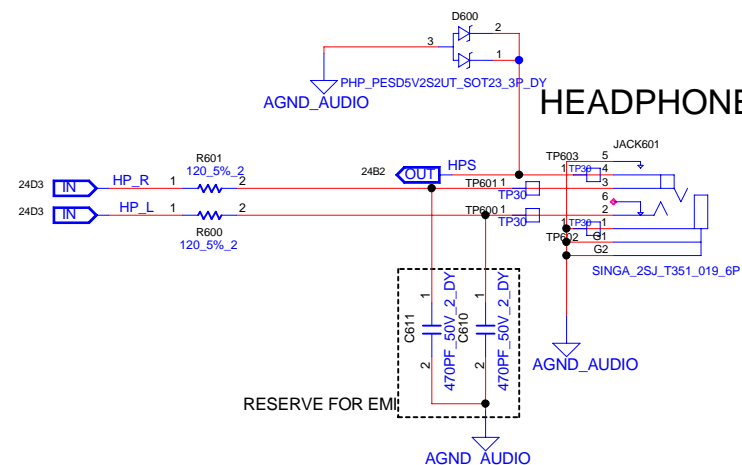
## MICPHONE



## INTERNAL SPEAKERS



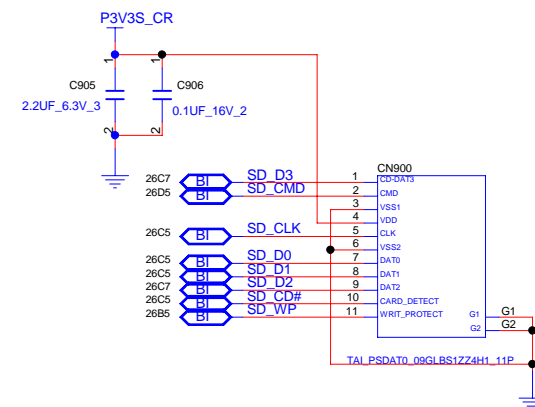
## HEADPHONE

**INVENTEC**

TITLE  
MODEL PROJECT FUNCTION  
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REV  
X01

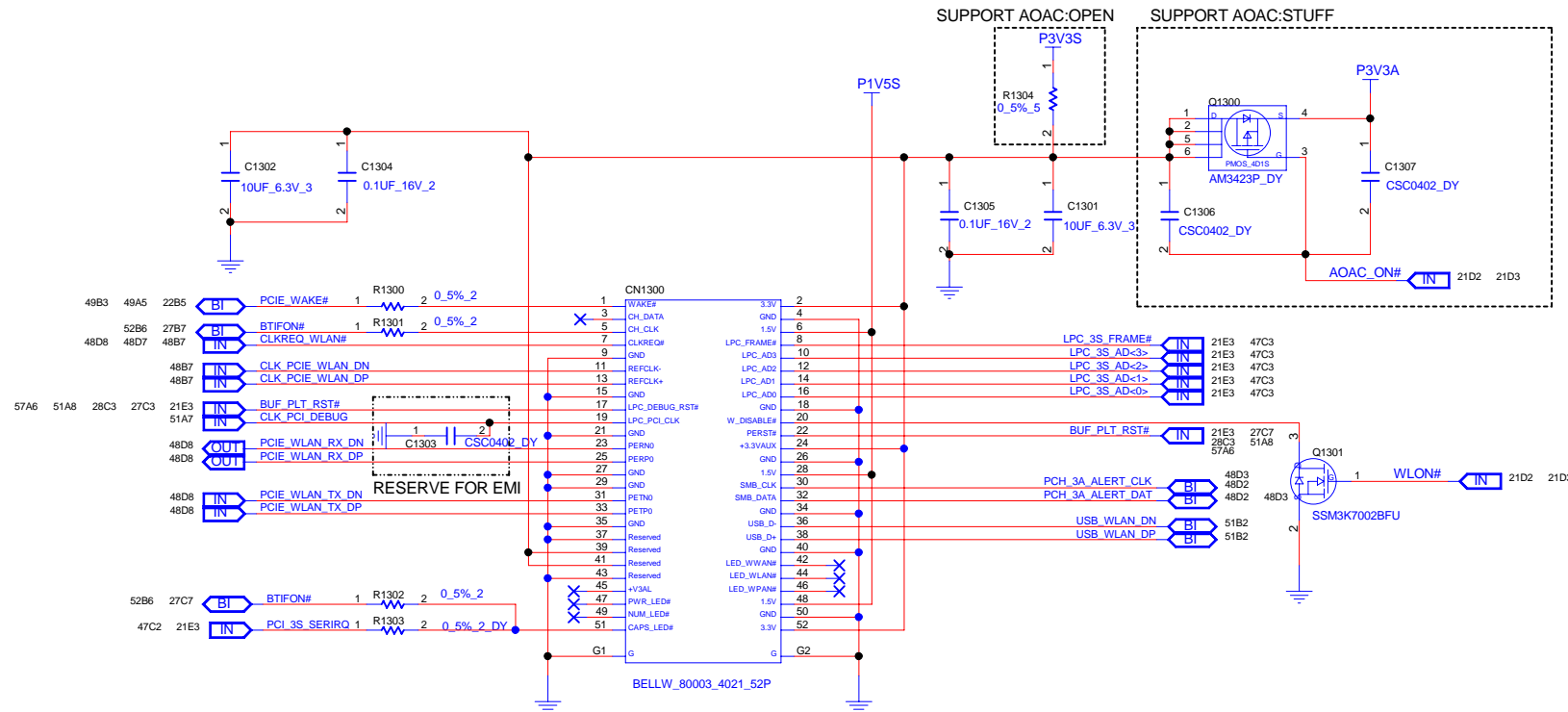
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REFERENCE 1300~1349(WLAN)



MINI CARD 1(WLAN)

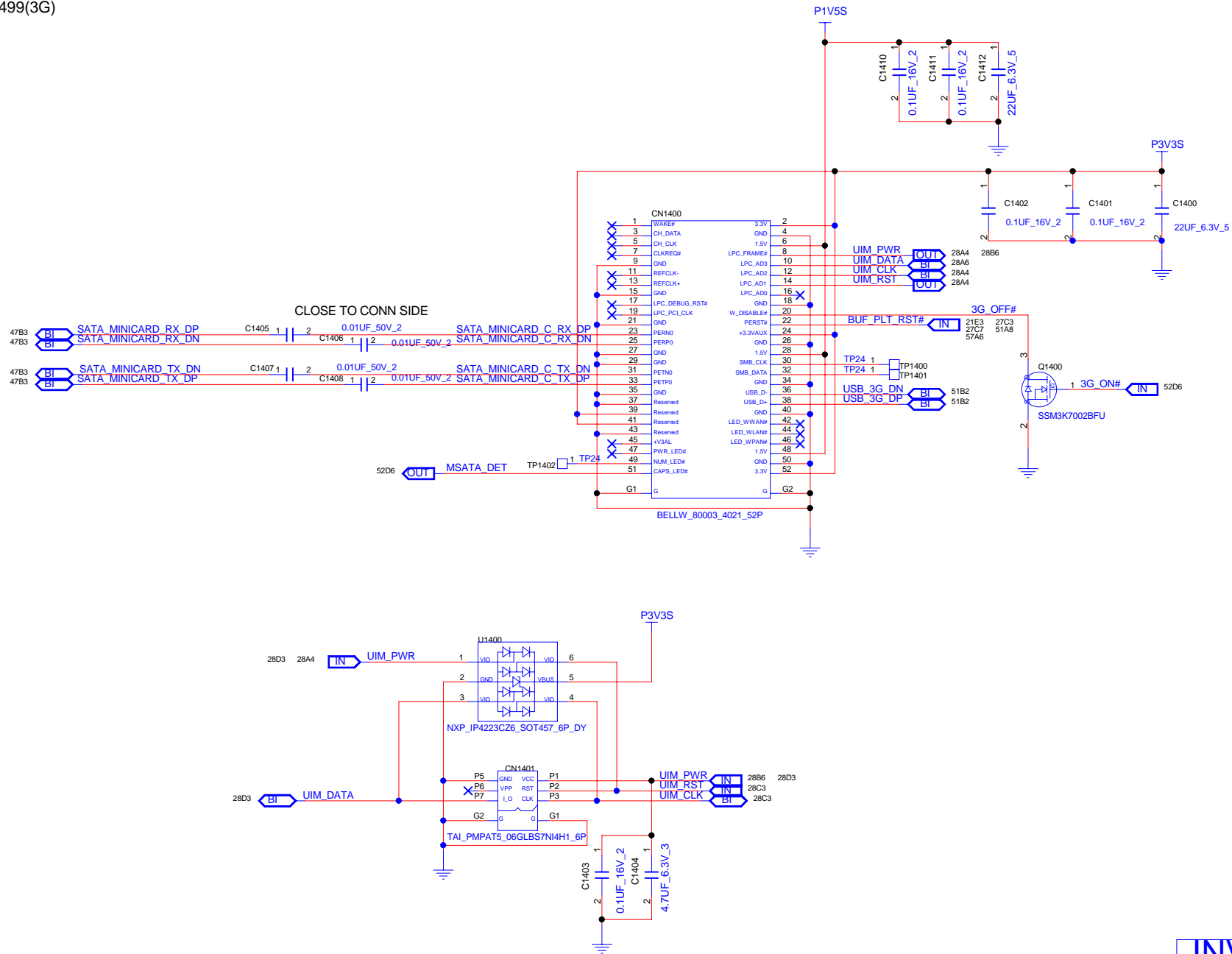
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TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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REFERENCE 1400~1499(3G)



INVENTEC

TITLE

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Block Diagram

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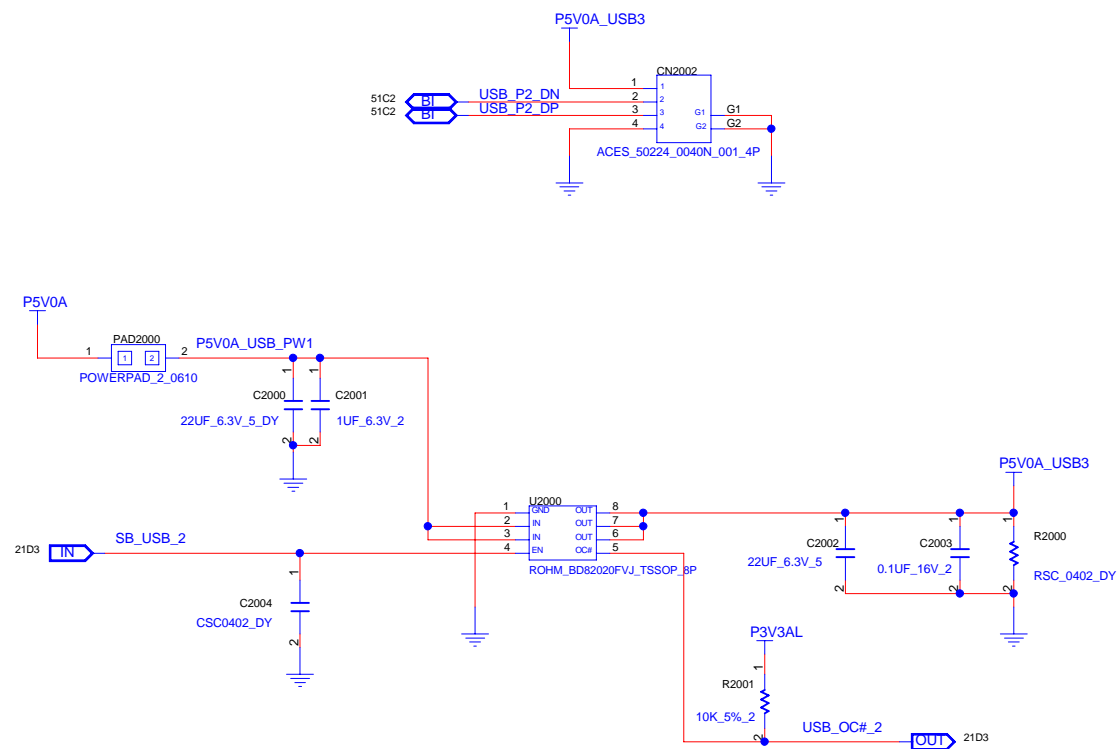
X01

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REFERENCE 2000~2099(USB)



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1. <b>Project Overview</b>	1. <b>Project Overview</b>
2. <b>Project Objectives</b>	2. <b>Project Objectives</b>
3. <b>Project Scope</b>	3. <b>Project Scope</b>
4. <b>Project Organization</b>	4. <b>Project Organization</b>
5. <b>Project Schedule</b>	5. <b>Project Schedule</b>
6. <b>Project Budget</b>	6. <b>Project Budget</b>
7. <b>Project Risk Management</b>	7. <b>Project Risk Management</b>
8. <b>Project Communication</b>	8. <b>Project Communication</b>
9. <b>Project Monitoring and Control</b>	9. <b>Project Monitoring and Control</b>
10. <b>Project Closure</b>	10. <b>Project Closure</b>

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AS	CS	SHEET 30 of 70
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1

REFERENCE 2400~2499

REMOVE USB3.0 CONTROLLER IC

INVENTEC

TITLE

MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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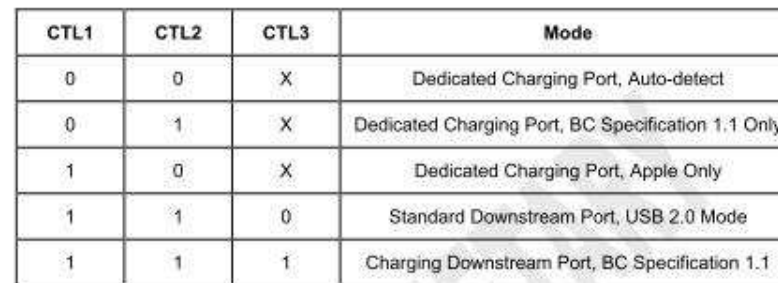


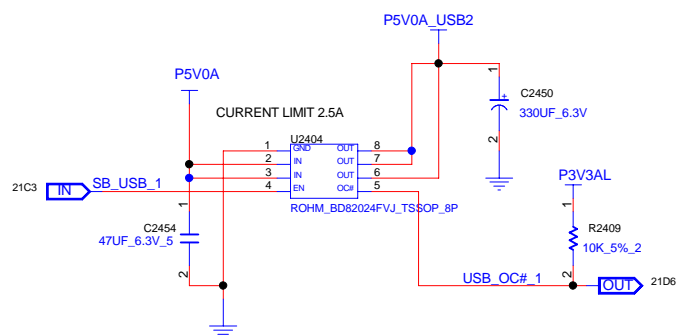
Table 3 – TPS2541 Control Truth Table

# INVENTEC

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MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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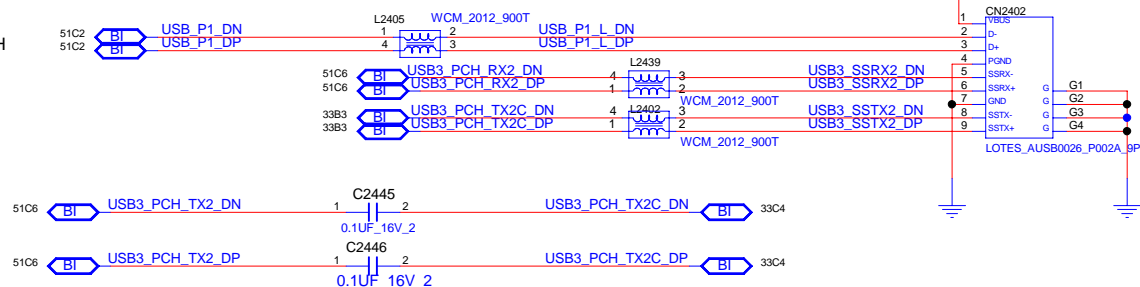


REFERENCE 2400~2499(USB3.0)



# USB 3.0 CONNECTOR

USB2.0 FROM PCH

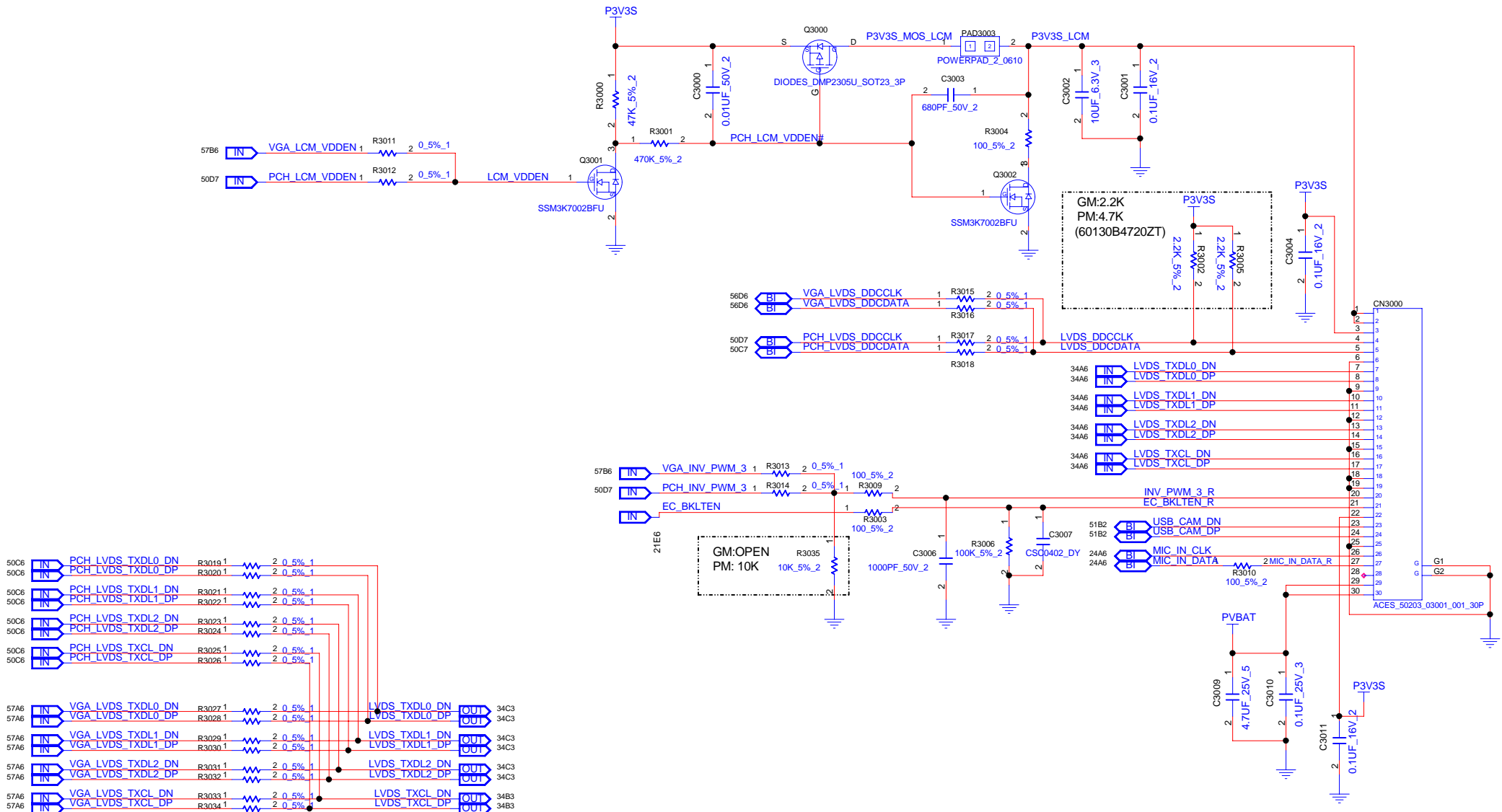
**INVENTEC**TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

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## REFERENCE 3000~3049(LCM)



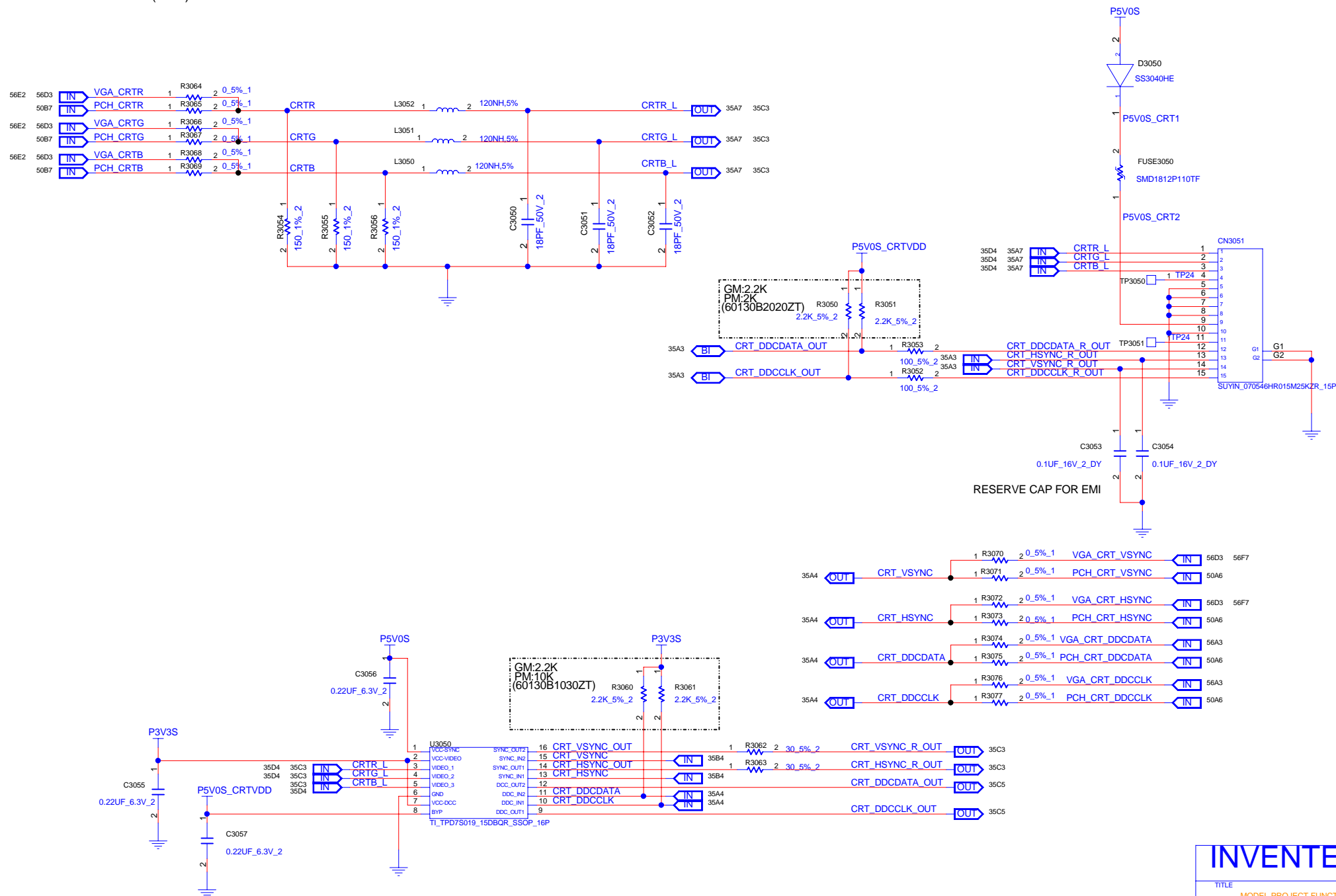
INVENTEC

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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE		CODE	
A3		CS	
DOC NUMBER		REV	
1310xxxx-0-0		X01	

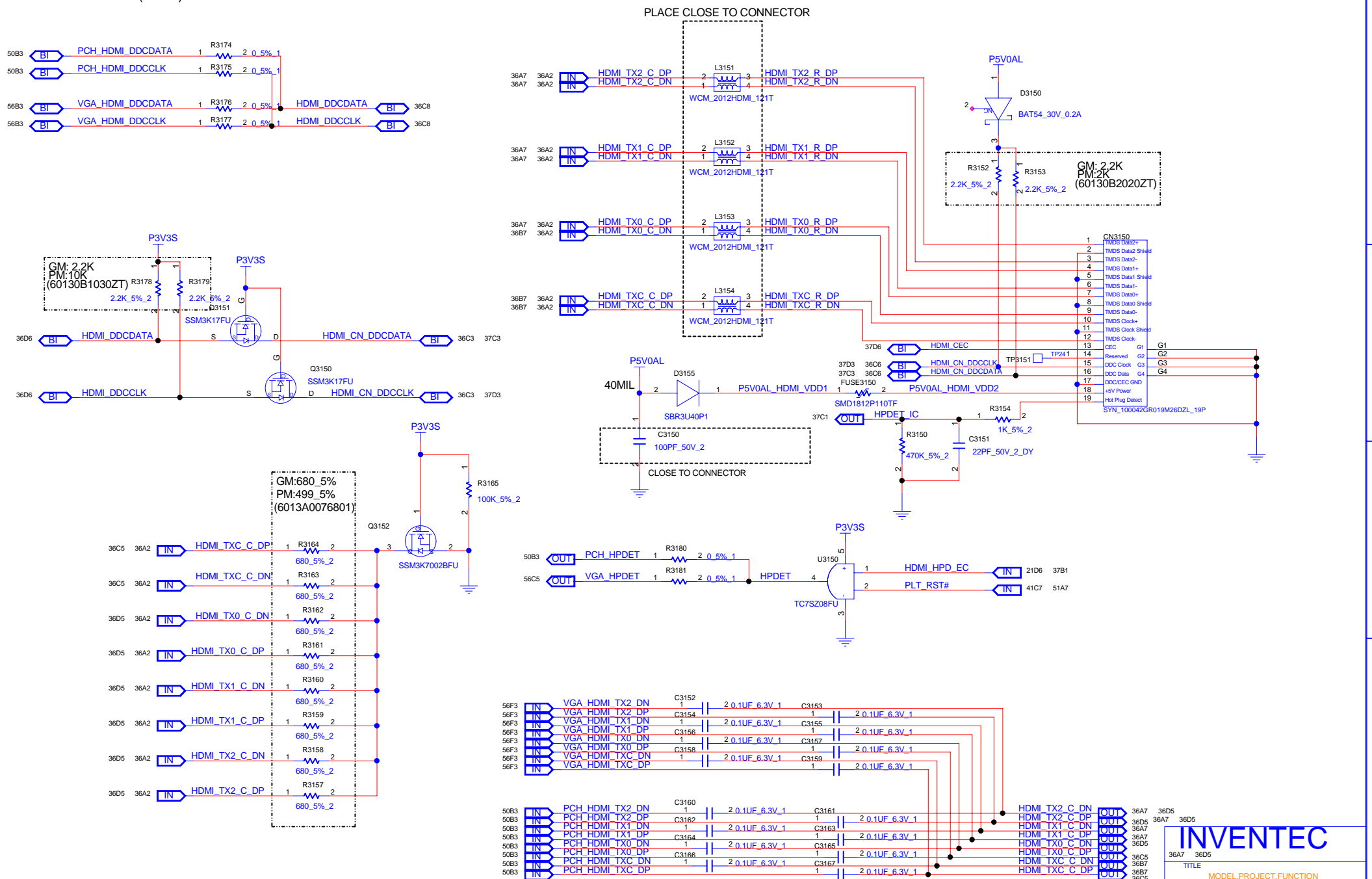
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## REFERENCE 3050~3099(CRT)



## REFERENCE 3150~3199(HDMI)



INVENTEC

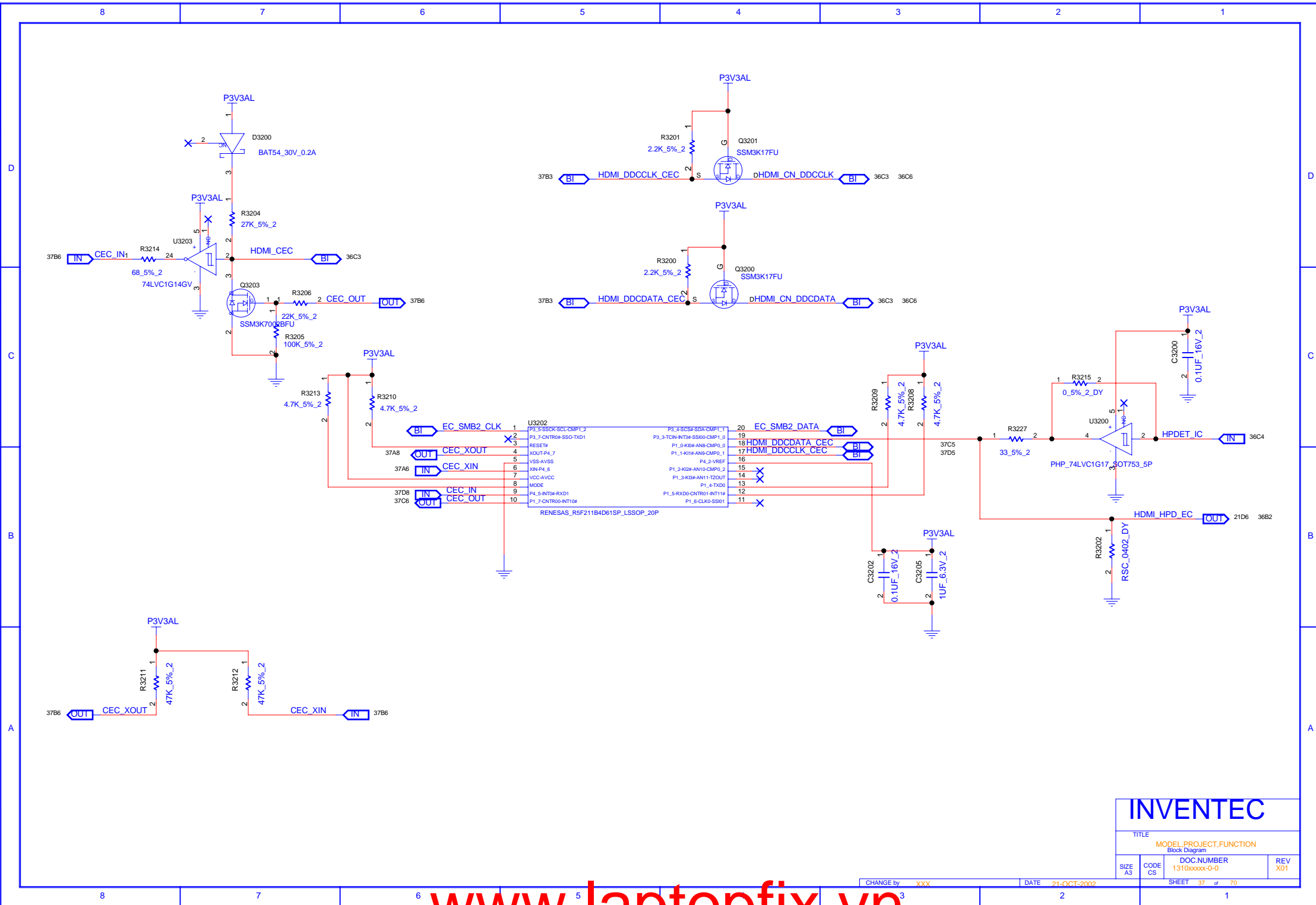
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MODEL PROJECT FUNCTION

DOC NUMBER

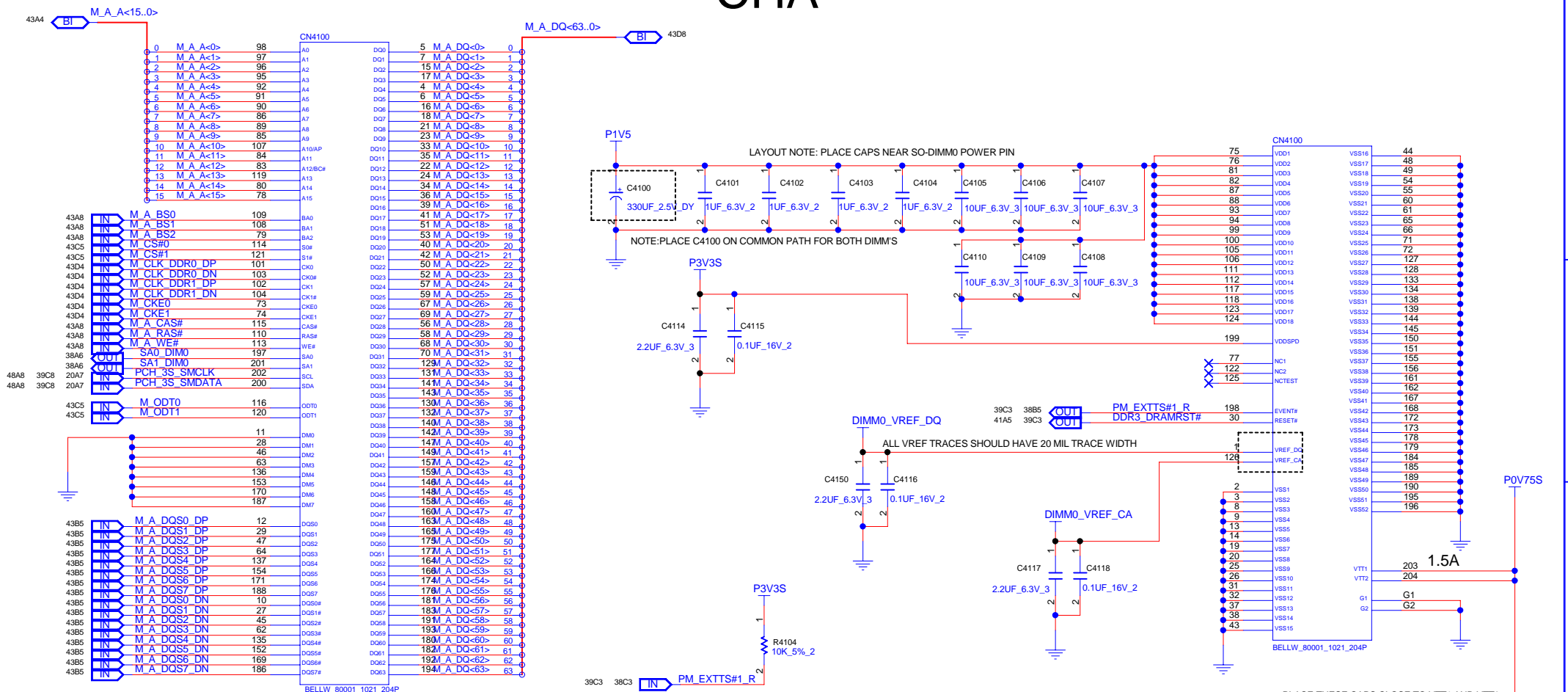
REV

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REFERENCE 4100~4299(DDR)

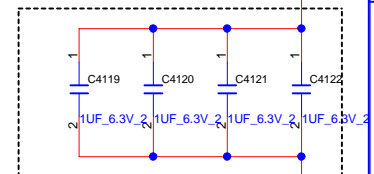
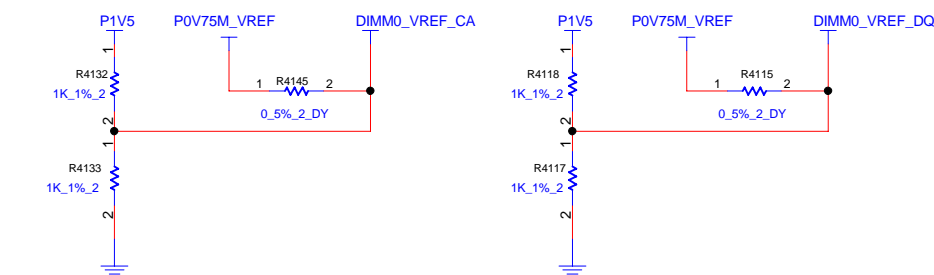
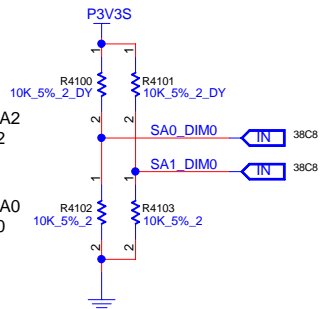
# CHA



NOTE:

IF SA0\_DIM0=1 , SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0XA2  
SO-DIMMA TS ADDRESS IS 0X32

IF SA0\_DIM0=0 , SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0XA0  
SO-DIMMA TS ADDRESS IS 0X30



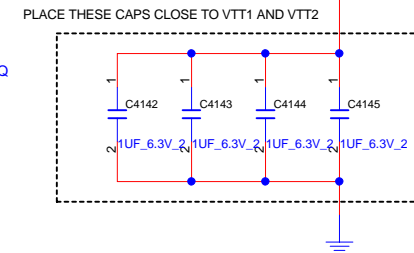
# INVENTEC

TITLE	MODEL PROJECT FUNCTION

MODEL, PROJECT, FUNCTION	
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# CHB

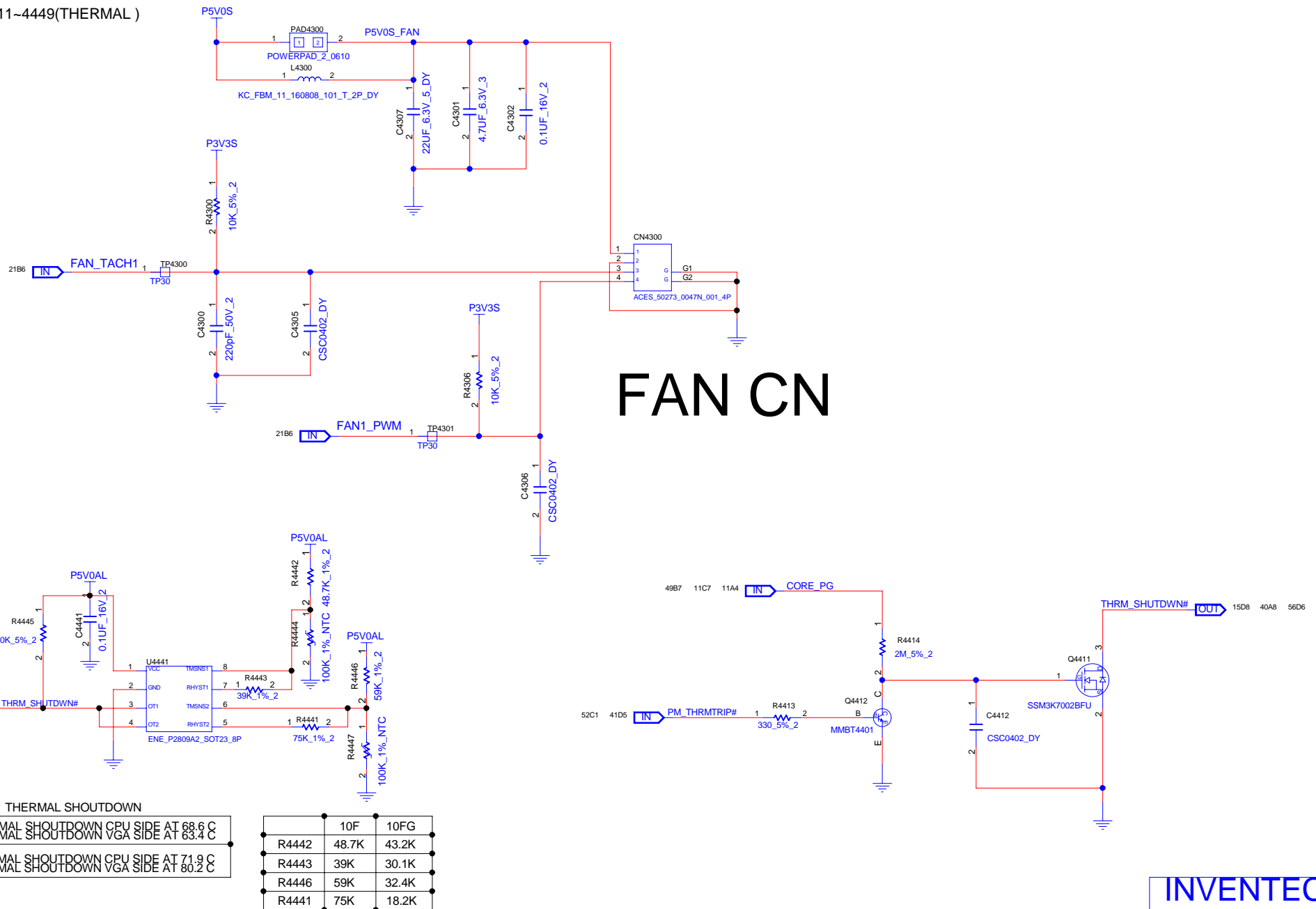


# INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0

REFERENCE 4300~4349(FAN)

REFERENCE 4411~4449(THERMAL )



INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 40 of 70



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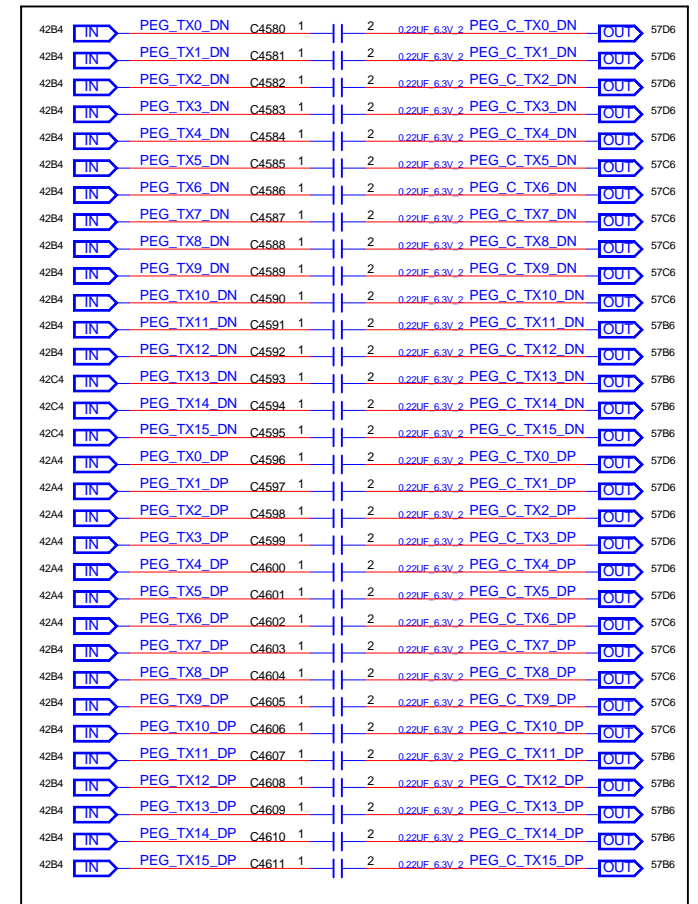
# REFERENCE 4500~4699(CPU)

CAD NOTE: DP\_COMPIO AND ICOMPO SIGNALS SHOULD BE SHORTED NEAR BALLS AND ROUTED WITH  
- TYPICAL IMPEDANCE < 25 MOHMS

CAD NOTE: PEG\_ICOMPI AND RCOMPO SIGNALS SHOULD BE SHORTED AND ROUTED WITH  
- MAX LENGTH = 500 MILS  
- TYPICAL IMPEDANCE = 43 MOHMS

PEG\_ICOMPO SIGNALS SHOULD BE ROUTED WITH  
- MAX LENGTH = 500 MILS  
- TYPICAL IMPEDANCE = 14.5 MOHMS

CLOSE TO CPU



INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 42 of 70



F

E

D

C

B

A

F

E

D

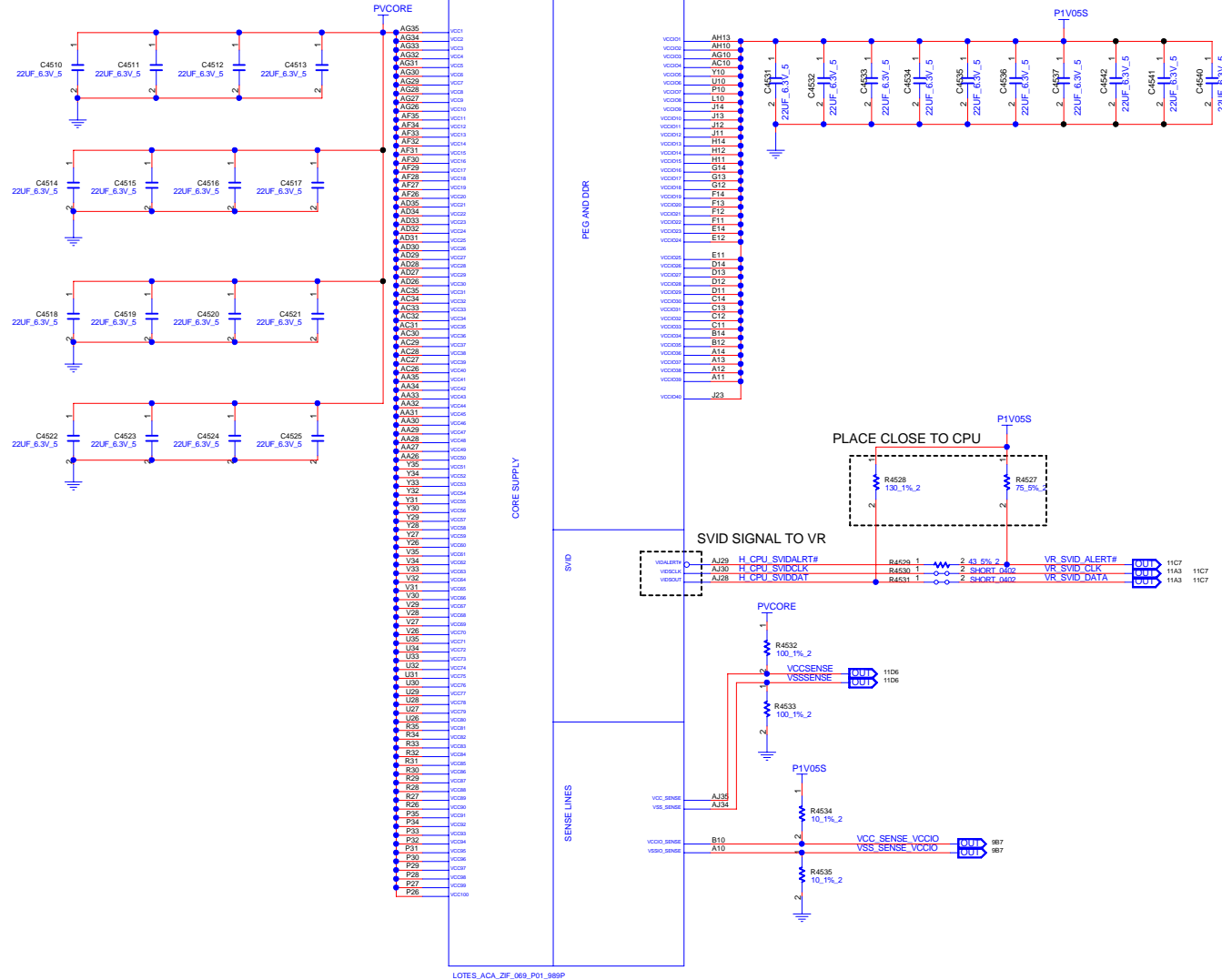
C

B

A

## POWER

REFERENCE 4500-4699(CPU)



L0TES\_ACA\_ZIF\_069\_P01\_989P

INVENTEC

TITLE			
MODEL PROJECT/FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	C3	131000000-050	X01
SHEET		44	of 50

D

C

B

A

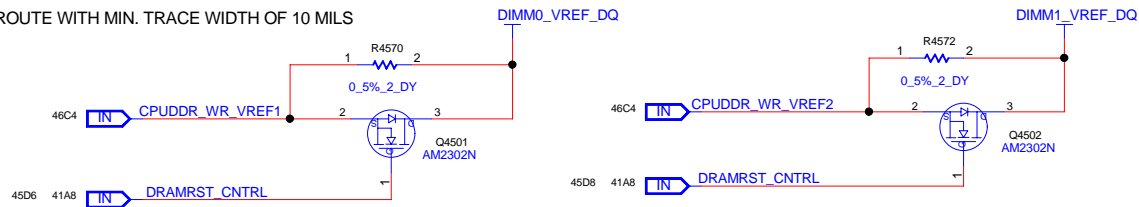
D

C

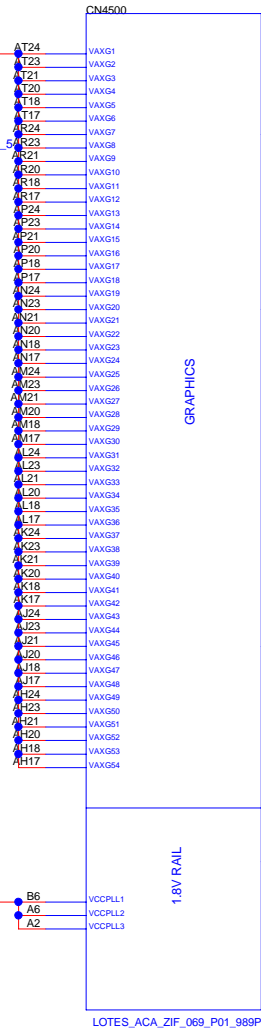
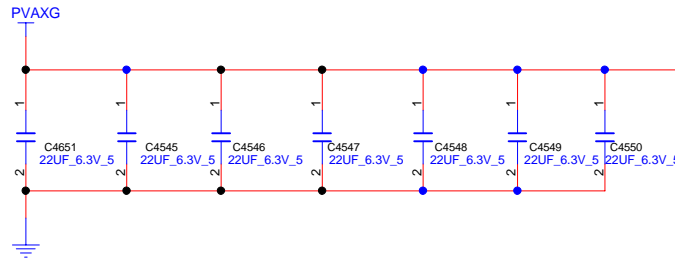
B

A

PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:  
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS



## POWER



SENSE LINES

VREF

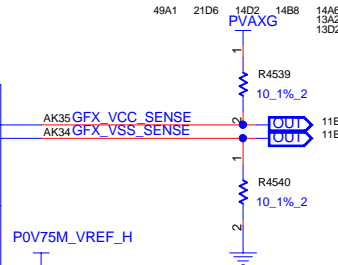
DDR3-1.5V RAILS

SERIAL

MISC

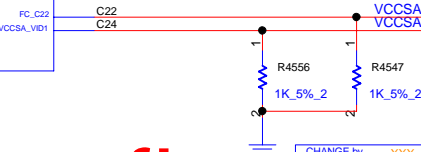
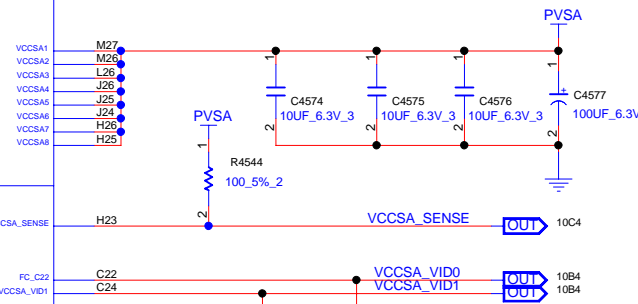
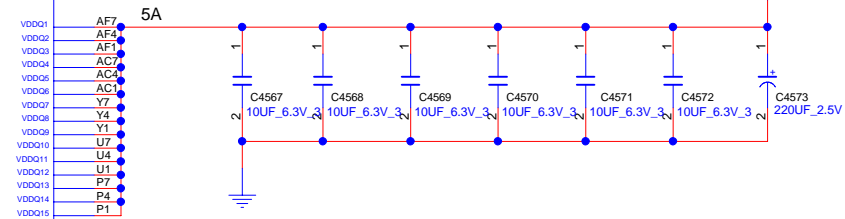
GRAPHICS

1.8V RAIL



PVXG

NOTE : DDR\_WR\_VREF SHOULD HAVE 20/20 MIL WHEREVER POSSIBLE

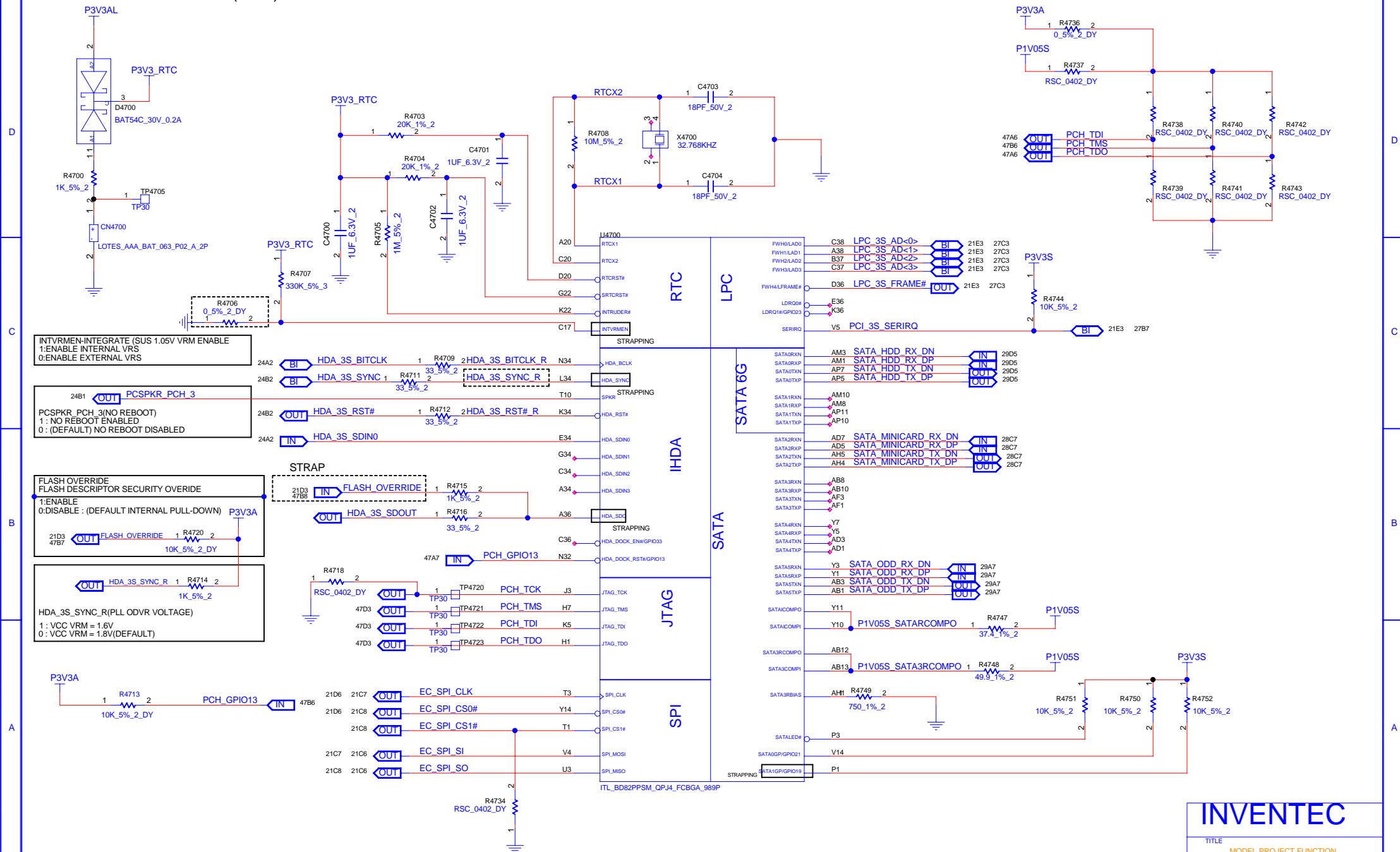


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 45 of 70			

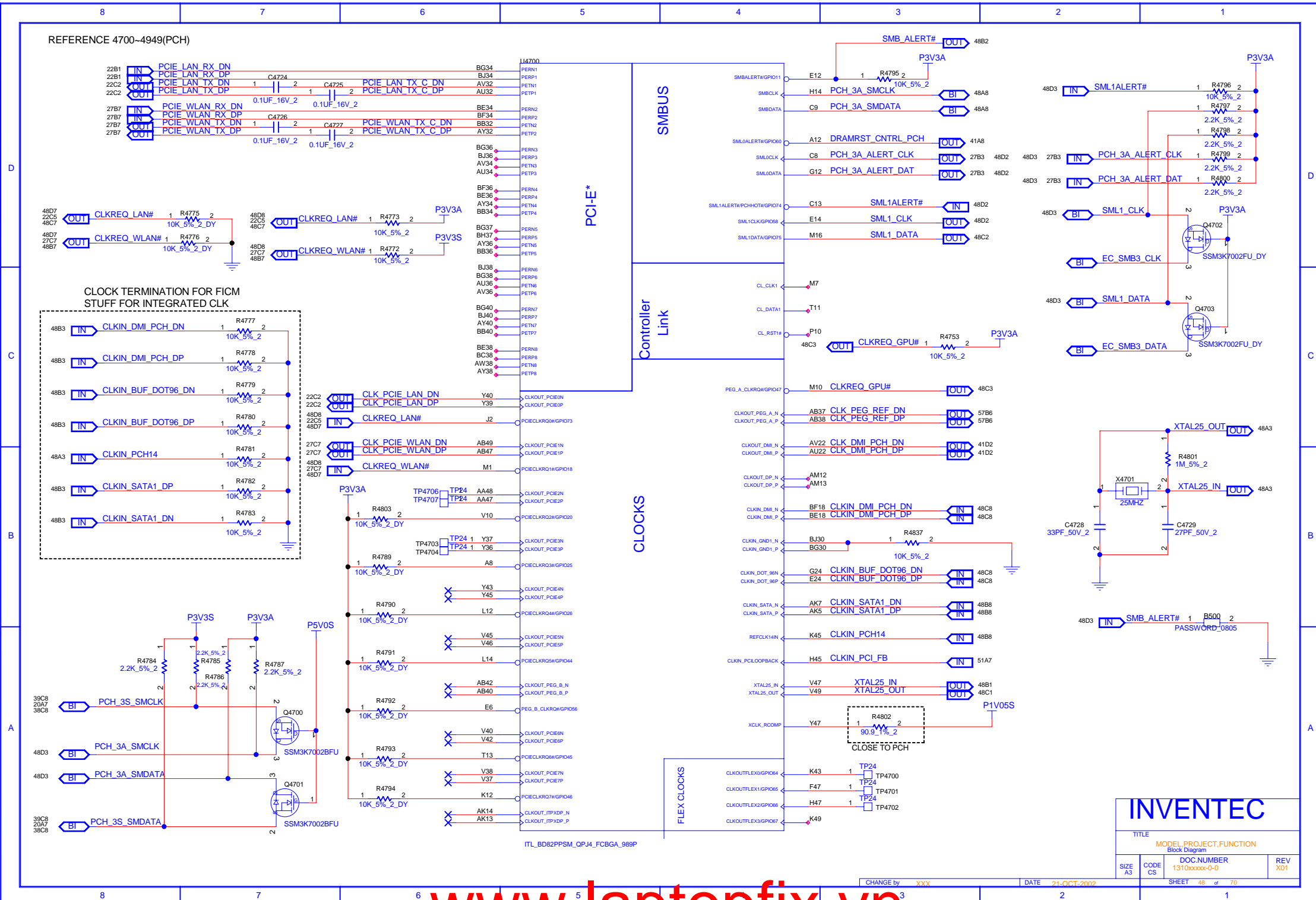
CHANGE by XXX DATE 21-OCT-2002



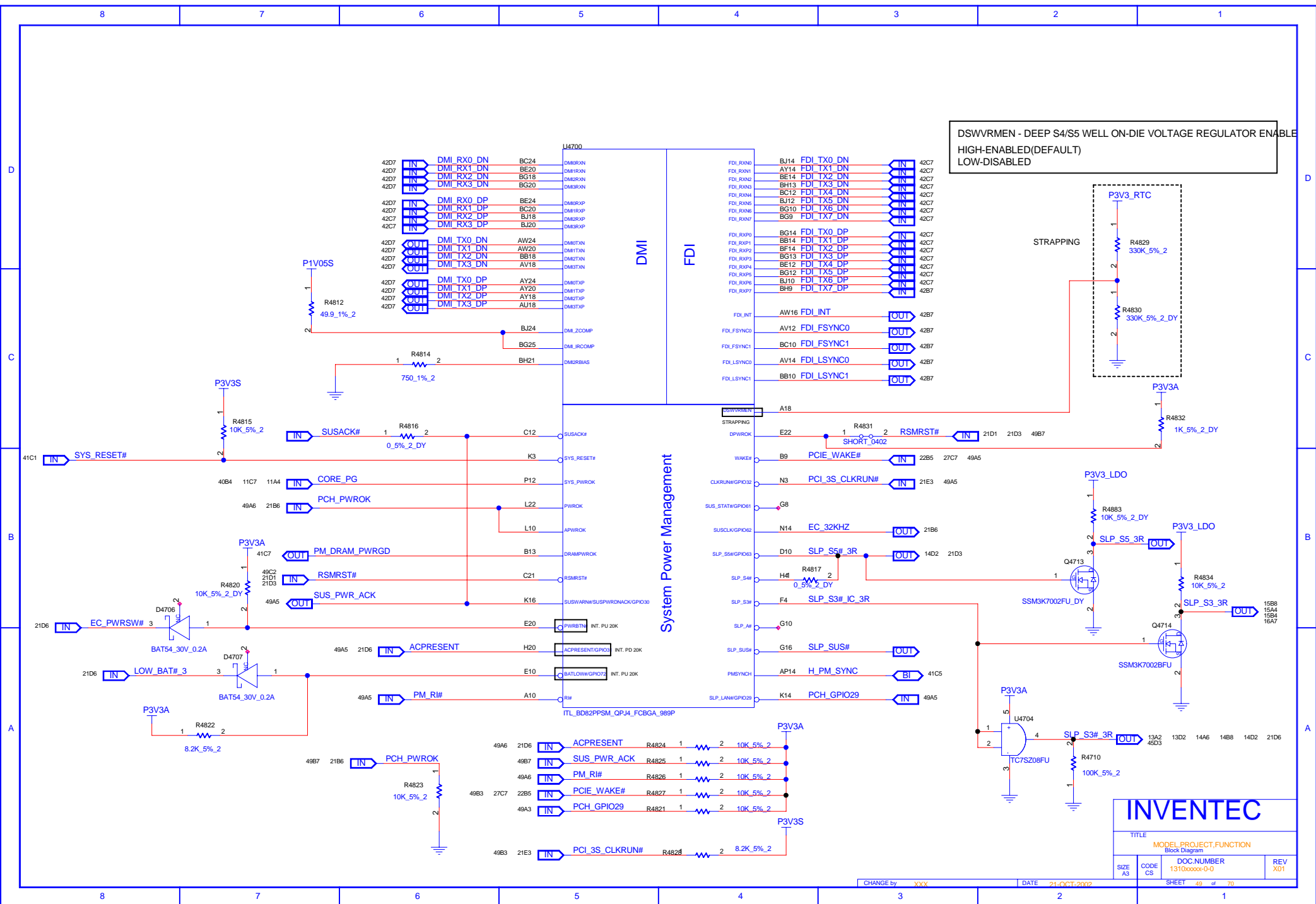
## REFERENCE 4700~4949(PCH)











D

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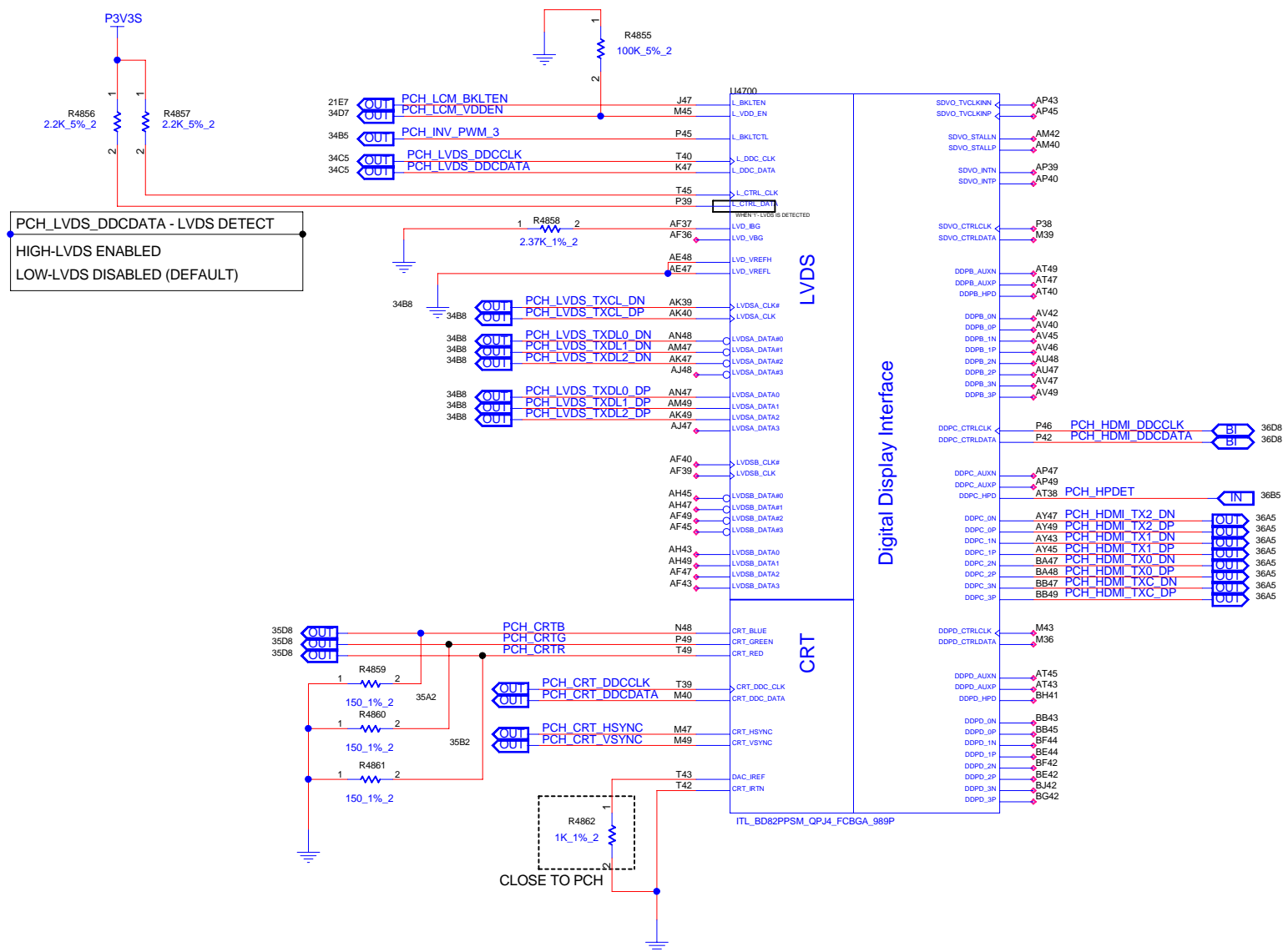
D

C

B

A

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

DOC NUMBER

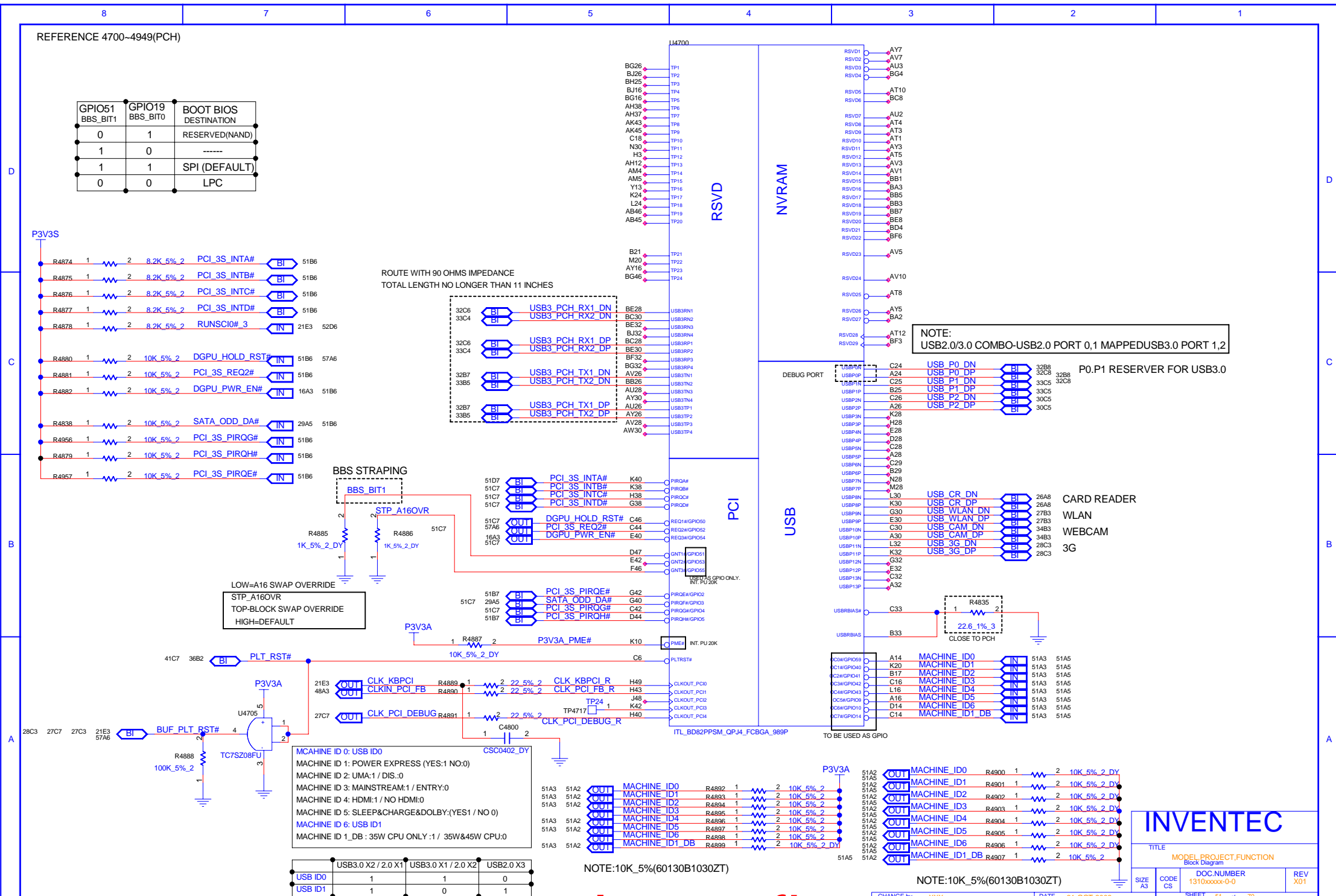
1310xxxx-0-0

REV

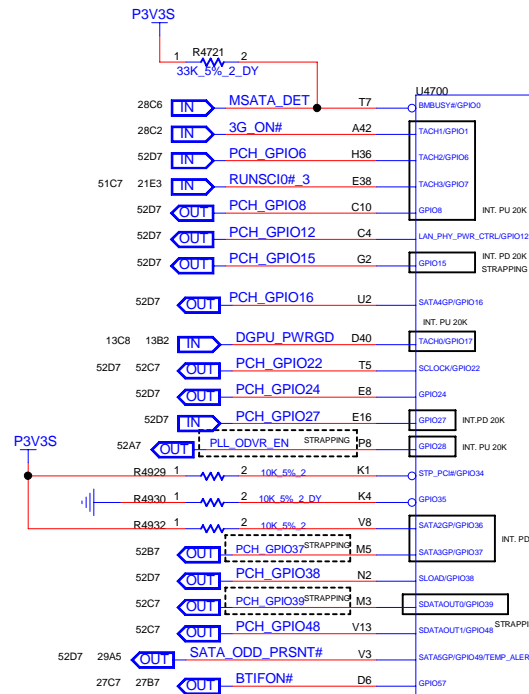
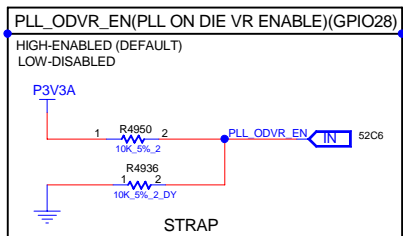
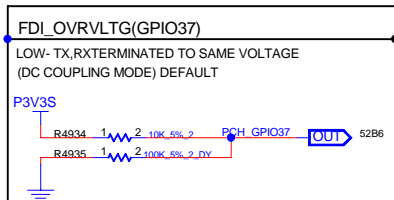
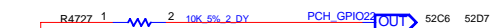
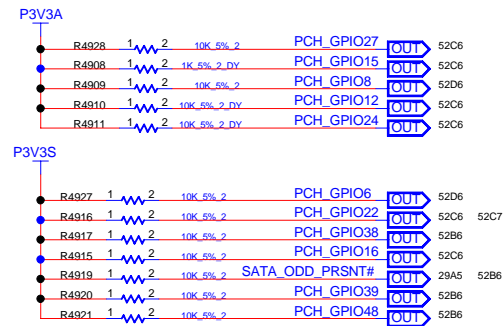
X01

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REFERENCE 4700~4949(PCH)



GPIO

CPU/MISC

NCTF

A4 VSS\_NCTF\_1

A44 VSS\_NCTF\_2

A45 VSS\_NCTF\_3

A46 VSS\_NCTF\_4

A5 VSS\_NCTF\_5

A6 VSS\_NCTF\_6

B3 VSS\_NCTF\_7

B47 VSS\_NCTF\_8

BD1 VSS\_NCTF\_9

BD49 VSS\_NCTF\_10

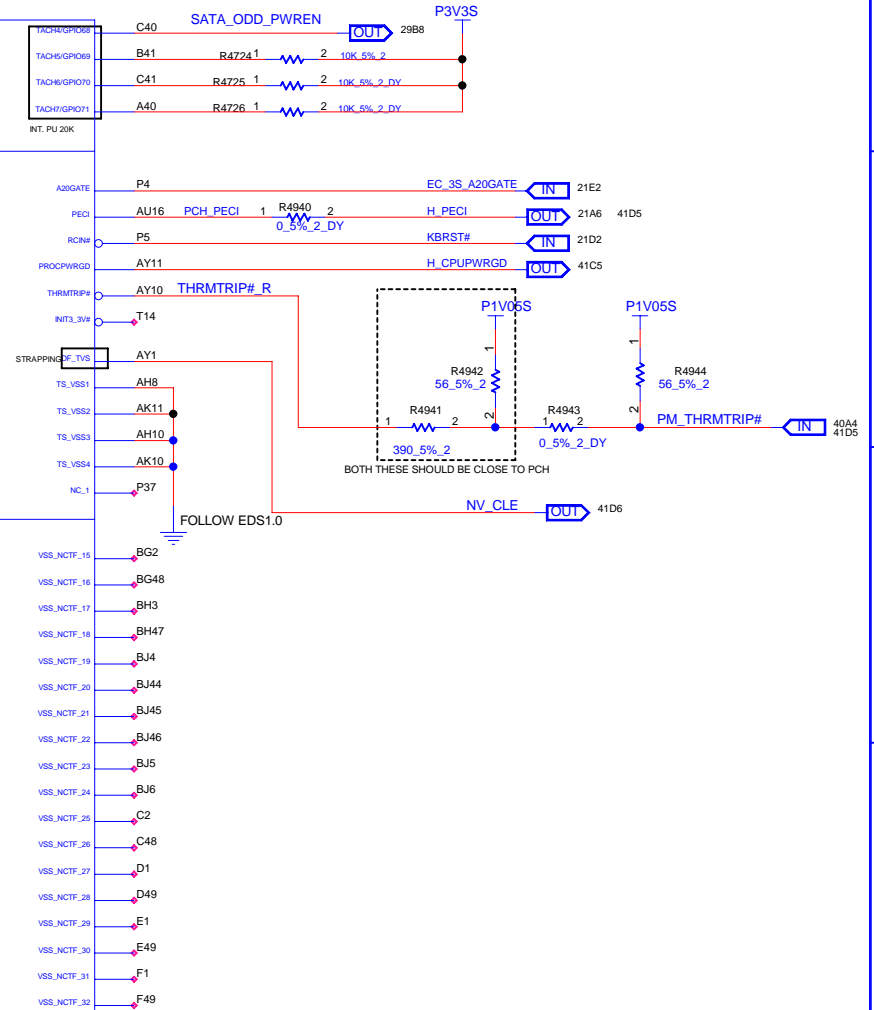
BE1 VSS\_NCTF\_11

BE49 VSS\_NCTF\_12

BF1 VSS\_NCTF\_13

BF49 VSS\_NCTF\_14

ITL\_BD82PPSM\_QPJ4\_FCBGA\_989P



INVENTEC

TITLE MODEL PROJECT FUNCTION  
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX

DATE 21-OCT-2002

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POWER



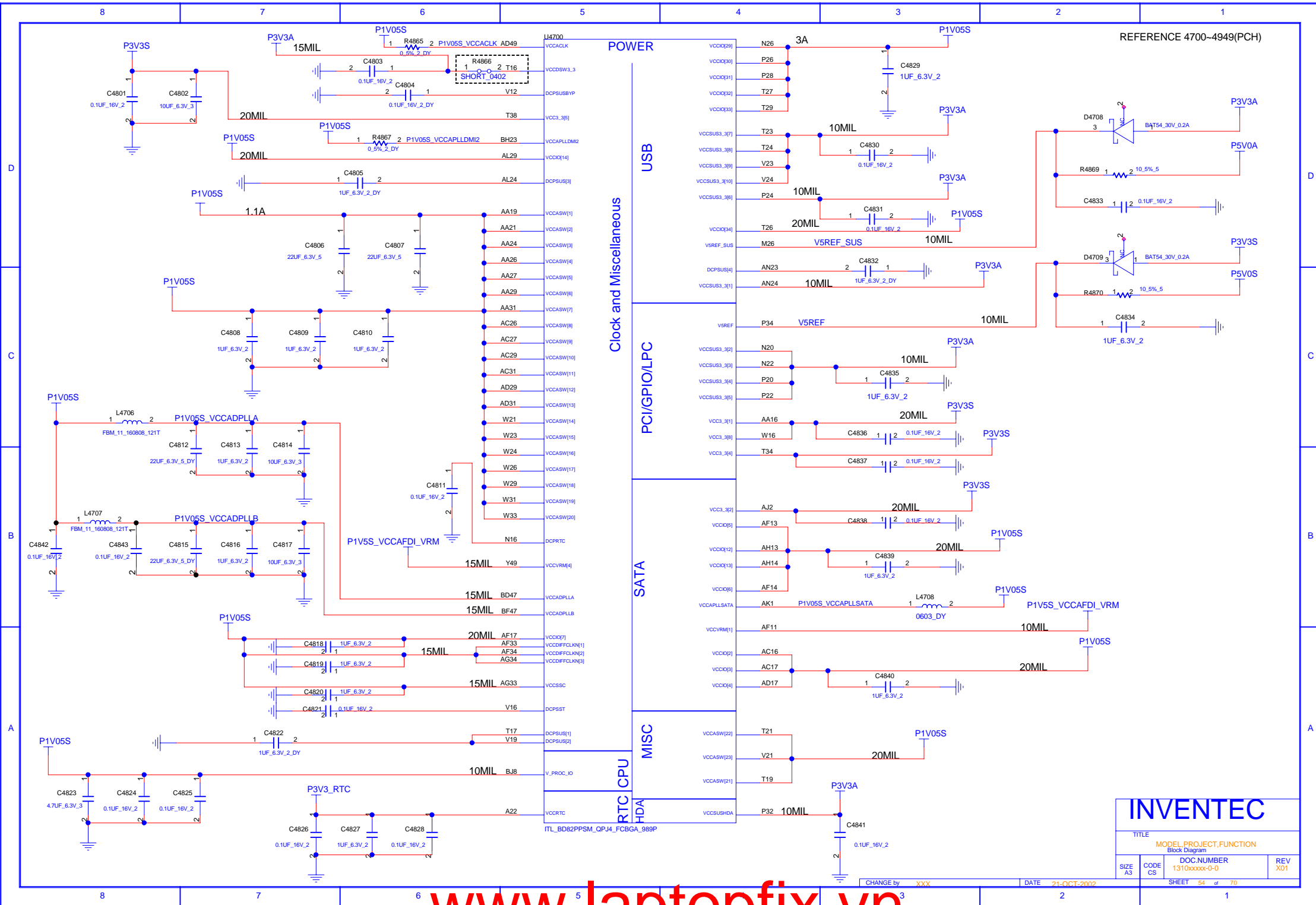
TITLE	MODEL,PROJECT,FUNCTION
	Block Diagram

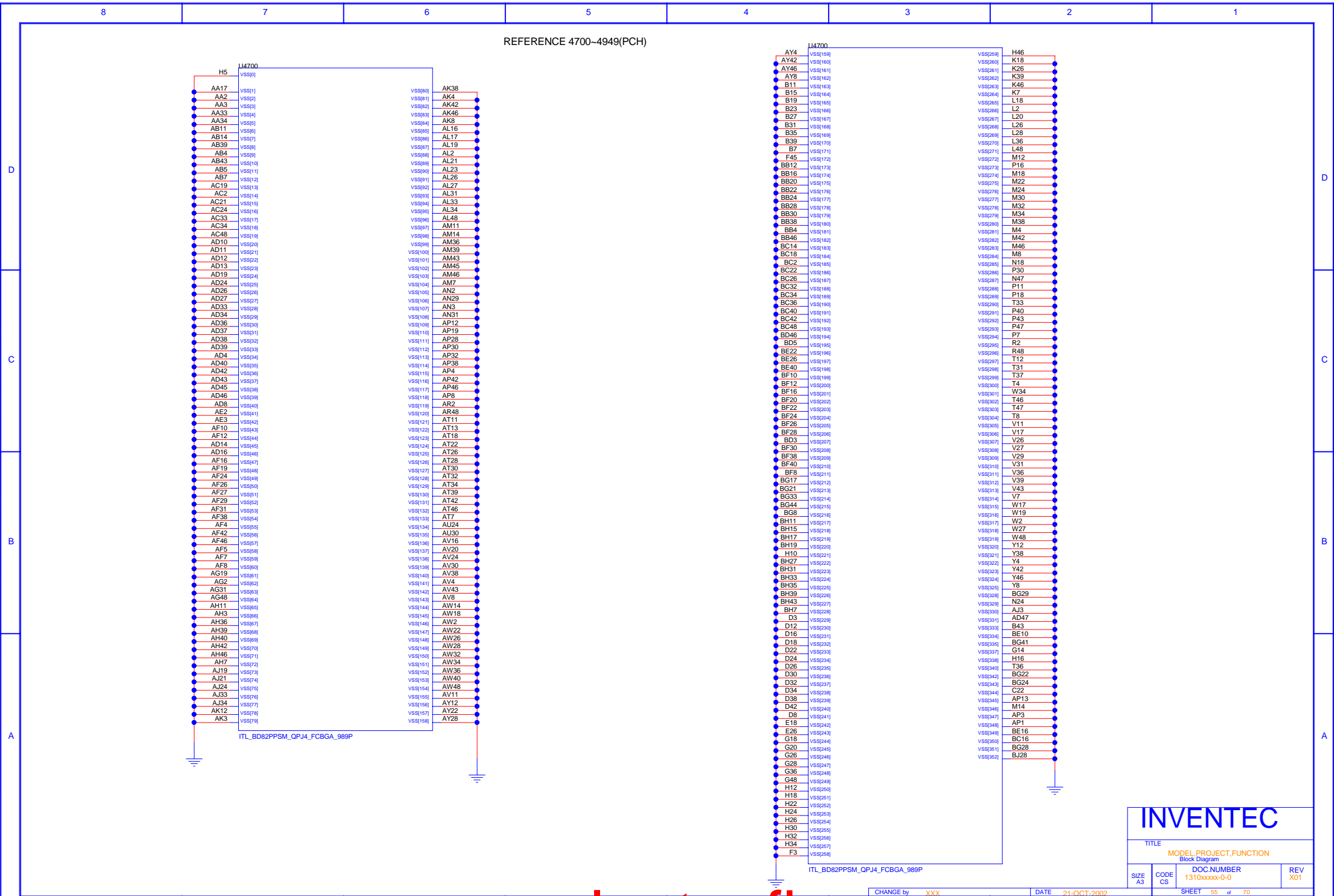
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0
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SHEET 53 of 70

CHANGE by	XXX	DATE	21-OCT-2002
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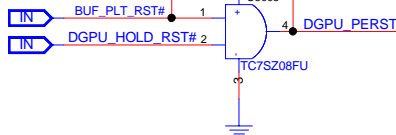
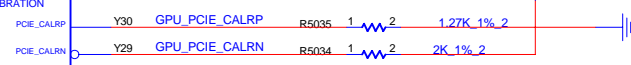
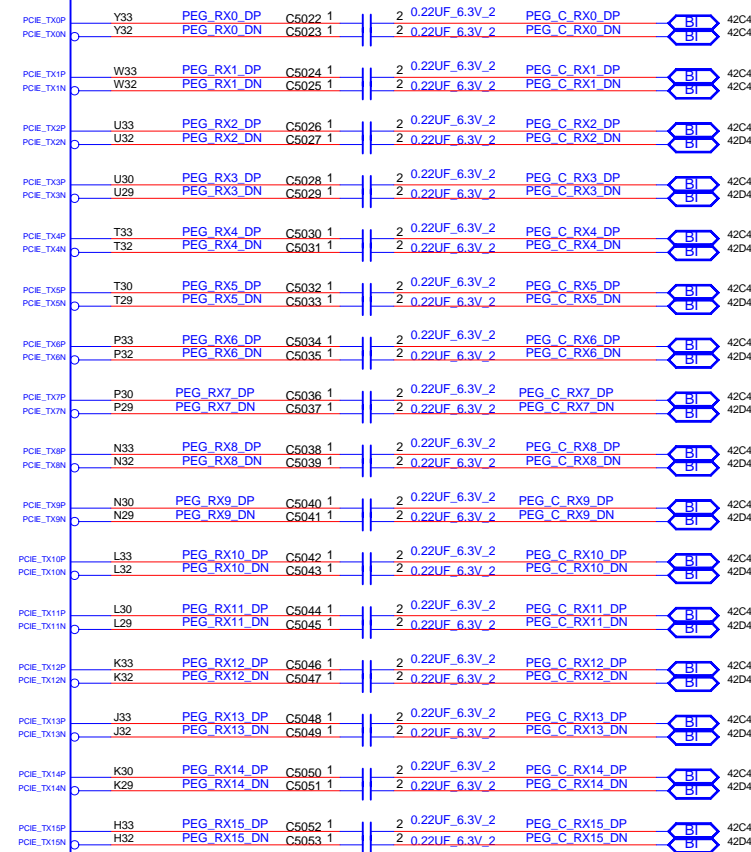
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# INVENTEC

TITL A

MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE  
A3

CODE  
CS

DOC.NUMBER  
1310xxxxx-0-0

RE  
X0

CHANGE by XX

DATE 21-OCT-2002

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70





SHEET

D

C

B

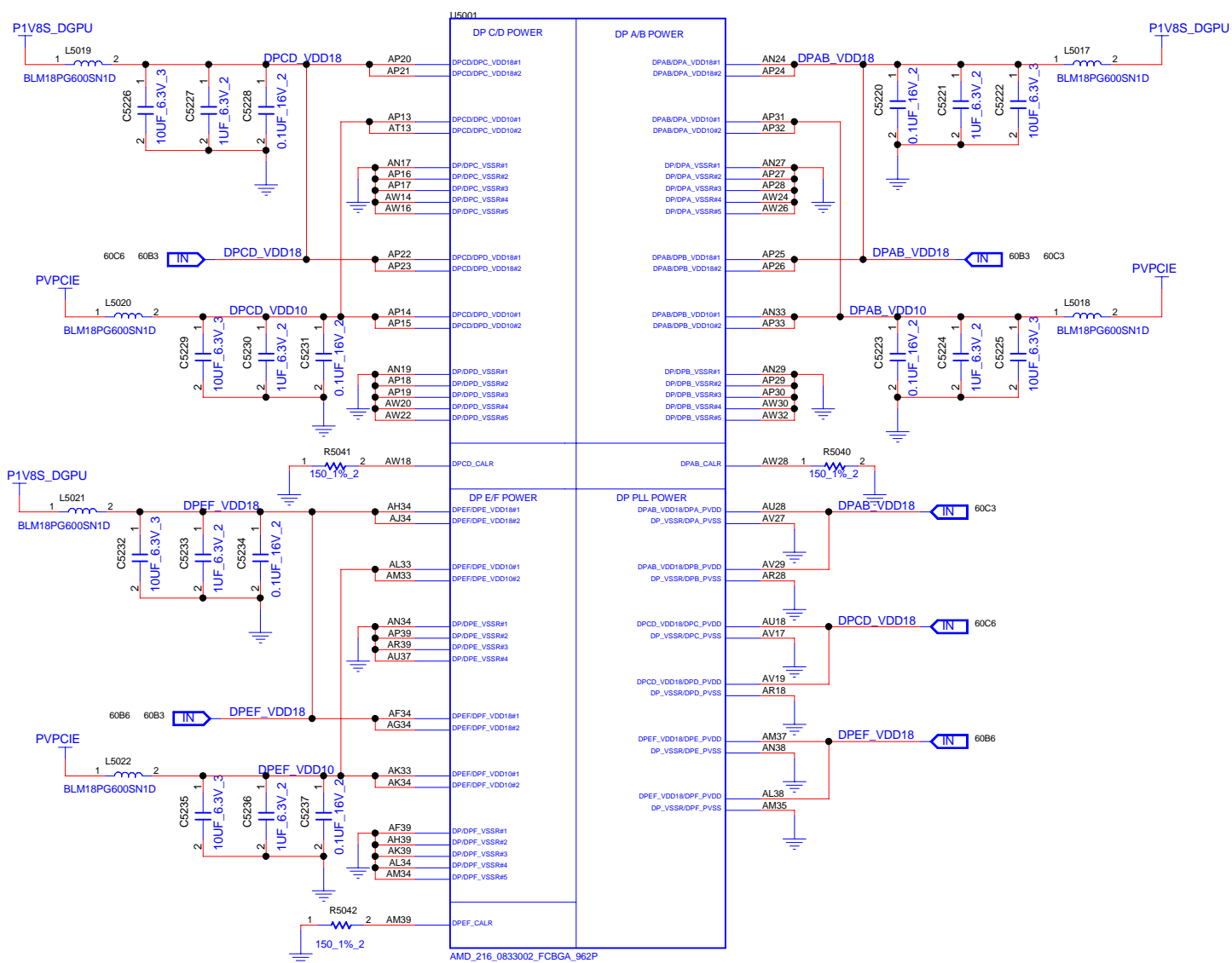
A

D

C

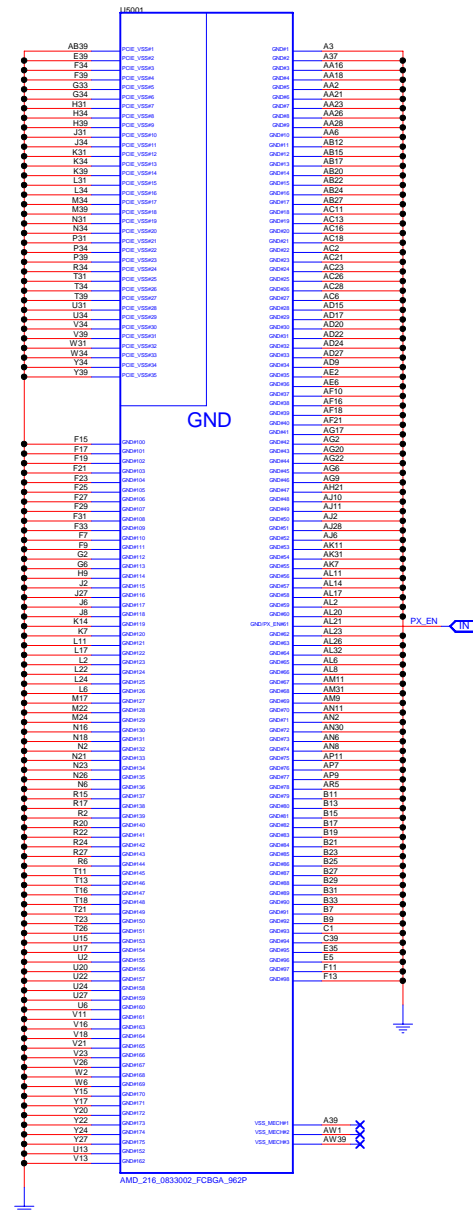
B

A

**INVENTEC**TITLE  
MODEL PROJECT FUNCTION  
Block DiagramSIZE CODE DOC NUMBER REV  
A3 CS 1310xxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

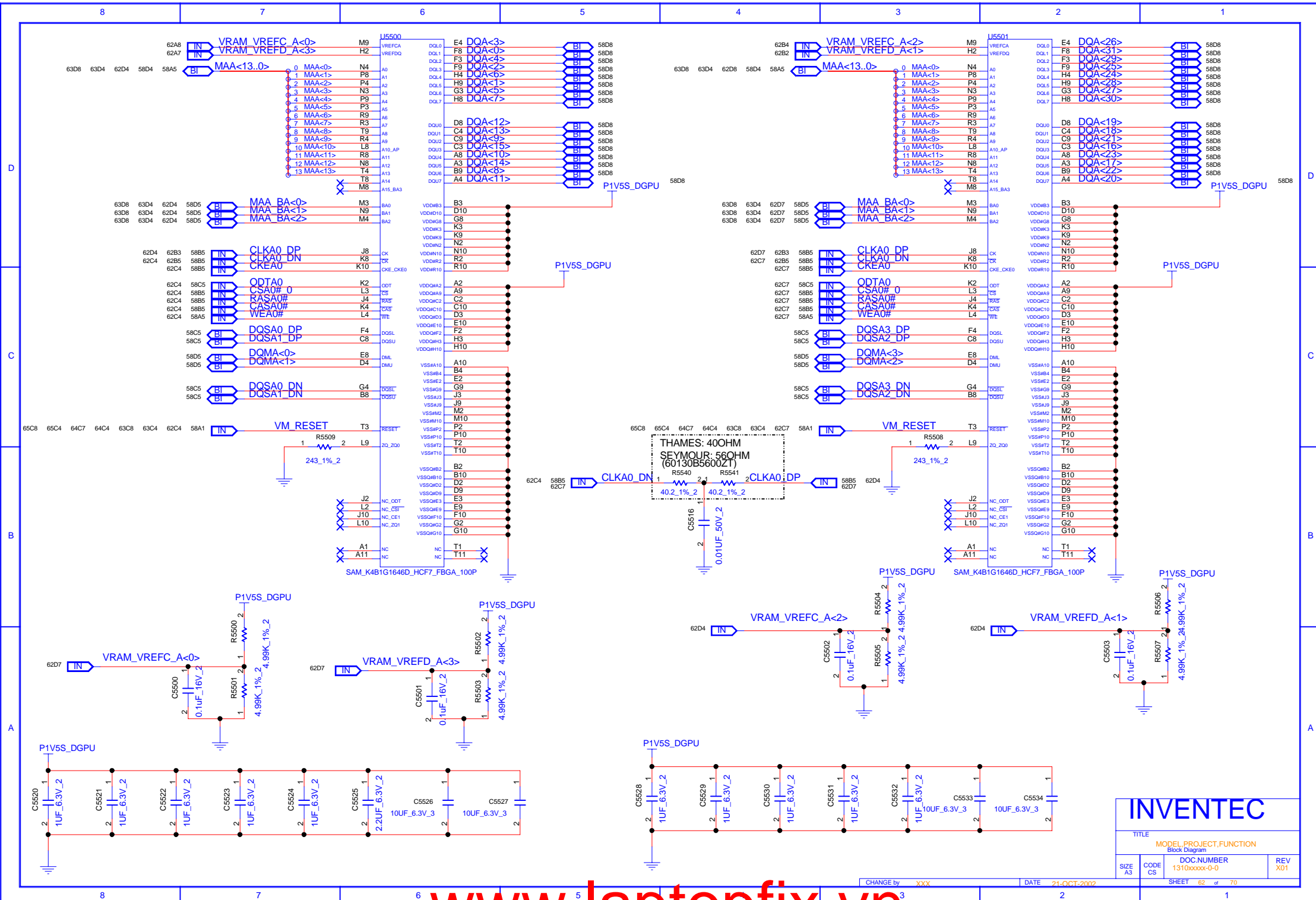
SHEET 60 of 70

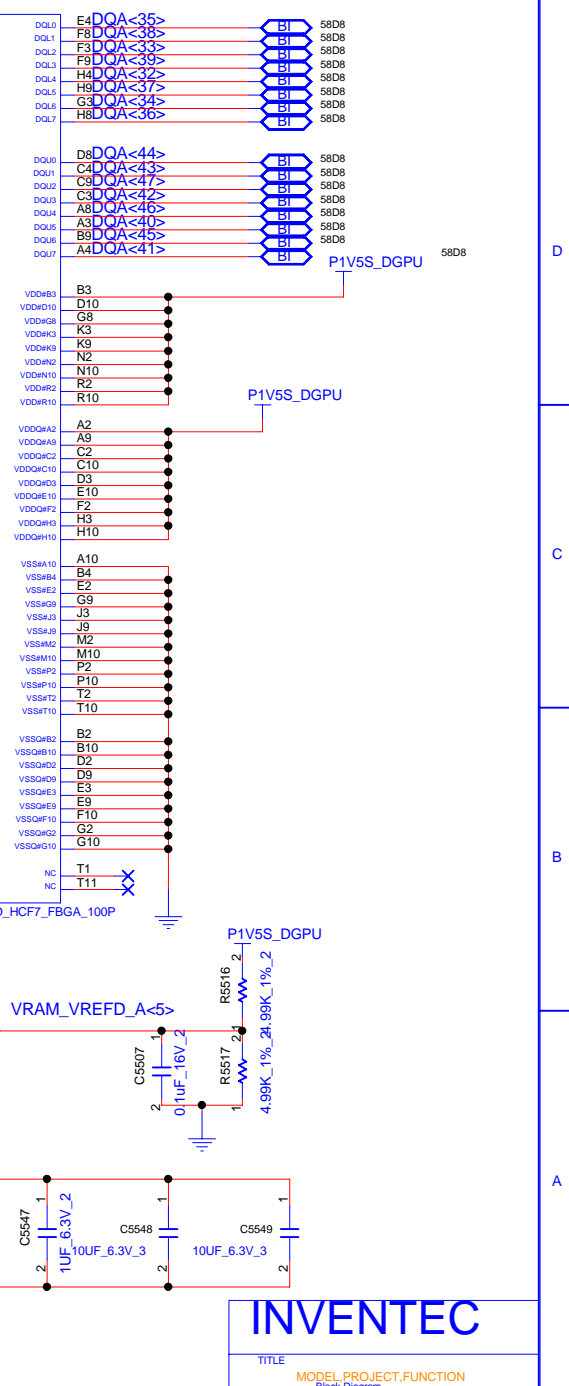
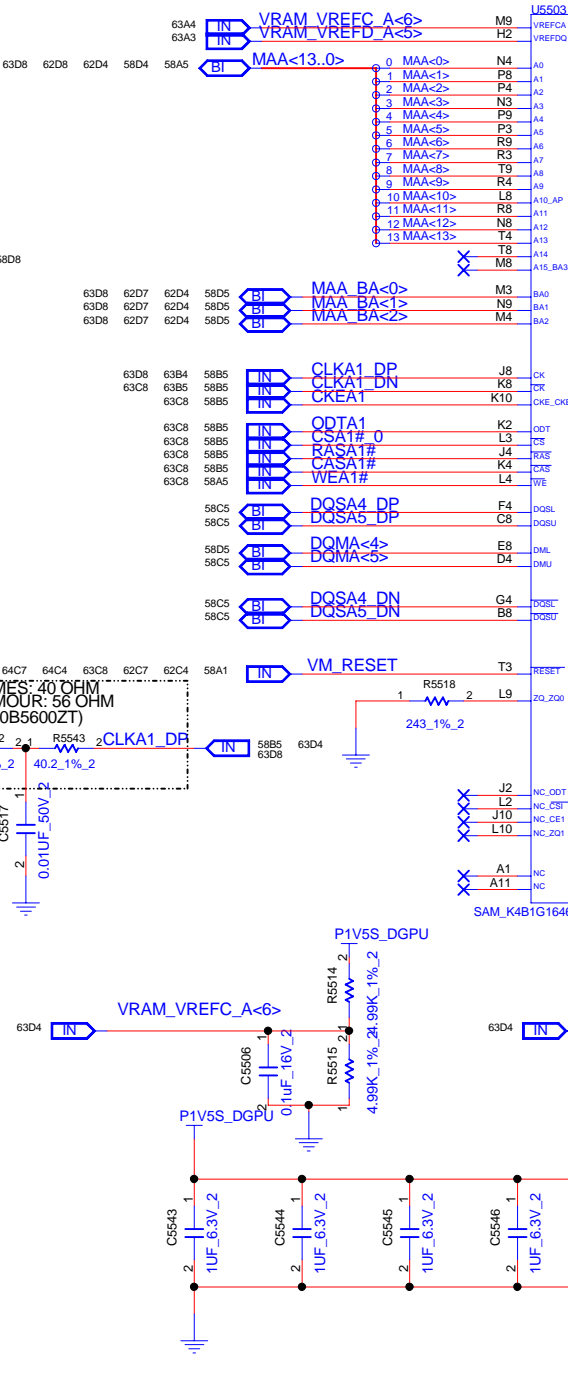
F  
E  
D  
C  
B  
AF  
E  
D  
C  
B  
A

AMD\_216\_0833002\_FCBGA\_962P

## INVENTEC

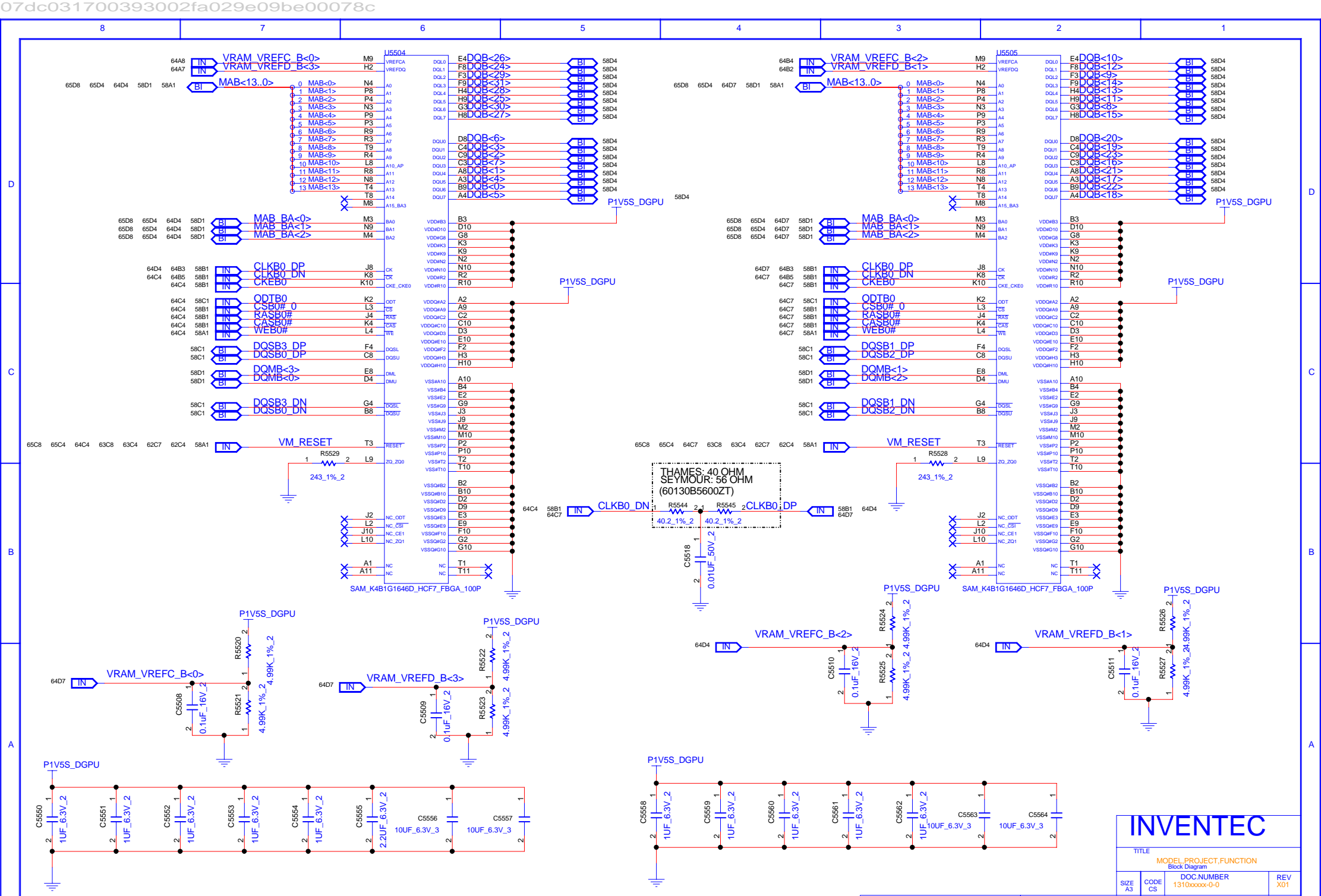
TITLE				
MODEL/PROJECT/FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
C	CS	1310000000000	01	01
SHEET		01	01	01





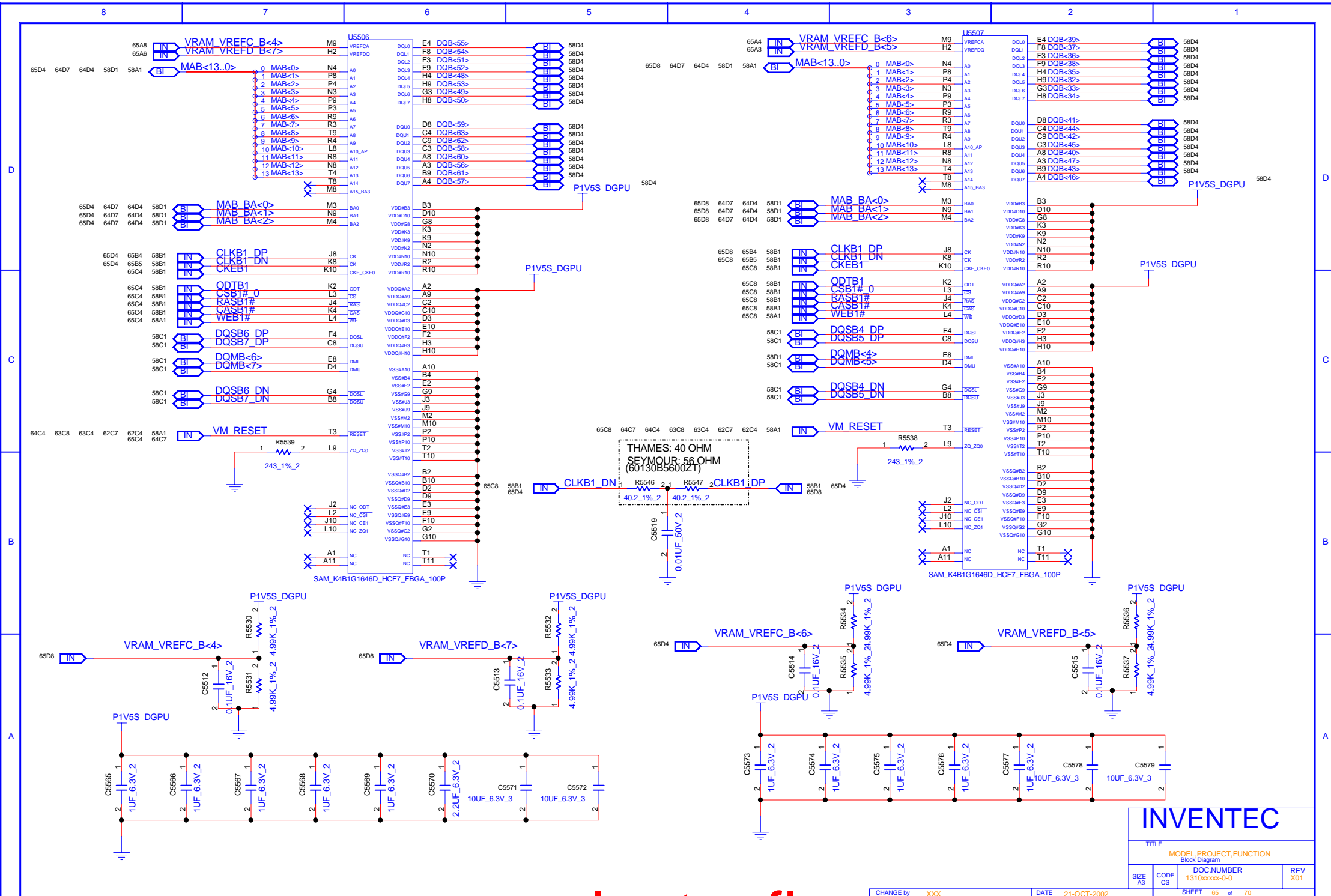
A3	CS	7075-T6	63	70
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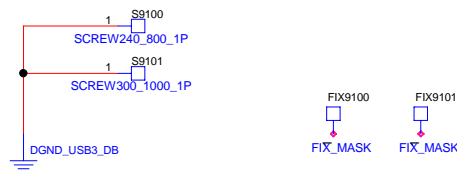
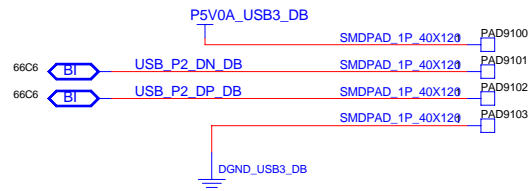
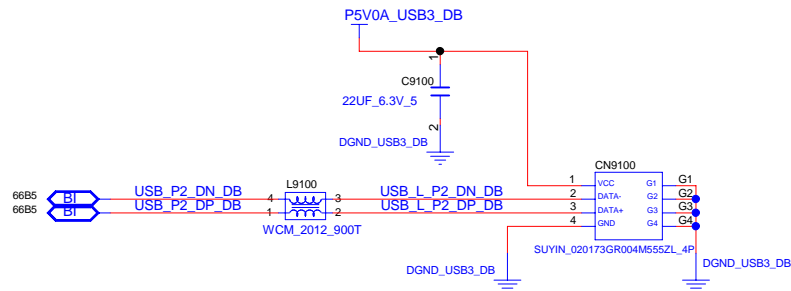


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE		CODE	REV
A3	CS	DOC NUMBER	X01
1310xxxx-0-0		SHEET 64 of 70	





# USB BOARD

**INVENTEC**TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

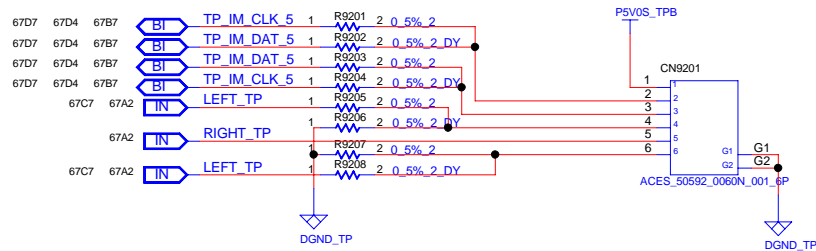
SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

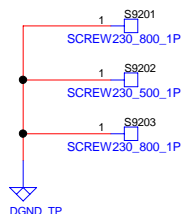
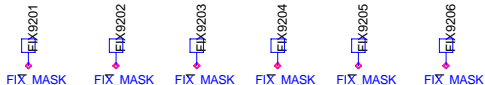
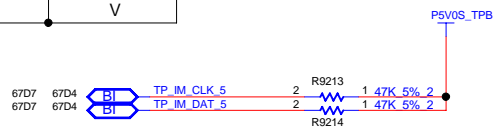
SHEET 66 of 70

# TOUCH PAD BOARD 4 PIN

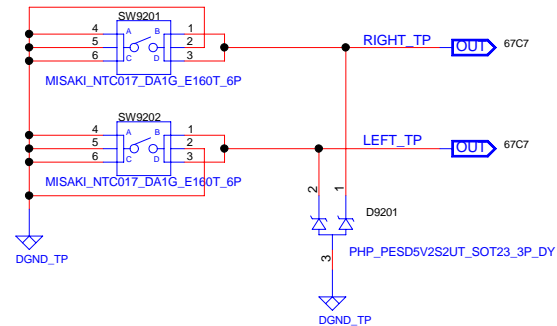
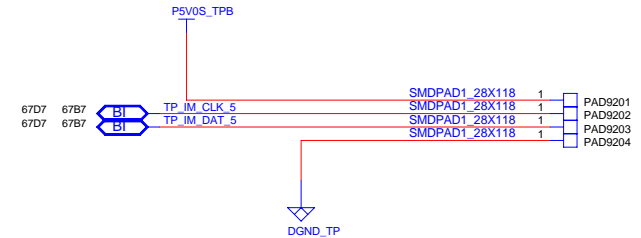
## TOUCHPAD TO MODULE CONN



4PIN	IMR/METAL	TEXTURE
R9201		V
R9202	V	
R9203		V
R9204	V	
R9205		V
R9206	V	
R9207		V
R9208	V	
R9213	V	V
R9214	V	V



## TOUCHPAD TO MB CONN



INVENTEC

TITLE MODEL PROJECT FUNCTION

DOC NUMBER 1310xxxx-0-0

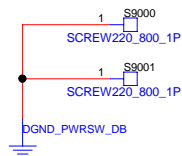
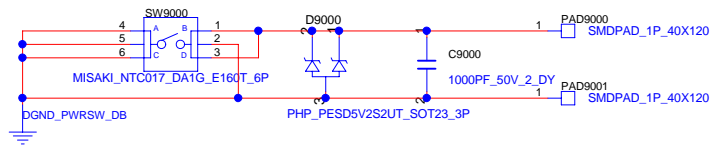
REV X01

CHANGE by XXX DATE 21-OCT-2002

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REFERENCE 9000~9999(SMALL BOARD)

## POWER BUTTON



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

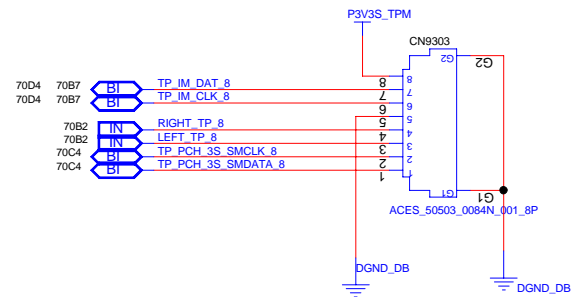
SHEET 68 of 70



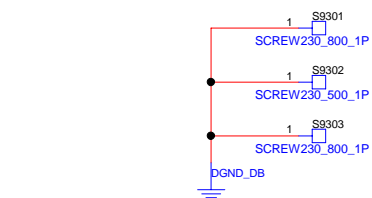
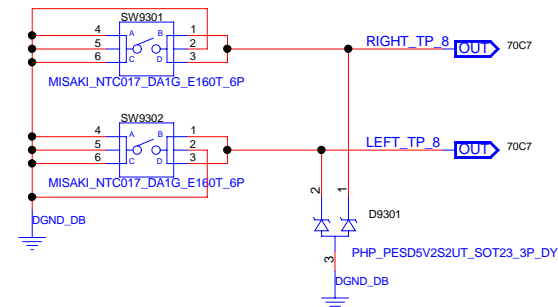
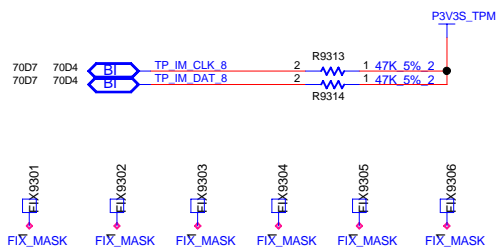
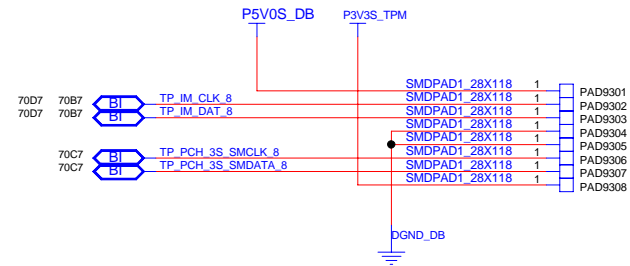
SHEET 69 of 70

# TOUCH PAD BOARD 8 PIN

## TOUCHPAD TO MODULE CONN



## TOUCHPAD TO MB CONN

**INVENTEC**TITLE  
MODEL,PROJECT,FUNCTION

Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

SHEET 70 of 70

CHANGE by XXX DATE 21-OCT-2002