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NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

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<div><div><div>BOM Variants</div><table><tr><th>BOM NUMBER</th><th>BOM NAME</th><th>BOM OPTIONS</th></tr><tr><td>630-9768</td><td>PCBA,MLB,GOOD,K22</td><td>K22,2P80GHZ_CPU,BASIC,IG</td></tr><tr><td>639-0036</td><td>PCBA,2.8 GHZ CPU,MXM,K22</td><td>K22,2P80GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0392</td><td>PCBA,2.8 GHZ-2M CPU,IG,K22</td><td>K22,2P80GHZ_2M_CPU,BASIC,IG</td></tr><tr><td>639-0393</td><td>PCBA,2.8 GHZ-2M CPU,MXM,K22</td><td>K22,2P80GHZ_2M_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0184</td><td>PCBA,2.93 GHZ CPU,IG,K22</td><td>K22,2P93GHZ_CPU,BASIC,IG</td></tr><tr><td>639-0186</td><td>PCBA,2.93 GHZ CPU,MXM,K22</td><td>K22,2P93GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0037</td><td>PCBA,3.0 GHZ CPU,IG,K22</td><td>K22,3P0GHZ_CPU,BASIC,IG</td></tr><tr><td>630-9878</td><td>PCBA,MLB,CTO,K22</td><td>K22,3P0GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0183</td><td>PCBA,3.06 GHZ CPU,IG,K22(Investigation)</td><td>K22,3P06GHZ_CPU,BASIC,IG</td></tr><tr><td>639-0511</td><td>PCBA,MLB,3.06GHZ,MXM,K22</td><td>K22,3P06GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0510</td><td>PCBA,MLB,3.16GHZ,IG,K22</td><td>K22,3P16GHZ_CPU,BASIC,IG</td></tr><tr><td>639-0324</td><td>PCBA,MLB,3.16GHZ,MXM,K22</td><td>K22,3P16GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>639-0206</td><td>PCBA,MLB,3.33GHZ,IG,K22</td><td>K22,3P33GHZ_CPU,BASIC,IG</td></tr><tr><td>639-0207</td><td>PCBA,MLB,3.33GHZ,MXM,K22</td><td>K22,3P33GHZ_CPU,BASIC,MXM,K22_MXM</td></tr><tr><td>607-4426</td><td>PCBA,MLB,DEV,K22</td><td>DEVELOPMENT,DEV_GROUP</td></tr></table></div><div><div>BOM GROUPS</div><table><tr><th>BOM GROUP</th><th>BOM OPTIONS</th></tr><tr><td>BASIC</td><td>COMMON,ALTERNATE,MCP7A,XDP,BETTER,MCP_ISL9563A,MLB_PNL_PWR,PRODUCTION</td></tr><tr><td>MCP7A</td><td>BOOT_MODE_USER,MEMRESET_HW,MEMRESET_MCP</td></tr><tr><td>DEV_GROUP</td><td>XDP_CONN,LPCPLUS,VREFMRGN,MCP_PWR_SENSE,MCP_CPU_TDIODE,PECI_SMB,MOJOMUX</td></tr></table></div><div><div>BOARD STACK-UP</div><table><tr><td>TOP</td><td>SIGNAL</td></tr><tr><td>2</td><td>GROUND</td></tr><tr><td>3</td><td>SIGNAL</td></tr><tr><td>4</td><td>POWER</td></tr><tr><td>5</td><td>POWER</td></tr><tr><td>6</td><td>SIGNAL</td></tr><tr><td>7</td><td>GROUND</td></tr><tr><td>BOTTOM</td><td>SIGNAL</td></tr></table></div></div> <div><div>COMMON</div><table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>338S0731</td><td>1</td><td>IC,GMCP,MCP7A-JA,B03,35X35MM,BGA1437,DT</td><td>U1400</td><td>CRITICAL</td><td>IG</td></tr><tr><td>338S0732</td><td>1</td><td>IC,MCP,MCP7A-DA,B03,35X35MM,BGA1437,DT</td><td>U1400</td><td>CRITICAL</td><td>MXM</td></tr><tr><td>341T0170</td><td>1</td><td>IC,EFT_BOOTROM,K22/K23</td><td>U6100</td><td>CRITICAL</td><td></td></tr><tr><td>338S0765</td><td>1</td><td>IC,XIO2211ZAY,1394B,167BGA</td><td>U4100</td><td>CRITICAL</td><td></td></tr><tr><td>338S0694</td><td>1</td><td>IC,RTL8251CA,GIGE TRANSCEIVER, 48P TQFP</td><td>U3700</td><td>CRITICAL</td><td></td></tr><tr><td>825-7122</td><td>1</td><td>MLB LABEL,48.0X4.8</td><td>X14</td><td>CRITICAL</td><td></td></tr></table></div> <div><div>CPUS</div><table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>337S3745</td><td>1</td><td>WLP,QXXX,QS,2.80G,65W,1066,R0,3M,LGA</td><td>CPU</td><td>CRITICAL</td><td>2P80GHZ_CPU</td></tr><tr><td>337S3742</td><td>1</td><td>WLP,SLB9J,PRQ,2.83G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>2P83GHZ_CPU</td></tr><tr><td>337S3726</td><td>1</td><td>WLP,SLB9J,PRQ,3.0G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>3P0GHZ_CPU</td></tr><tr><td>337S3715</td><td>1</td><td>WLP,SLB9K,PRQ,3.16G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>3P16GHZ_CPU</td></tr><tr><td>337S3727</td><td>1</td><td>WLP,SLB9L,PRQ,3.33G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>3P33GHZ_CPU</td></tr><tr><td>337S3807</td><td>1</td><td>WLP,SLB9L,PRQ,2.93G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>2P93GHZ_CPU</td></tr><tr><td>337S3765</td><td>1</td><td>WLP,SLB9L,PRQ,3.06G,65W,1333,R0,6M,LGA</td><td>CPU</td><td>CRITICAL</td><td>3P06GHZ_CPU</td></tr><tr><td>337S3804</td><td>1</td><td>WLP,SL0U9,PRQ,2.80G,65W,1066,R0,2M,LGA</td><td>CPU</td><td>CRITICAL</td><td>2P80GHZ_2M_CPU</td></tr></table></div> <div><div>K22 PARTS</div><table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>051-7845</td><td>1</td><td>SCH,K22,MLB</td><td>SCH1</td><td></td><td>K22</td></tr><tr><td>820-2494</td><td>1</td><td>PCBF,K22,MLB</td><td>MLB1</td><td></td><td>K22</td></tr><tr><td>341T0168</td><td>1</td><td>IC,SMC,K22</td><td>U4900</td><td>CRITICAL</td><td>K22</td></tr></table></div> <div><div>ALTERNATES</div><table><tr><th>PART NUMBER</th><th>ALTERNATE FOR PART NUMBER</th><th>BOM OPTION</th><th>REF DES</th><th>COMMENTS:</th></tr><tr><td>127S0111</td><td>127S0060</td><td></td><td>C6211</td><td>AUDIO, NEED QUAL</td></tr></table></div>	BOM NUMBER	BOM NAME	BOM OPTIONS	630-9768	PCBA,MLB,GOOD,K22	K22,2P80GHZ_CPU,BASIC,IG	639-0036	PCBA,2.8 GHZ CPU,MXM,K22	K22,2P80GHZ_CPU,BASIC,MXM,K22_MXM	639-0392	PCBA,2.8 GHZ-2M CPU,IG,K22	K22,2P80GHZ_2M_CPU,BASIC,IG	639-0393	PCBA,2.8 GHZ-2M CPU,MXM,K22	K22,2P80GHZ_2M_CPU,BASIC,MXM,K22_MXM	639-0184	PCBA,2.93 GHZ CPU,IG,K22	K22,2P93GHZ_CPU,BASIC,IG	639-0186	PCBA,2.93 GHZ CPU,MXM,K22	K22,2P93GHZ_CPU,BASIC,MXM,K22_MXM	639-0037	PCBA,3.0 GHZ CPU,IG,K22	K22,3P0GHZ_CPU,BASIC,IG	630-9878	PCBA,MLB,CTO,K22	K22,3P0GHZ_CPU,BASIC,MXM,K22_MXM	639-0183	PCBA,3.06 GHZ CPU,IG,K22(Investigation)	K22,3P06GHZ_CPU,BASIC,IG	639-0511	PCBA,MLB,3.06GHZ,MXM,K22	K22,3P06GHZ_CPU,BASIC,MXM,K22_MXM	639-0510	PCBA,MLB,3.16GHZ,IG,K22	K22,3P16GHZ_CPU,BASIC,IG	639-0324	PCBA,MLB,3.16GHZ,MXM,K22	K22,3P16GHZ_CPU,BASIC,MXM,K22_MXM	639-0206	PCBA,MLB,3.33GHZ,IG,K22	K22,3P33GHZ_CPU,BASIC,IG	639-0207	PCBA,MLB,3.33GHZ,MXM,K22	K22,3P33GHZ_CPU,BASIC,MXM,K22_MXM	607-4426	PCBA,MLB,DEV,K22	DEVELOPMENT,DEV_GROUP	BOM GROUP	BOM OPTIONS	BASIC	COMMON,ALTERNATE,MCP7A,XDP,BETTER,MCP_ISL9563A,MLB_PNL_PWR,PRODUCTION	MCP7A	BOOT_MODE_USER,MEMRESET_HW,MEMRESET_MCP	DEV_GROUP	XDP_CONN,LPCPLUS,VREFMRGN,MCP_PWR_SENSE,MCP_CPU_TDIODE,PECI_SMB,MOJOMUX	TOP	SIGNAL	2	GROUND	3	SIGNAL	4	POWER	5	POWER	6	SIGNAL	7	GROUND	BOTTOM	SIGNAL	PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	338S0731	1	IC,GMCP,MCP7A-JA,B03,35X35MM,BGA1437,DT	U1400	CRITICAL	IG	338S0732	1	IC,MCP,MCP7A-DA,B03,35X35MM,BGA1437,DT	U1400	CRITICAL	MXM	341T0170	1	IC,EFT_BOOTROM,K22/K23	U6100	CRITICAL		338S0765	1	IC,XIO2211ZAY,1394B,167BGA	U4100	CRITICAL		338S0694	1	IC,RTL8251CA,GIGE TRANSCEIVER, 48P TQFP	U3700	CRITICAL		825-7122	1	MLB LABEL,48.0X4.8	X14	CRITICAL		PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	337S3745	1	WLP,QXXX,QS,2.80G,65W,1066,R0,3M,LGA	CPU	CRITICAL	2P80GHZ_CPU	337S3742	1	WLP,SLB9J,PRQ,2.83G,65W,1333,R0,6M,LGA	CPU	CRITICAL	2P83GHZ_CPU	337S3726	1	WLP,SLB9J,PRQ,3.0G,65W,1333,R0,6M,LGA	CPU	CRITICAL	3P0GHZ_CPU	337S3715	1	WLP,SLB9K,PRQ,3.16G,65W,1333,R0,6M,LGA	CPU	CRITICAL	3P16GHZ_CPU	337S3727	1	WLP,SLB9L,PRQ,3.33G,65W,1333,R0,6M,LGA	CPU	CRITICAL	3P33GHZ_CPU	337S3807	1	WLP,SLB9L,PRQ,2.93G,65W,1333,R0,6M,LGA	CPU	CRITICAL	2P93GHZ_CPU	337S3765	1	WLP,SLB9L,PRQ,3.06G,65W,1333,R0,6M,LGA	CPU	CRITICAL	3P06GHZ_CPU	337S3804	1	WLP,SL0U9,PRQ,2.80G,65W,1066,R0,2M,LGA	CPU	CRITICAL	2P80GHZ_2M_CPU	PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	051-7845	1	SCH,K22,MLB	SCH1		K22	820-2494	1	PCBF,K22,MLB	MLB1		K22	341T0168	1	IC,SMC,K22	U4900	CRITICAL	K22	PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:	127S0111	127S0060		C6211	AUDIO, NEED QUAL
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BOM Configuration

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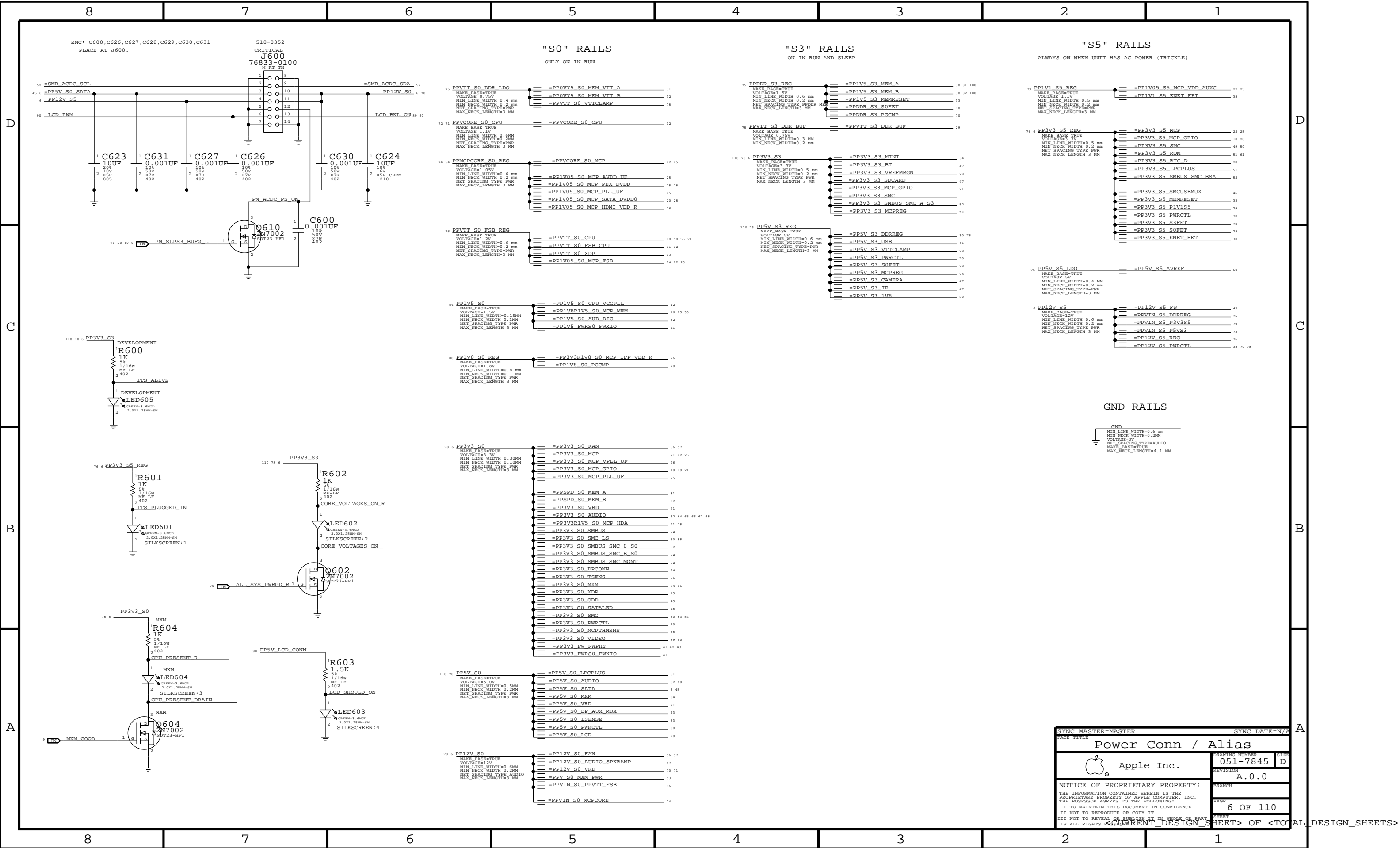
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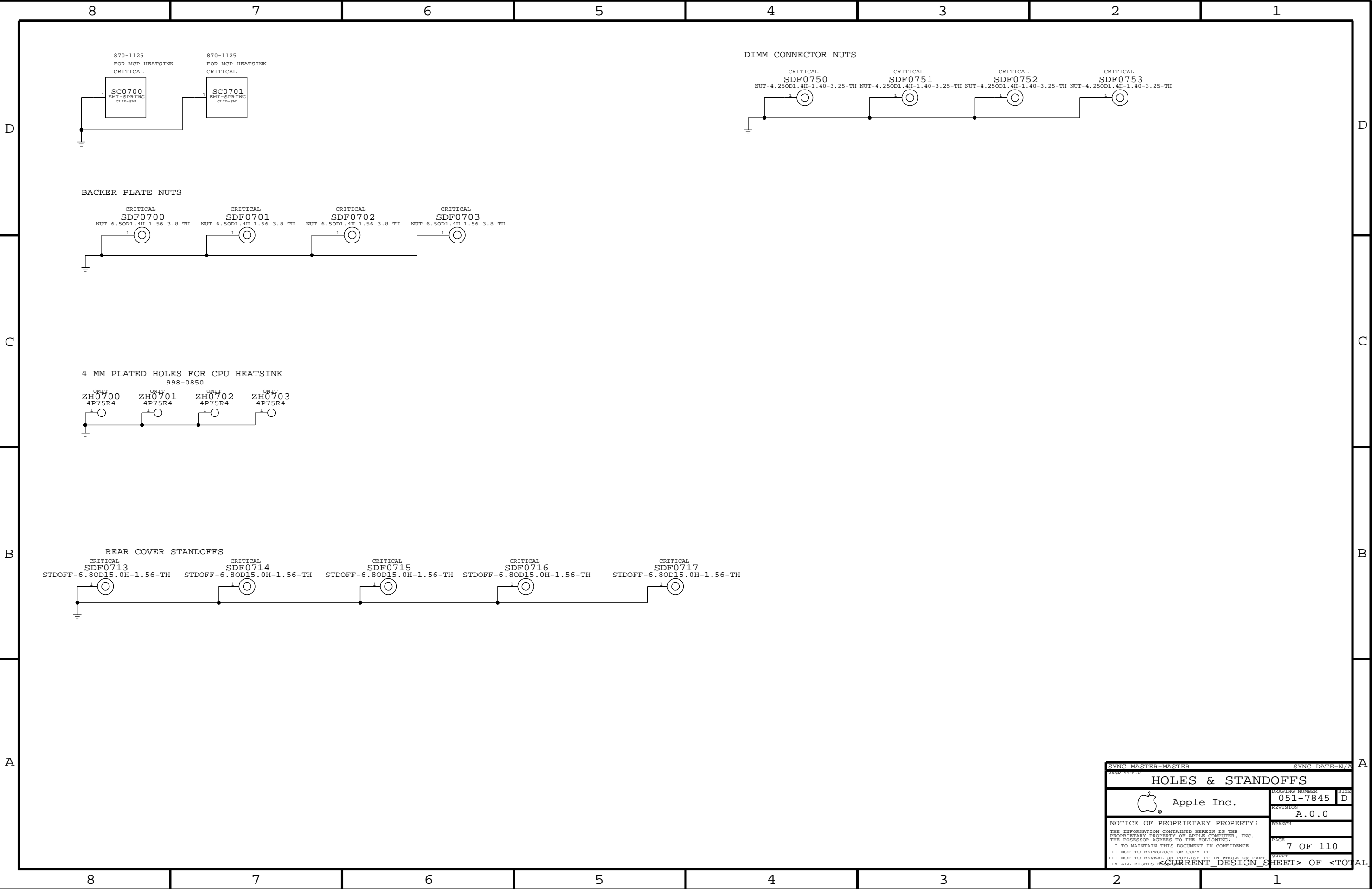
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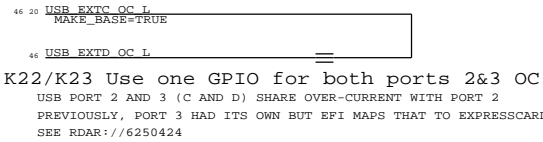
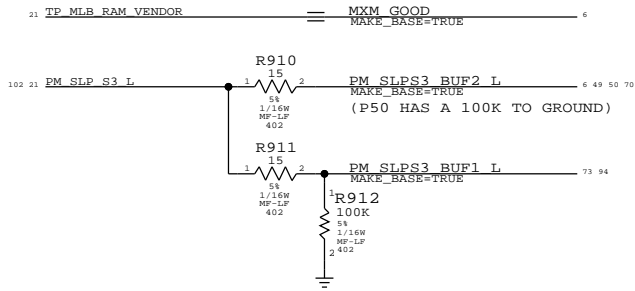
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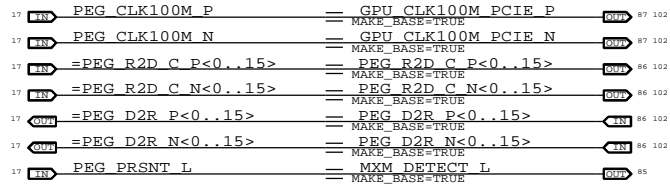




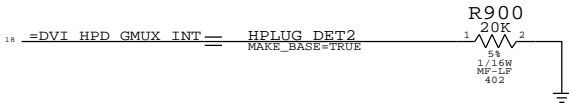
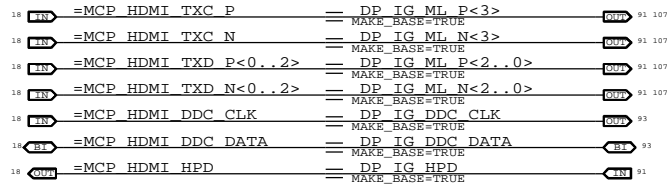
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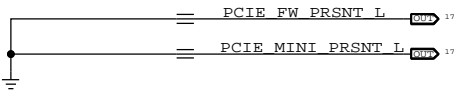
PEG Slot Support



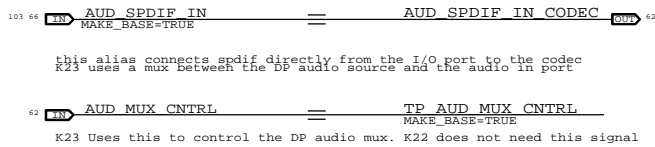
DisplayPort / TMDS Support



MCP79 PCIe PRSENT# Straps

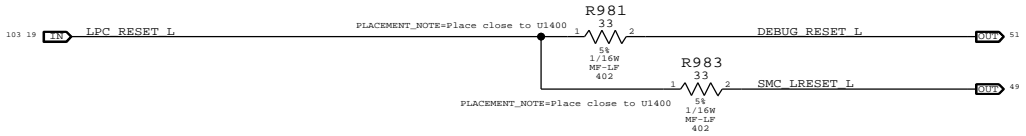


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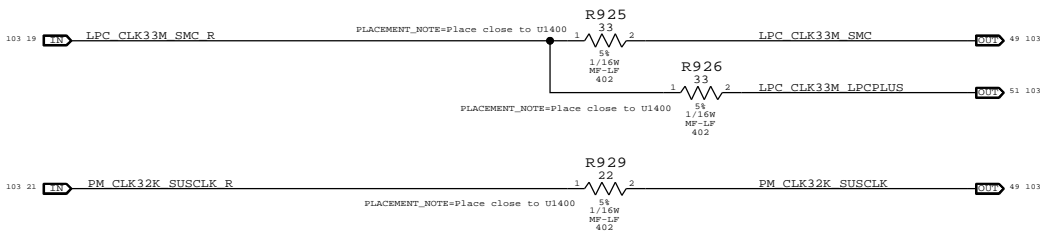
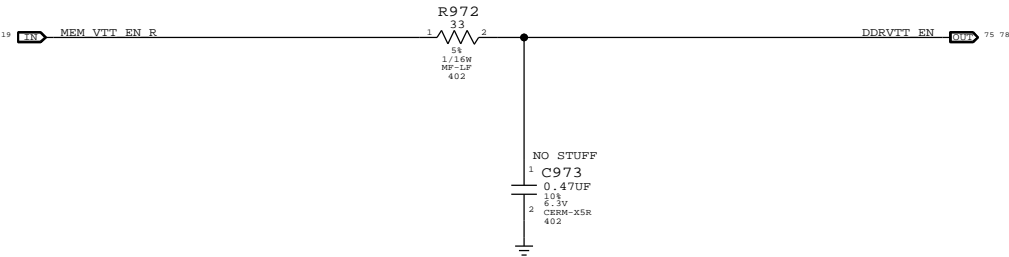
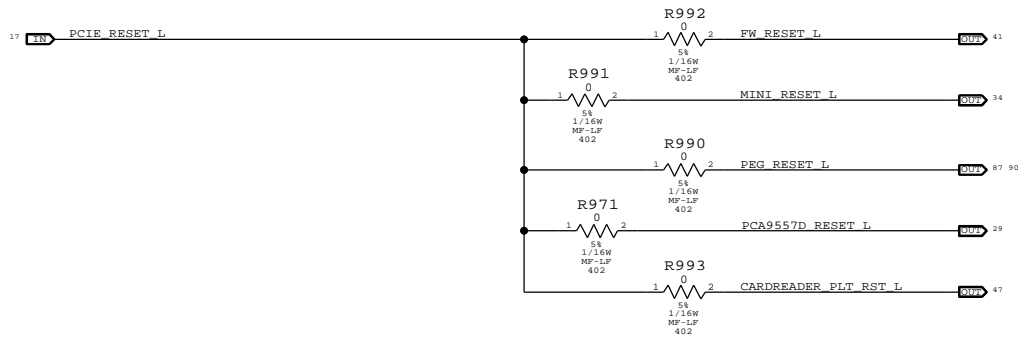


Platform Reset Connections

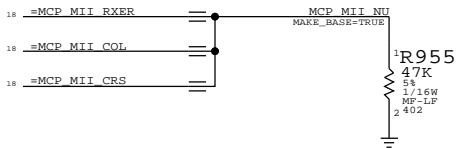
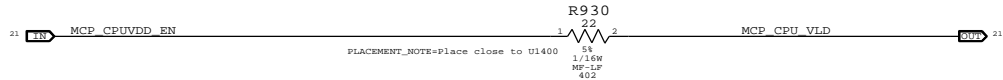
LPC Reset (Unbuffered)



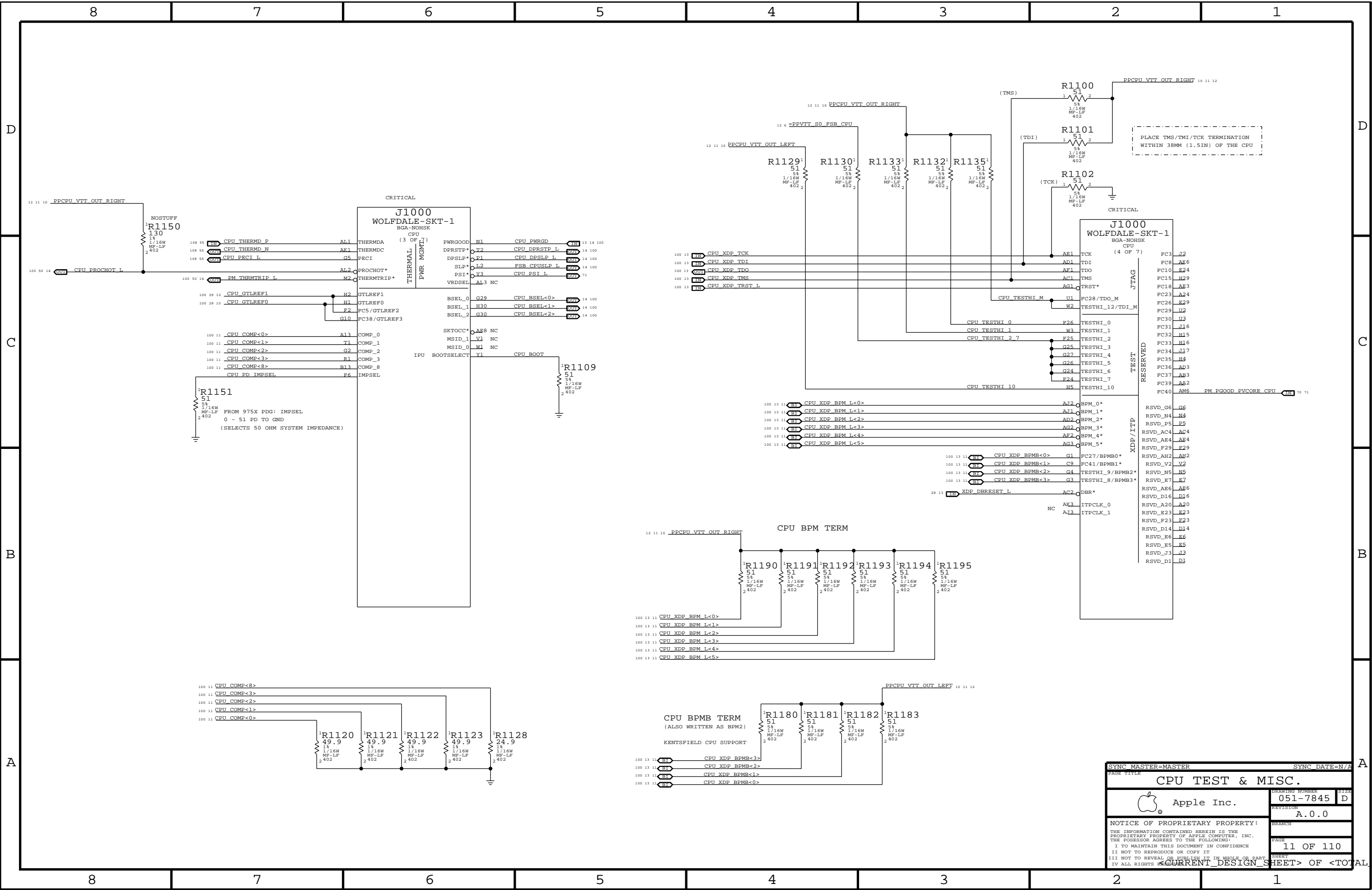
PCIE Reset (Unbuffered)



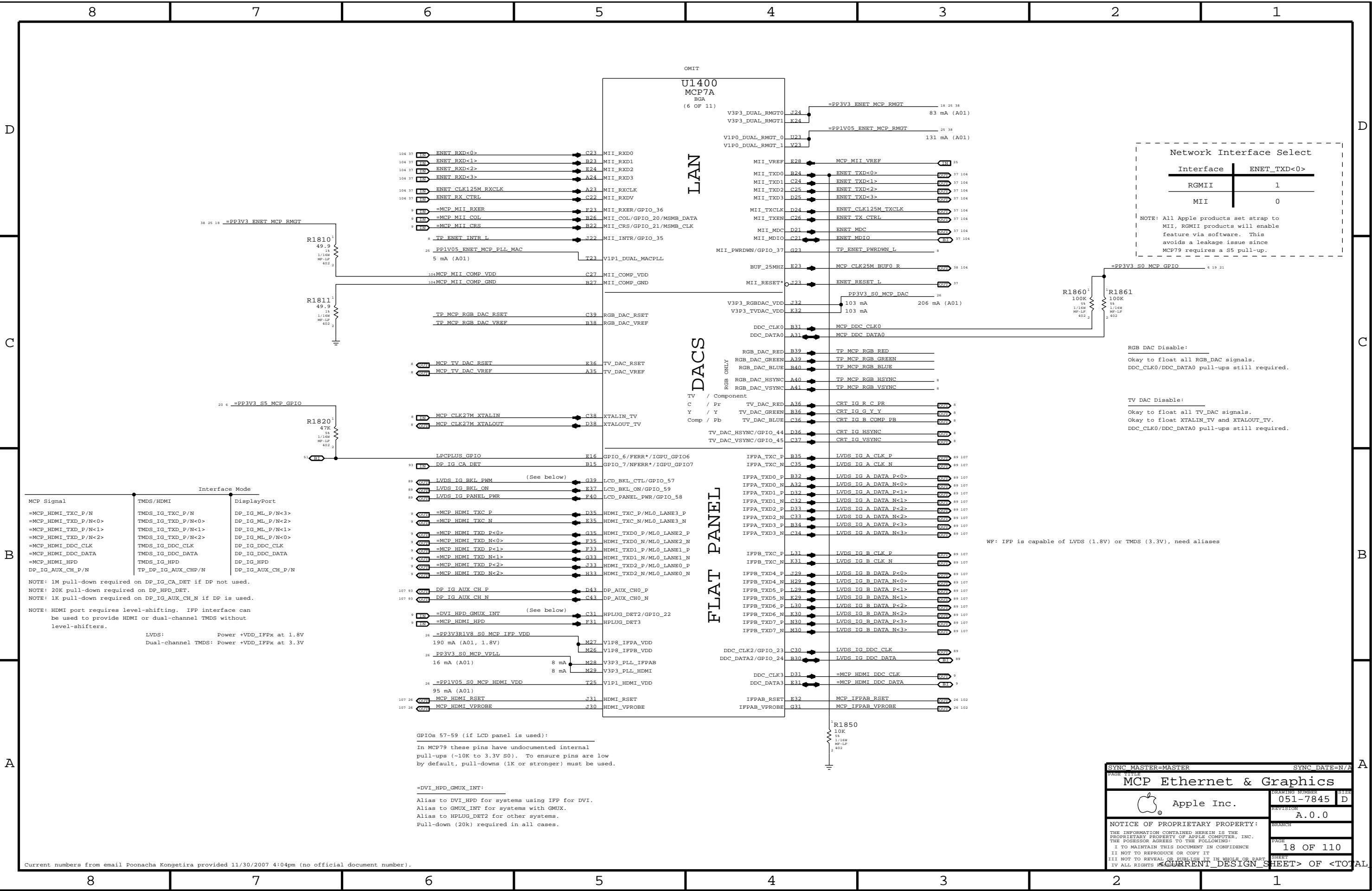
MCP_CPUVDD_EN WILL ASSERT AFTER MCP_PS_PWRGD IS UP

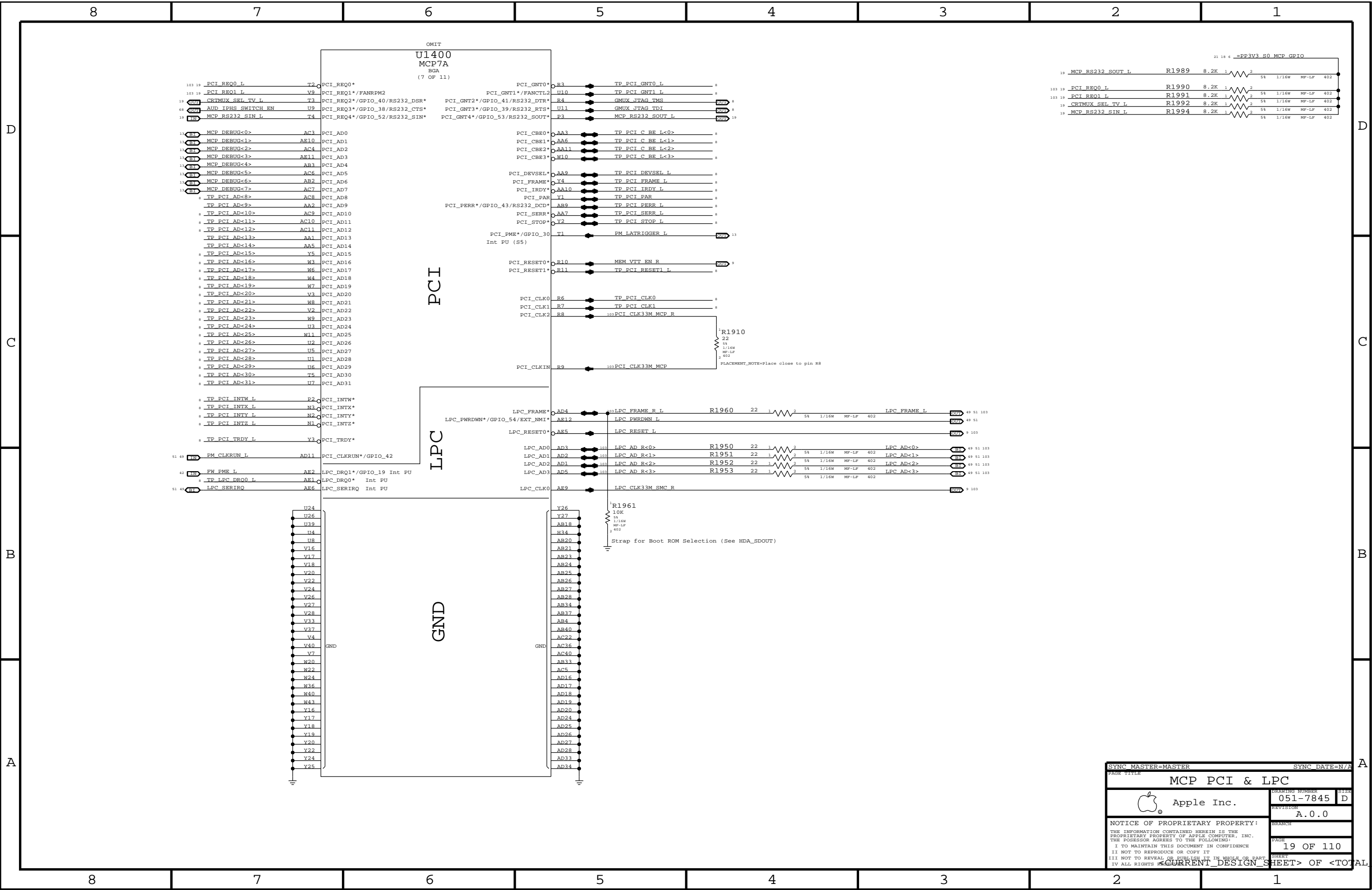


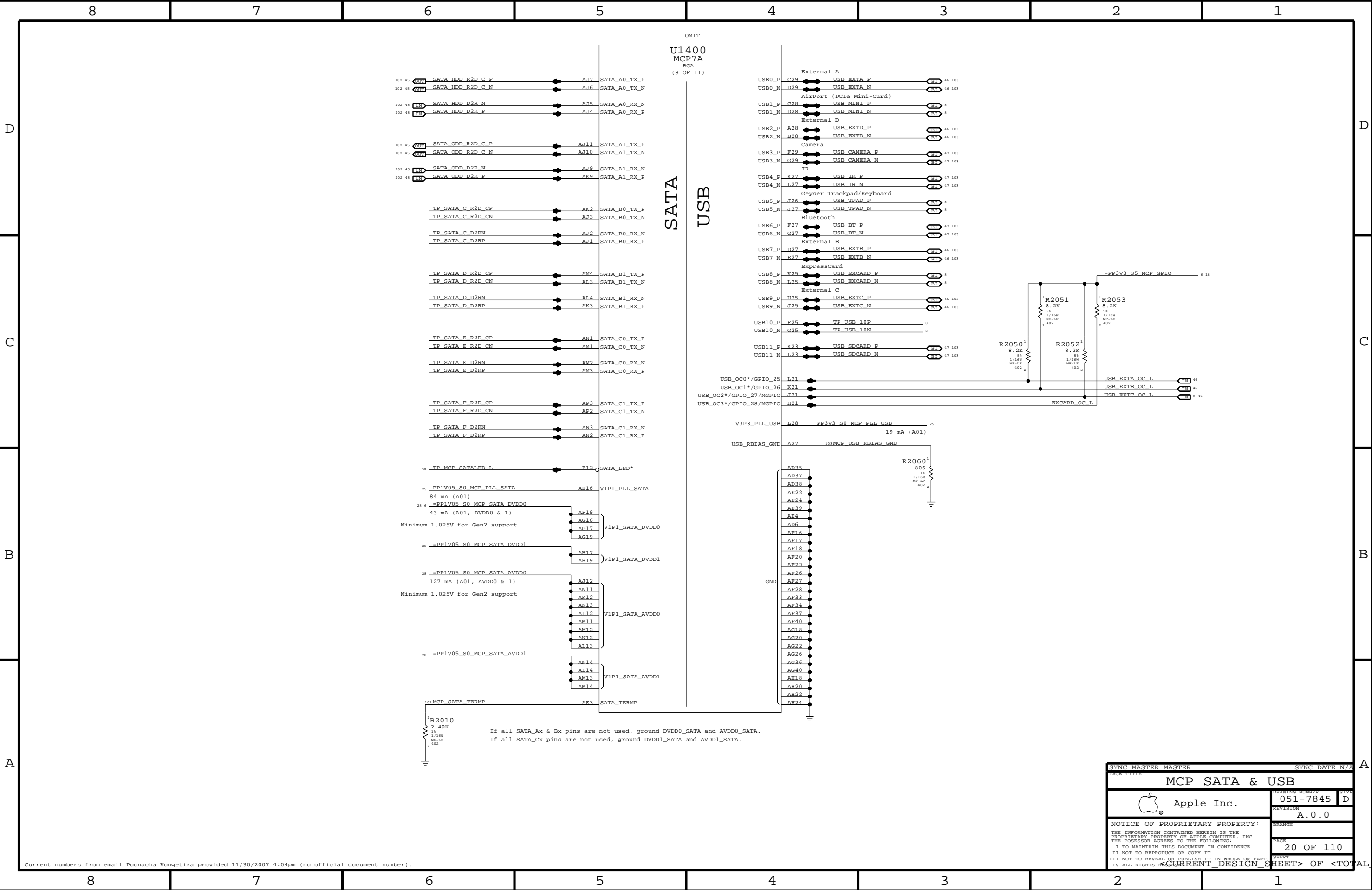
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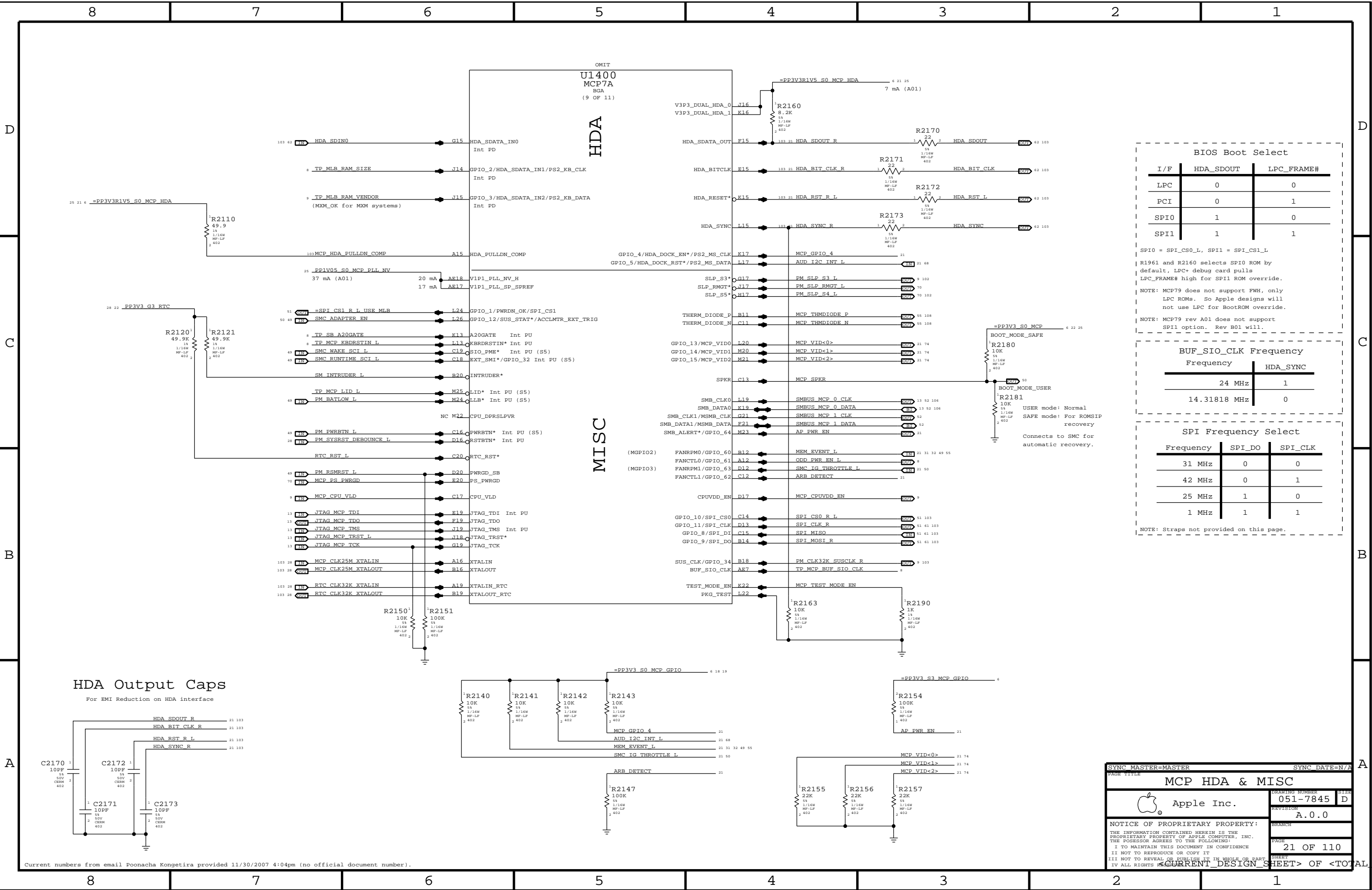








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BIOS Boot Select		
I/F	HDA_SDOUT	LPC_FRAME#
LPC	0	0
PCI	0	1
SPI0	1	0
SPI1	1	1

SPI0 = SPI_CS0_L, SPI1 = SPI_CS1_L
R1961 and R2160 selects SPI0 ROM by default, LPC+ debug card pulls LPC_FRAME# high for SPI1 ROM override.
NOTE: MCP79 does not support FWH, only LPC ROMs. So Apple designs will not use LPC for BootROM override.
NOTE: MCP79 rev A01 does not support SPI1 option. Rev B01 will.

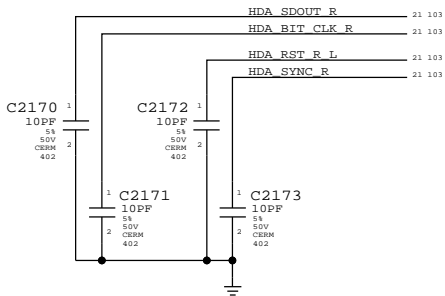
BUF_SIO_CLK Frequency	
Frequency	HDA_SYNC
24 MHz	1
14.31818 MHz	0

SPI Frequency Select		
Frequency	SPI_DO	SPI_CLK
31 MHz	0	0
42 MHz	0	1
25 MHz	1	0
1 MHz	1	1

NOTE: Straps not provided on this page.

HDA Output Caps

For EMI Reduction on HDA interface




Current numbers from email Poonacha Kongetira provided 11/30/2007 4:04pm (no official document number).

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MCP HDA & MISC

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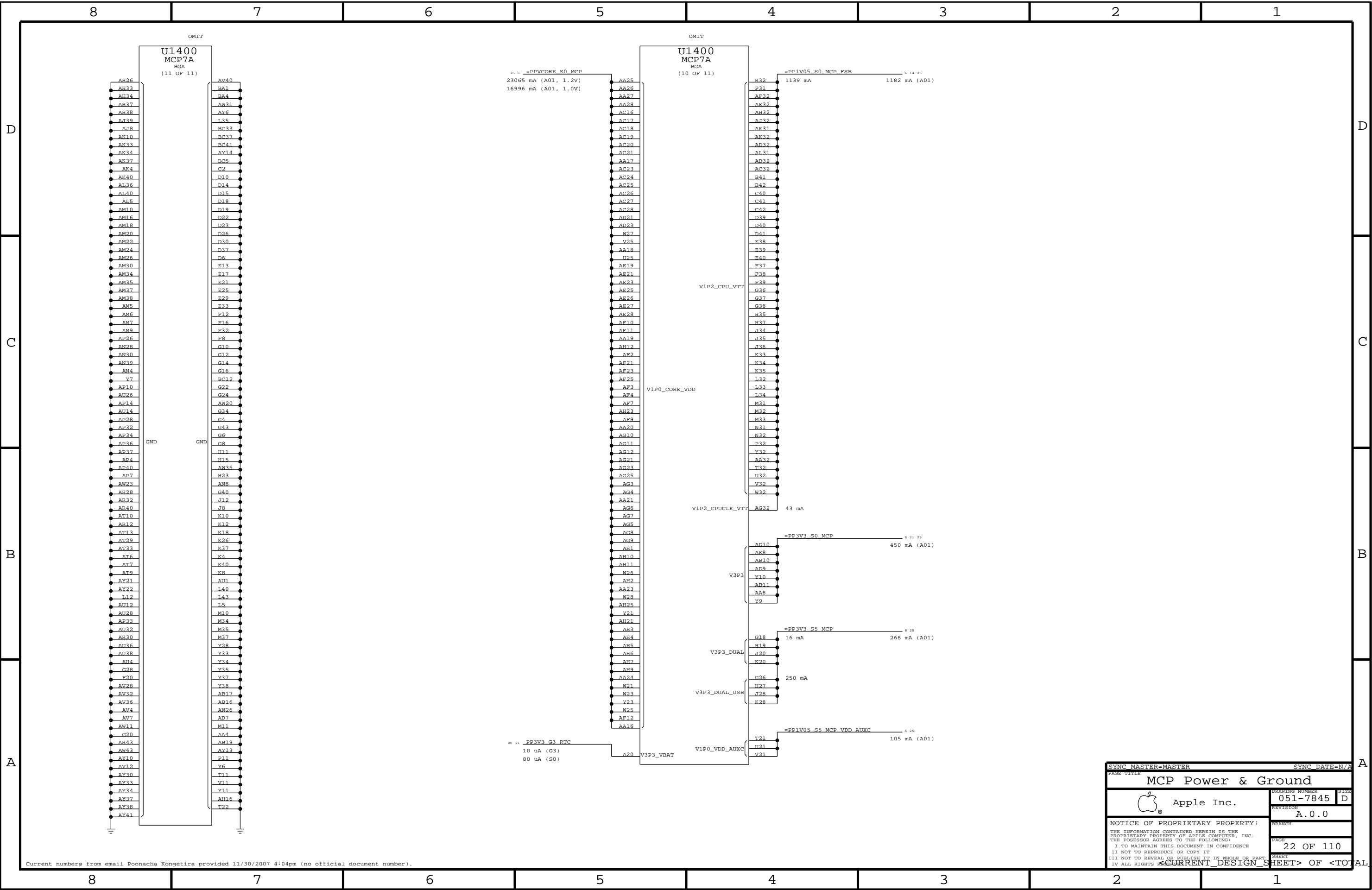
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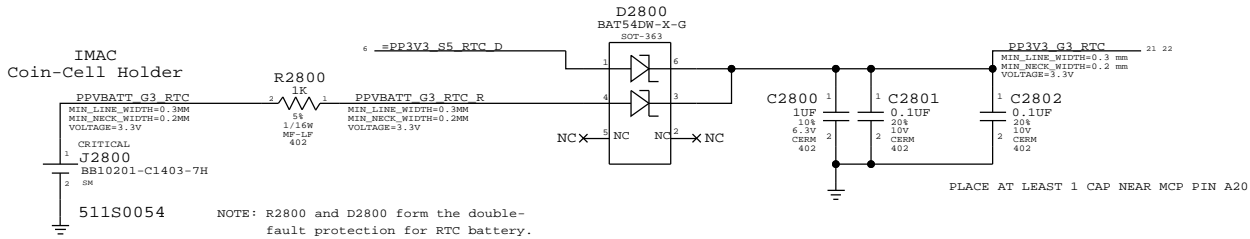
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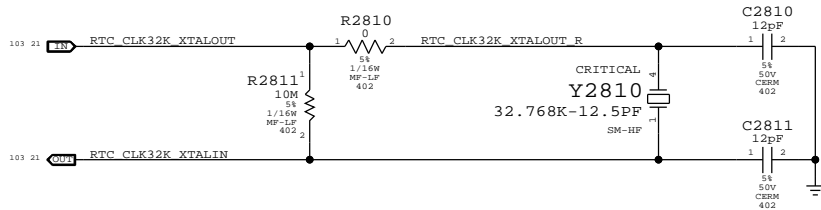
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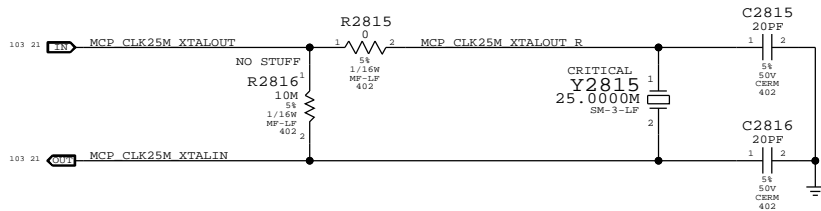
RTC Power Sources



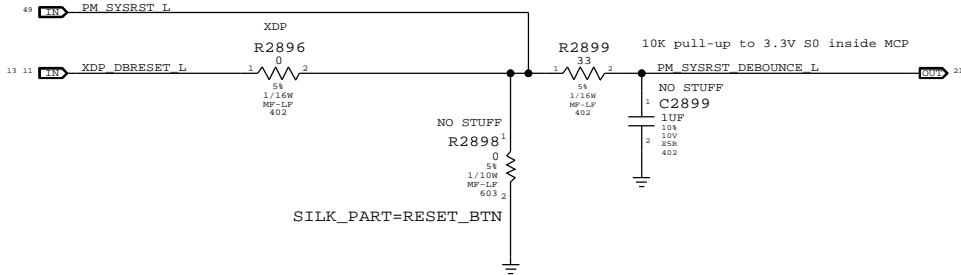
RTC Crystal



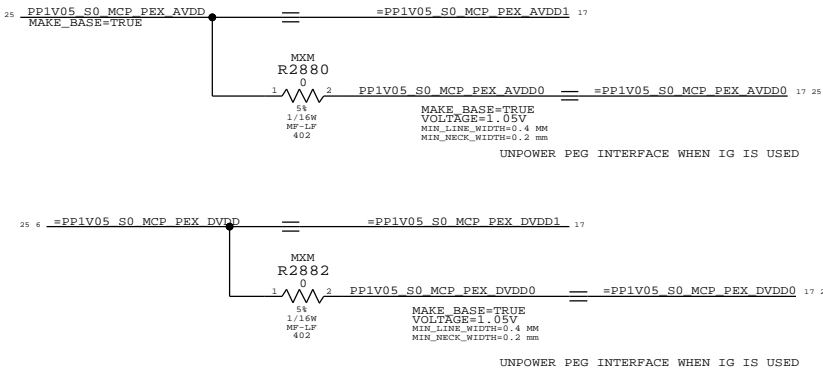
MCP 25MHz Crystal



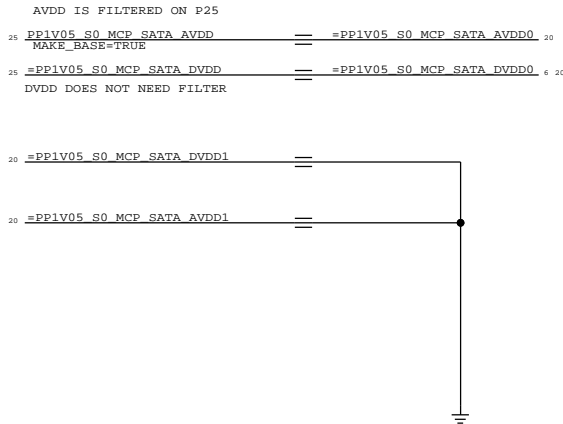
Reset Button



PEG POWER ALIAS/OPTION TO GND UNUSED POWER PIN



SATA ALIAS/GROUNDING UNUSED DVDD1 AND AVDD1



Page Notes

Power aliases required by this page:
- =PP3V3_S3_VREFMRGN
- =PP3V3_S5_VREFMRGN
- =PPVTT_S3_DDR_BUF

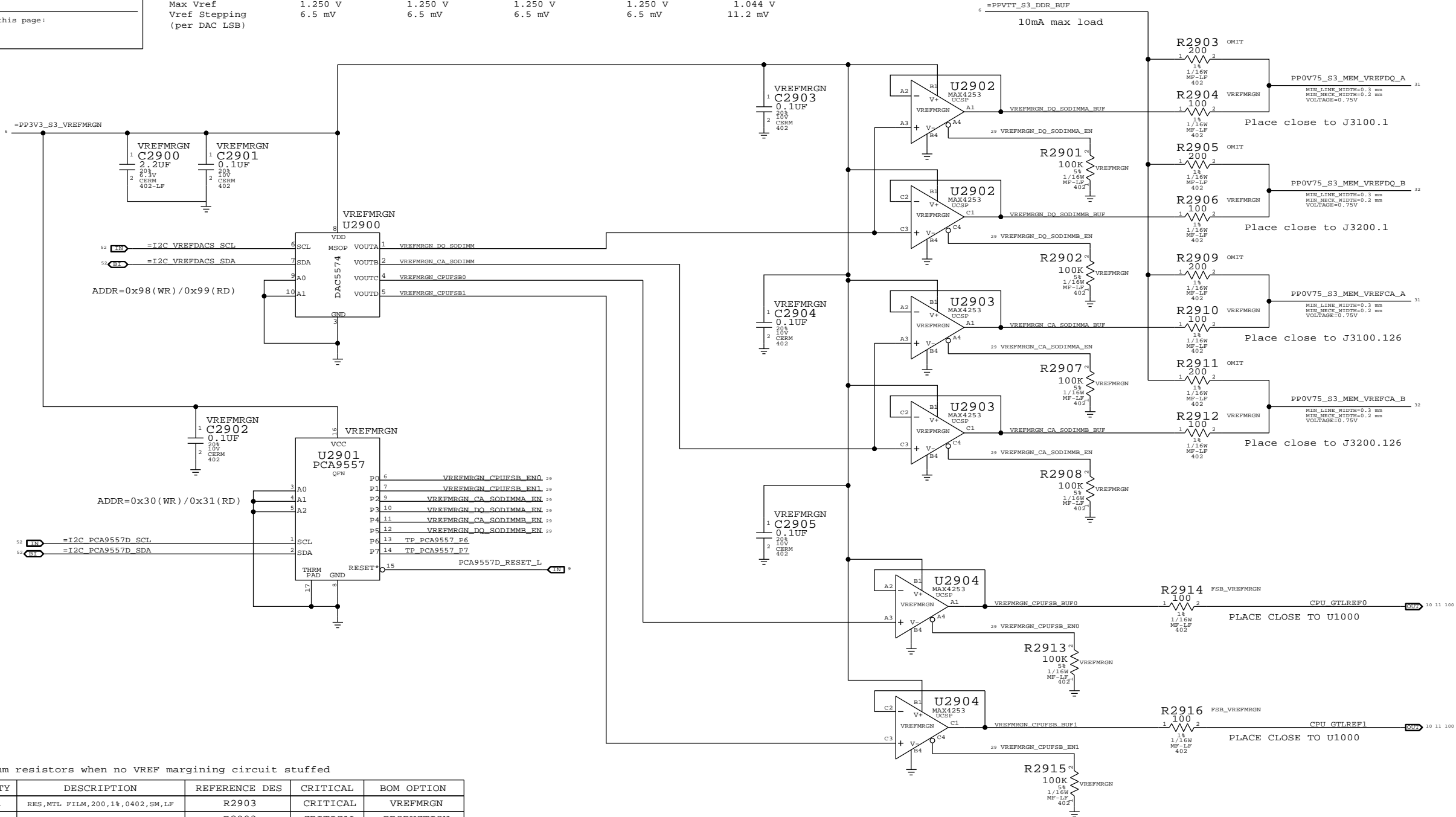
Signal aliases required by this page:
- =I2C_VREFDACS_SCL
- =I2C_VREFDACS_SDA
- =I2C_PCA9557D_SCL
- =I2C_PCA9557D_SDA

BOM options provided by this page:
VREFMRGN
PRODUCTION

DAC channel
Min DAC code
Max DAC code
Max sink I
Max source I
Nominal Vref
Min Vref
Max Vref
Vref Stepping
(per DAC LSB)

	MEM A VREF DQ	MEM A VREF CA	MEM B VREF DQ	MEM B VREF CA	CPU FSB VREF
A	0x00	0x00	0x00	0x00	0x00
B	0x87	0x87	0x87	0x87	0x55
Max sink I	-3.75 mA	-3.75 mA	-3.75 mA	-3.75 mA	-0.91 mA
Max source I	5 mA	5 mA	5 mA	5 mA	0.52 mA
Nominal Vref	0.75 V	0.75 V	0.75 V	0.75 V	0.70 V
Min Vref	0.375 V	0.375 V	0.375 V	0.375 V	0.091 V
Max Vref	1.250 V	1.250 V	1.250 V	1.250 V	1.044 V
Vref Stepping	6.5 mV	6.5 mV	6.5 mV	6.5 mV	11.2 mV

SO-DIMM A and SO-DIMM B Vref settings should be margined separately
(i.e. not simultaneously) due to current limitation of TPS51116 regulator.



Required zero ohm resistors when no VREF margining circuit stuffed

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S0149	1	RES,MTL FILM,200,1%,0402,SM,LF	R2903	CRITICAL	VREFMRGN
116S0004	1	RES,MTL FILM,0,5%,0402,SM,LF	R2903	CRITICAL	PRODUCTION
114S0149	1	RES,MTL FILM,200,1%,0402,SM,LF	R2905	CRITICAL	VREFMRGN
116S0004	1	RES,MTL FILM,0,5%,0402,SM,LF	R2905	CRITICAL	PRODUCTION
114S0149	1	RES,MTL FILM,200,1%,0402,SM,LF	R2909	CRITICAL	VREFMRGN
116S0004	1	RES,MTL FILM,0,5%,0402,SM,LF	R2909	CRITICAL	PRODUCTION
114S0149	1	RES,MTL FILM,200,1%,0402,SM,LF	R2911	CRITICAL	VREFMRGN
116S0004	1	RES,MTL FILM,0,5%,0402,SM,LF	R2911	CRITICAL	PRODUCTION

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FSB/DDR3 Vref Margining

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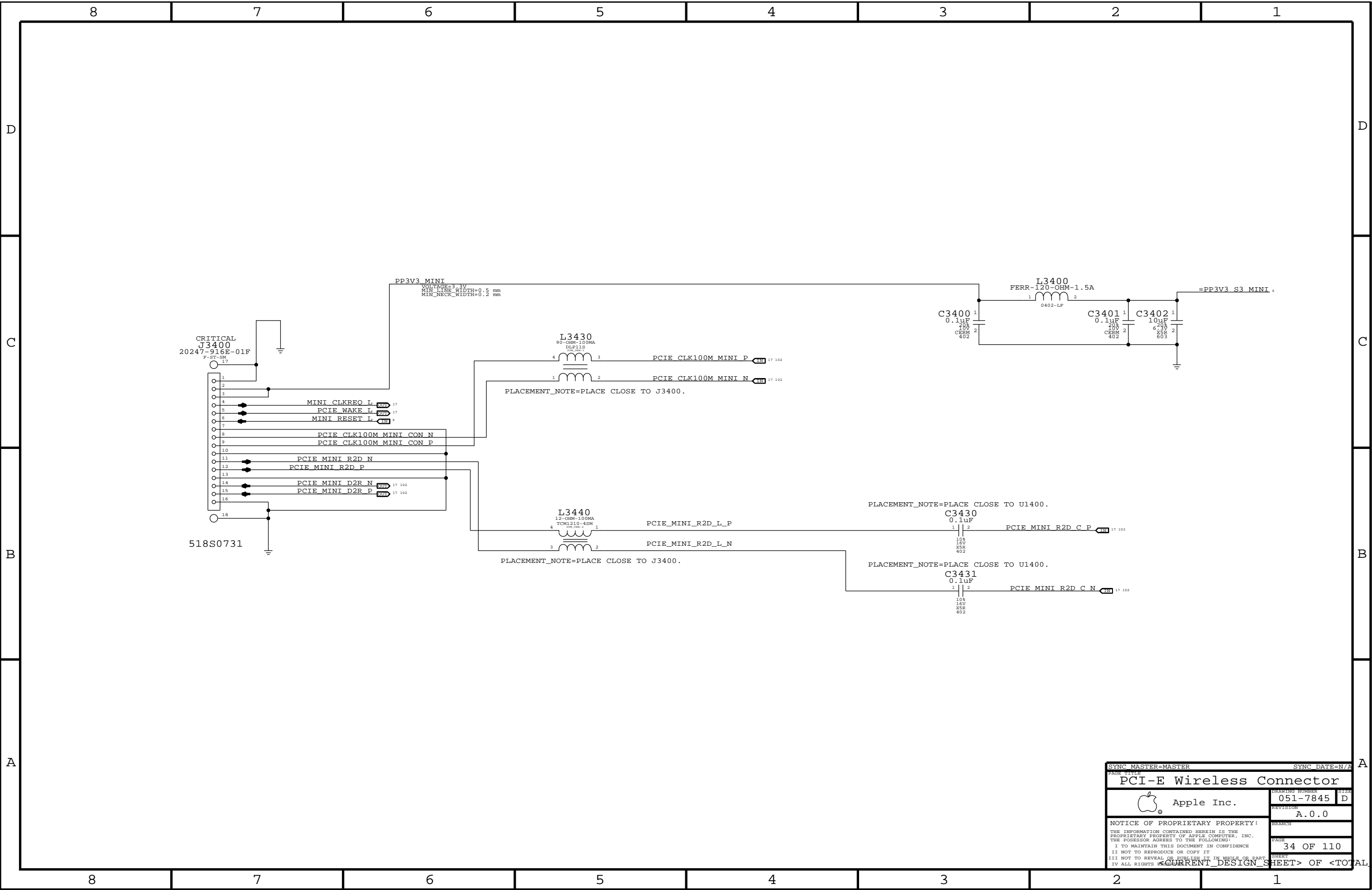
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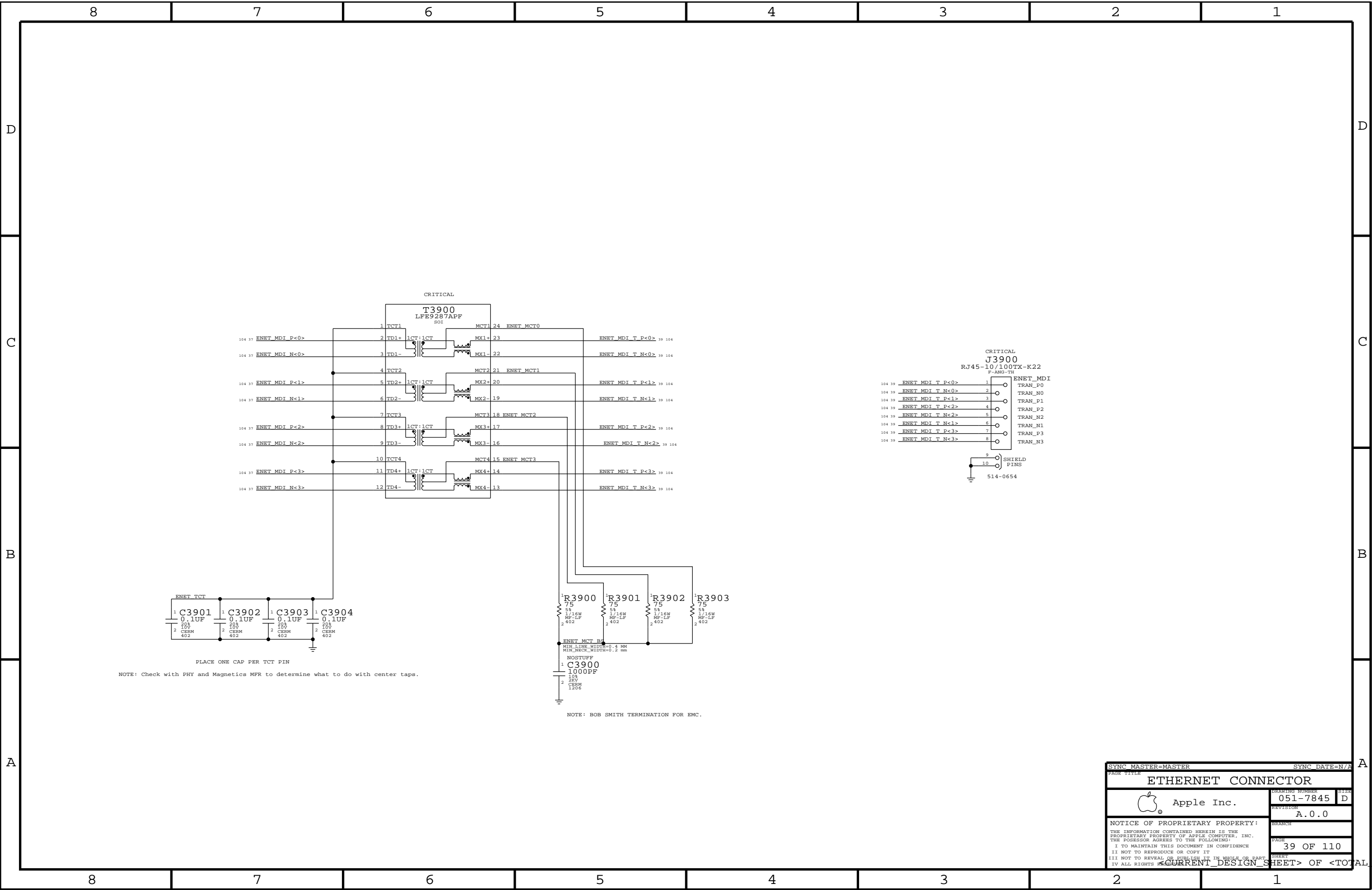
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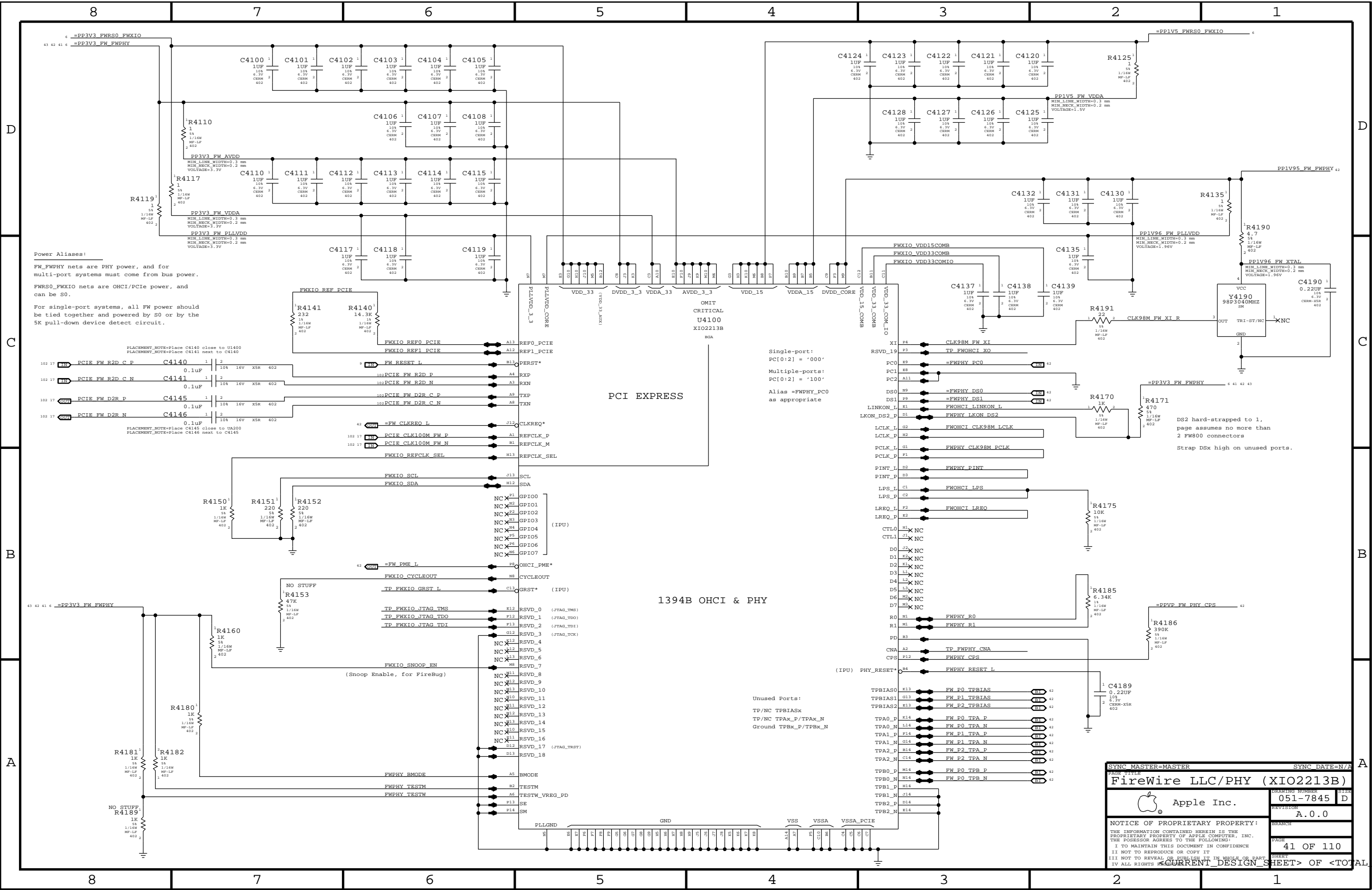
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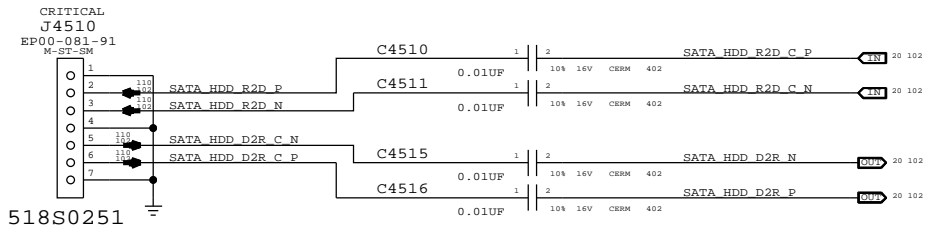
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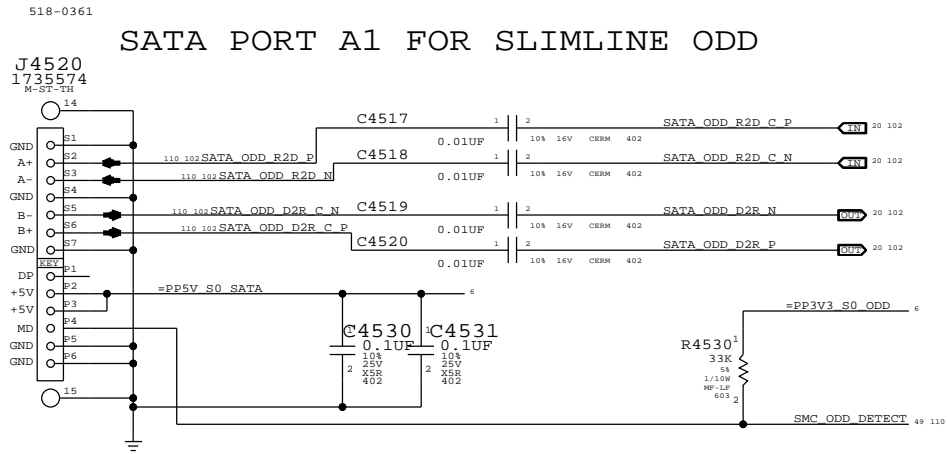
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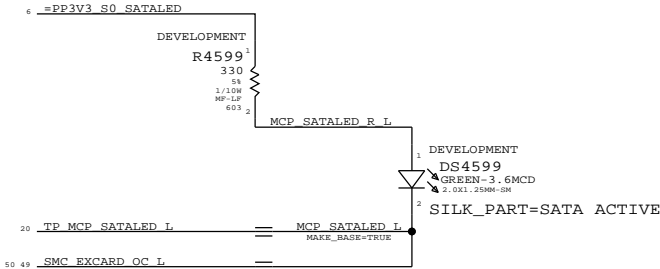
SATA PORT A0 FOR HDD



SATA PORT A1 FOR SLIMLINE ODD



SATA Activity LED




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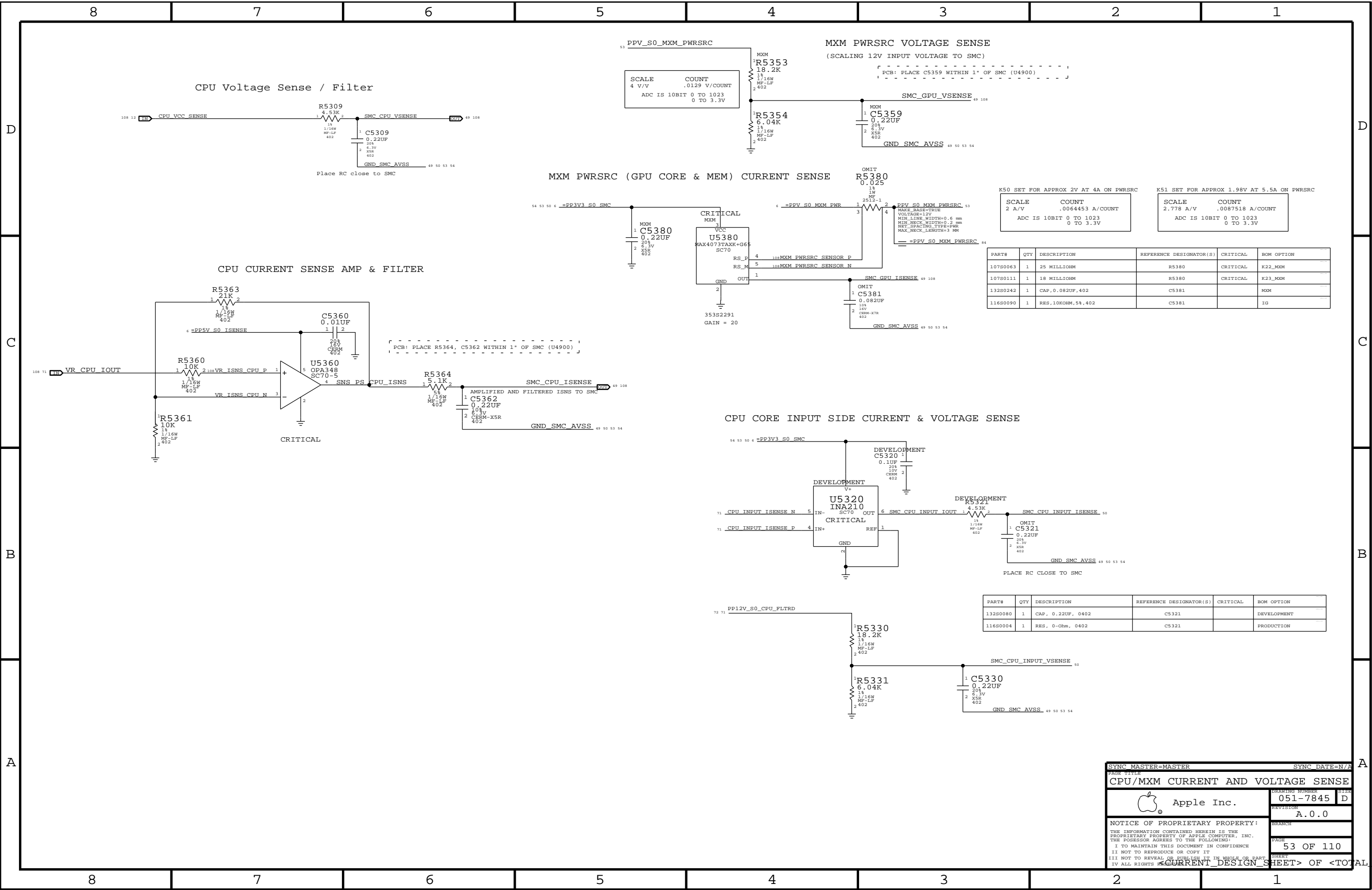
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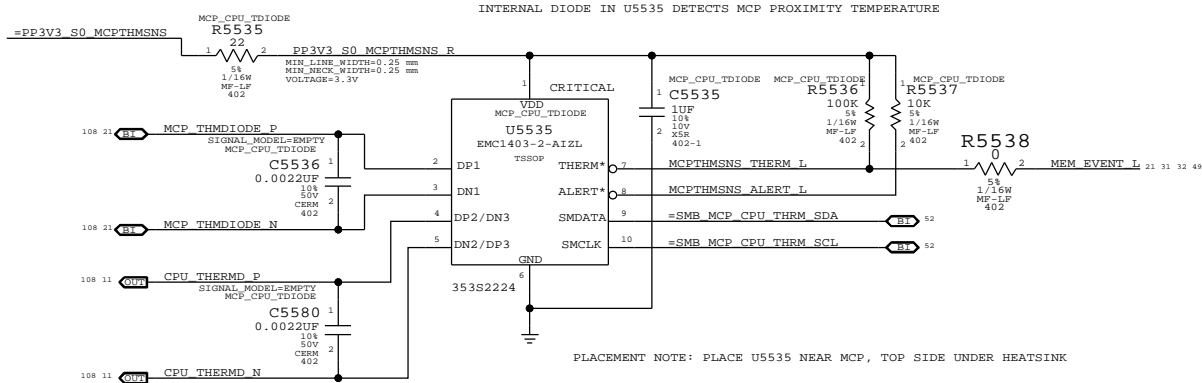
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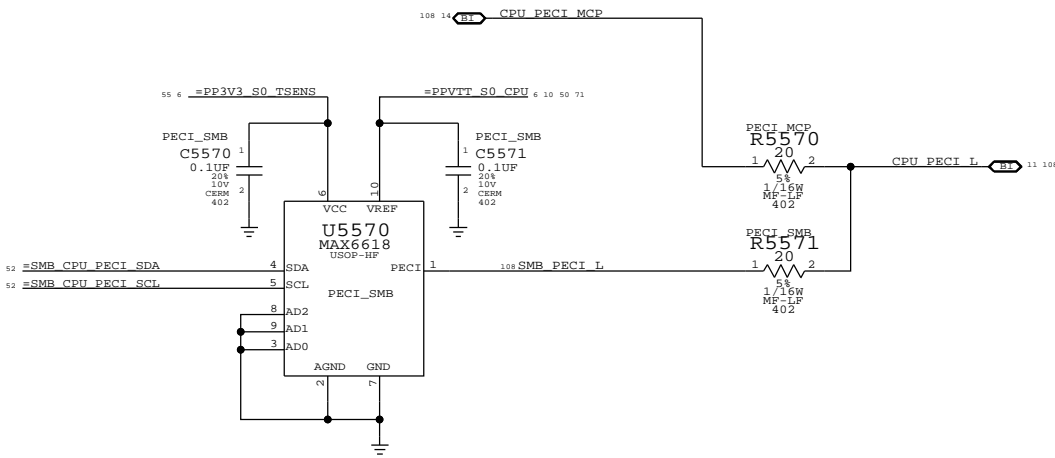


REMOTE THERMAL SENSORS
HEATSINKS, AMBIENT, PANEL AND ODD

MCP & CPU T-Diode Thermal Sensor

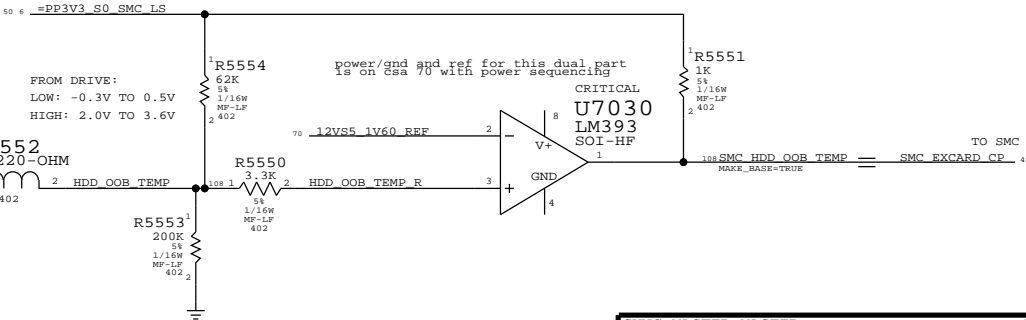
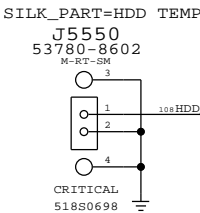


CPU PECCI DTS OPTIONS

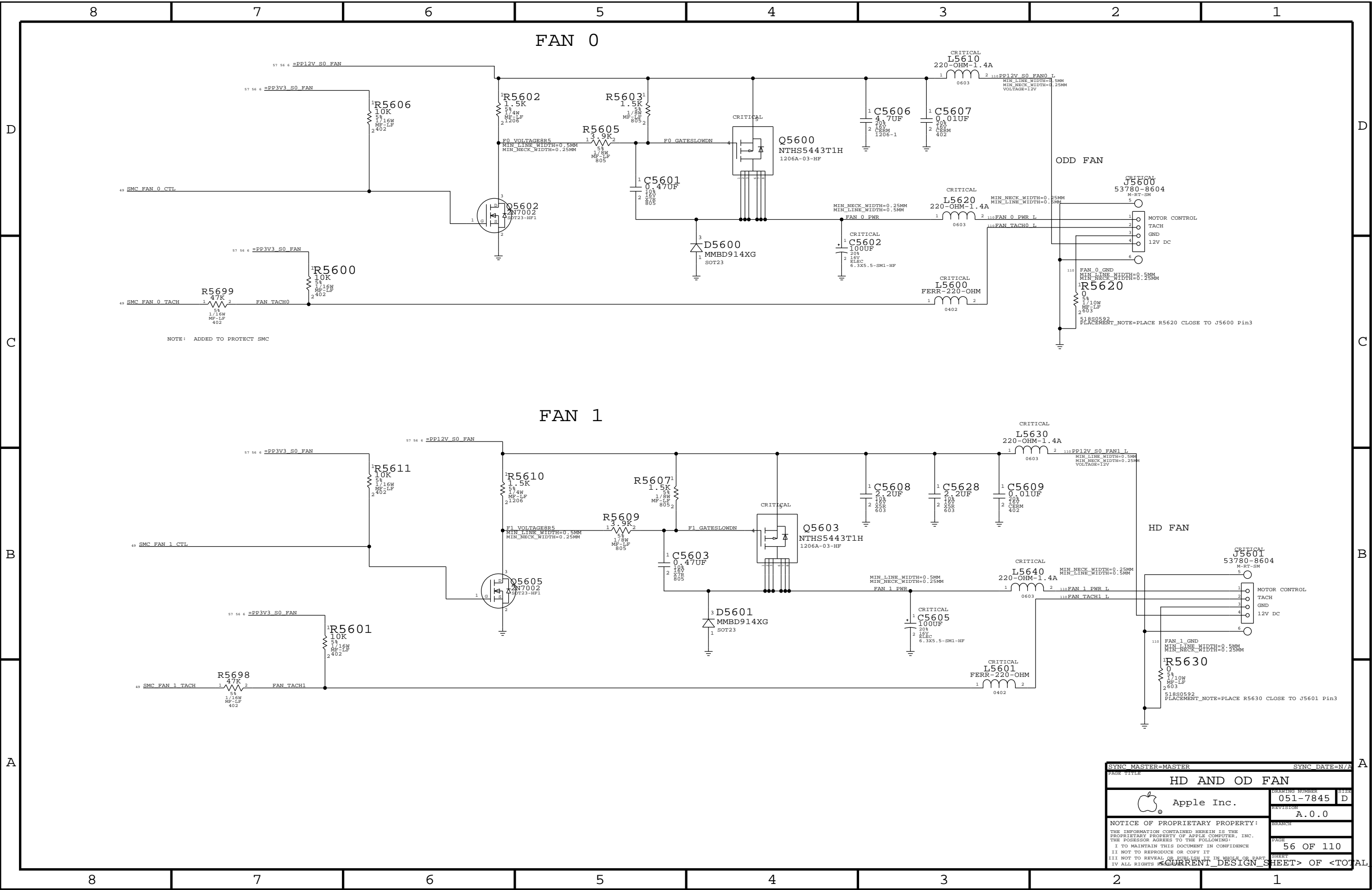



REMOTE THERMAL SENSORS (HEATSINKS AND ODD)

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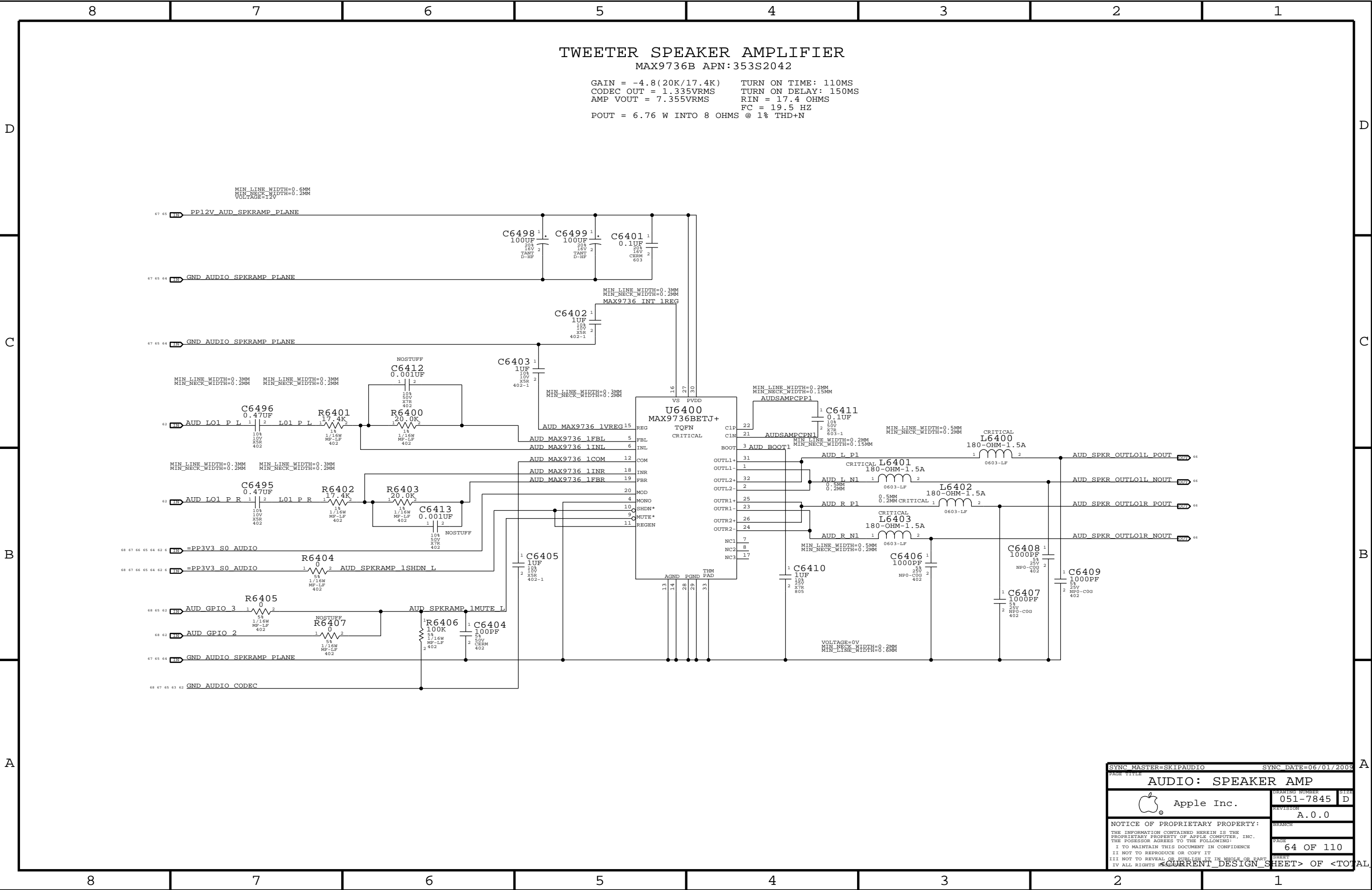
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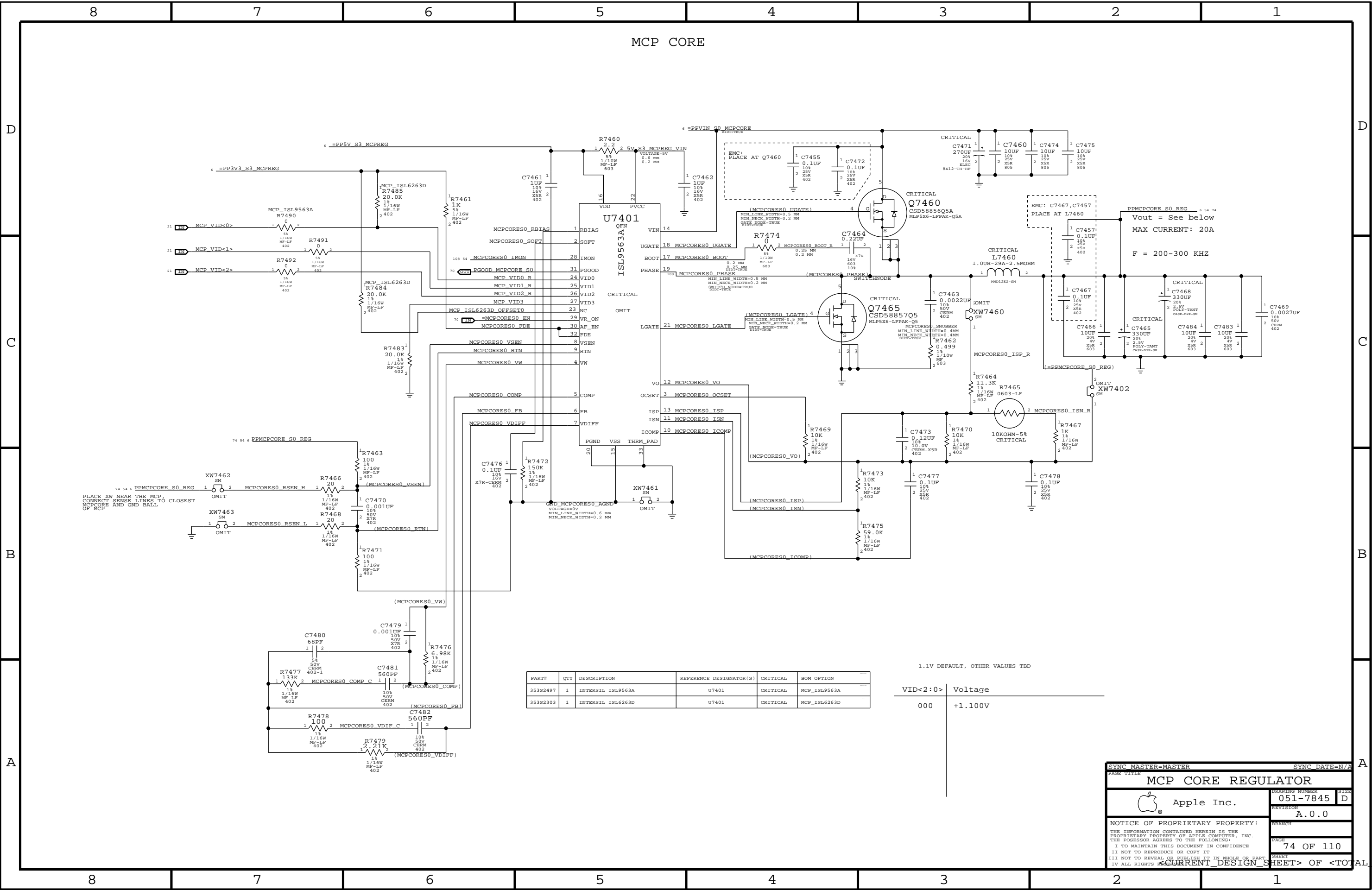
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