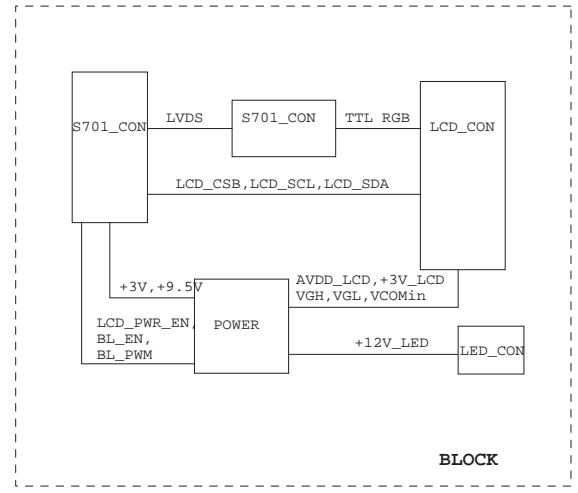
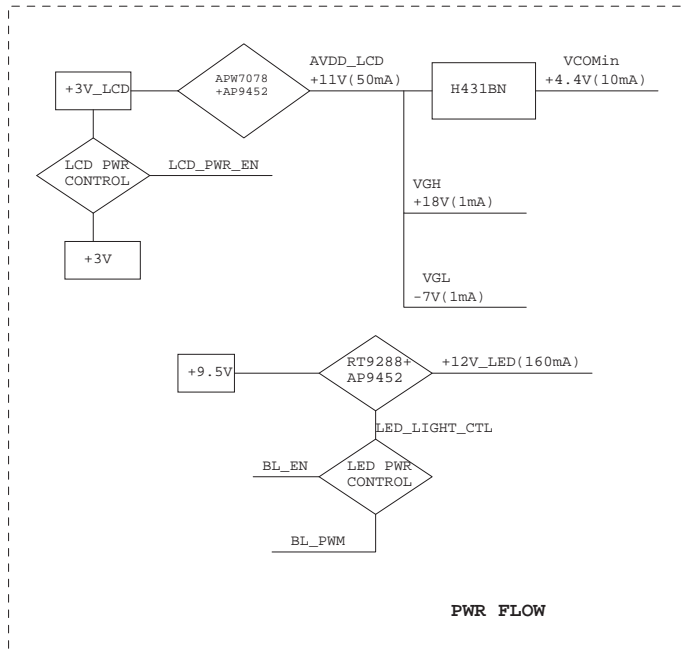


# P701-LCD

R1.6G

TITLE	VERSION
01.BLOCK DIAGRAM	
02.LVDS2TTL	
03.LCD cCONNECTOR	
04.12V LED	
05.LCD PWR	



<Variant Name>		Title : BLOCK	
ASUS		Engineer: RAUL HUANG	
Size	Project Name	Rev	
A3	S701-LCD	1.05	
Date: Friday, November 23, 2007		Sheet 1 of 5	



