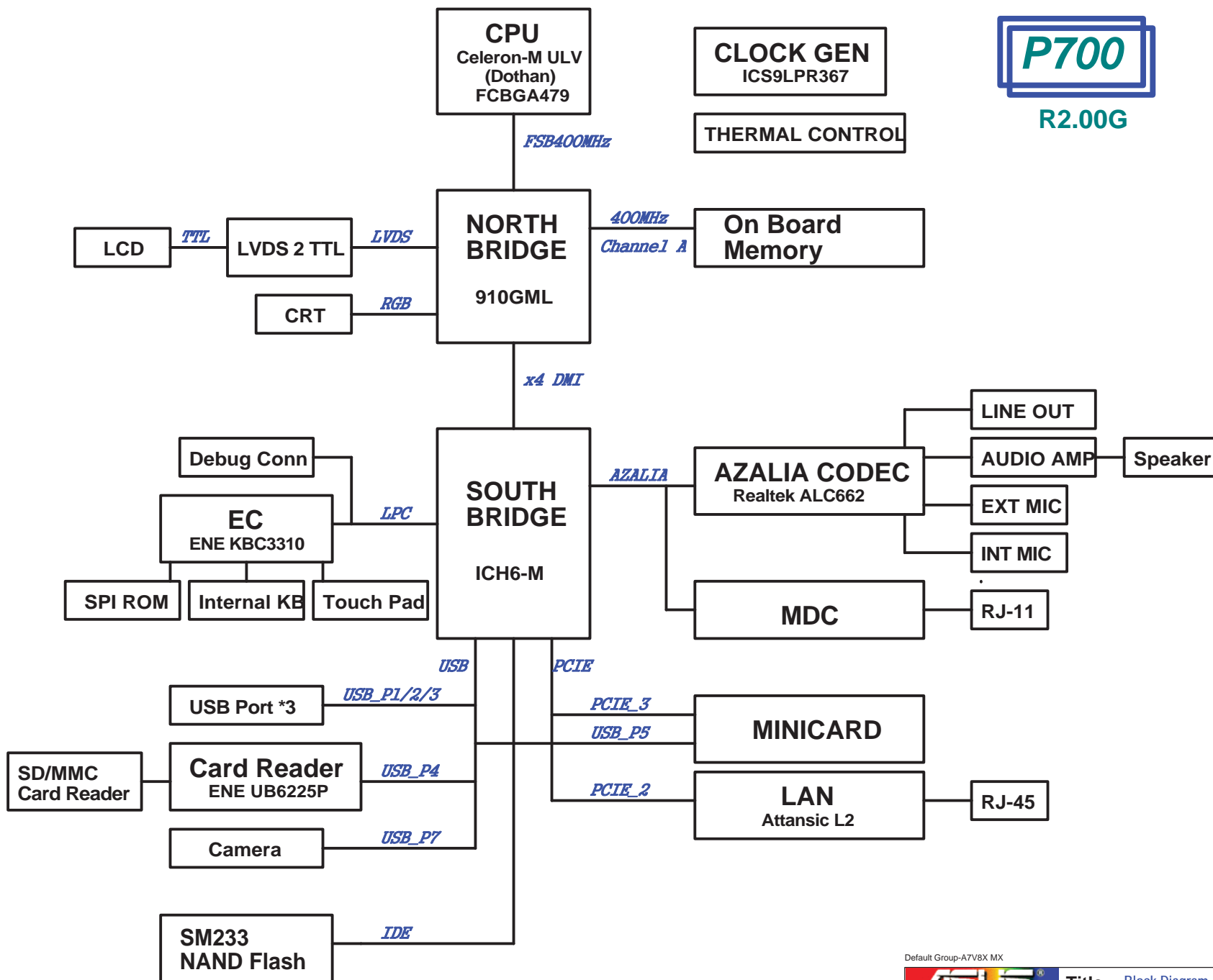


01_Block Diagram
 02_System Setting
 03_Power Sequence
 04_EC Pin Define
 05_History
 06_*
 07_Clock Gen_CY28442-2
 08_Dothan_HOST
 09_Dothan_PWR_GND
 10_910GML_HOST_DMI
 11_910GML_DRAM
 12_910GML_VGA_LVDS_TV
 13_910GML_PWR
 14_910GML_GND
 15_ICH6-M_Azalia_GPIO_PCI_LAN
 16_ICH6-M_USB_PCIE_DMI_IDE_SATA
 17_ICH6-M_PWR_GND
 18_Onboard DRAM_1
 19_Onboard DRAM_2
 20_DDR2_Termination
 21_Onboard VGA
 22_LCD Conn
 23_Minicard
 24_LAN_Attansic L2
 25_MDC_RJ11_RJ45
 26_Flash Conn
 27_USB Port
 28_Card Reader_ENE UB6225P
 29_Camera Conn
 30_Codec_ALC662
 31_Audio_AMP_Jack
 32_EC_ENE KB3310
 33_Switch_SPI ROM_Debug Conn
 34_KB_Touch Pad
 35_Thermal Sensor
 36_LED
 37_Discharge
 38_PWR Jack
 39_Srew Hole
 40_EMI
 41_POWER FLOW
 42_CHARGER
 43_VCORE(7A)
 44_POWER_3V_5V_VTT_DDR
 45_POWER_3VA_3VSB
 46_POWER_1.05V_1.5V_2.5V
 47_POWER_1.8V_DUAL_5VSB



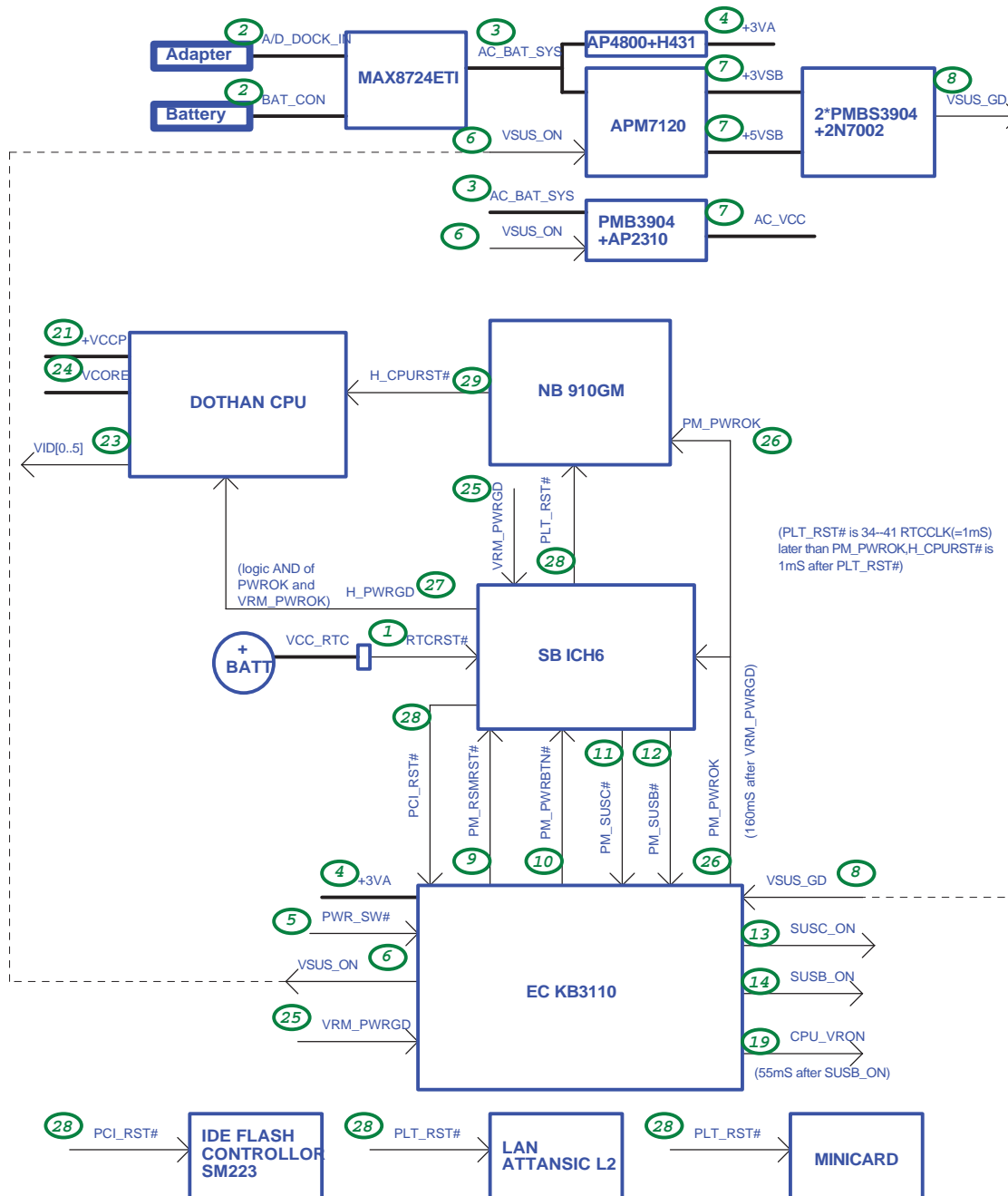
P700
R2.00G

ICH6 GPIO SETTING

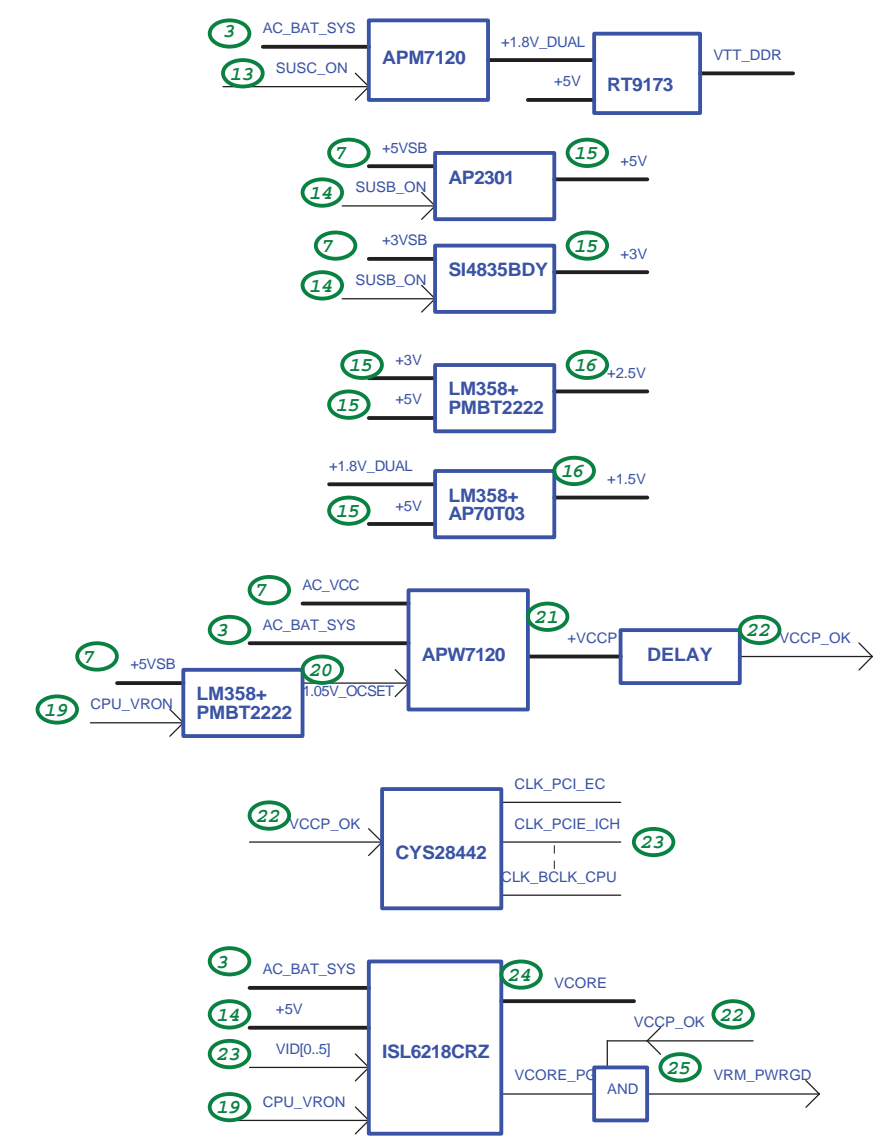
Pin	Pin Name	Connect to	Type	Input/Output Set
B7	GPIO/REQ6#	10K Pull +3V	I	fixed as Input only
E8	GP11 / REQ5#	10K Pull +3V	I	fixed as Input only
D9	GP12 / PIRQE#	10K Pull +3V	I	fixed as Input only
C7	GP13 / PIRQF#	10K Pull +3V	I	fixed as Input only
C6	GP14 / PIRQG#	10K Pull +3V	I	fixed as Input only
M3	GP15 / PIRQH#	10K Pull +3V	I	fixed as Input only
AD19	GP16 / BMBUSY#	NB BMBUSY#	I	Input
AE19	GP17	NC	GPI	fixed as Input only
R1	GP18	EC KBC_SCI#	GPI	fixed as Input only
C23	GP19/OC4#	10K Pull +3V	I	Input
D23	GP110/OC5#	10K Pull +3V	I	Input
W6	GP111 / SMBALERT#	10K Pull +3V	I	Input
M2	GP112	NC	GPI	fixed as Input only
R6	GP113	EC EXTSMI#	GPI	fixed as Input only
C25	GP114/OC6#	10K Pull +3V	I	Input
C24	GP115 /OC7#	10K Pull +3V	I	Input
D8	GPO16/GTN6#	NC	O	Output
F6	GPO17 / GNT5#	NC	O	Output
AC21	GPO18 / STP_PC#	Clock GEN STP_PC#	O	Output
AB21	GPO19	WLAN_LED#	GPO	fixed as Output only
AD22	GPO20 / STP_CPU#	STP_CPU#	O	Output
AD20	GPO21	NC	GPO	fixed as Output only
NA	GPIO22	NA	NA	NA
AD21	GPO23	NC	GPO	fixed as Output only
V3	GPIO24	WLAN	I/O	Output
P5	GPIO25	NC	I/O	Output

Pin	Pin Name	Connect to	Type	Input/Output Set
AF17	GP126/SATA0GP	NC	GPI	(GPI)Input
R3	GPIO27	NC	I/O	Output
T3	GPIO28	NC	I/O	Output
AE18	GP129 / SATA1GP	PCBVER0	GPI	(GPI)Input
AF18	GP130 / SATA2GP	NC	GPI	(GPI)Input
AG18	GP131 / SATA3GP	PCBVER1	GPI	(GPI)Input
AF19	GPIO32 / CLKRUN#	10K Pull +3V	I/O	Input
AF20	GPIO33	NC	I/O	Output
AC18	GPIO34	NC	I/O	Output
NA	GPIO35	NA	NA	NA
NA	GPIO36	NA	NA	NA
NA	GPIO37	NA	NA	NA
NA	GPIO38	NA	NA	NA
NA	GPIO39	NA	NA	NA
F7	GP140 / REQ4#	10K Pull +3V	I	Input
P4	GP141 / LDRQ1#	NC	I	Input
NA	GPIO42	NA	NA	NA
NA	GPIO43	NA	NA	NA
NA	GPIO44	NA	NA	NA
NA	GPIO45	NA	NA	NA
NA	GPIO46	NA	NA	NA
NA	GPIO47	NA	NA	NA
E7	GPO48 / GNT4#	NC	O	Output
AC25	GPO49 / CPUPWRGD	CPU Power Ok	O	Output

*This sequence is for Battery Plug-in and no Adapter,
if Adapter Plug-in, the sequence change to:
A/D_DOCK_IN-->AC_BAT_SYS-->+3VA-->VSUS_ON-->+3VSB & +5VSB
-->VSUS_GD-->PM_REMRST#-->PWR_SW#-->PM_PWRBTN-->PM_SUSC#-->PM_SUSB#



	Signal	S0/S1	S3	S4/S5	Power
Only Battery	VSUS_ON	H	H	L	VSUS
Adapter In	VSUS_ON	H	H	H	VSUS
	SUSB_ON	H	L	L	Main
	SUSC_ON	H	H	L	DUAL



EC KB3310 GPIO SETTING

Pin No.	Pin Name	Signal Name	Type	NOTE
1	GA20	A20GATE	O	A20GATE
2	KBRST#	RC_IN#	O	KBRST#
6	GPIO04	CTRL_CAMER_PWR	I	Default : High
13	PCIRST#	PCI_RST#	I	PCI Reset
14	GPIO07	N.C	O	Reserved
15	GPIO08	EXTSMH#	O	EXTSMH#, 10K Pull +3VSUS
16	GPIO0A	LID_EC#	I	LID_EC#, *
17	GPIO0B	LCD_CSB	O	LCD chip select
18	GPIO0C	LCD_SDA	I/O	LCD Data
19	GPIO0D	DISTP_SW#	I	Touch Pad Disabled,*
20	SC#	KBC_SC#	O	KBC_SC#, 10K Pull +3VSUS
21	PWM1	BL_PWM_DA	O	LCD Light Switch
23	PWM2	LCD_SCL	O	LCD clock
25	GPIO11	PM_PWRBTN#	OD	Power Button to SB,*
26	FANPWM1	FAN0_PWM	O	CPU Fan(Unused)
27	FANPWM2	FAN1_PWM	O	VGA Fan(Unused)
28	FANFB1	FAN0_TACH	I	CPU FanTach(Unused)
29	FANFB2	FAN1_TACH	I	VGA FanTach(Unused)
30	GPIO16	E51_TX	O	RS232 debug port
31	GPIO17	N.C	O	Reserved
32	GPIO18	PWR_SW#	I	power button,*
34	GPIO19	MAIL_LED#	O	Mail LED(Unused)
36	GPIO1A	CTRL_Mincard_PWR	O	Default : High
38	CLKRUN#	N.C	O	Reserved
39	KSO0	KSO0	O	For Keyboard interface
40	KSO1	KSO1	O	For Keyboard interface
41	KSO2	KSO2	O	For Keyboard interface
42	KSO3	KSO3	O	For Keyboard interface
43	KSO4	KSO4	O	For Keyboard interface
44	KSO5	KSO5	O	For Keyboard interface
45	KSO6	KSO6	O	For Keyboard interface
46	KSO7	KSO7	O	For Keyboard interface
47	KSO8	KSO8	O	For Keyboard interface
48	KSO9	KSO9	O	For Keyboard interface
49	KSO10	KSO10	O	For Keyboard interface
50	KSO11	KSO11	O	For Keyboard interface
51	KSO12	KSO12	O	For Keyboard interface
52	KSO13	KSO13	O	For Keyboard interface
53	KSO14	KSO14	O	For Keyboard interface
54	KSO15	KSO15	O	For Keyboard interface
55	KSI0	KSI0	I	For Keyboard interface
56	KSI1	KSI1	I	For Keyboard interface
57	KSI2	KSI2	I	For Keyboard interface
58	KSI3	KSI3	I	For Keyboard interface
59	KSI4	KSI4	I	For Keyboard interface
60	KSI5	KSI5	I	For Keyboard interface
61	KSI6	KSI6	I	For Keyboard interface
62	KSI7	KSI7	I	For Keyboard interface
63	AD0	P_PMON_10	I	Sense Power Loading
64	AD1	BAT_IN	I	sense Battery
65	AD2	N.C	I	Reserved
66	AD3	N.C	I	Reserved
68	GPO3C	DOC	O	Trigger Clock Gen

Pin No.	Pin Name	Signal Name	Type	NOTE
70	GPO3D	LCD_BACKOFF#	O	LCD_BACKOFF#
71	GPO3E	CLK_PWRSERVE#	O	Active when BAT_IN=1 and AC_OK=0(Unused)
72	GPO3F	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K Pull GND
75	GPIO42	N.C	O	Reserved
76	GPIO43	N.C	O	Reserved
77	SCL1	SMB0_CLK	I/OD	4.7K Pull +3VA_EC
78	SDA1	SMB0_DAT	I/OD	4.7K Pull +3VA_EC
79	SCL2	SMB1_CLK	I/OD	10K Pull +3VS
80	SDA2	SMB1_DAT	I/OD	10K Pull +3VS
81	KSO16	N.C	O	Reserved
82	KSO17	N.C	O	Reserved
83	PSCLK1	N.C	O	Reserved
84	PSDAT1	N.C	O	Reserved
85	PSCLK2	N.C	O	Reserved
86	PSDAT2	N.C	O	Reserved
87	PSCLK3	TP_CLK	I/OD	10K Pull +3VS
88	PSDAT3	TP_DAT	I/OD	10K Pull +3VS
89	GPIO50	BATSEL_3S	O	Battery series, Hi:3S, Lo:4S(Unused)
90	GPIO52	CHG_LED_UP#	O	charger LED
91	GPIO53	CTRL_L2_PWR	O	Default : High
92	GPIO54	PWR_LED_UP	O	EC H/W blinking
93	GPIO55	SCRL_LED#	O	EC H/W controls
95	GPIO56	PWR4G_SW#	I	*
97	GPXOA00	SPI_MODE#	O	*HW Strap for SPI Flash deExternal Pull Down 100K ohm to GND"
98	GPXOA01	SUSC_ON	O	
99	GPXOA02	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	ICH8_PWROK	O	
103	GPXOA06	N.C	O	Reserved
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0	BATSEL_2P#	O	Battery parallel, Hi:1P, Lo:2P-3P
110	GPXID1	N.C	O	Reserved
112	GPXID2	THRO_CPU	O	Active if Battery Temperature is Pull Down 100K ohm to GND
114	GPXID3	SUSB#	I	
115	GPXID4	SUSC#	I	Pull Down 100K ohm to GND
116	GPXID5	CPUPWR_GD	I	10K Pull +3VS
117	GPXID6	VSUS_GD	I	Disabled **
118	GPXID7	N.C	O	Reserved
121	GPIO57	INTERNET#	I	*
126	SPICLK	SPI_CLK	O	SPI Clock
127	GPIO59	N.C	O	Reserved

EC KB3310 Other Pin SETTING


Pin No.	Pin Name	Signal Name	Type	NOTE
3	SERIRQ	INT_SERIRQ	I/OD	8.2K Pull +3VS
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	Add 100K ohm to GND
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#	SPI_SO	I	
120	WR#	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	K_V18R		Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#	SPI_CE#	O	

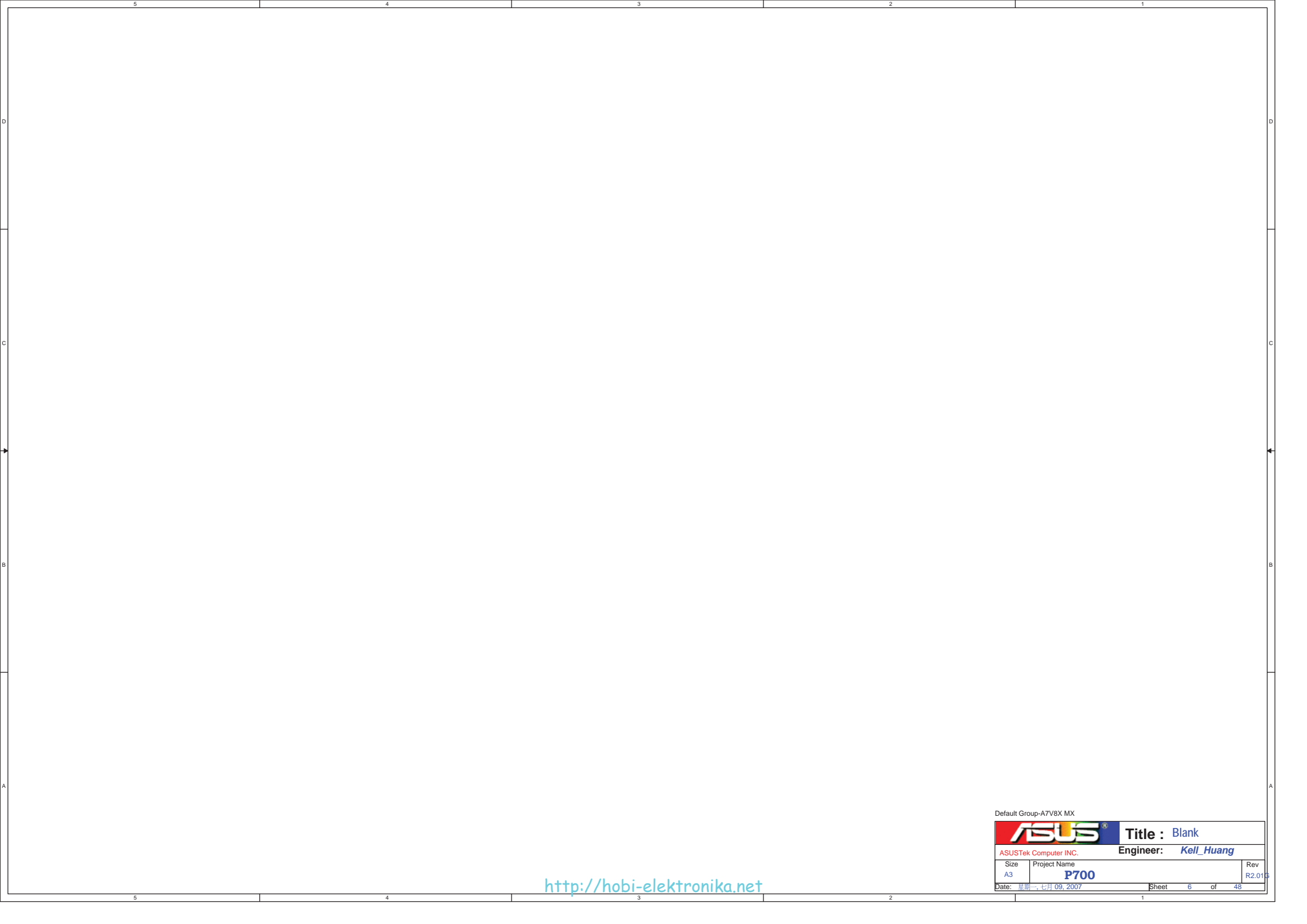
CIRCUIT UPDATED HISTORY

Rev	Date	Description
1.0G	2007/02/26 ↓	S701L Schematic 1.0G Beginning
	2007/03/16 ↓	S701L 1.0G Gerber Out
1.1G	2007/03/24 ↓	S701L Schematic 1.1G Beginning
	2007/04/19 ↓	S701L 1.1G Gerber Out
1.2G	2007/04/24	<p>P701(S701L renamed) Schematic 1.0G Beginning</p> <ol style="list-style-type: none">1. PC8054, PR6075 /X to N/A2. Attansic L2 change to Atheros L2(pin to pin)3. LC1, LC33 /CAP/X to N/A4. C87 change to X5R to cost down5. L1, L2, L3 change to 56 NH, R5, R6 change to 75 Ohm to pass CRT EA measure6. PR48 change to 22K Ohm, PC35 change to 4700PF to fix no VCORE issue7. PR6074 change to 4.7K Ohm to fix +3VSB OCP issue8. Clock Gen CY28442-2 change to ICS9LPR3679. Phase in Power Level Reduce solution, mark "Taipei0508"10. Card Reader Socket change to SD Socket 12G25100091E11. Add System FAN circuit12. Camera change to USB port 7, Minicard change to USB port 513. Use SB GPIO27 to Enable/Disable Card Reader UB6225P14. Use SB GPIO28 to Enable/Disable Modem15. Stuff CC33, CC34, CC35, CC36, CC37, CC38 for EMI16. Card Reader UB6225P share 48M clock from CLock Gen with SB USB part17. Add D29 to fix LCD_CSB leakage current issue18. LC29, LC30 change to 27PF to pass EA crystal measure19. Change vaule of PR73, PR74, PC56 and add PC60 to adjust the power sequence timing between Stand By power and RSMRST#20. Remove USB port 121. Add +5V generate +3V_LCD circuit22. Remove +5V_CHG generate circuit23. Use SB GPIO33, GPIO34 to controll the level of VCORE24. U31 use APL5315BI-TRL to replace MAX8863TEUK(pin to pin, but reference voltage level different)25. PR59 change to 130K Ohm for both 12V Adapter and 9.8V Adapter


Rev	Date	Description

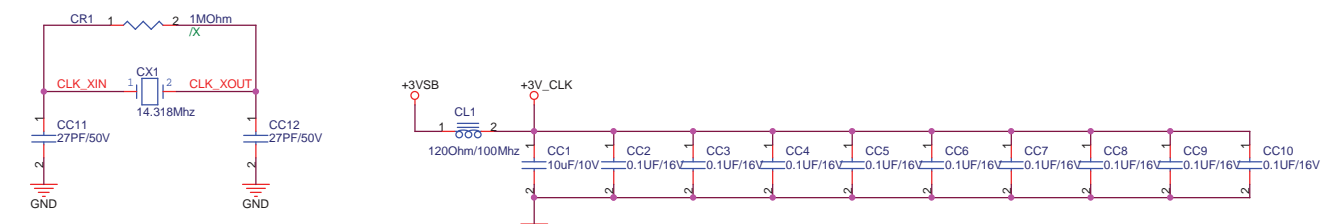
Default Group-A7V8X MX

		Title : History	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name P700	Rev R2.01	
Date: 星期一, 七月 09, 2007		Sheet 5 of 48	

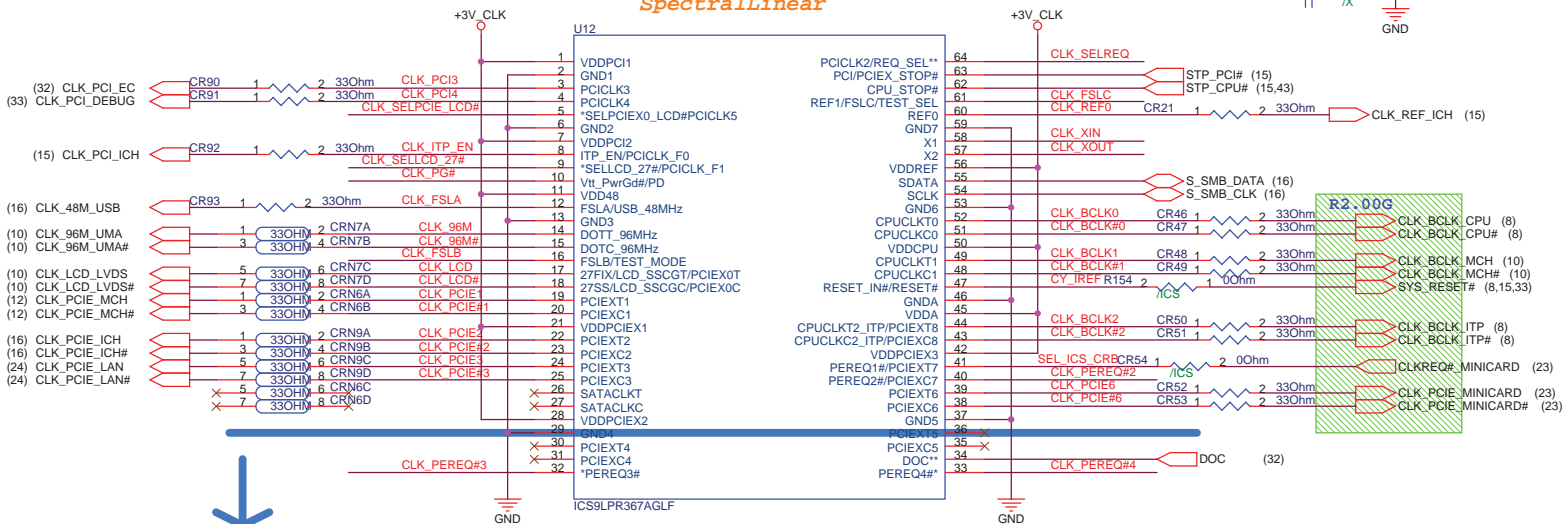


Default Group-A7V8X MX

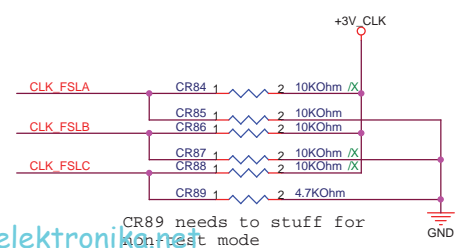
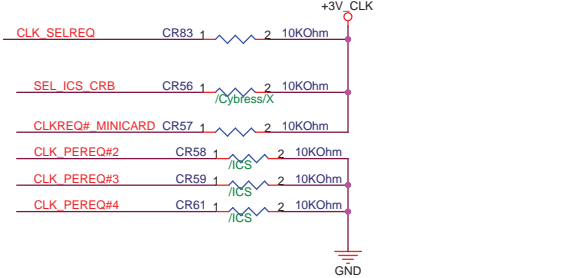
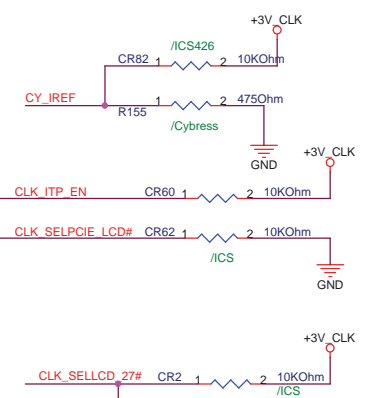
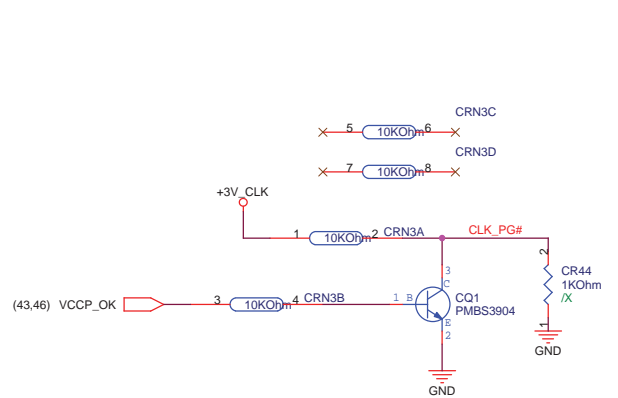
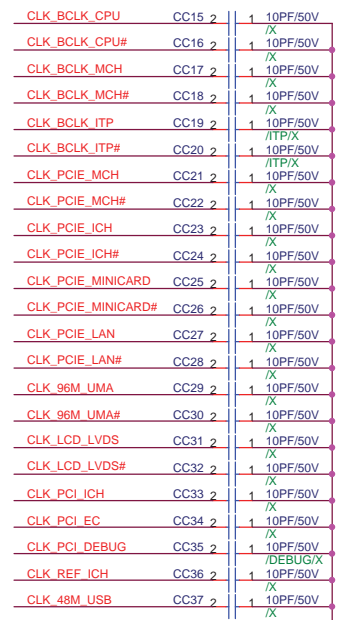
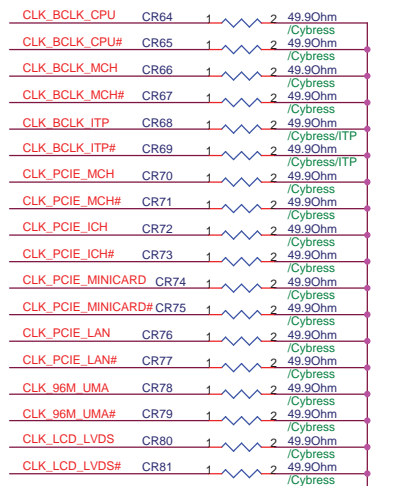
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ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name		Rev
A3	P700		R2.01
Date: 星期一, 七月 09, 2007		Sheet	6 of 48



U12 use
06G011504010 For
SpectralLinear



Pin 29,30,31,32,33,34,35
and 36 for ICS



FS	C	FS	B	FS	A	CPU
1	0	1	1	1	1	100MHz
0	0	1	0	1	1	66.6MHz
0	1	1	1	1	1	83.3MHz
0	1	0	1	1	1	71.4MHz

Default Group-A7V8X MX

Title : Clock Gen_ICS9LPR367

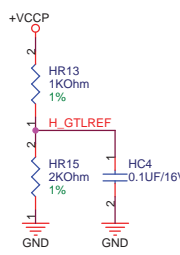
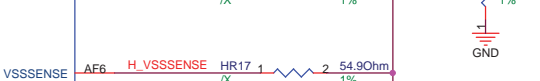
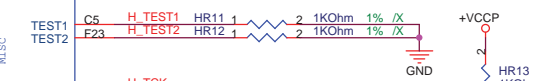
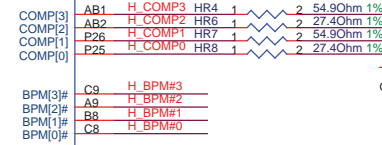
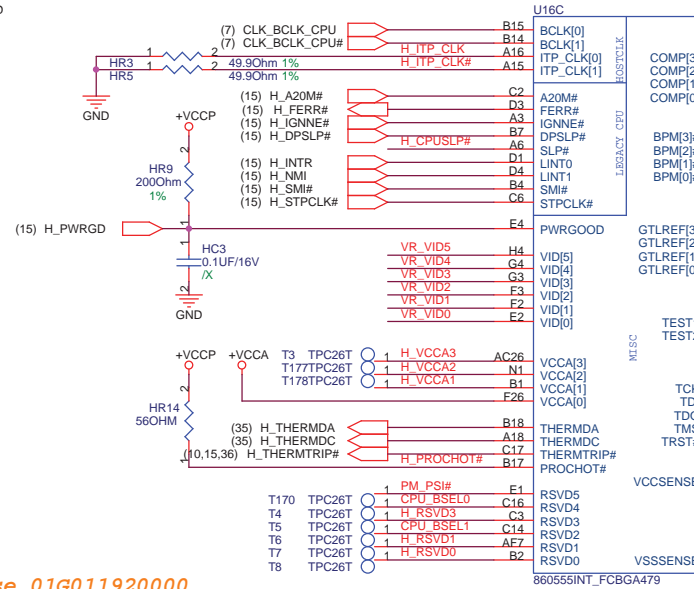
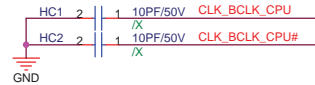
ASUSTek Computer INC. Engineer: **Kell Huang**

Size: A3 Project Name: **P700**

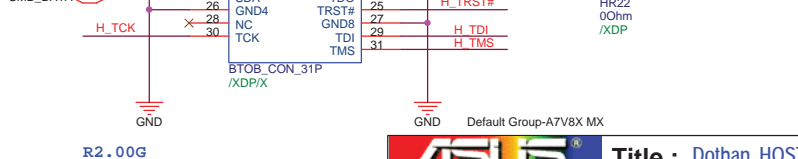
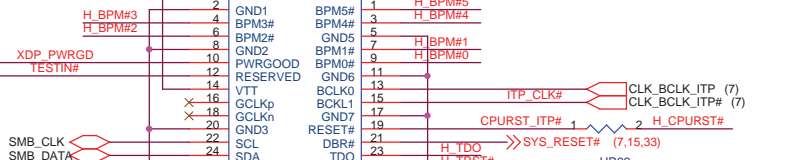
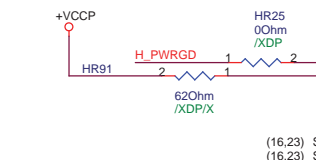
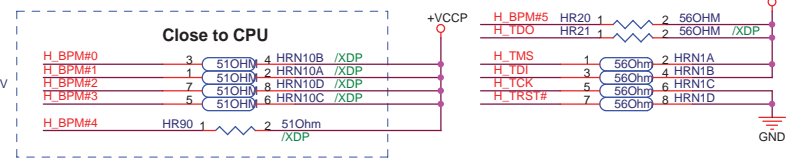
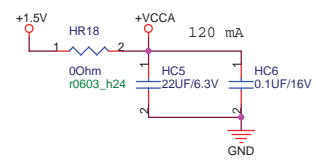
Date: 2007-07-09, 2007 Sheet 7 of 48

<http://hobi-elektronika.net>

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H_DINV#3:0] (10)
H_DSTBN#3:0] (10)
H_DSTBP#3:0] (10)

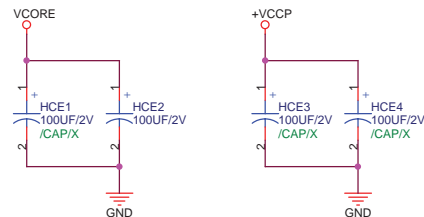
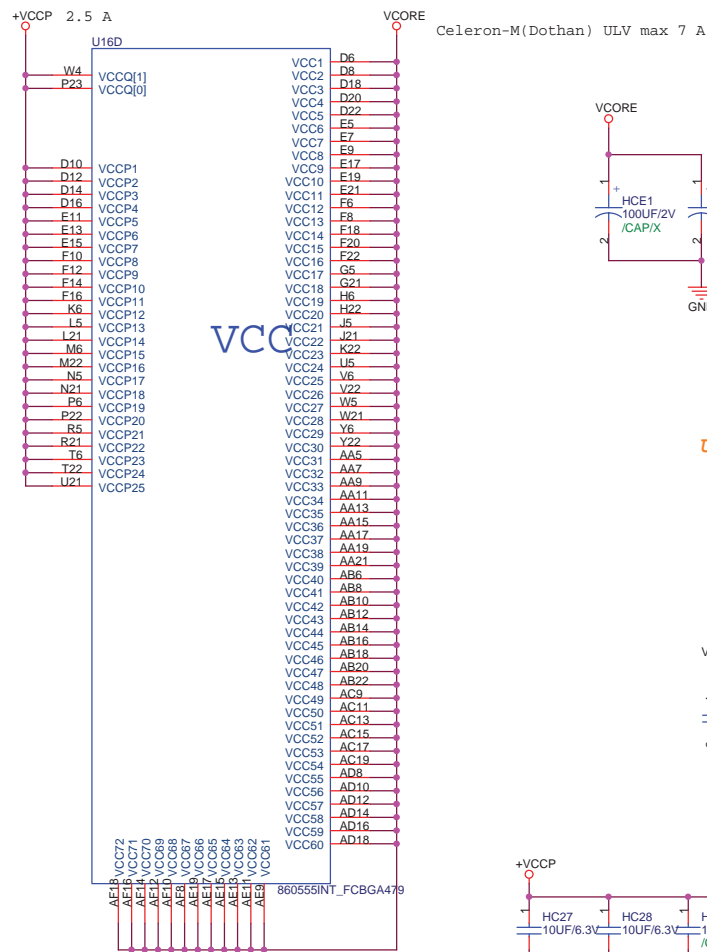


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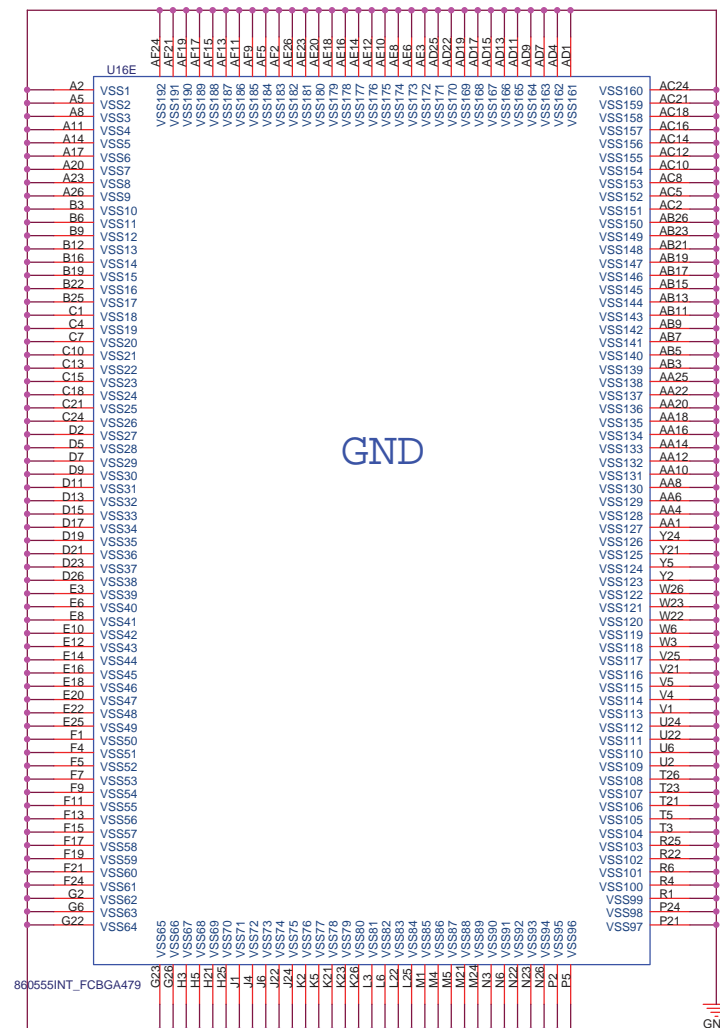
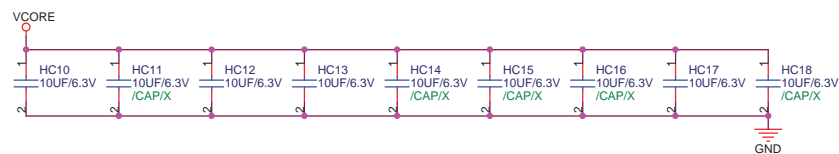
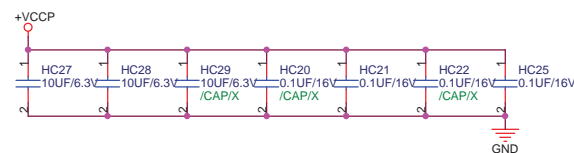
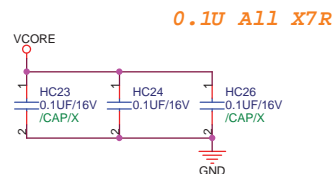


R2.00G

Default Group-A7V8X MX

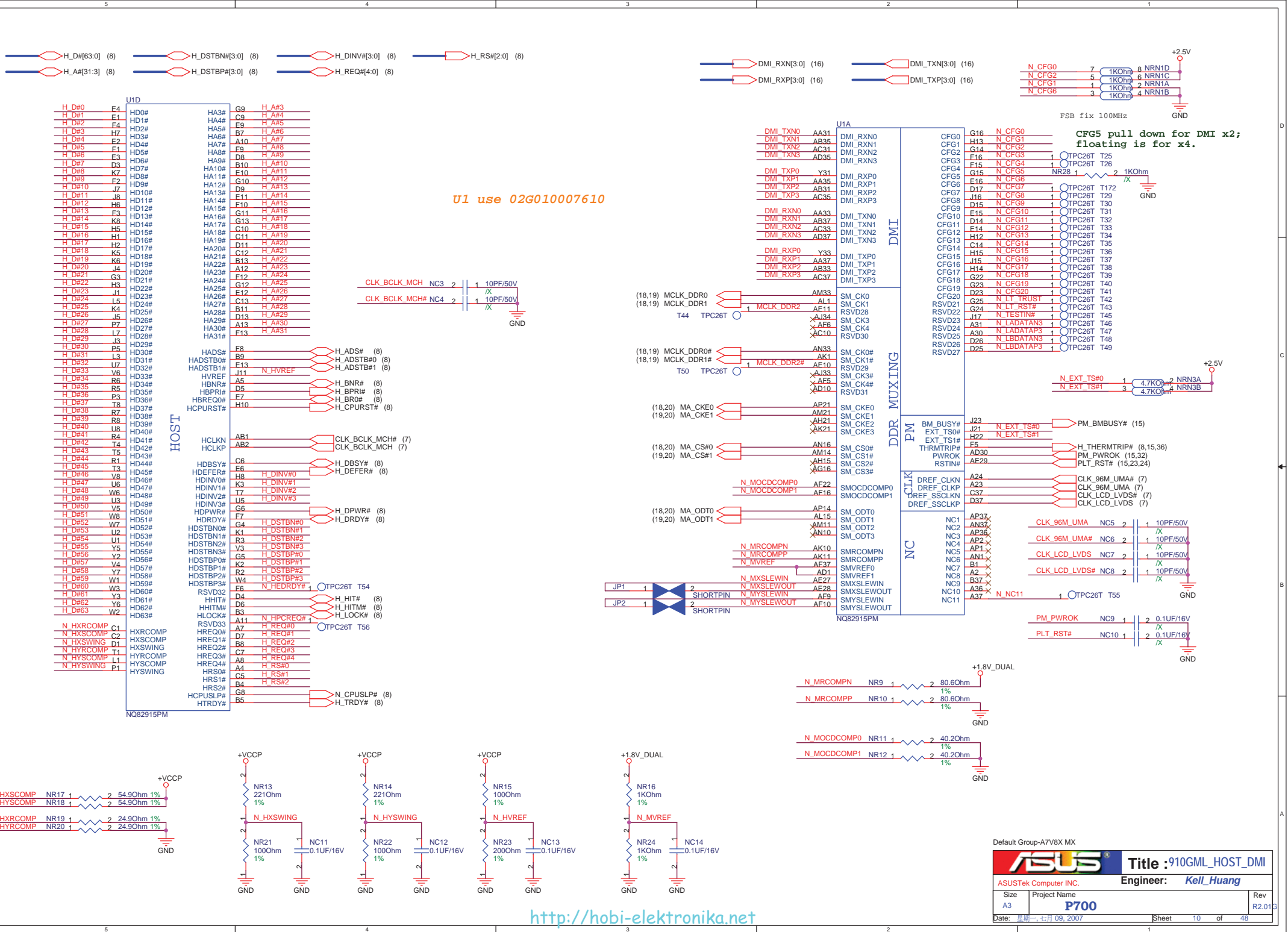


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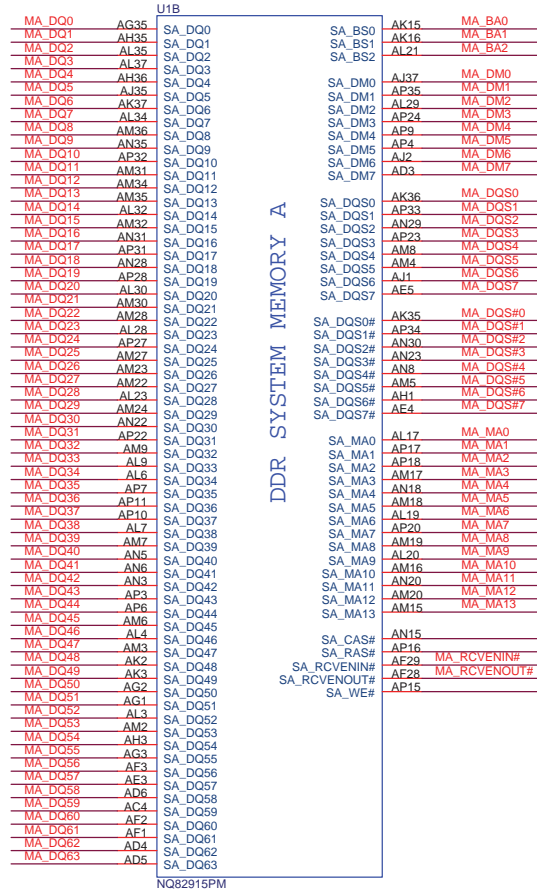
Default Group-A7V8X MX

ASUS		Title : Dothan_PWR_GND	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name P700	Rev R2.01	
Date: 2020-07-09	Sheet 9 of 48		





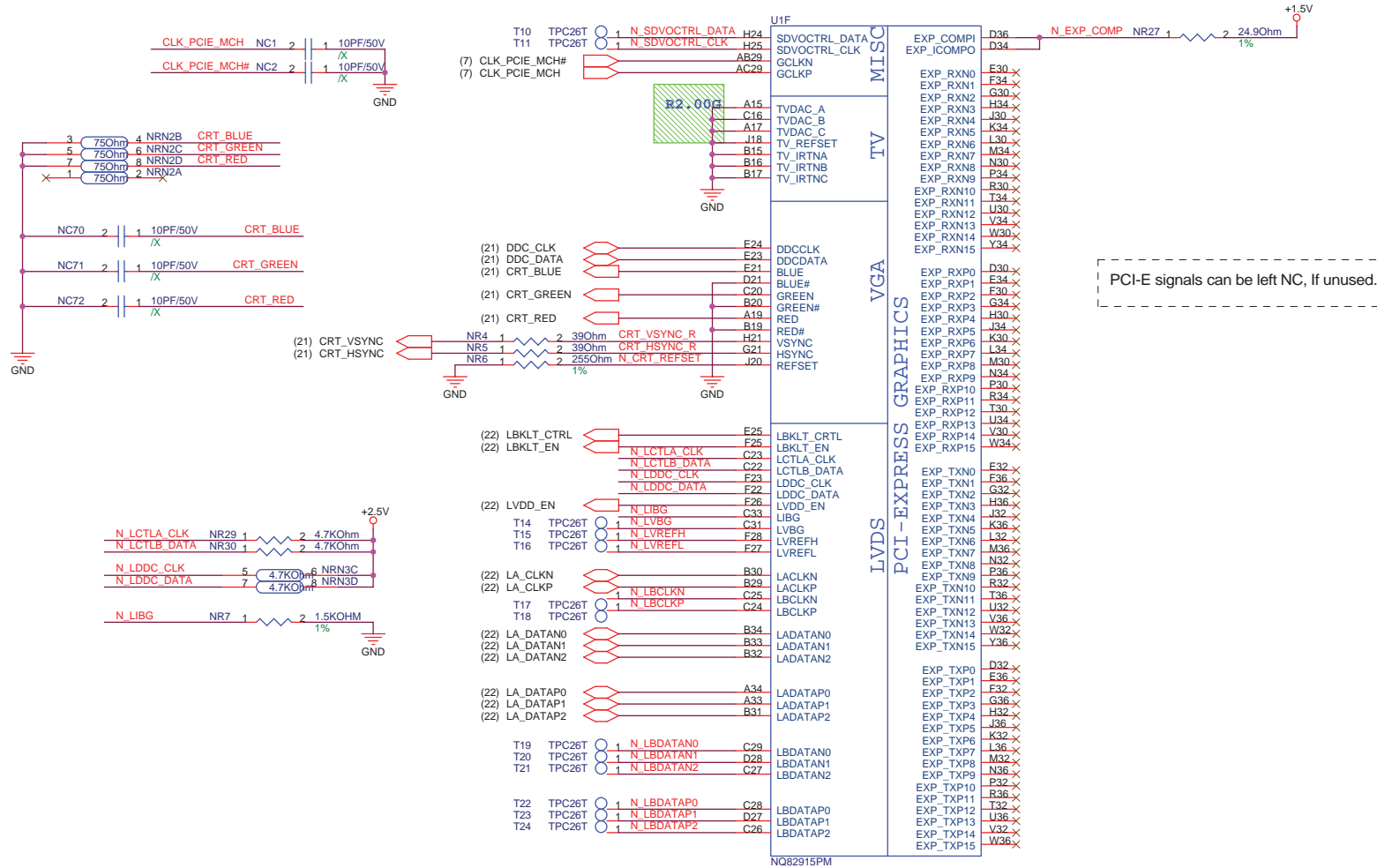
U1 use 02G010007610



SDVO SMBus have
internal pull down

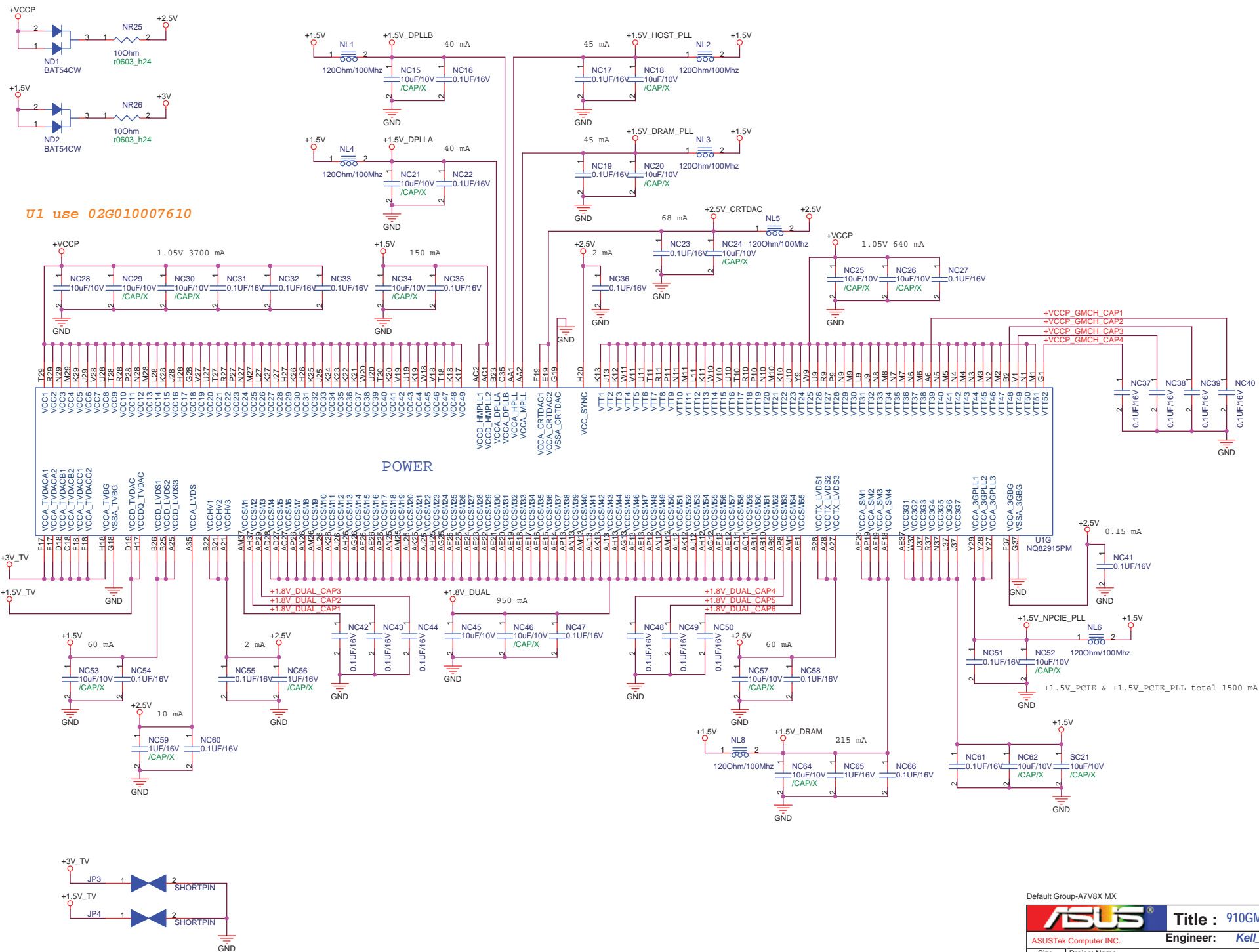
SDVOCTRL_DATA Int PD
0 : No SDVO device
1 : SDVO device present

U1 use 02G010007610



Default Group-A7V8X MX

ASUS		Title : 910GML_VGA_LVDS	
ASUSTek Computer INC.		Engineer: <i>Kell Huang</i>	
Size A3	Project Name P700	Rev R2.01	
Date: 星期一, 七月 09, 2007		Sheet	12 of 48



U1 use 02G010007610

POWER

U1 use 02G010007610

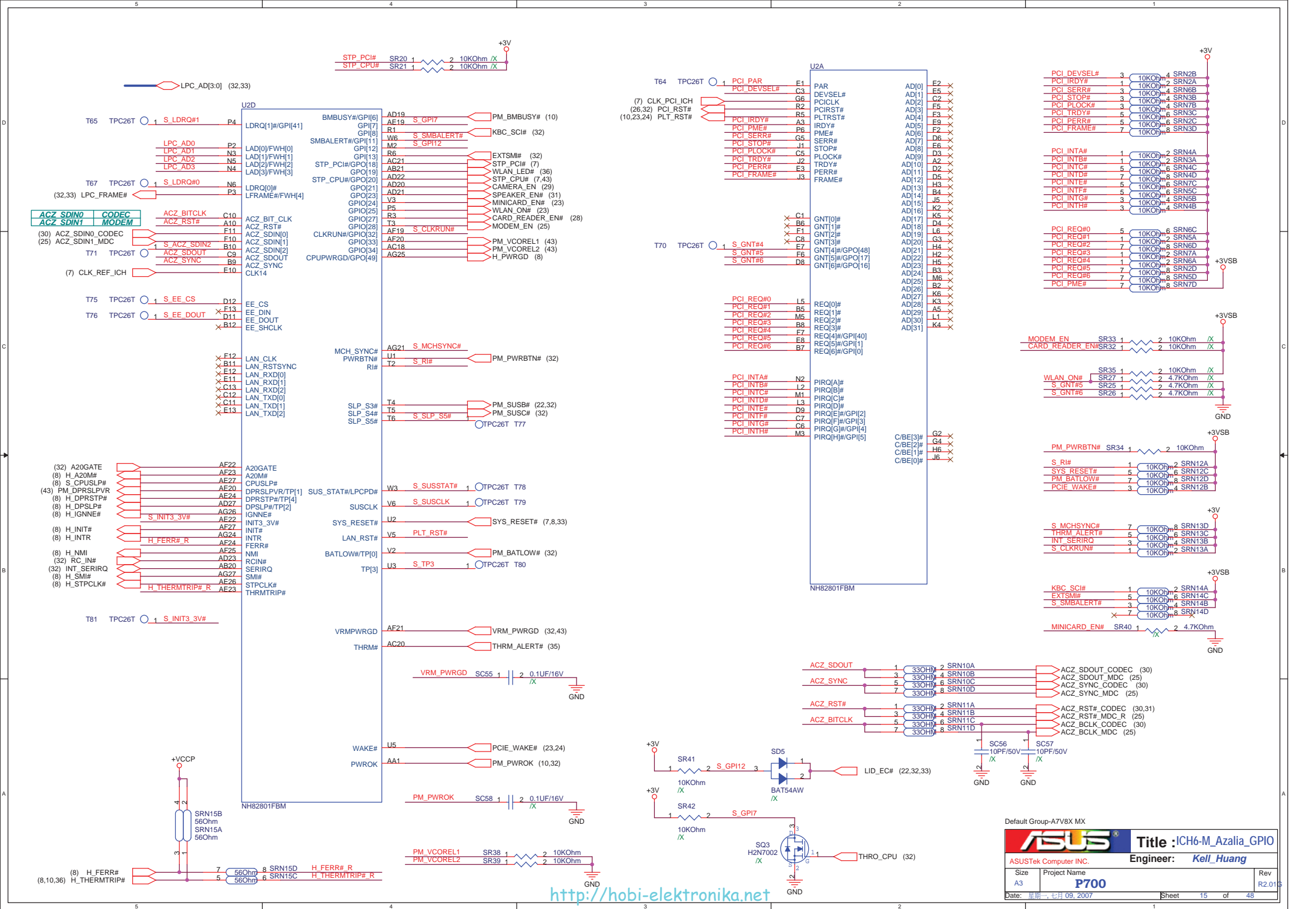
VSS

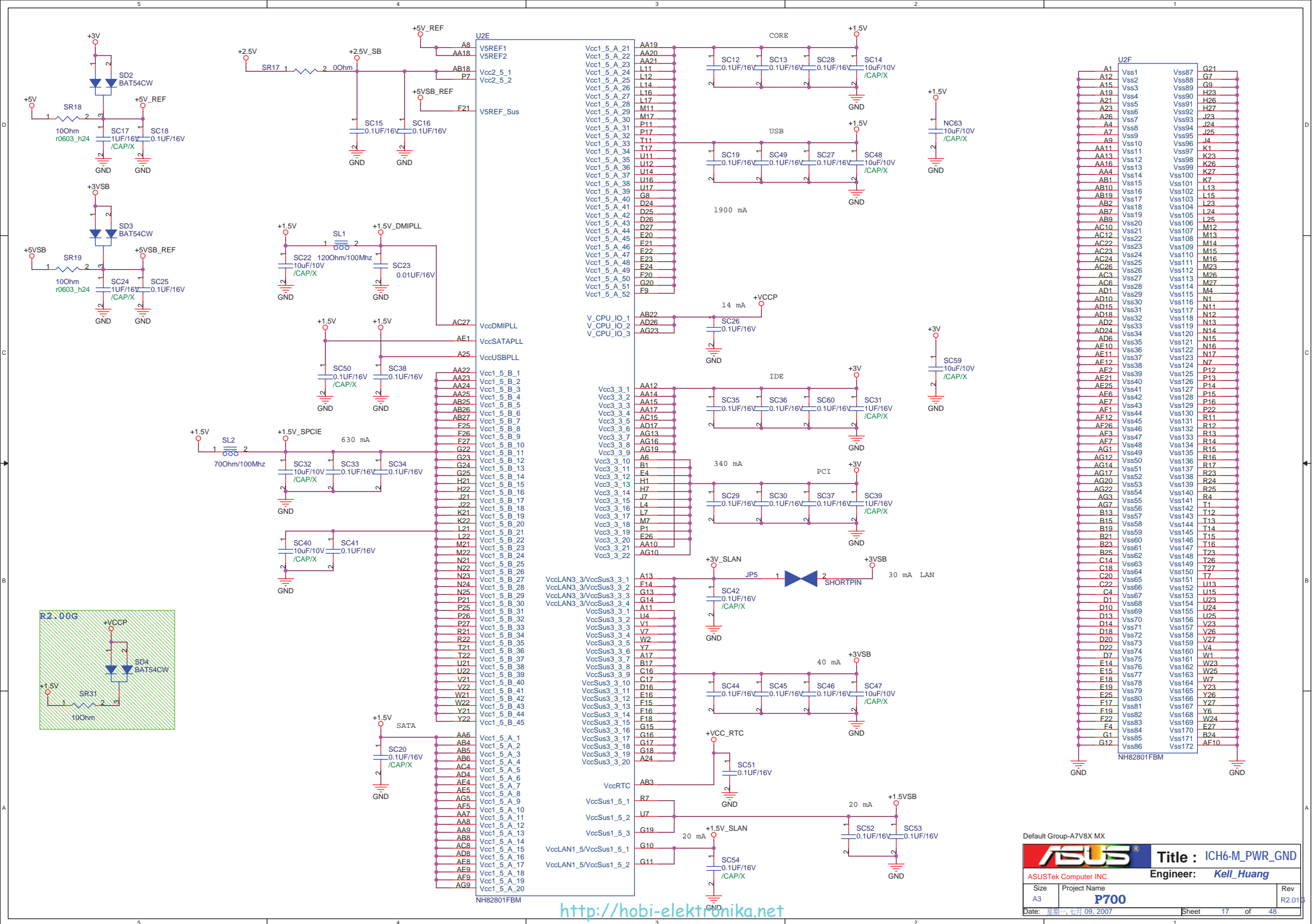
NCTF

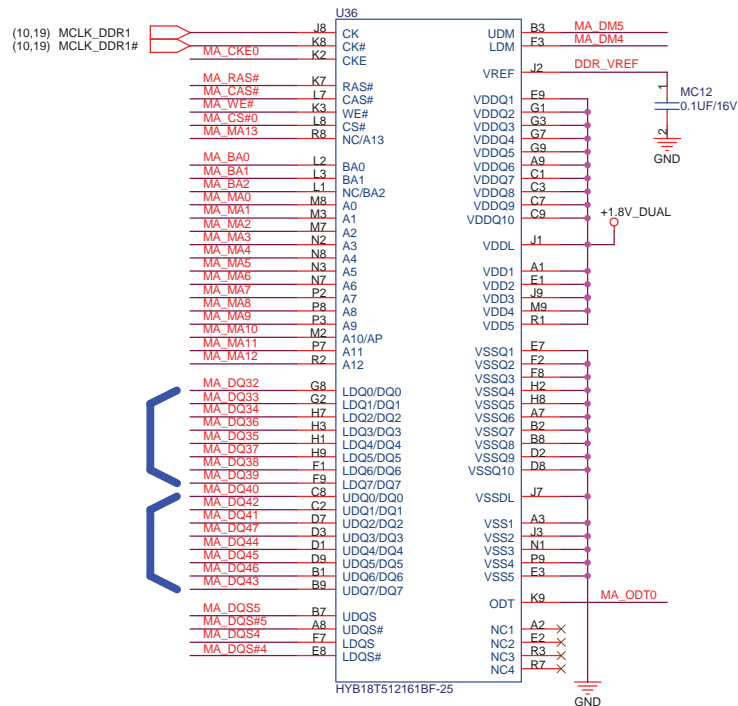
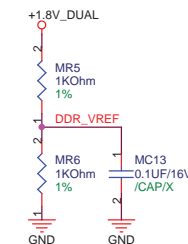
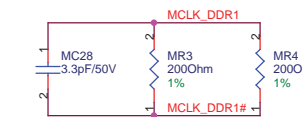
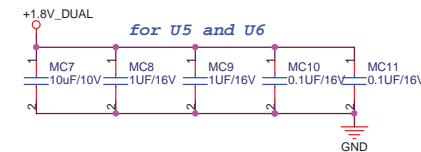
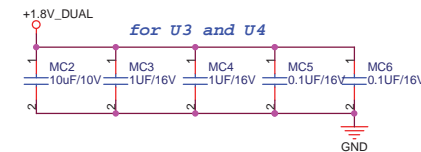
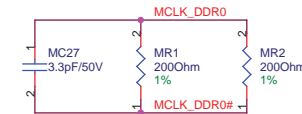
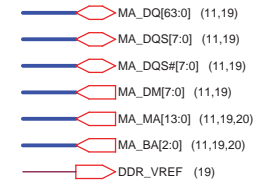
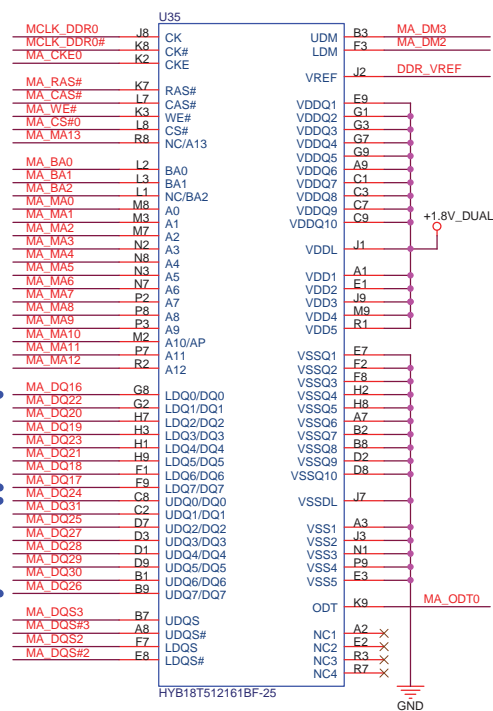
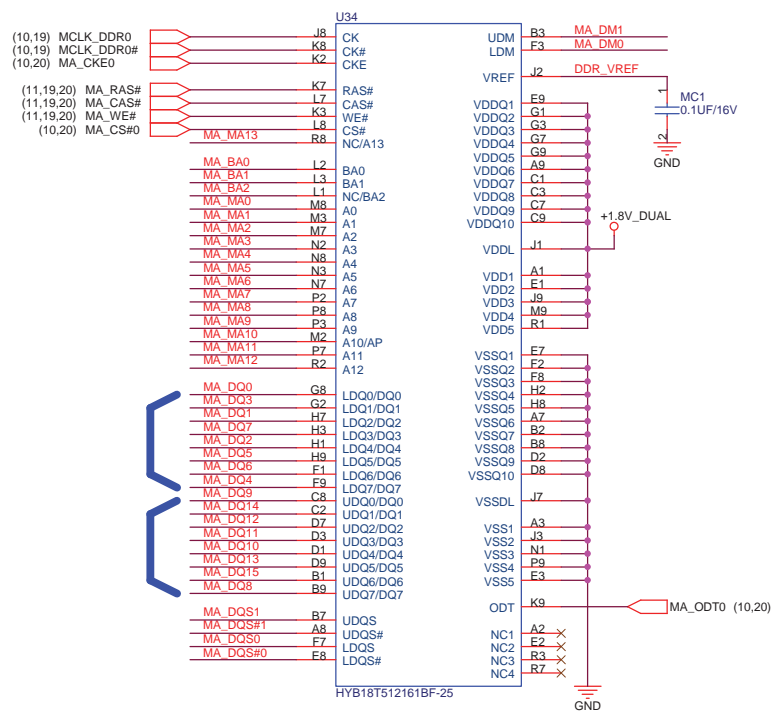
Default Group-A7V8X M2

ASUS		Title : 910GML_GND	
ASUSTek Computer INC.		Engineer: Kell Huang	
Size	Project Name		Rev
A3	P700		R2.015
Date: 2007-07-09	Sheet 14 of 48		

http://hobi-elektronika.net

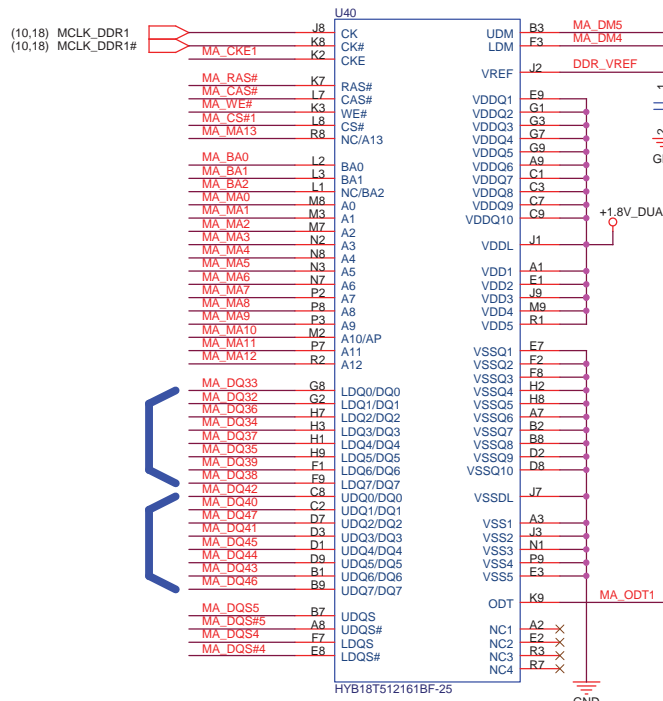
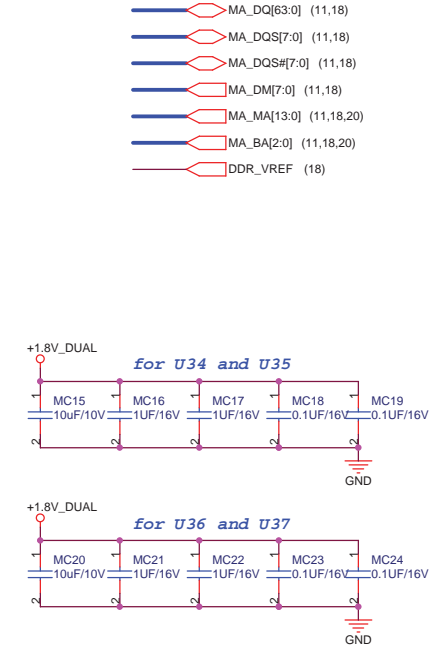
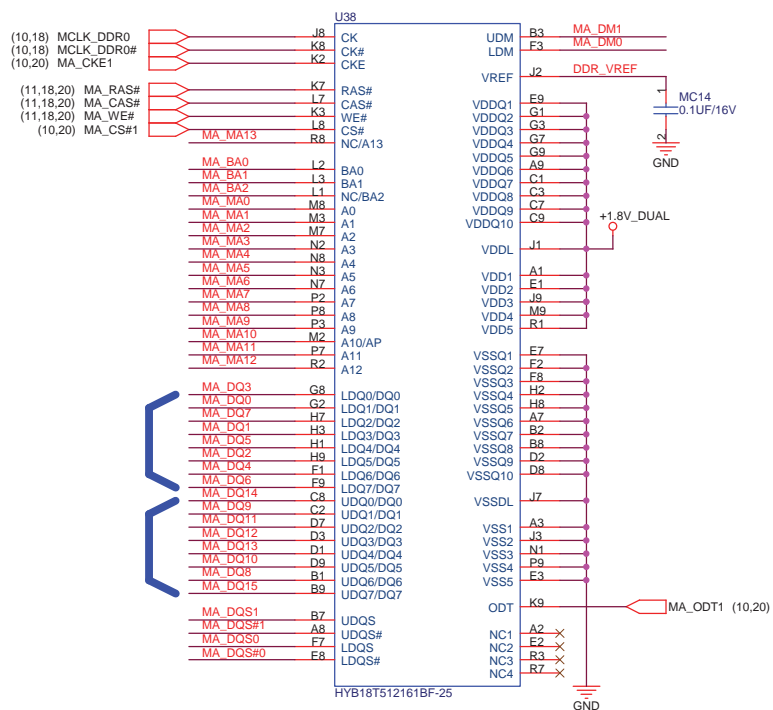






Default Group-A7V8X MX

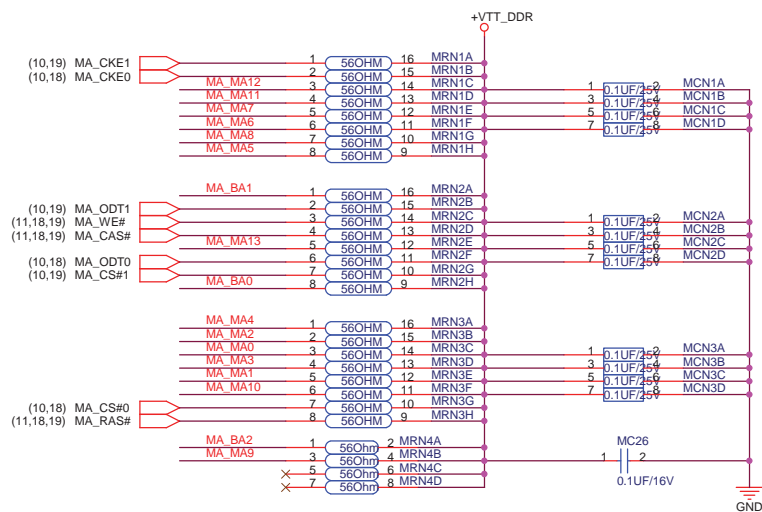
ASUS		Title : Onboard DRAM_1	
ASUSTek Computer INC.		Engineer: TylerYuan	
Size A3	Project Name P700	Rev R2.01	
Date: 2007-07-09	Sheet 18 of 48		

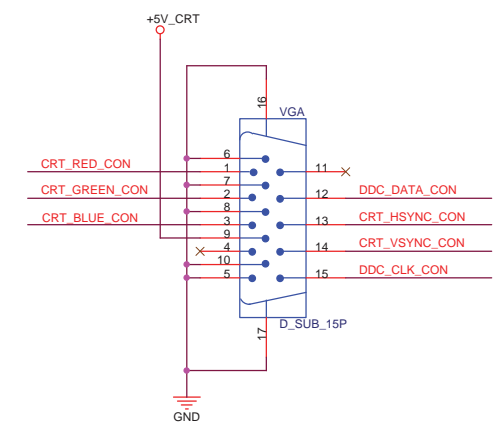
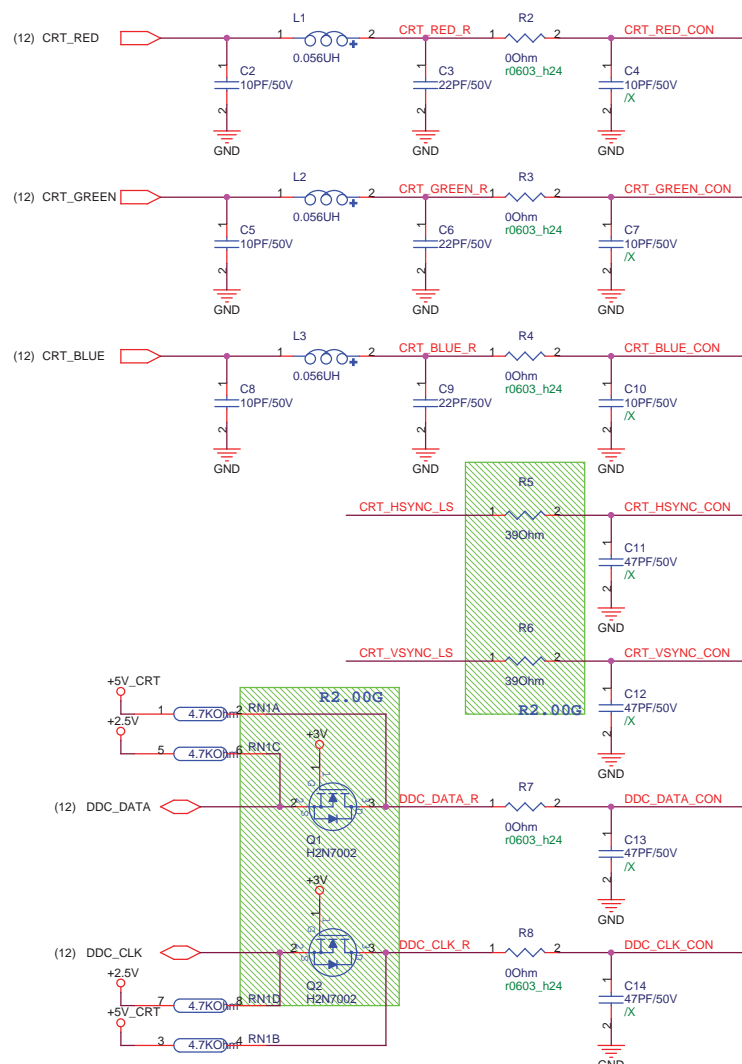


Default Group-A7V8X MX

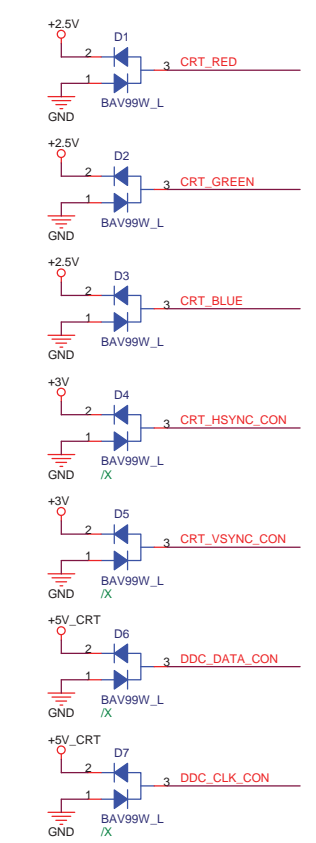
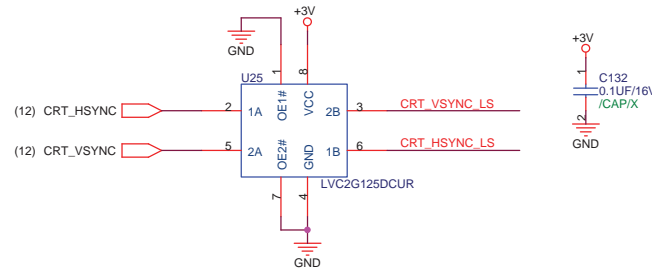
ASUS		Title : Onboard DRAM_2	
ASUSTek Computer INC.		Engineer: TylerYuan	
Size A3	Project Name P700	Rev R2.01	
Date: 星期日, 七月 09, 2007		Sheet	19 of 48

MA_MA[13:0] (11,18,19)
MA_BA[2:0] (11,18,19)

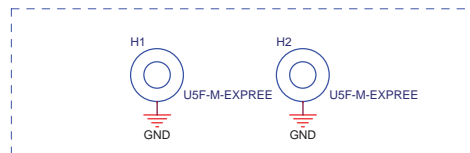
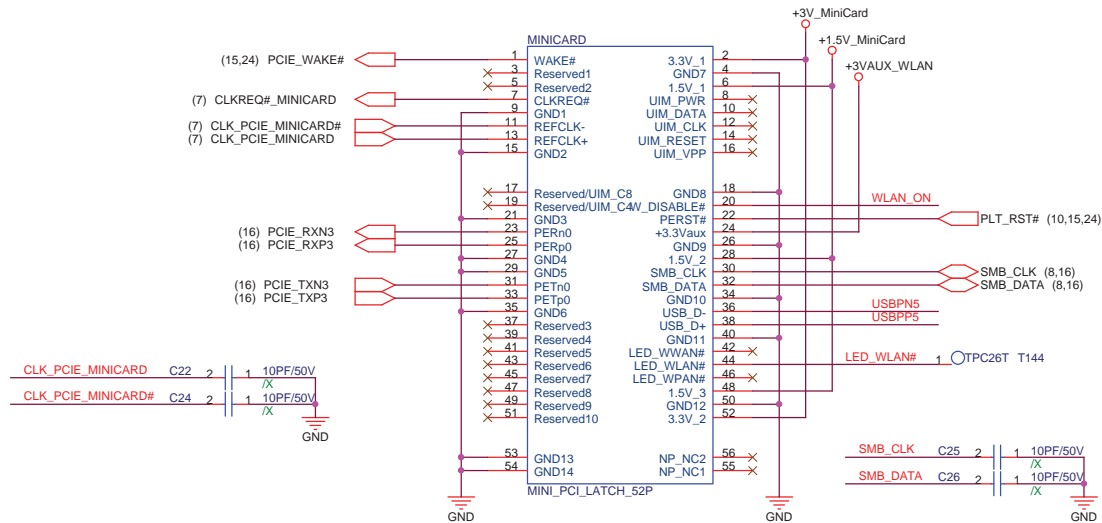




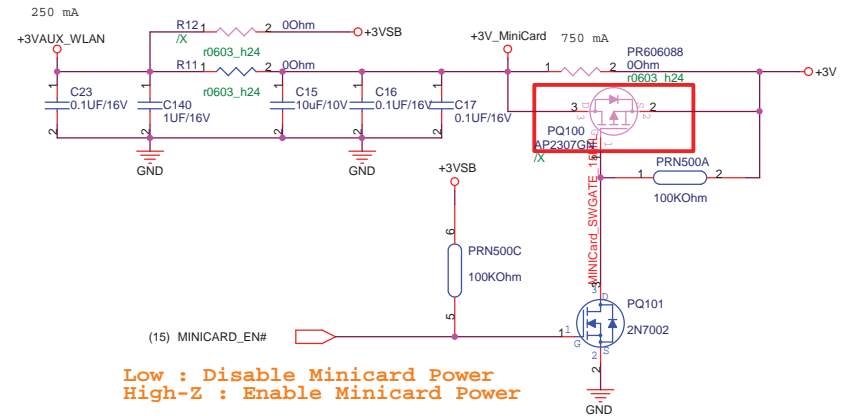
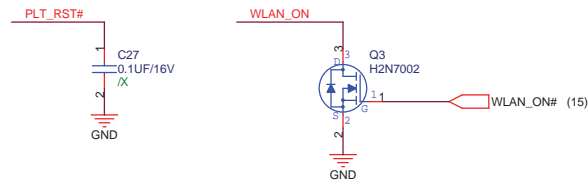
VGA use 12G10110015W or 12G10110015N



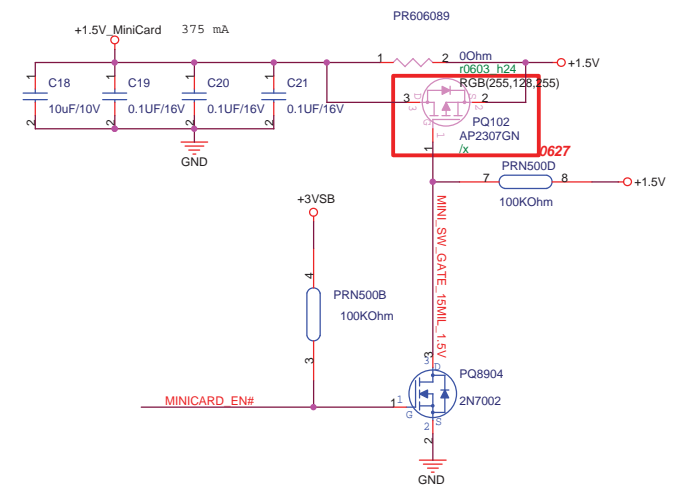
MINICARD use 12G03010052Q

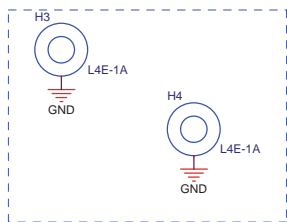


MINI CARD NUT(1.6mm) *2

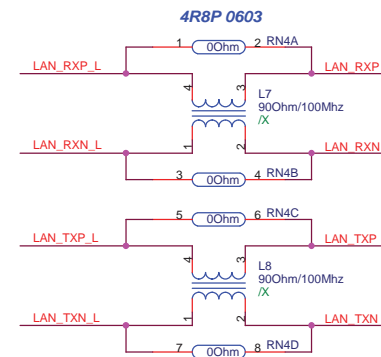
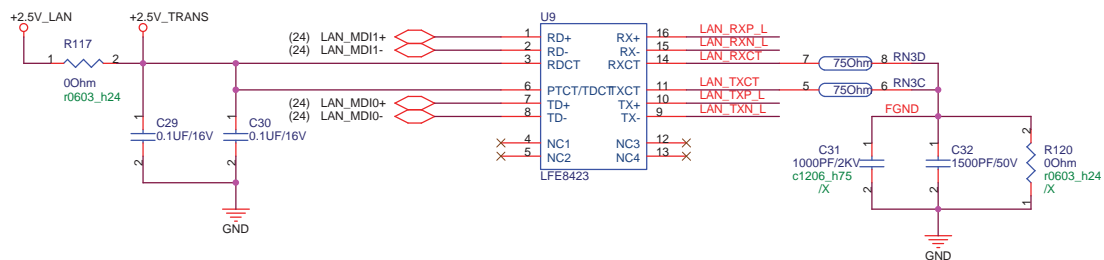
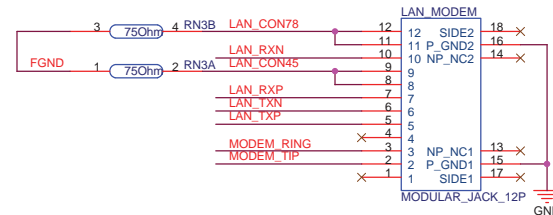
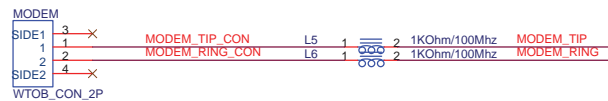
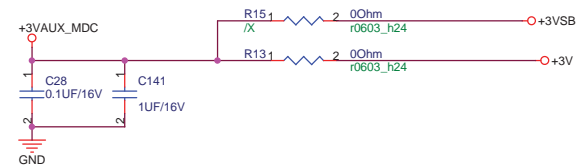
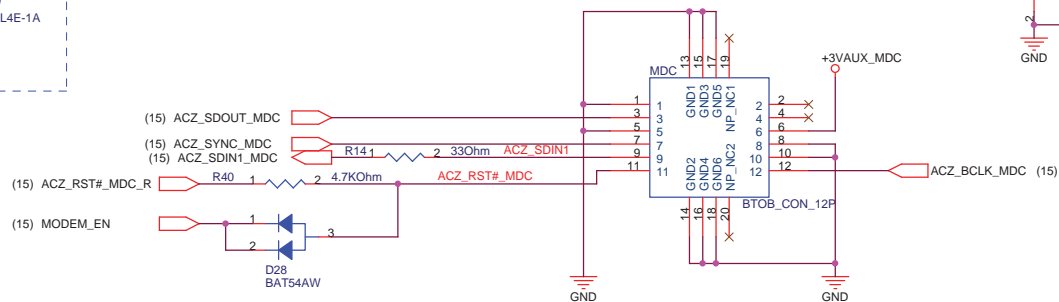


Low : Disable Minicard Power
High-Z : Enable Minicard Power



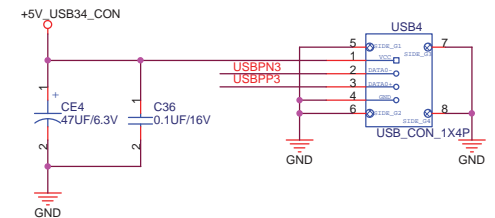
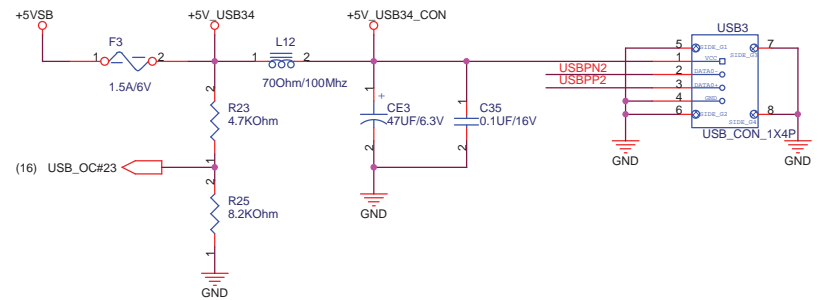
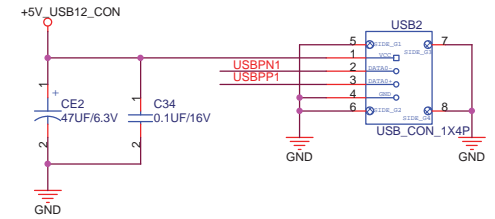
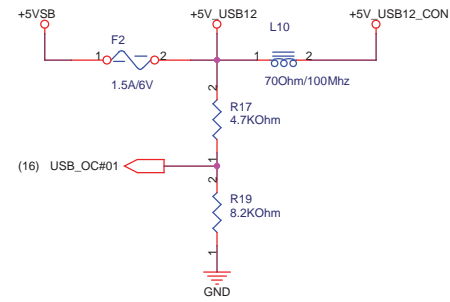
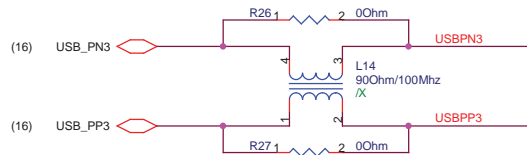
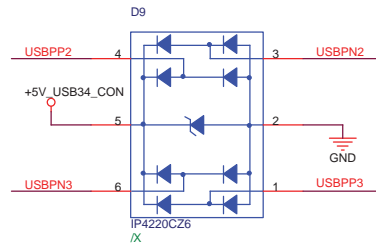
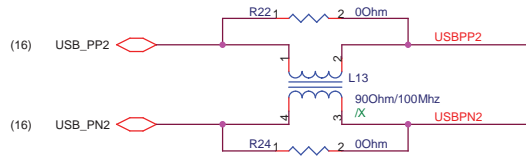
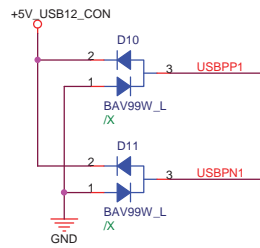
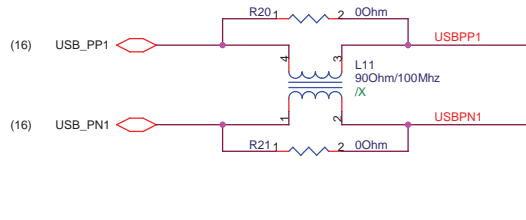


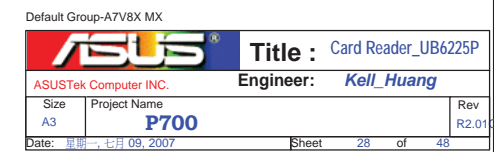
MODEM NUT(3.0mm) *2

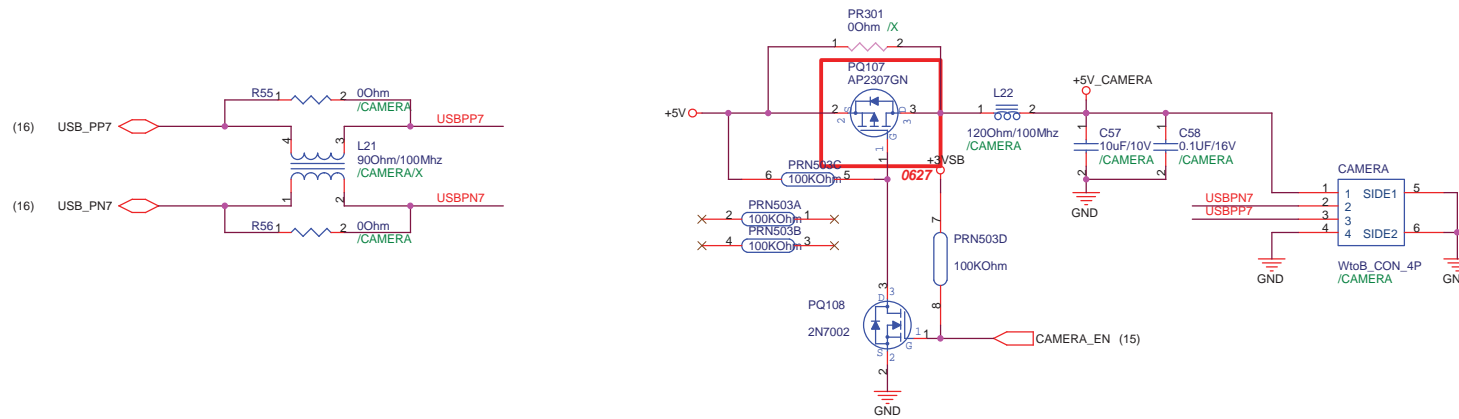


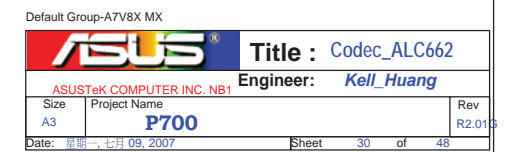
Default Group-A7V8X MX

ASUS		Title : MDC_RJ11_RJ45	
ASUSTek Computer INC.		Engineer: Kell Huang	
Size A3	Project Name P700	Rev R2.01	
Date: 星期一, 七月 09, 2007	Sheet 25 of 48		

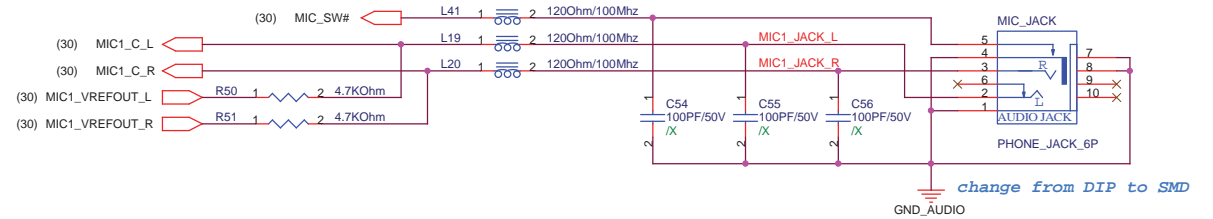
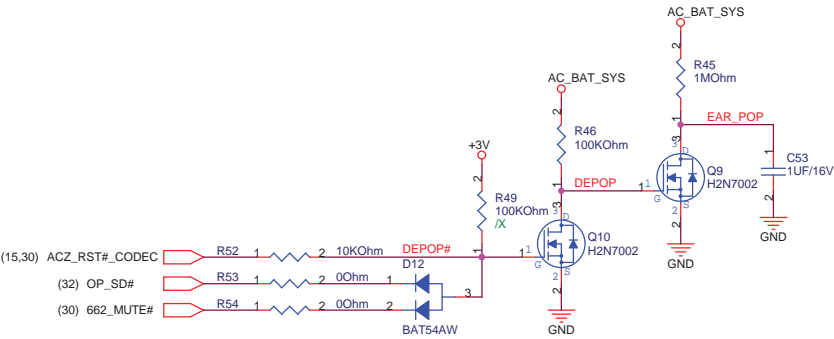
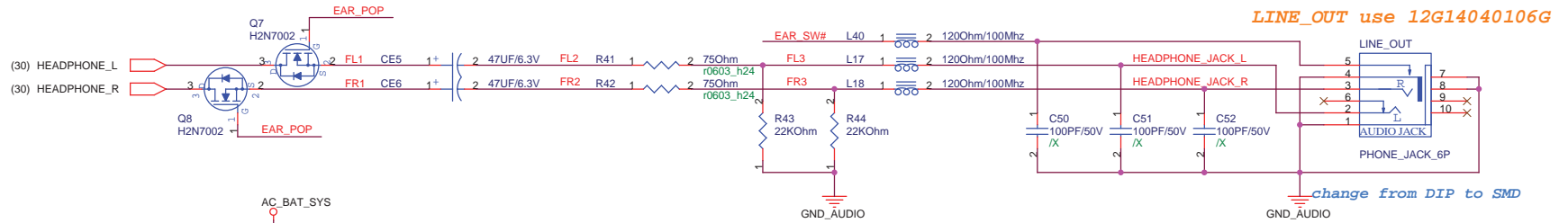
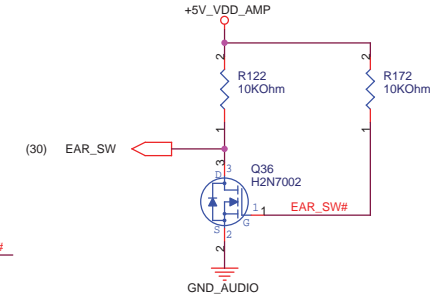
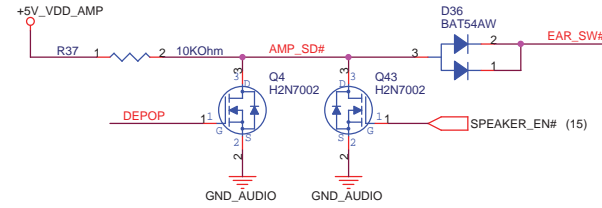
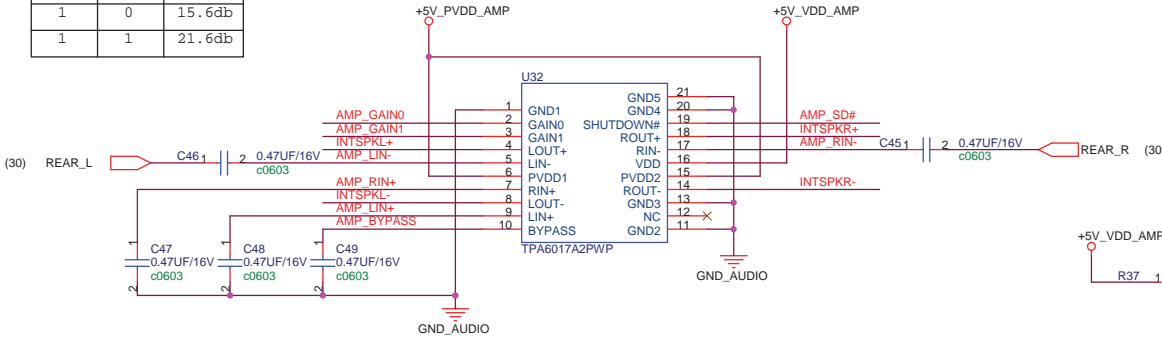
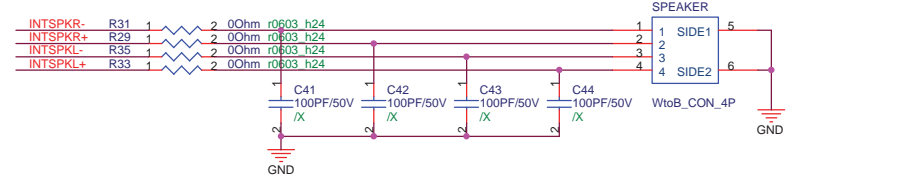
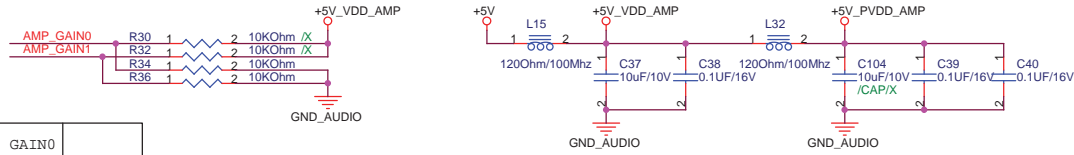


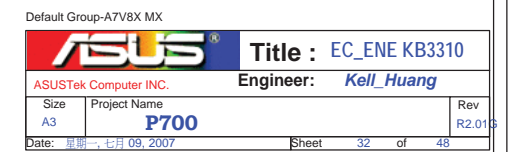


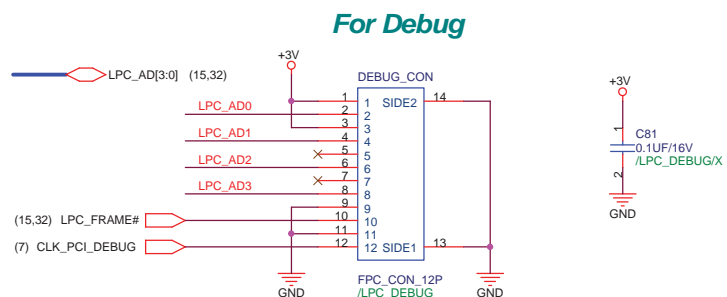
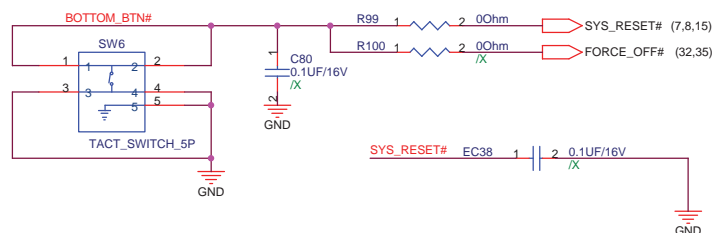
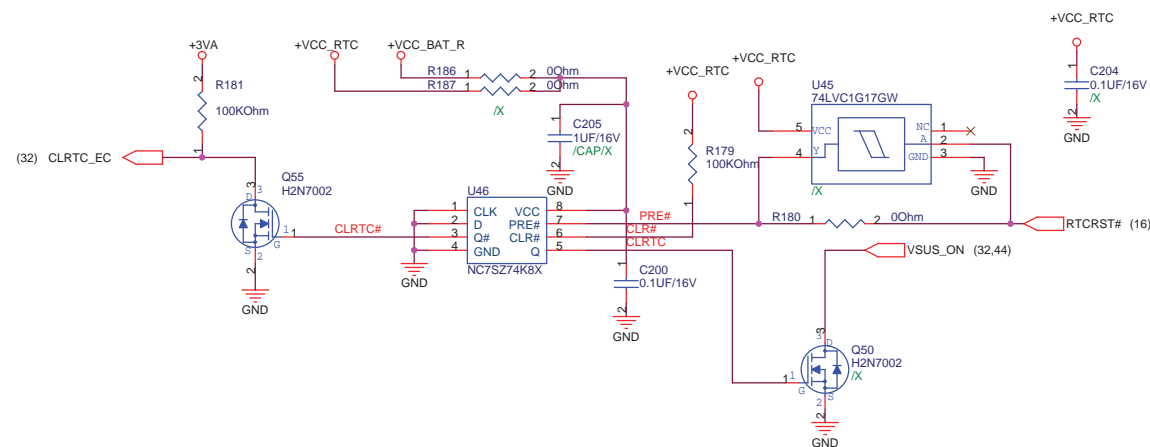
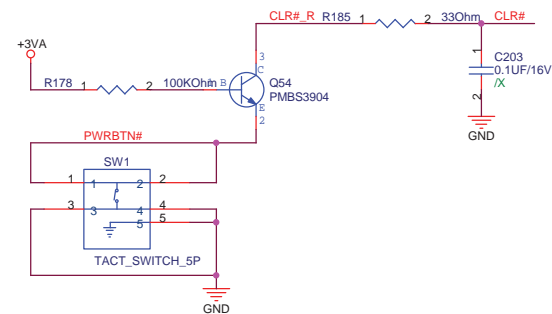
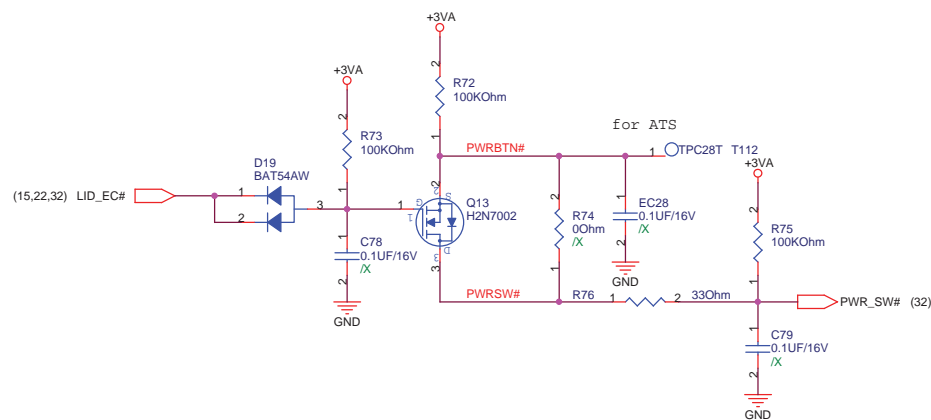




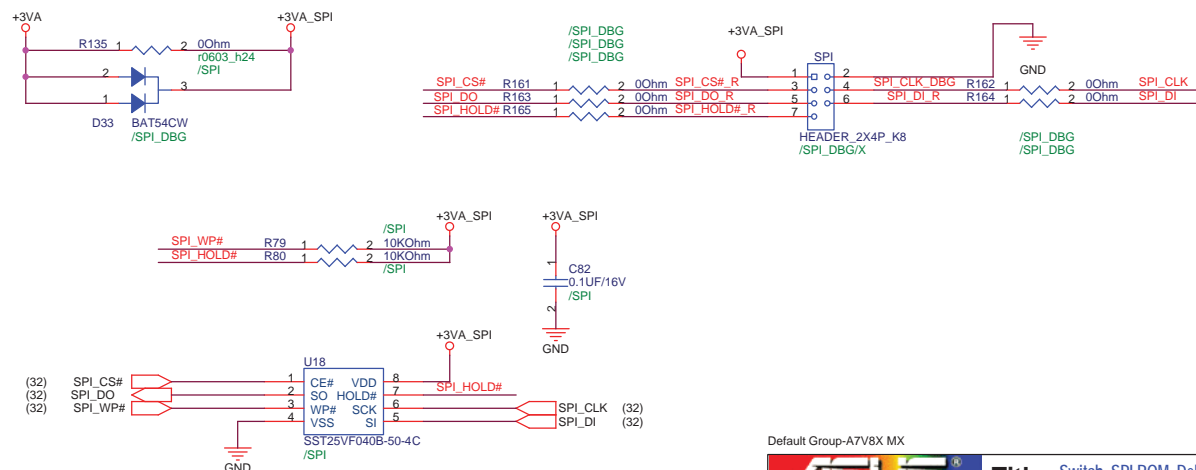
GAIN1	GAIN0	
0	0	6db
0	1	10db
1	0	15.6db
1	1	21.6db







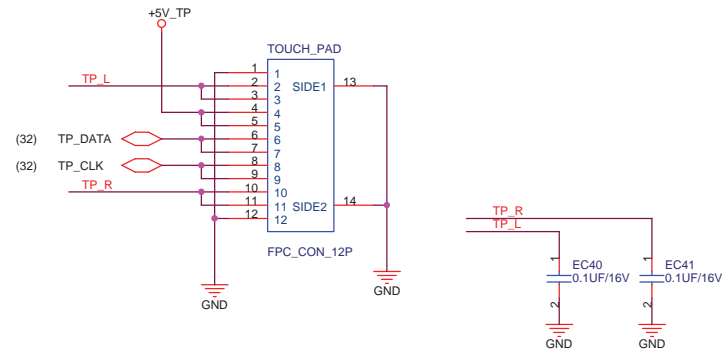
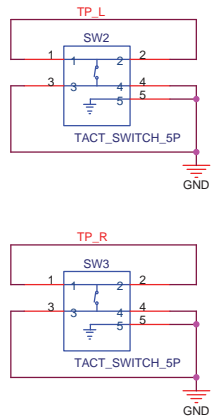
Debug Card cable use Z96 Touch Pad cable, P/N:
14G124110126, 14G124110120, 14G124110121
14G124110124, 14G124110125



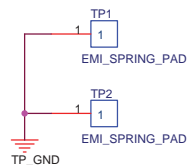
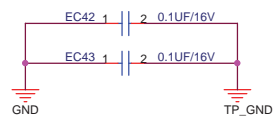
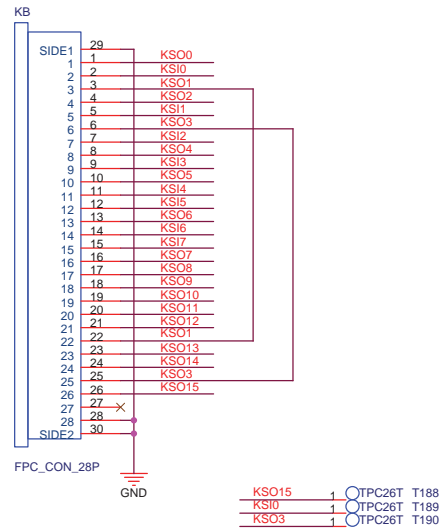
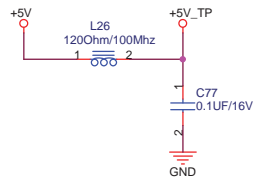
<http://hobi-elektronika.net>

Default Group-A7V8X MX		Title : Switch_SPI ROM_Debug	
ASUSTek Computer INC.		Engineer: Keli_Huang	
Size	Project Name		Rev
A3	P700		R2.01
Date: 星期一, 七月 09, 2007	Sheet	33	of 48

For Touch-Pad

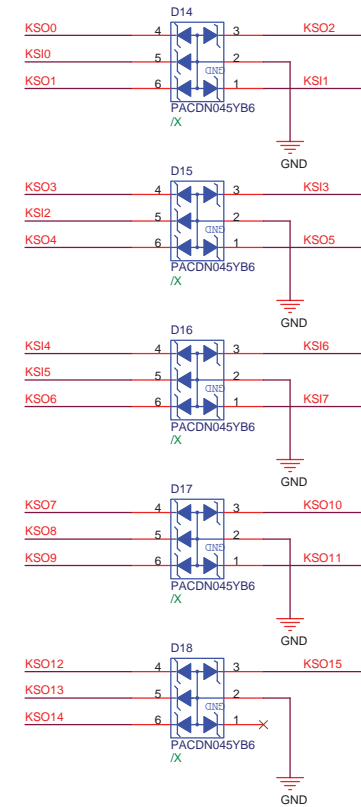


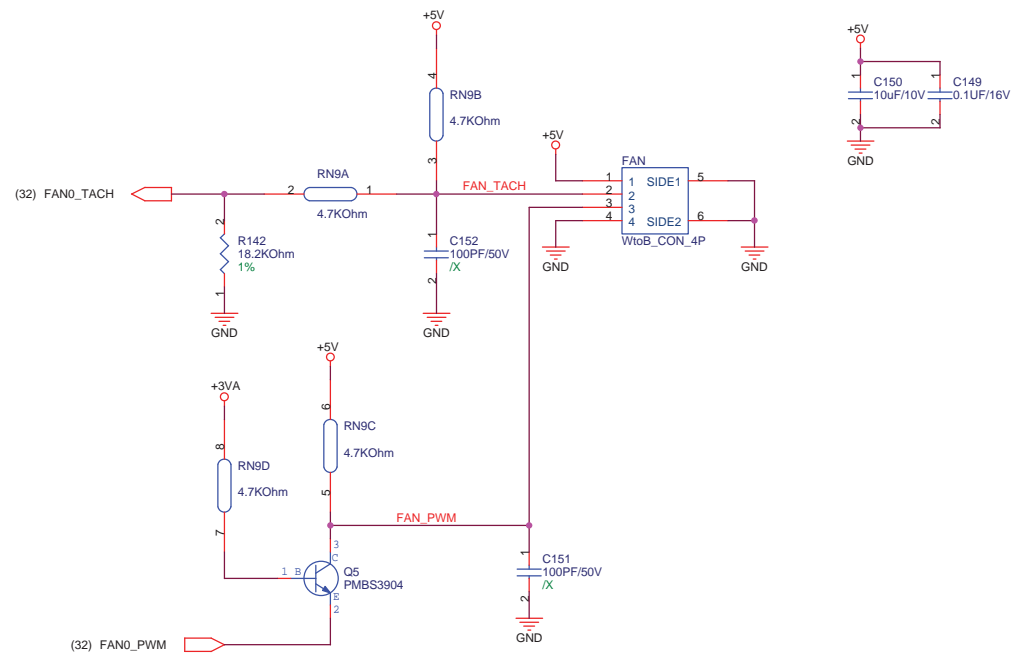
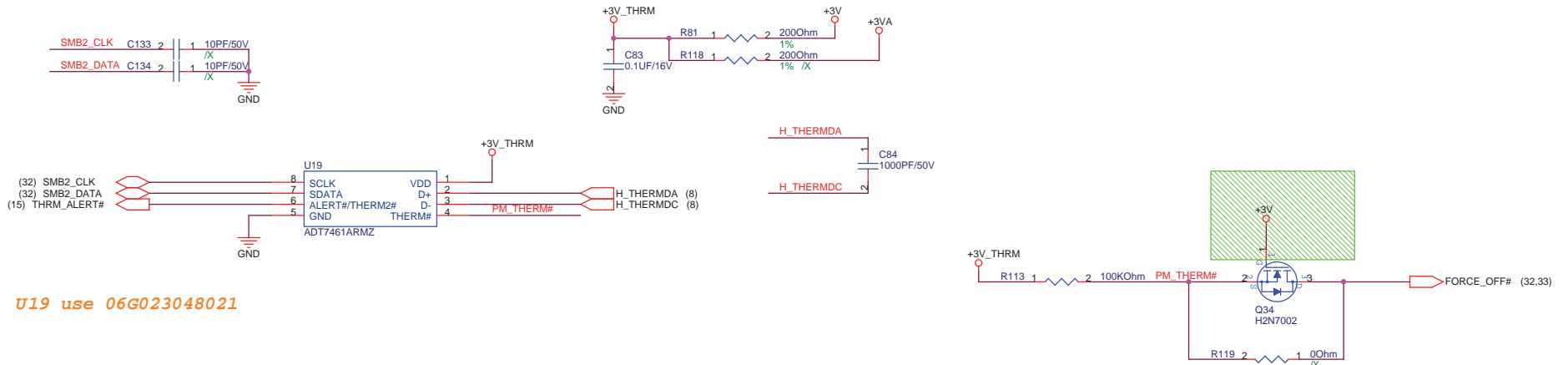
For Keyboard



KSO[15:0] (32)

KSI[7:0] (32)

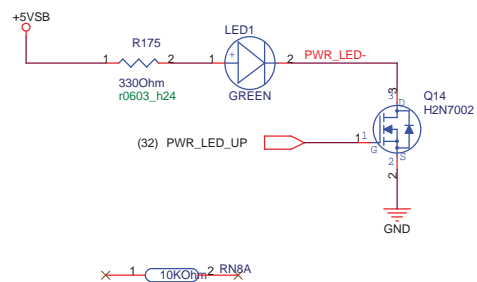




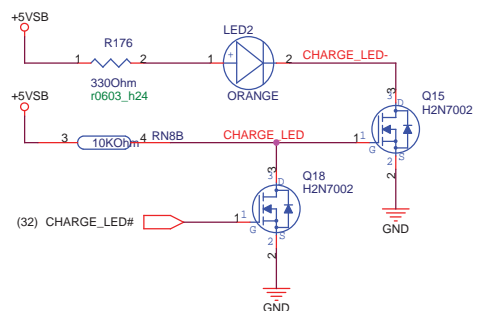
Default Group-A7V8X MX

ASUS		Title : Thermal Sensor_FAN	
ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size	Project Name		Rev
A3	P700		R2.01
Date: 星期一, 七月 09, 2007	Sheet	35 of 48	

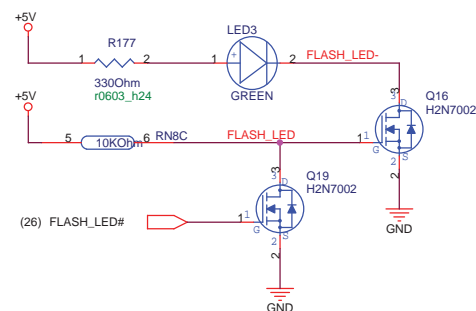
for POWER LED



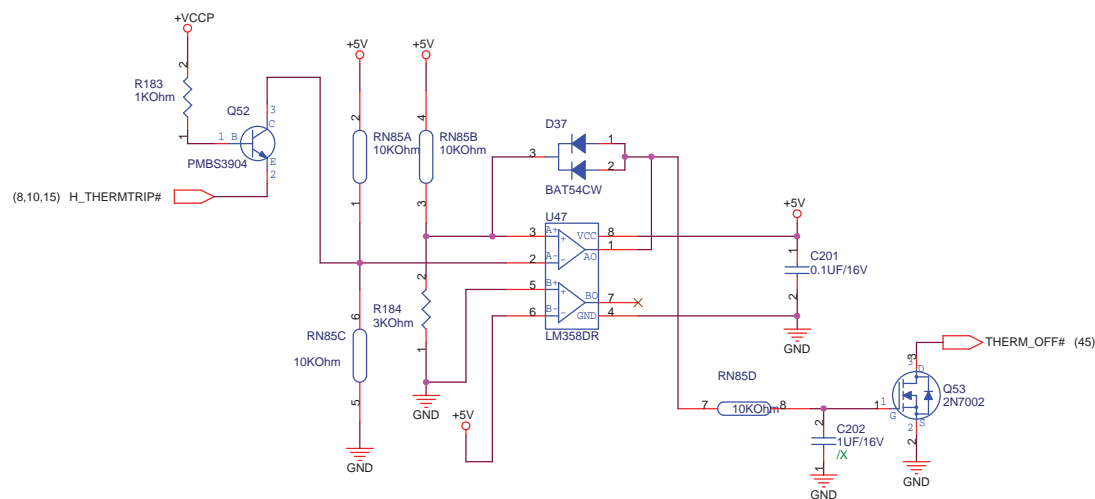
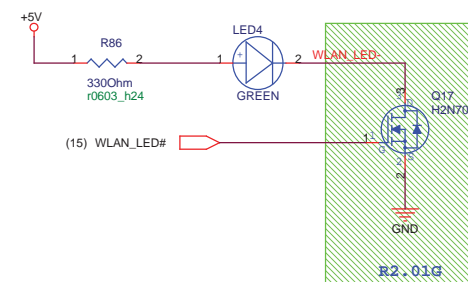
for CHARGE LED

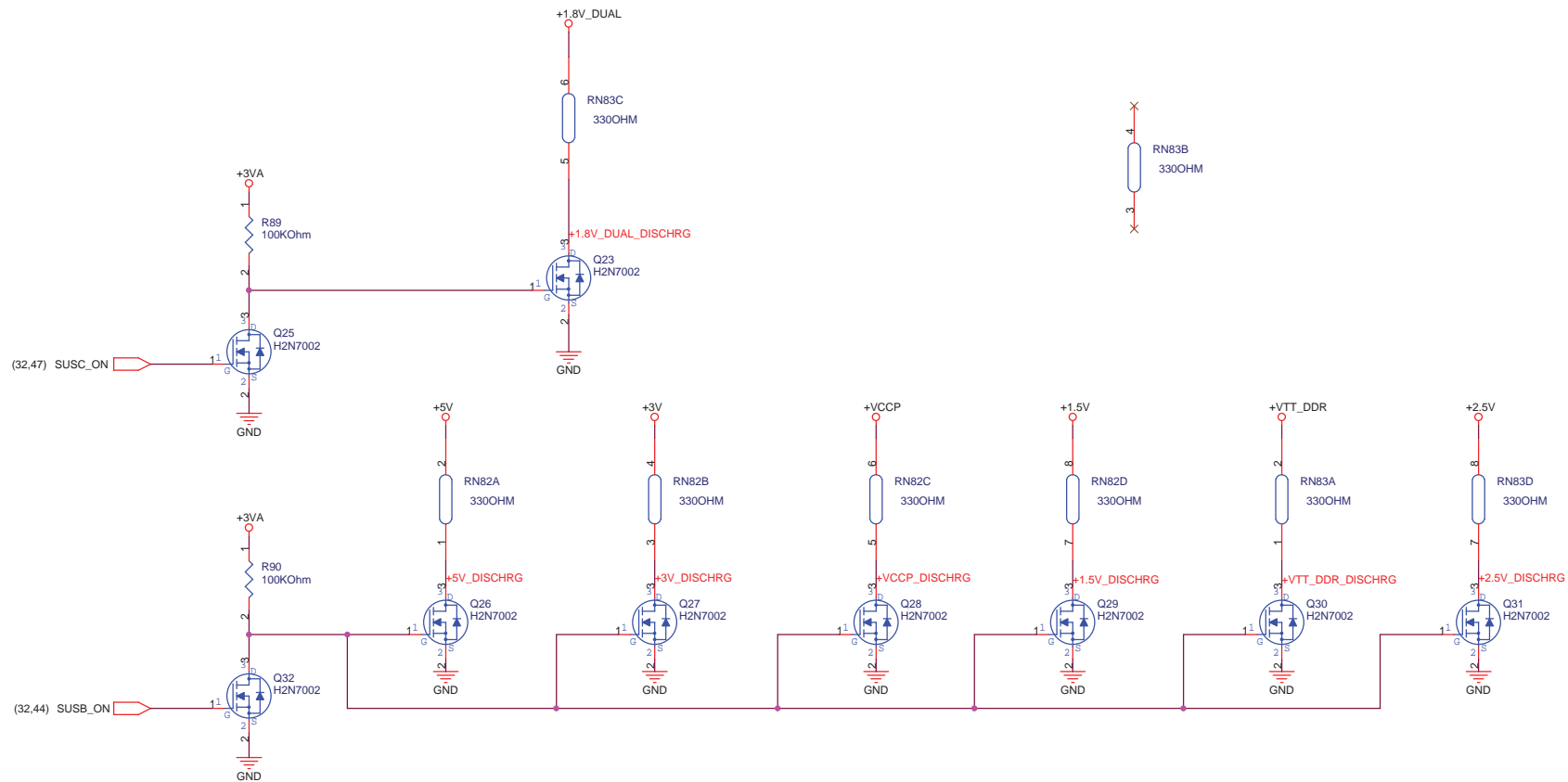


for FLASH LED

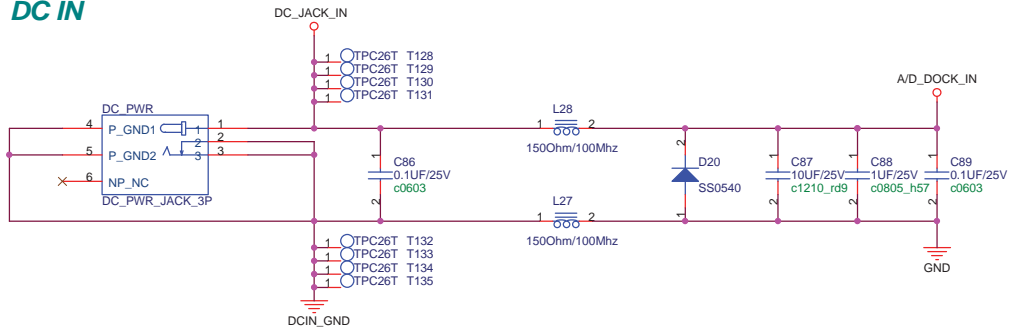


for WLAN LED

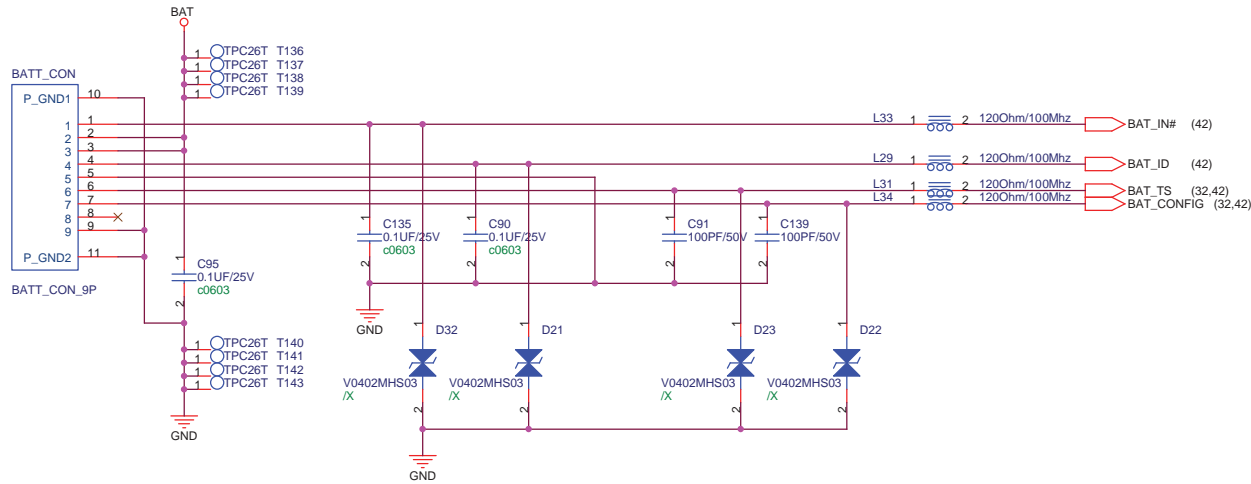




DC IN

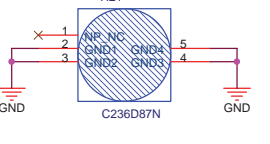
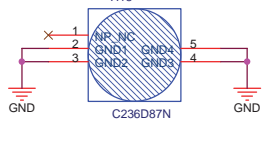
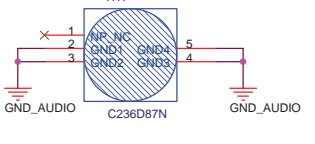
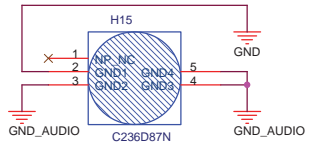
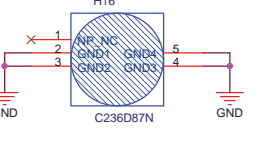
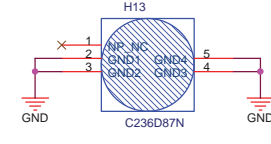
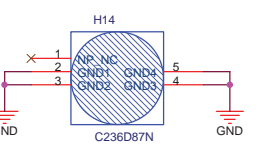
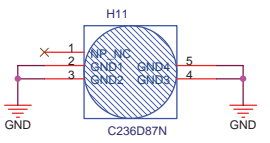
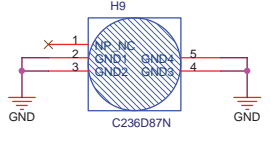
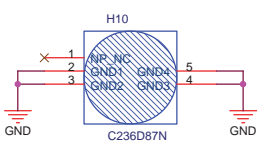
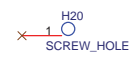
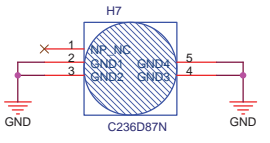
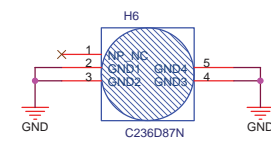
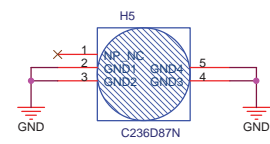


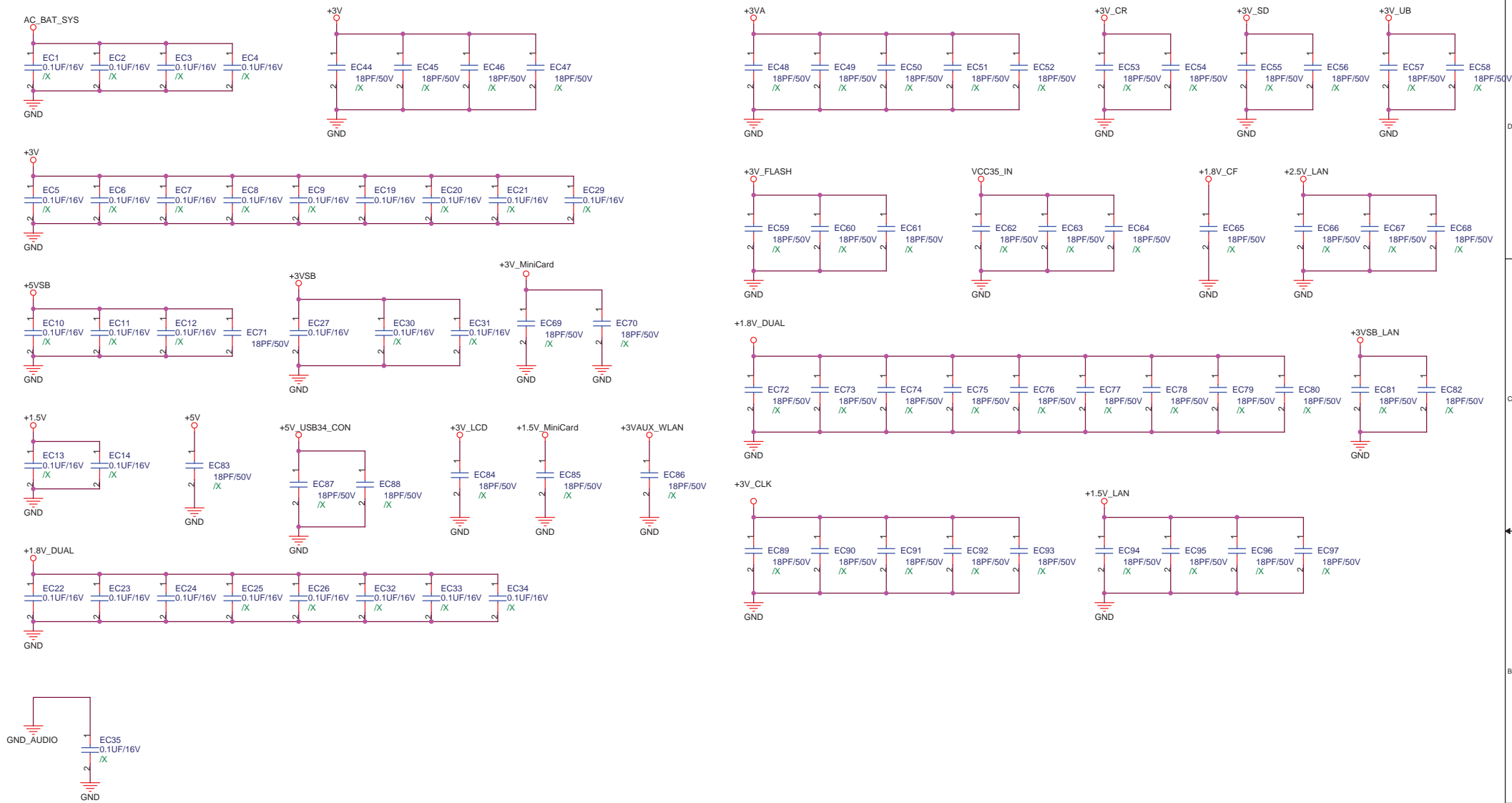
BAT IN

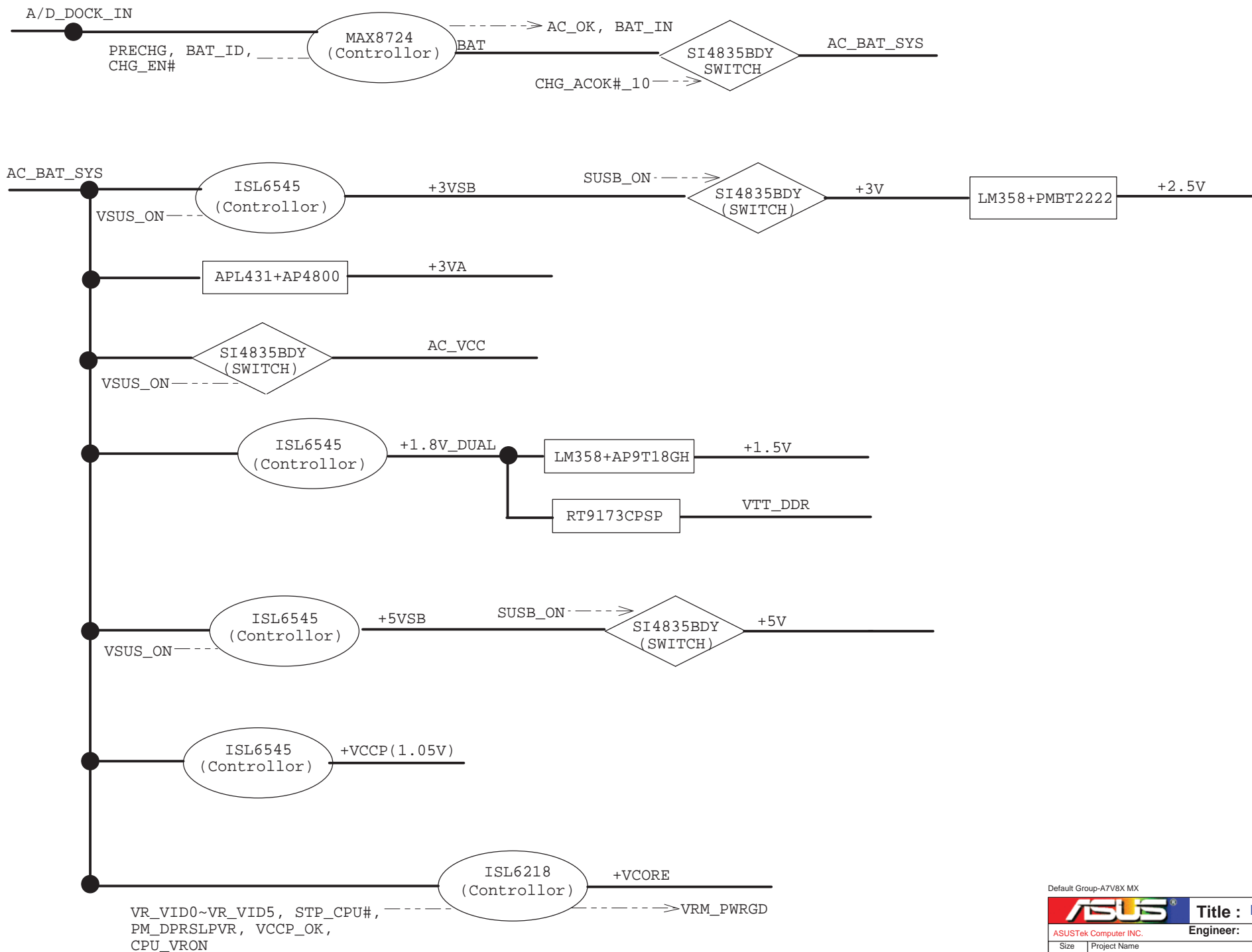


Default Group-A7V8X MX

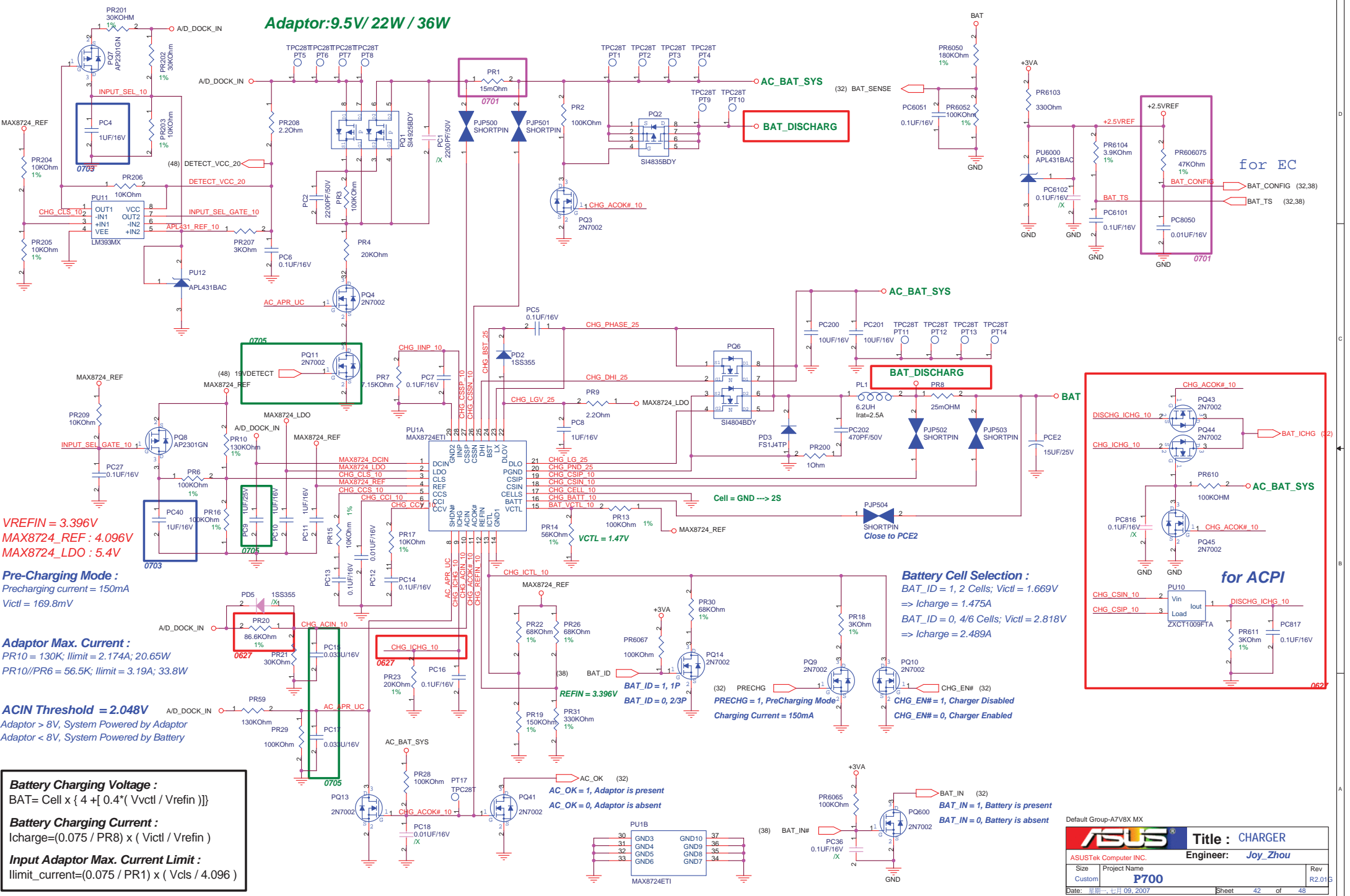
ASUS		Title : PWR Jack	
ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size A3	Project Name P700		Rev R2.01G
Date: 星期一, 七月 09, 2007		Sheet	38 of 48







Adaptor:9.5V/ 22W / 36W



VREFIN = 3.396V
 MAX8724_REF : 4.096V
 MAX8724_LDO : 5.4V

Pre-Charging Mode :
 Precharging current = 150mA
 Vctl = 169.8mV

Adaptor Max. Current :
 PR10 = 130K; Ilimit = 2.174A; 20.65W
 PR10//PR6 = 56.5K; Ilimit = 3.19A; 33.8W

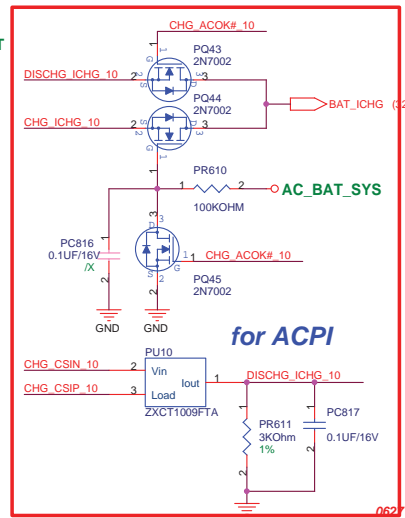
ACIN Threshold = 2.048V
 Adaptor > 8V, System Powered by Adaptor
 Adaptor < 8V, System Powered by Battery

Battery Charging Voltage :
 BAT= Cell x { 4 + [0.4* (Vvctl / Vrefin)] }

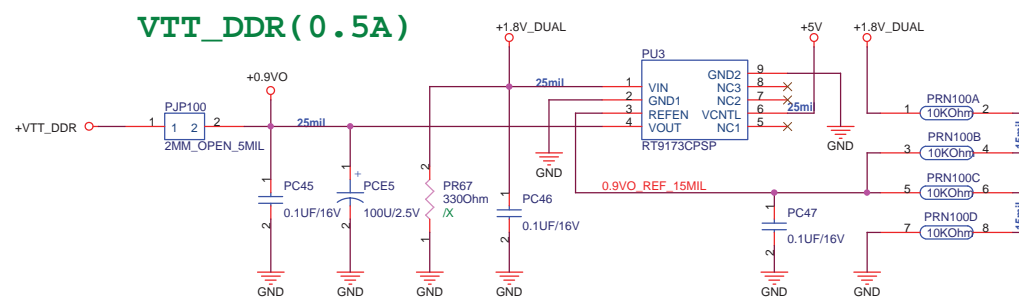
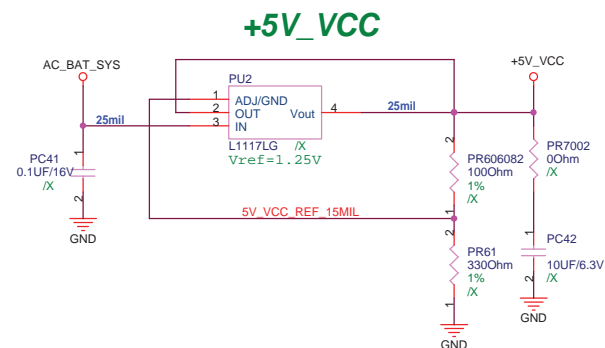
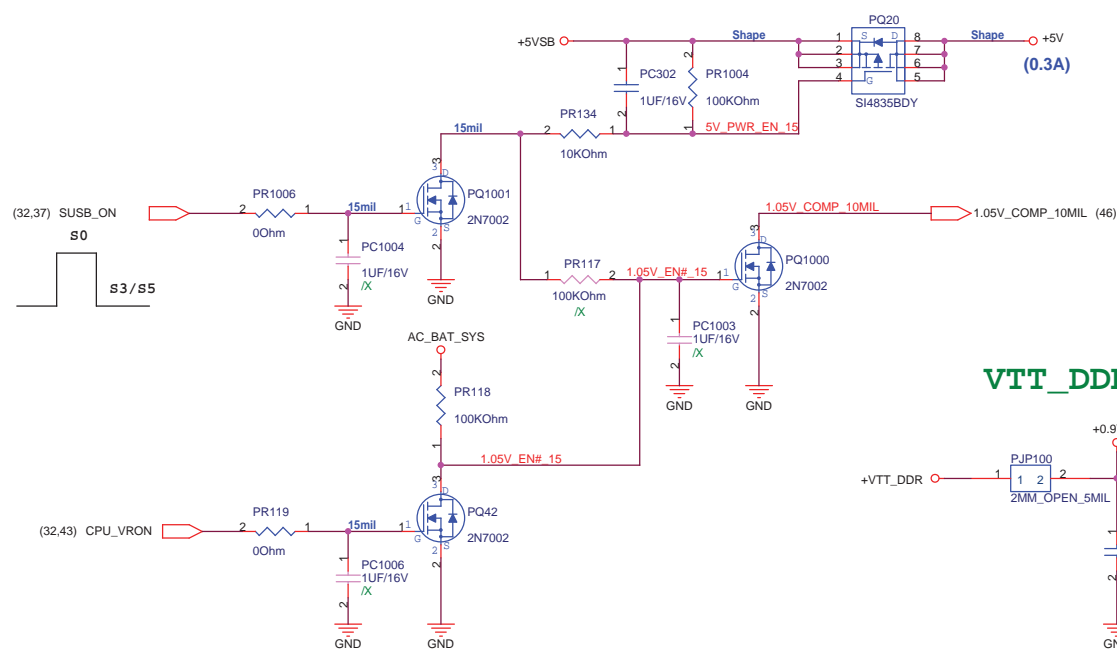
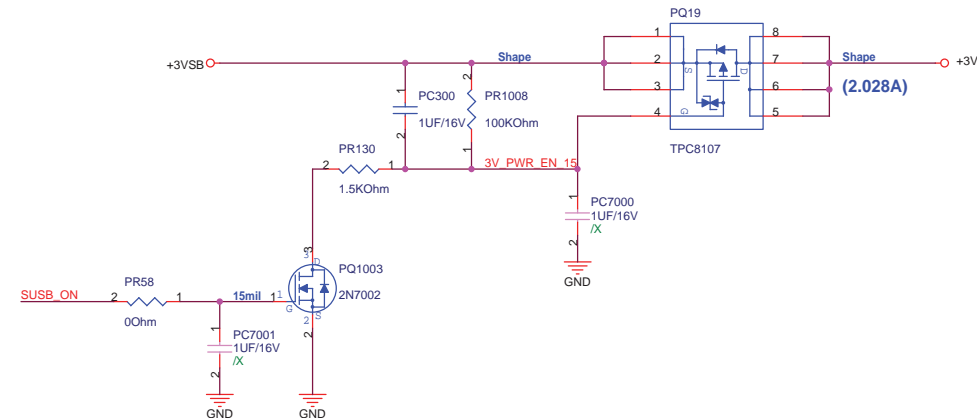
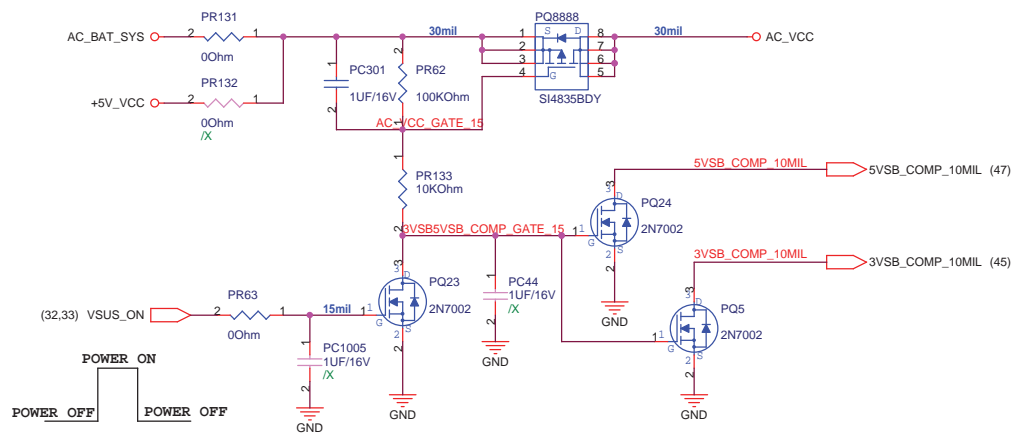
Battery Charging Current :
 Icharge=(0.075 / PR8) x (Vctl / Vrefin)

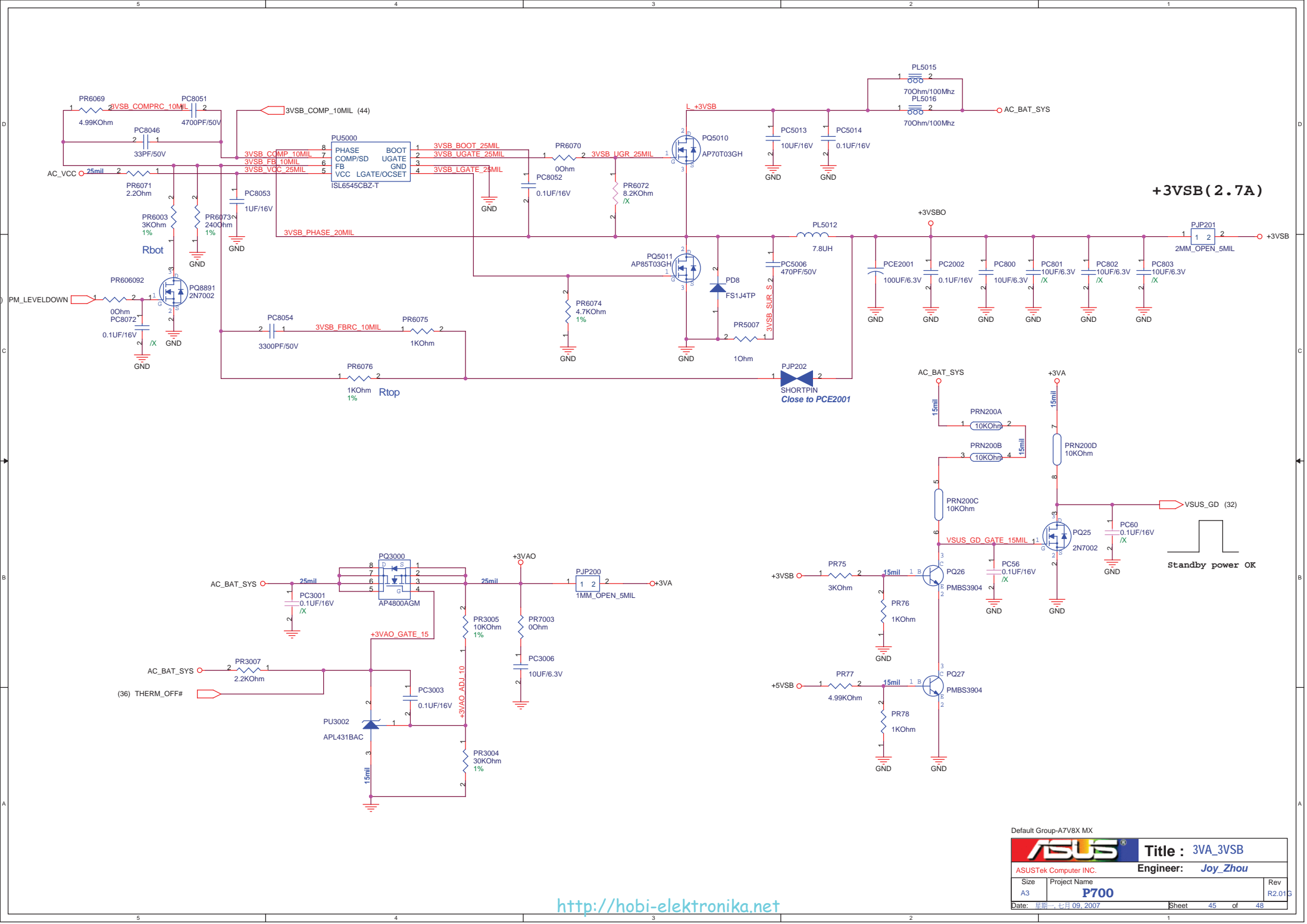
Input Adaptor Max. Current Limit :
 Ilimit_current=(0.075 / PR1) x (Vcls / 4.096)

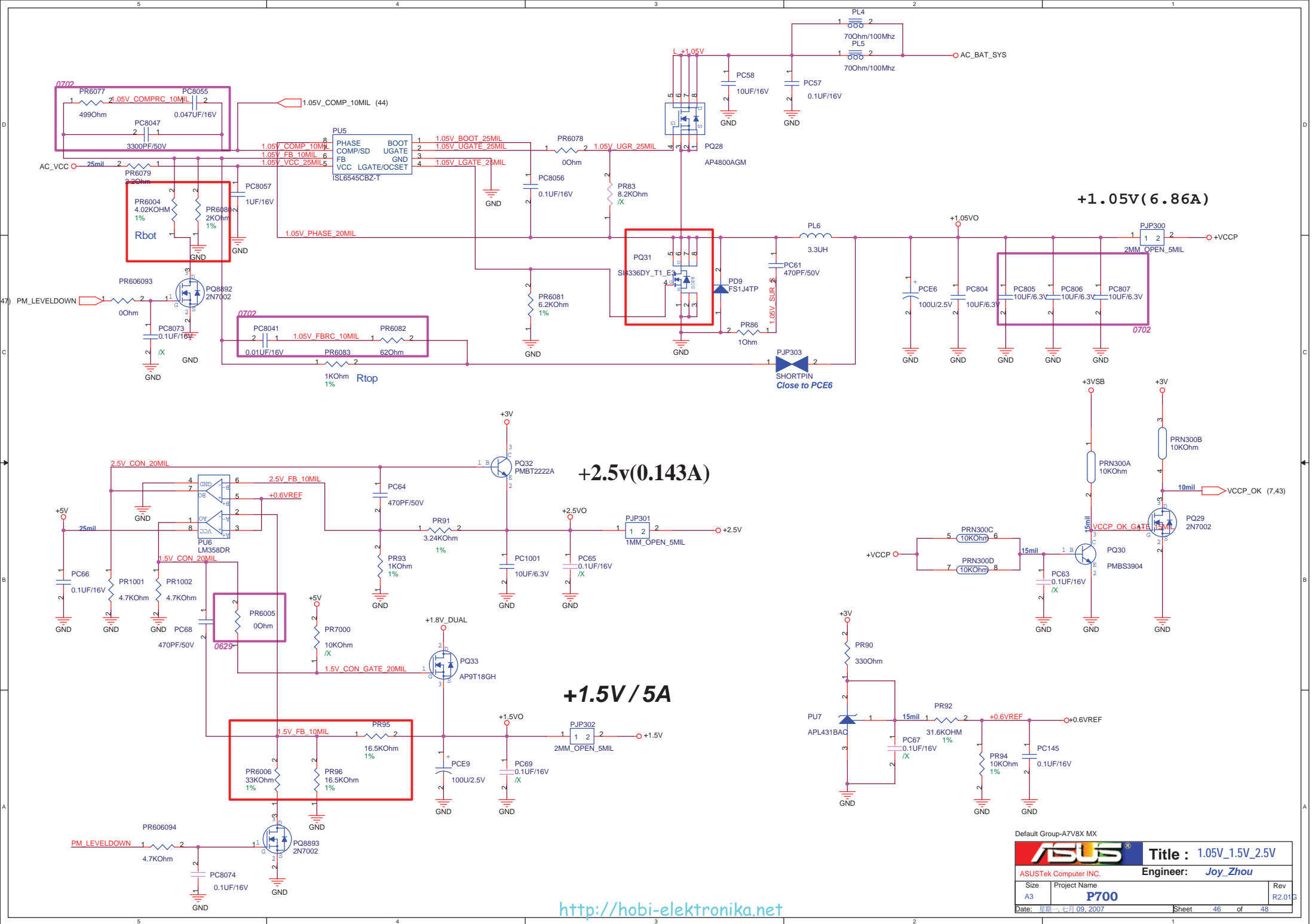
Battery Cell Selection :
 BAT_ID = 1, 2 Cells; Vctl = 1.669V
 => Icharge = 1.475A
 BAT_ID = 0, 4/6 Cells; Vctl = 2.818V
 => Icharge = 2.489A

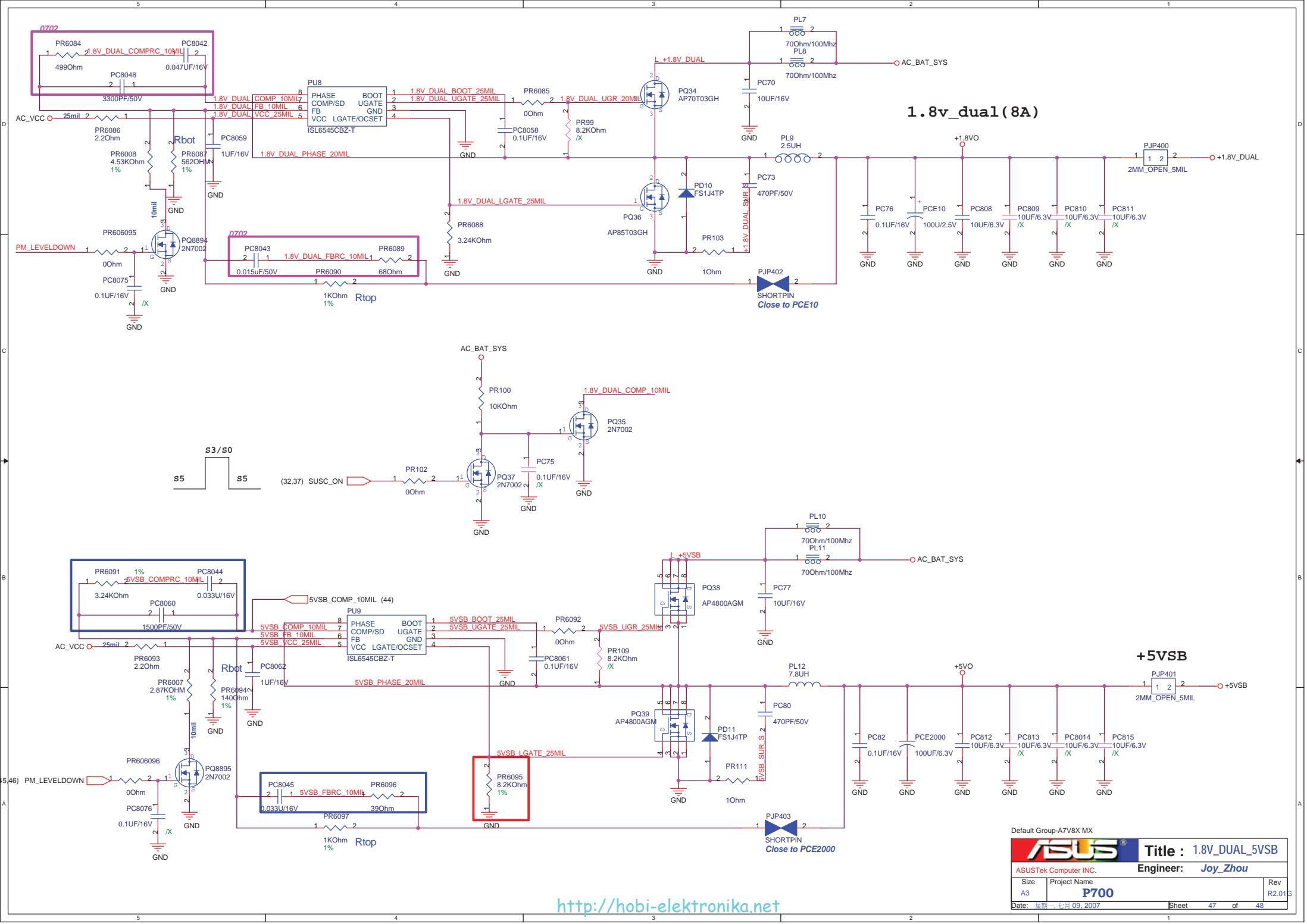


Default Group-A7V8X MX		Title : CHARGER	
ASUS		Engineer: Joy_Zhou	
ASUSTek Computer INC.		P700	
Size	Project Name	Rev	R2.015
Custom			
Date: 2007-09-09	Sheet 42 of 48		

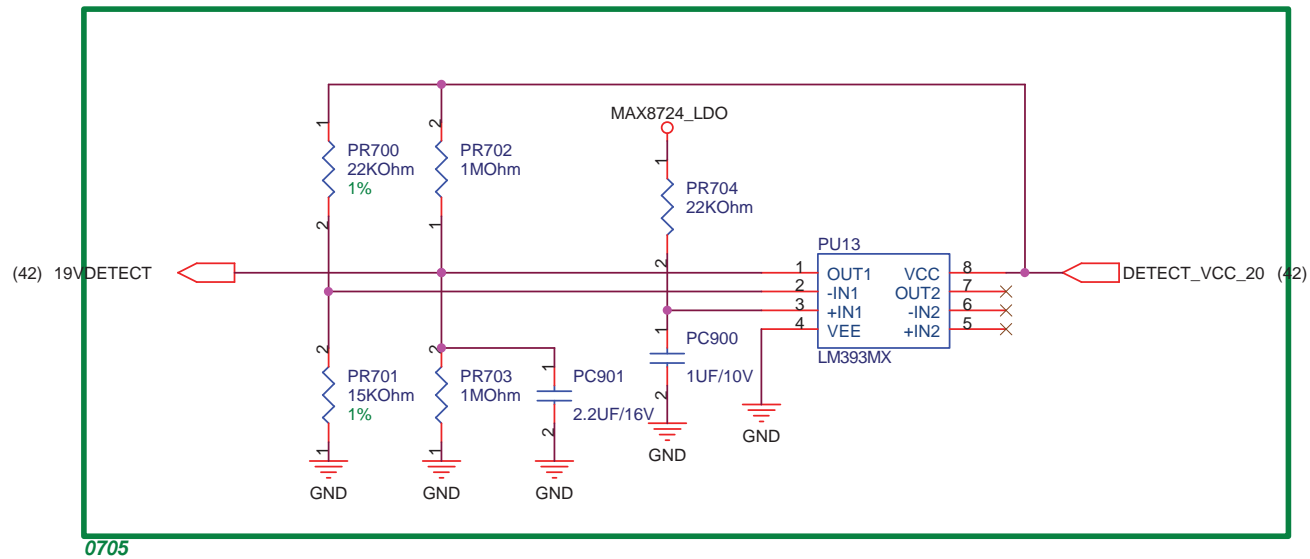








19V Detection Circuit



Default Group-A7V8X MX

ASUS		Title : 19VDetect	
ASUSTek COMPUTER Inc.		Engineer: Carl_Chiou	
Size A4	Project Name P700		Rev R2.01G
Date: 星期一, 七月 09, 2007		Sheet 48 of 48	