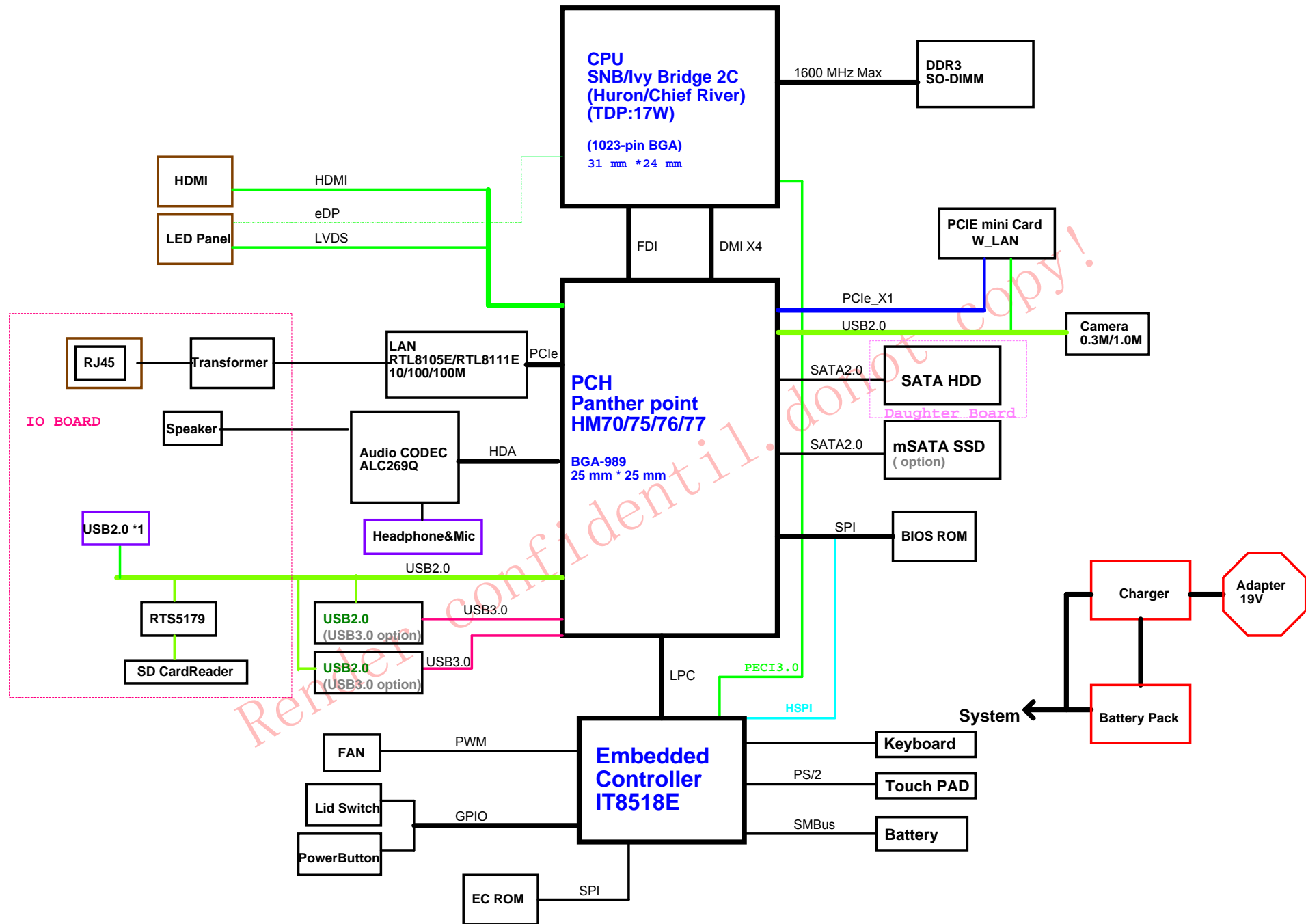


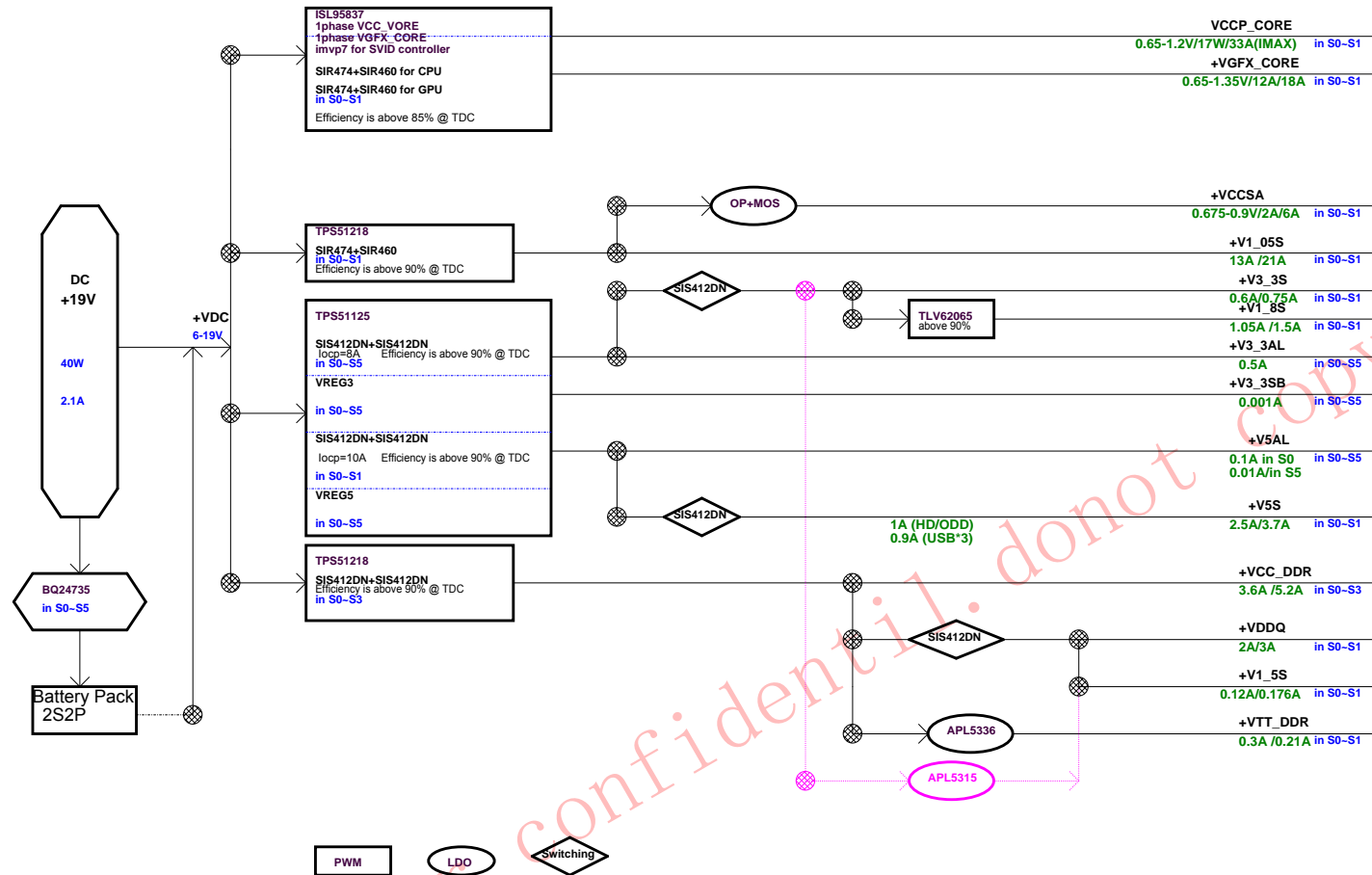
Project Name: C21

SCH Vertion: VC

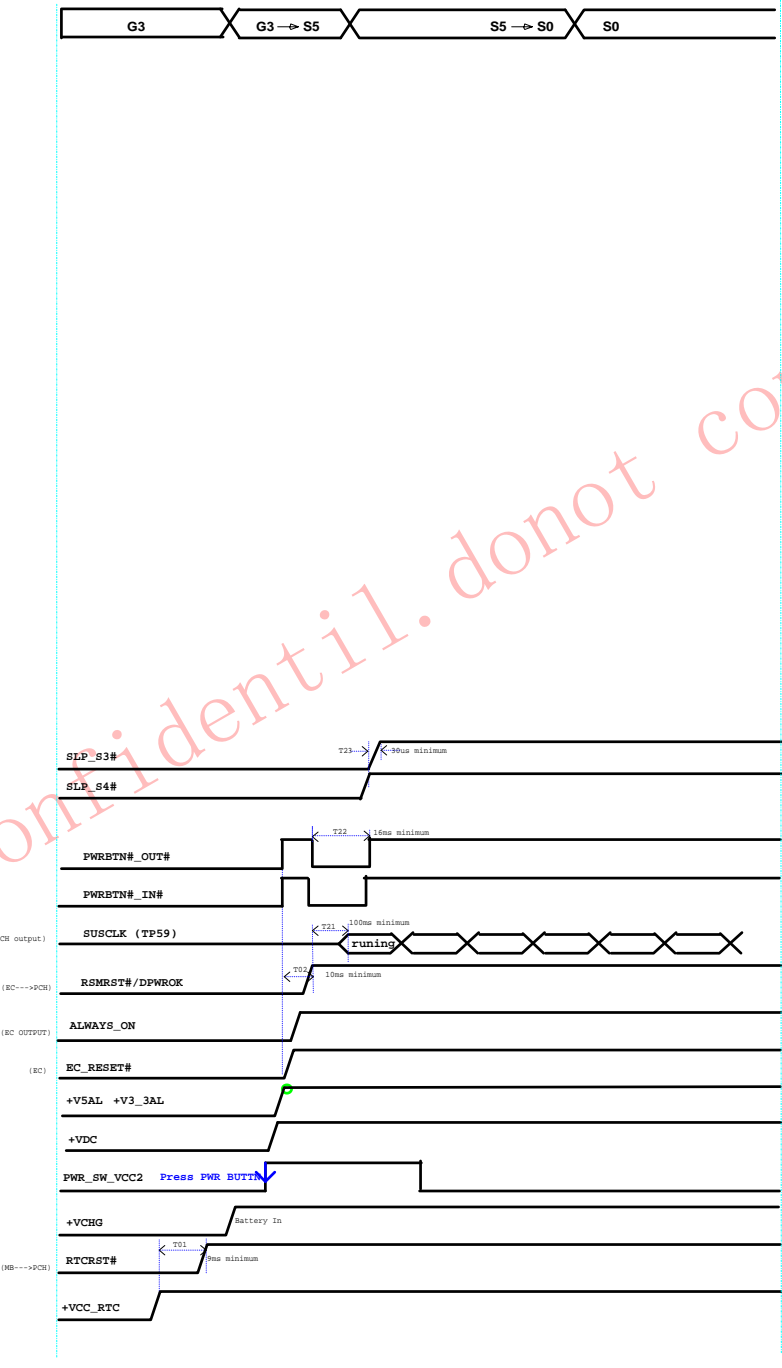
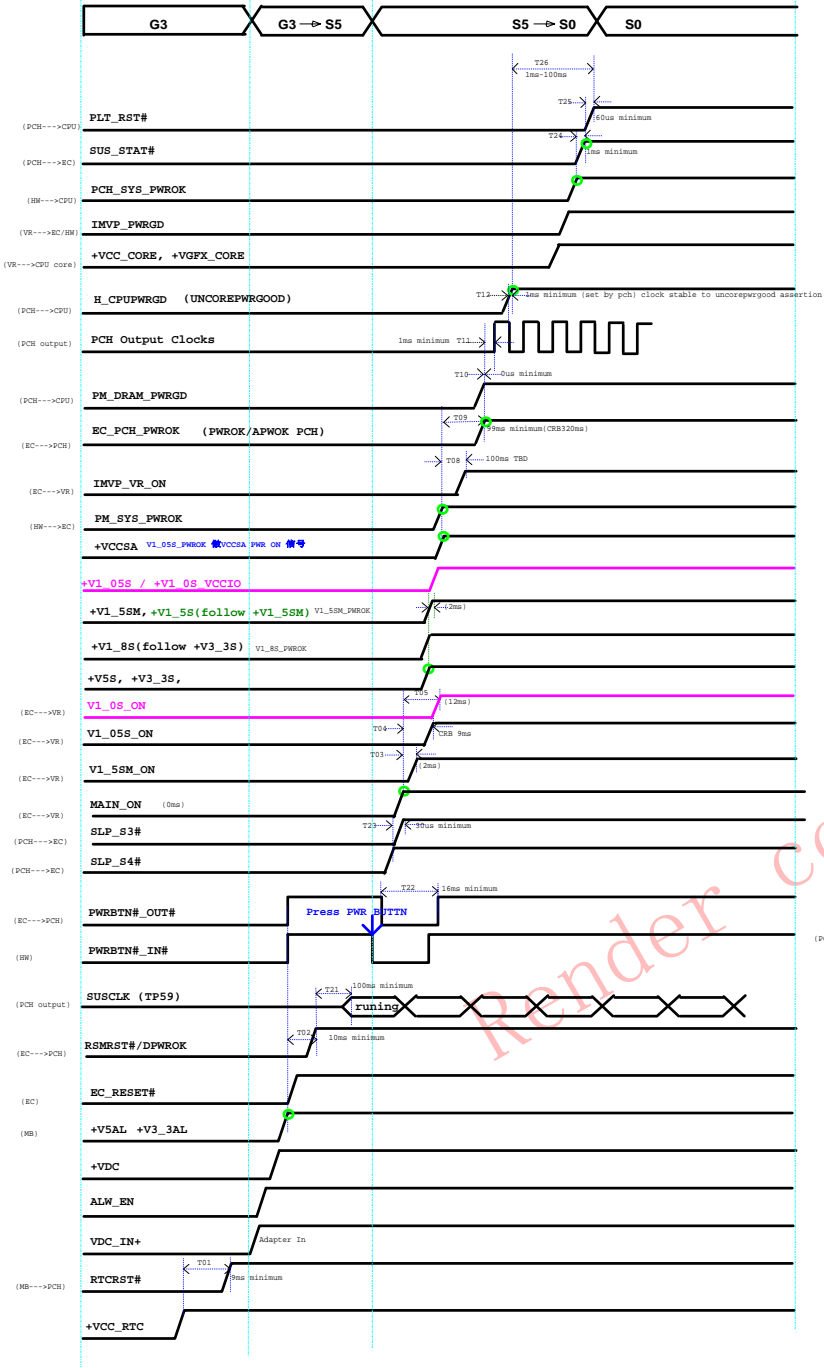
Render confidential. donot copy!

EA EXCELSIOR RENDER			
Title		COVER	
Size	Document Number		Rev
Custom	C21		C
Date:	Thursday, March 14, 2013		Sheet 1 of 57

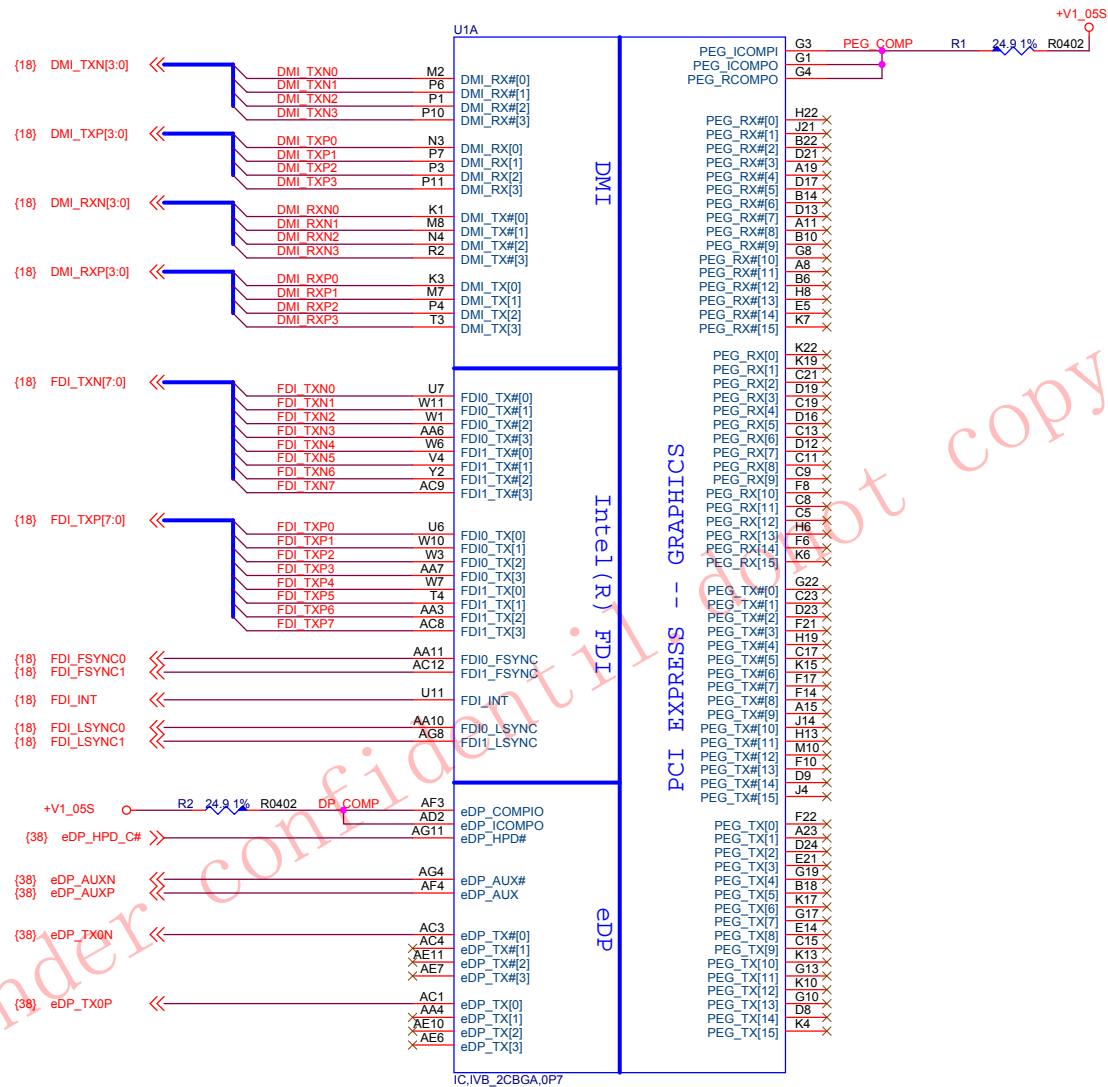




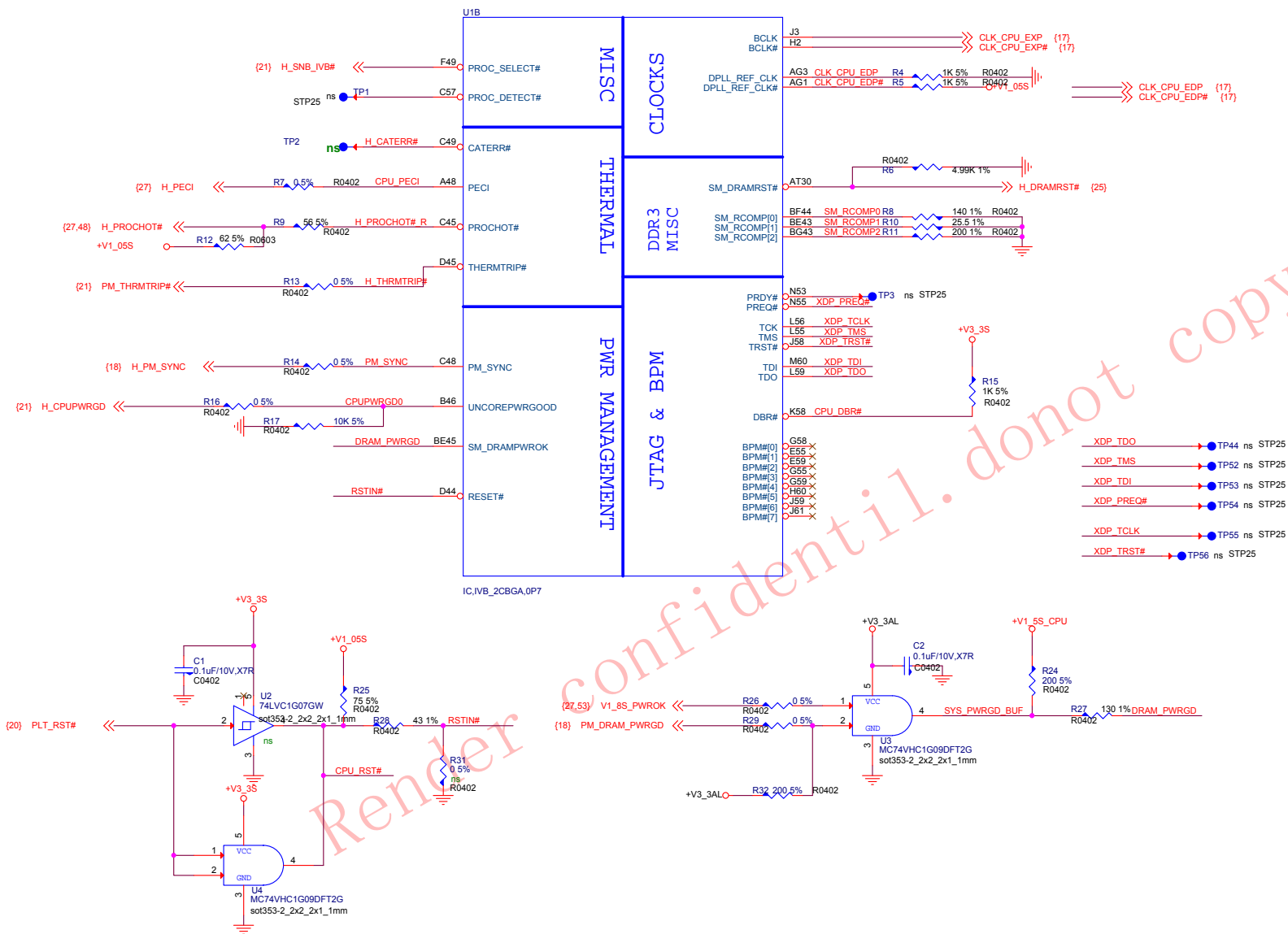
POWER ON SEQUENCE(Adapter Mode)POWER ON SEQUENCE(BatteryMode)



Platform Sequencing Timing Spec				
Label	Description	Min(ms)	Max(ms)	Units
T01	VccRTC active to RTCRST# deassertion	9	--	ms
T02	VccSUS active to RSMRST# deassertion	10	--	ms
T03	platform power rails active to PCH PWROK high	100	300	ms
T04	PWROK assertion to DRAMPWROK assertion	0	--	us
T05	PWROK high to PCH clock outputs stable	1	--	ms
T06	SYS_PWROK high to PLTRST# deassertion	1.06	--	ms
Ta	SUS_STAT# active to PLTRST# active	210	--	us
Tb	PLTRST# active to PROCPWRGD inactive	30	--	us
Tc	PROCPWRGD inactive to clocks invalid	10	--	us
Td	Clocks invalid to SLP_S3# assertion	1	--	us
	SLP_S3# assertion to VccCore (PCH) rails fallin	--	--	us



EA EXCELSIOR RENDER				
Title IVB (DMI,PEG,FDI)				
Size Custom	Document Number C21			Rev C
Date:	Thursday, March 14, 2013	Sheet	9 of 57	



(25) SDDR\_A\_DQ[63..0] <<

(25) SDDR\_A\_BS0 << BF37  
(25) SDDR\_A\_BS1 << BA28  
(25) SDDR\_A\_BS2 << BA28

(25) SDDR\_A\_CAS# << BE39  
(25) SDDR\_A\_RAS# << BD39C  
(25) SDDR\_A\_WE# << AT41C

SDDR A DQ0 AG6  
SDDR A DQ1 AJ6  
SDDR A DQ2 AP11  
SDDR A DQ3 AL6  
SDDR A DQ4 AJ10  
SDDR A DQ5 AJ8  
SDDR A DQ6 AL8  
SDDR A DQ7 AL7  
SDDR A DQ8 AR11  
SDDR A DQ9 AP6  
SDDR A DQ10 AU6  
SDDR A DQ11 AV9  
SDDR A DQ12 AR6  
SDDR A DQ13 AP8  
SDDR A DQ14 AT13  
SDDR A DQ15 AU13  
SDDR A DQ16 BC7  
SDDR A DQ17 BB7  
SDDR A DQ18 BA13  
SDDR A DQ19 BB11  
SDDR A DQ20 BA7  
SDDR A DQ21 BA9  
SDDR A DQ22 BB9  
SDDR A DQ23 AY13  
SDDR A DQ24 AY14  
SDDR A DQ25 AR14  
SDDR A DQ26 AY17  
SDDR A DQ27 AR19  
SDDR A DQ28 BA14  
SDDR A DQ29 AU14  
SDDR A DQ30 BB14  
SDDR A DQ31 BB17  
SDDR A DQ32 BA45  
SDDR A DQ33 AR43  
SDDR A DQ34 AW48  
SDDR A DQ35 BC48  
SDDR A DQ36 BC45  
SDDR A DQ37 AR45  
SDDR A DQ38 AT48  
SDDR A DQ39 AY48  
SDDR A DQ40 BA49  
SDDR A DQ41 AV49  
SDDR A DQ42 BB51  
SDDR A DQ43 AY53  
SDDR A DQ44 BB49  
SDDR A DQ45 AU49  
SDDR A DQ46 BA53  
SDDR A DQ47 BB55  
SDDR A DQ48 BA55  
SDDR A DQ49 AV56  
SDDR A DQ50 AP50  
SDDR A DQ51 AP53  
SDDR A DQ52 AV54  
SDDR A DQ53 AT54  
SDDR A DQ54 AP56  
SDDR A DQ55 AP52  
SDDR A DQ56 AN57  
SDDR A DQ57 AN53  
SDDR A DQ58 AG56  
SDDR A DQ59 AG53  
SDDR A DQ60 AN55  
SDDR A DQ61 AN52  
SDDR A DQ62 AG55  
SDDR A DQ63 AK56

DDR SYSTEM MEMORY A

SA\_CK[0]  
SA\_CK#[0]  
SA\_CKE[0]

SA\_CK[1]  
SA\_CK#[1]  
SA\_CKE[1]

SA\_CS#[0]  
SA\_CS#[1]

SA\_ODT[0]  
SA\_ODT[1]

SA\_DQS#[0]  
SA\_DQS#[1]  
SA\_DQS#[2]  
SA\_DQS#[3]  
SA\_DQS#[4]  
SA\_DQS#[5]  
SA\_DQS#[6]  
SA\_DQS#[7]

SA\_DQS[0]  
SA\_DQS[1]  
SA\_DQS[2]  
SA\_DQS[3]  
SA\_DQS[4]  
SA\_DQS[5]  
SA\_DQS[6]  
SA\_DQS[7]

SA\_MA[0]  
SA\_MA[1]  
SA\_MA[2]  
SA\_MA[3]  
SA\_MA[4]  
SA\_MA[5]  
SA\_MA[6]  
SA\_MA[7]  
SA\_MA[8]  
SA\_MA[9]  
SA\_MA[10]  
SA\_MA[11]  
SA\_MA[12]  
SA\_MA[13]  
SA\_MA[14]  
SA\_MA[15]

BG35 SDDR A A0  
BB34 SDDR A A1  
BE35 SDDR A A2  
BD35 SDDR A A3  
AT34 SDDR A A4  
AU34 SDDR A A5  
BB32 SDDR A A6  
AT32 SDDR A A7  
AY32 SDDR A A8  
AV32 SDDR A A9  
BE37 SDDR A A10  
BA30 SDDR A A11  
BC30 SDDR A A12  
AW41 SDDR A A13  
AY28 SDDR A A14  
AU26 SDDR A A15

AU36 >>> SDDR\_A\_CLK\_DDR0 (25)  
AV36 >>> SDDR\_A\_CLK\_DDR#0 (25)  
AY26 >>> SDDR\_A\_CKE0 (25)

AT40 >>> SDDR\_A\_CLK\_DDR1 (25)  
AU40 >>> SDDR\_A\_CLK\_DDR#1 (25)  
BB26 >>> SDDR\_A\_CKE1 (25)

BB40 >>> SDDR\_A\_CS#0 (25)  
BC41 >>> SDDR\_A\_CS#1 (25)

AY40 >>> SDDR\_A\_ODT0 (25)  
BA41 >>> SDDR\_A\_ODT1 (25)

AL11 SDDR A DQS#0 >>> SDDR\_A\_DQS#[7..0] (25)

AJ11 SDDR A DQS0 >>> SDDR\_A\_DQS[7..0] (25)

BG35 SDDR A A0 >>> SDDR\_A\_A[15..0] (25)

U1D

AL4  
AL1  
AN3  
AR4  
AK4  
AK3  
AN4  
AR1  
AU4  
AT2  
AV4  
BA4  
AU3  
AR3  
AY2  
BA3  
BF8  
BD9  
BD13  
BF12  
BF8  
BD10  
BD14  
BE13  
BE16  
BE17  
BE18  
BE21  
BE14  
BG14  
BG18  
BF19  
BD50  
BF49  
BD53  
BF52  
BD49  
BE49  
BD54  
BE53  
BF56  
BE57  
BC59  
AY60  
BE54  
BG54  
BA58  
AW59  
AW58  
AU58  
AN61  
AN59  
AU59  
AU61  
AN58  
AR58  
AK58  
AL58  
AG58  
AG59  
AM60  
AL59  
AF61  
AH60

DDR SYSTEM MEMORY B

SB\_CK[0]  
SB\_CK#[0]  
SB\_CKE[0]

SB\_CK[1]  
SB\_CK#[1]  
SB\_CKE[1]

SB\_CS#[0]  
SB\_CS#[1]

SB\_ODT[0]  
SB\_ODT[1]

SB\_DQS#[0]  
SB\_DQS#[1]  
SB\_DQS#[2]  
SB\_DQS#[3]  
SB\_DQS#[4]  
SB\_DQS#[5]  
SB\_DQS#[6]  
SB\_DQS#[7]

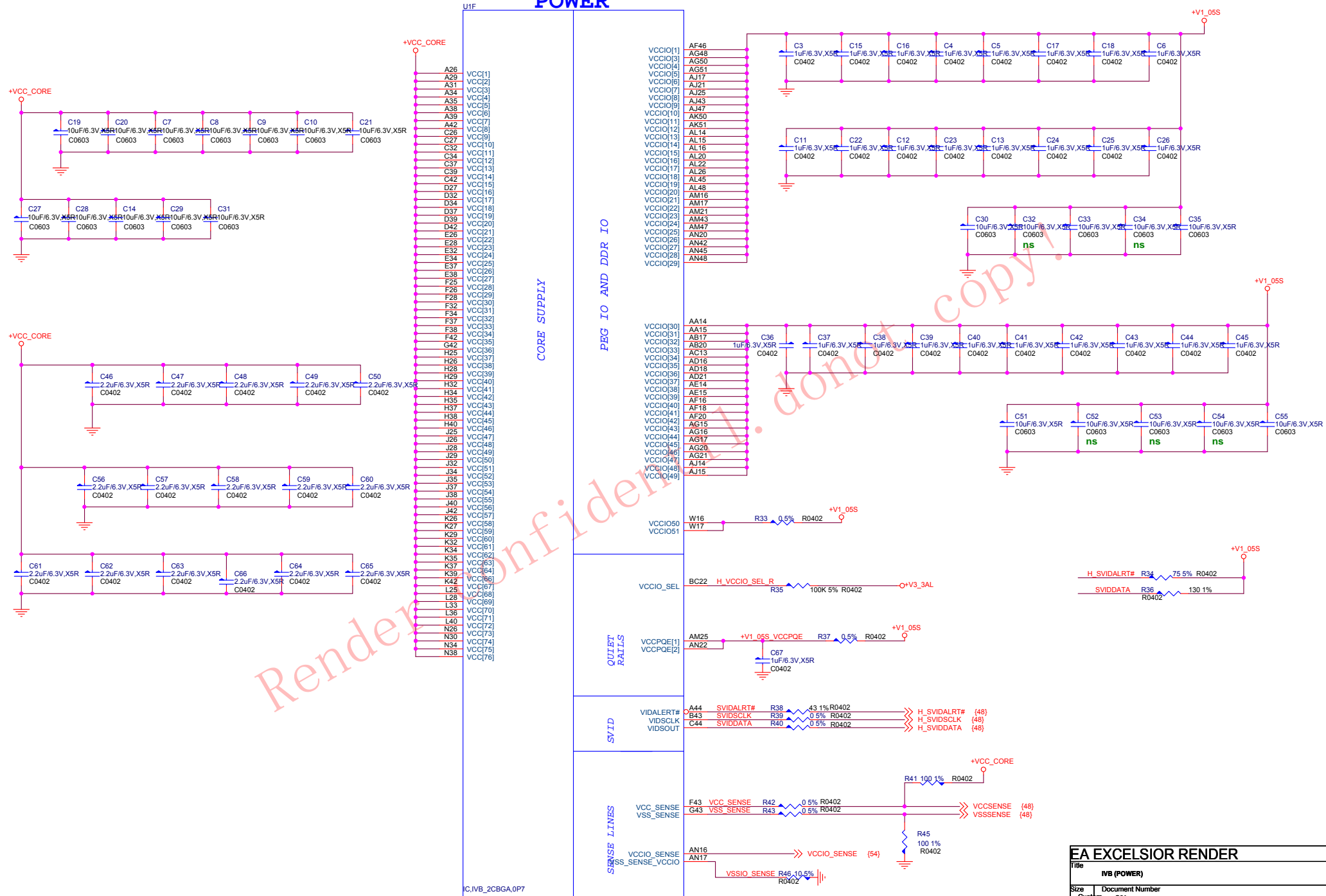
SB\_DQS[0]  
SB\_DQS[1]  
SB\_DQS[2]  
SB\_DQS[3]  
SB\_DQS[4]  
SB\_DQS[5]  
SB\_DQS[6]  
SB\_DQS[7]

SB\_MA[0]  
SB\_MA[1]  
SB\_MA[2]  
SB\_MA[3]  
SB\_MA[4]  
SB\_MA[5]  
SB\_MA[6]  
SB\_MA[7]  
SB\_MA[8]  
SB\_MA[9]  
SB\_MA[10]  
SB\_MA[11]  
SB\_MA[12]  
SB\_MA[13]  
SB\_MA[14]  
SB\_MA[15]

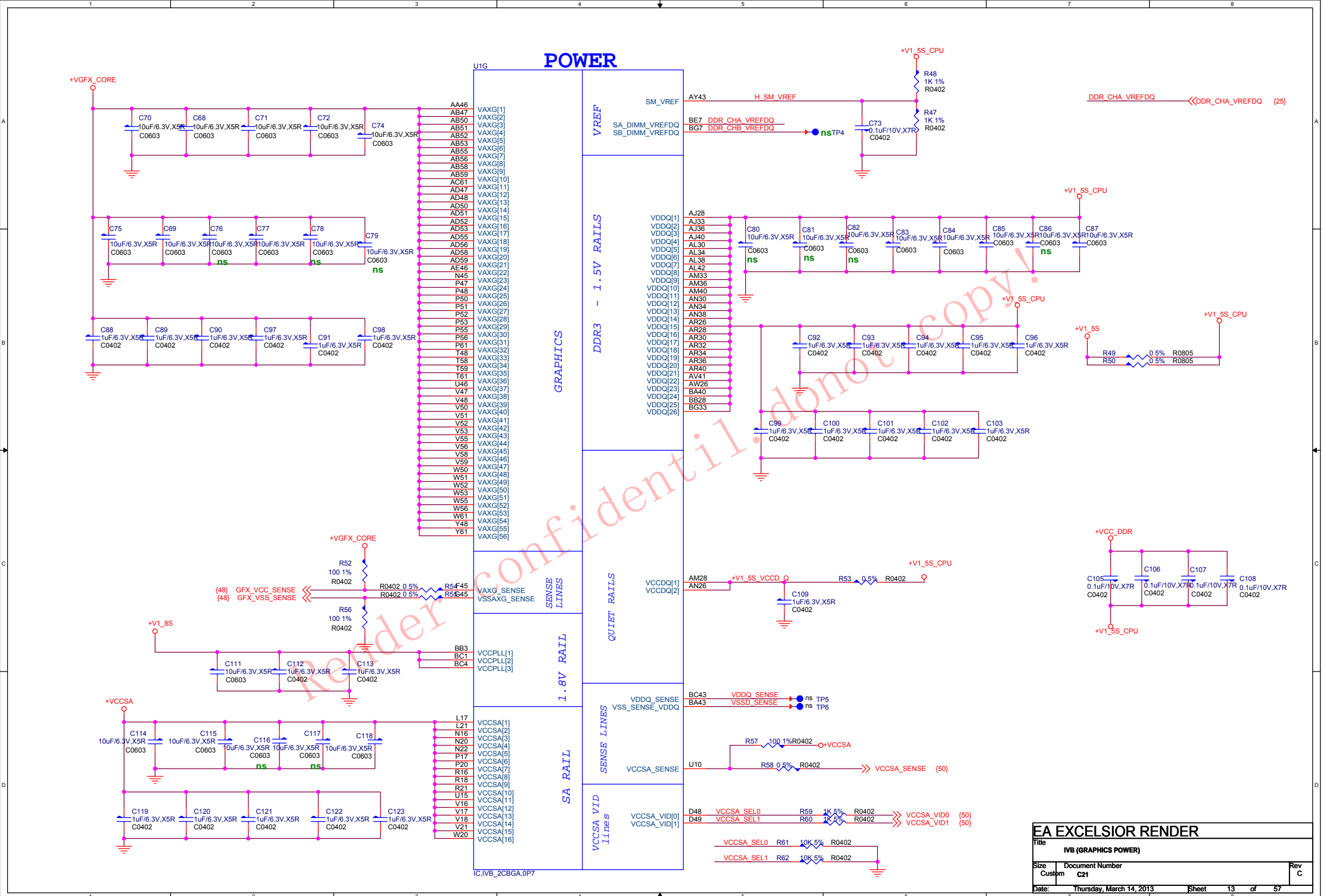
BF32  
BE33  
BD33  
AU30  
BD30  
AV30  
BG30  
BD29  
BE30  
BE28  
BD43  
AT28  
AV28  
BD46  
AT26  
AU22

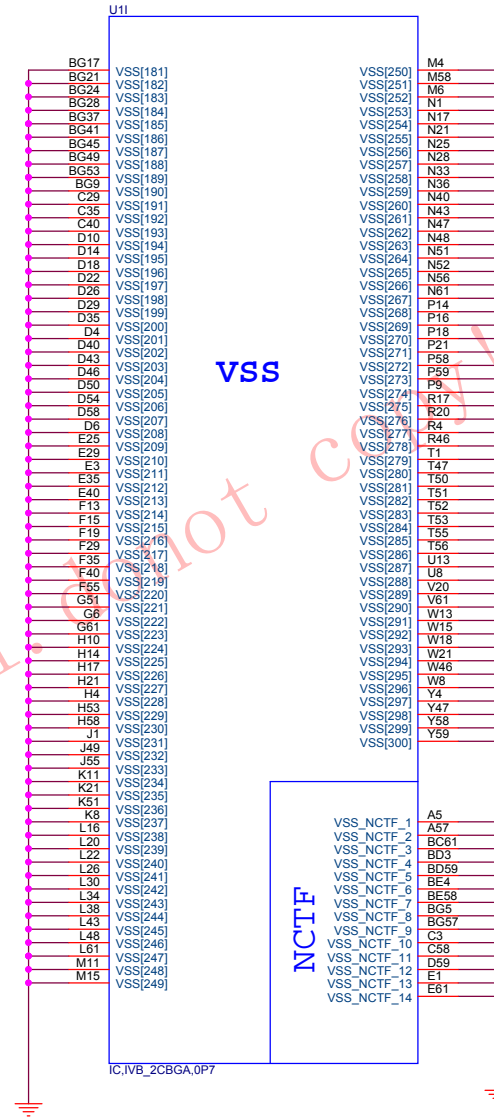
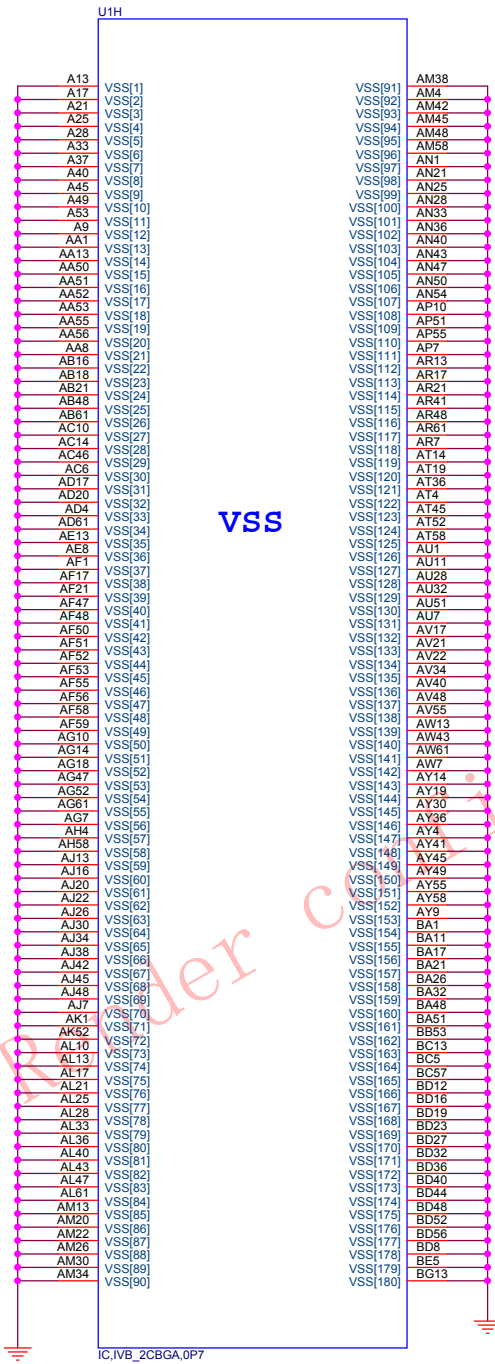
EA EXCELSIOR RENDER			
Title			
DDR3			
Size	Document Number	Rev	
Custom	C21	C	
Date:	Thursday, March 14, 2013	Sheet	11 of 57

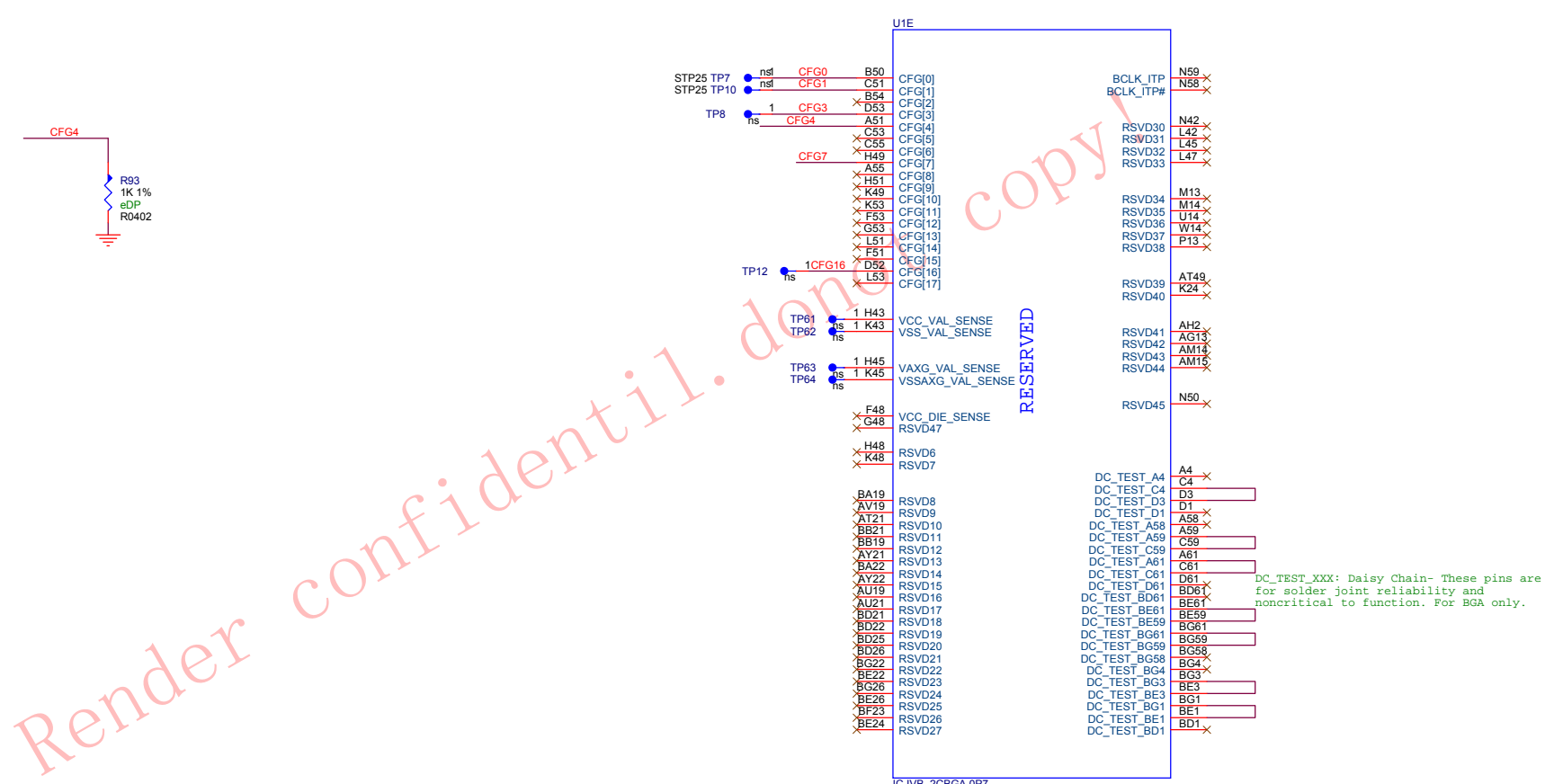
# POWER



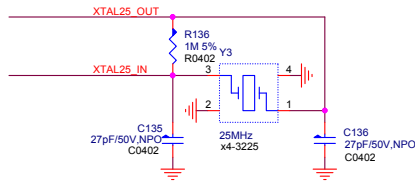
EA EXCELSIOR RENDER				
File				
IBV (POWER)				
Size	Document Number			Rev
Custom	C21			C
Date:	Thursday, March 14, 2013	Sheet	12	of 57



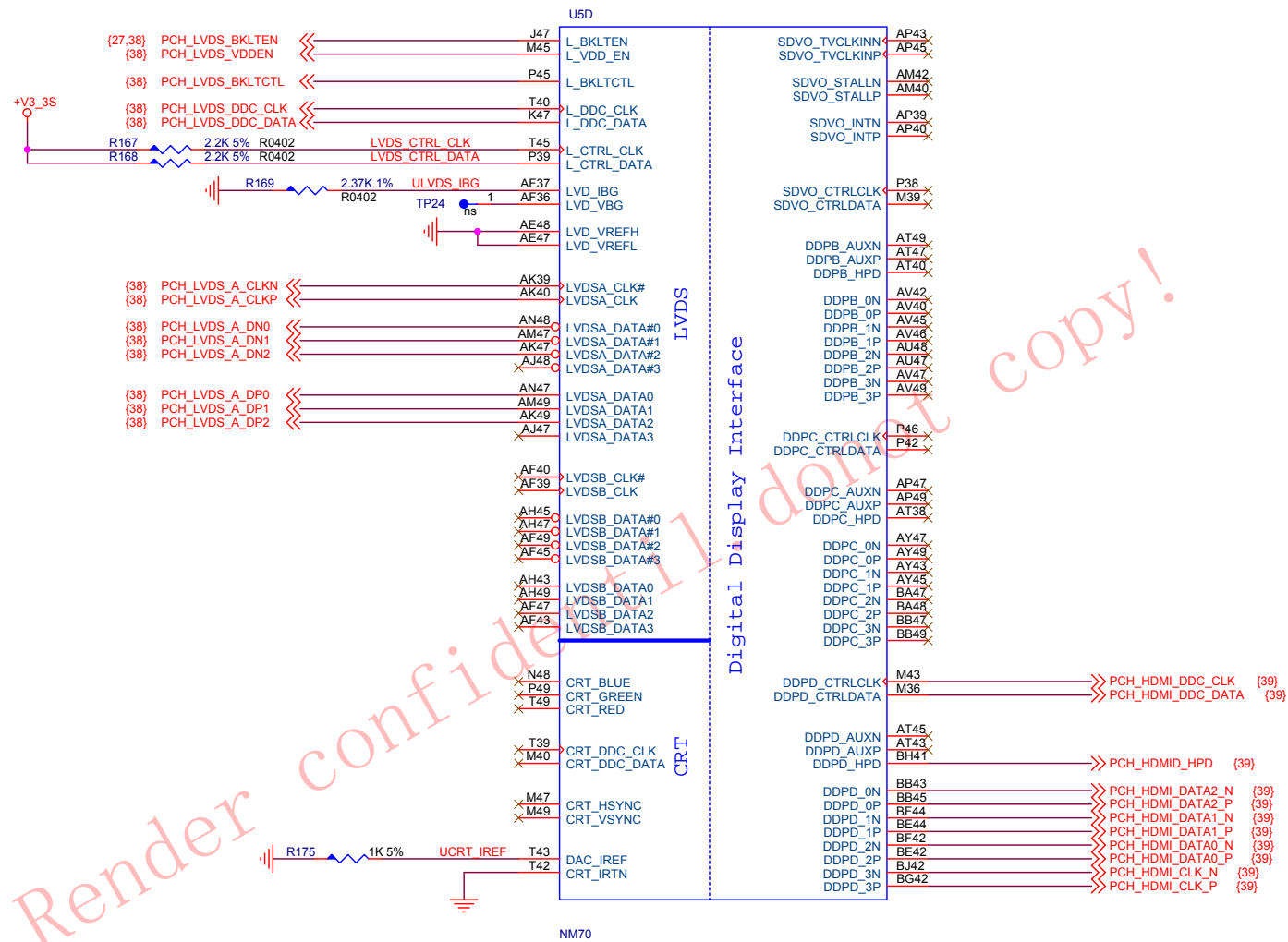


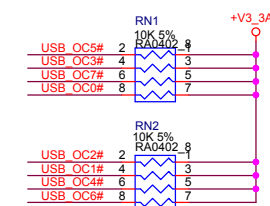




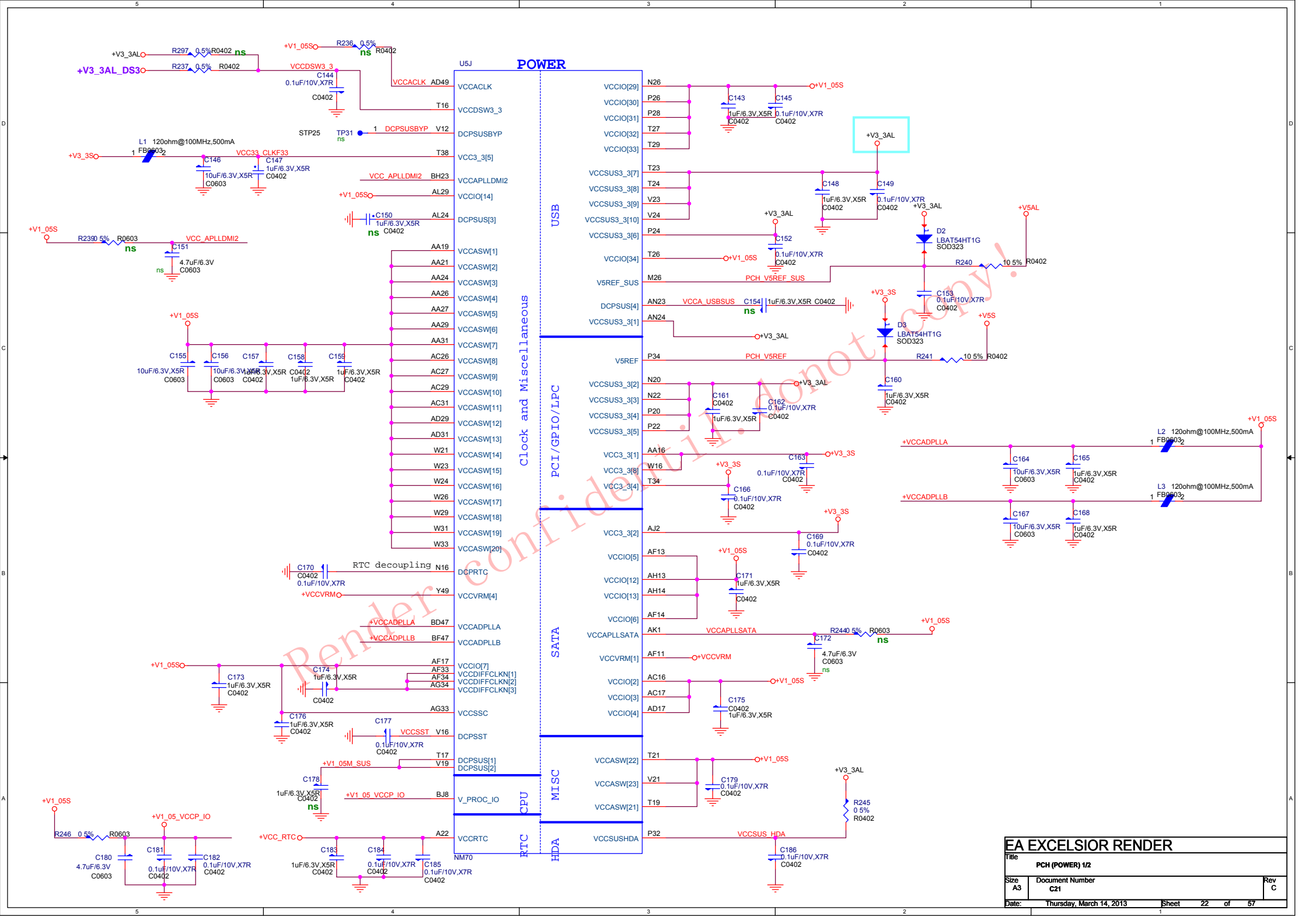


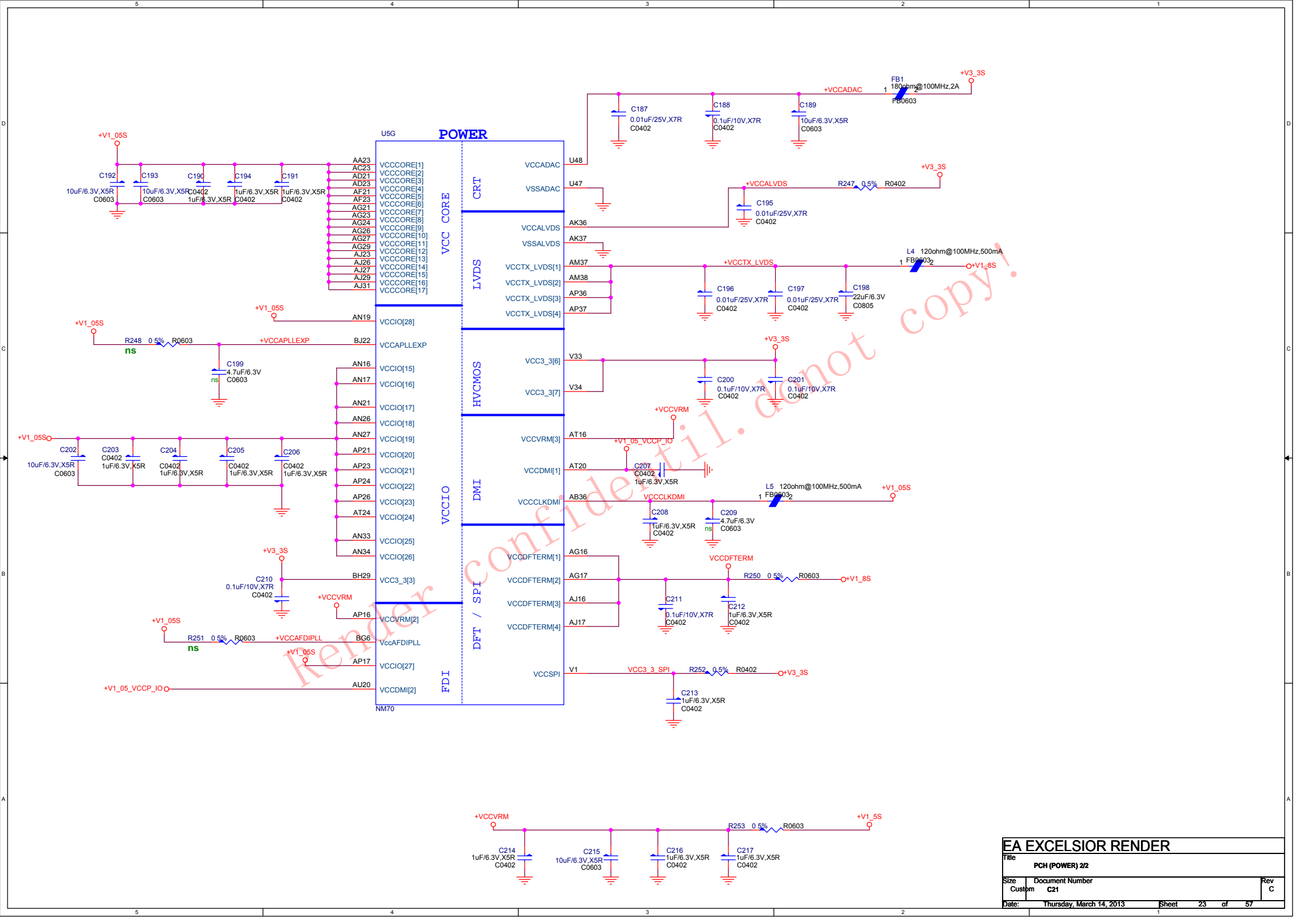


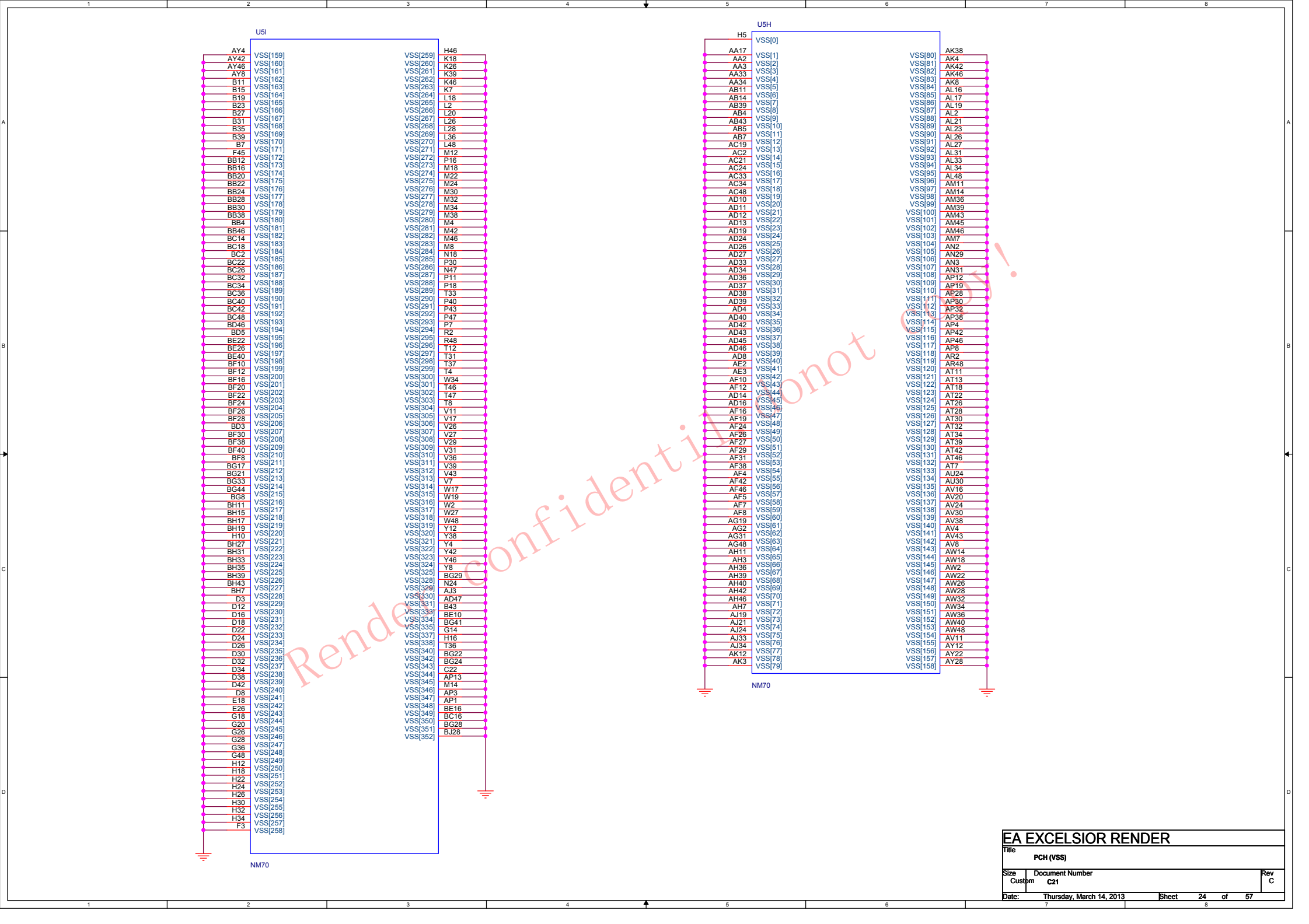




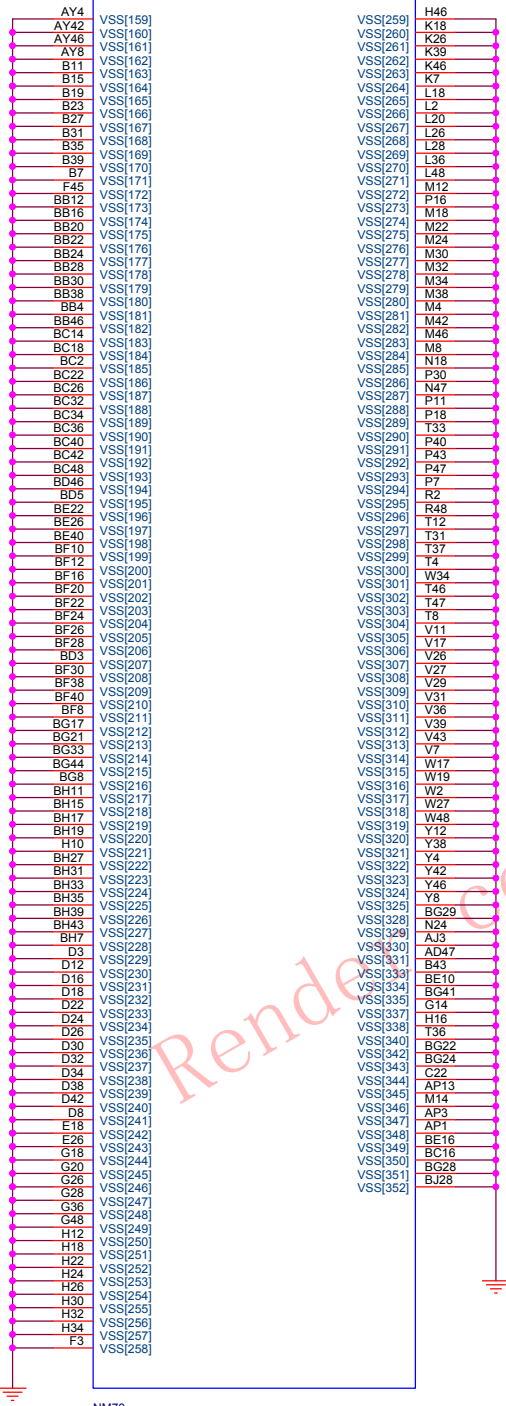






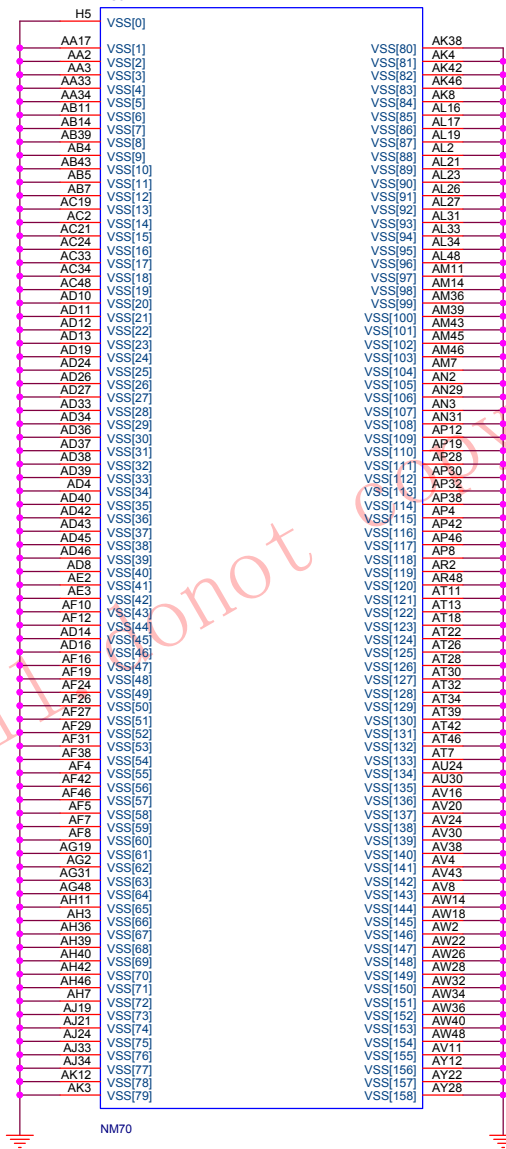


U5I



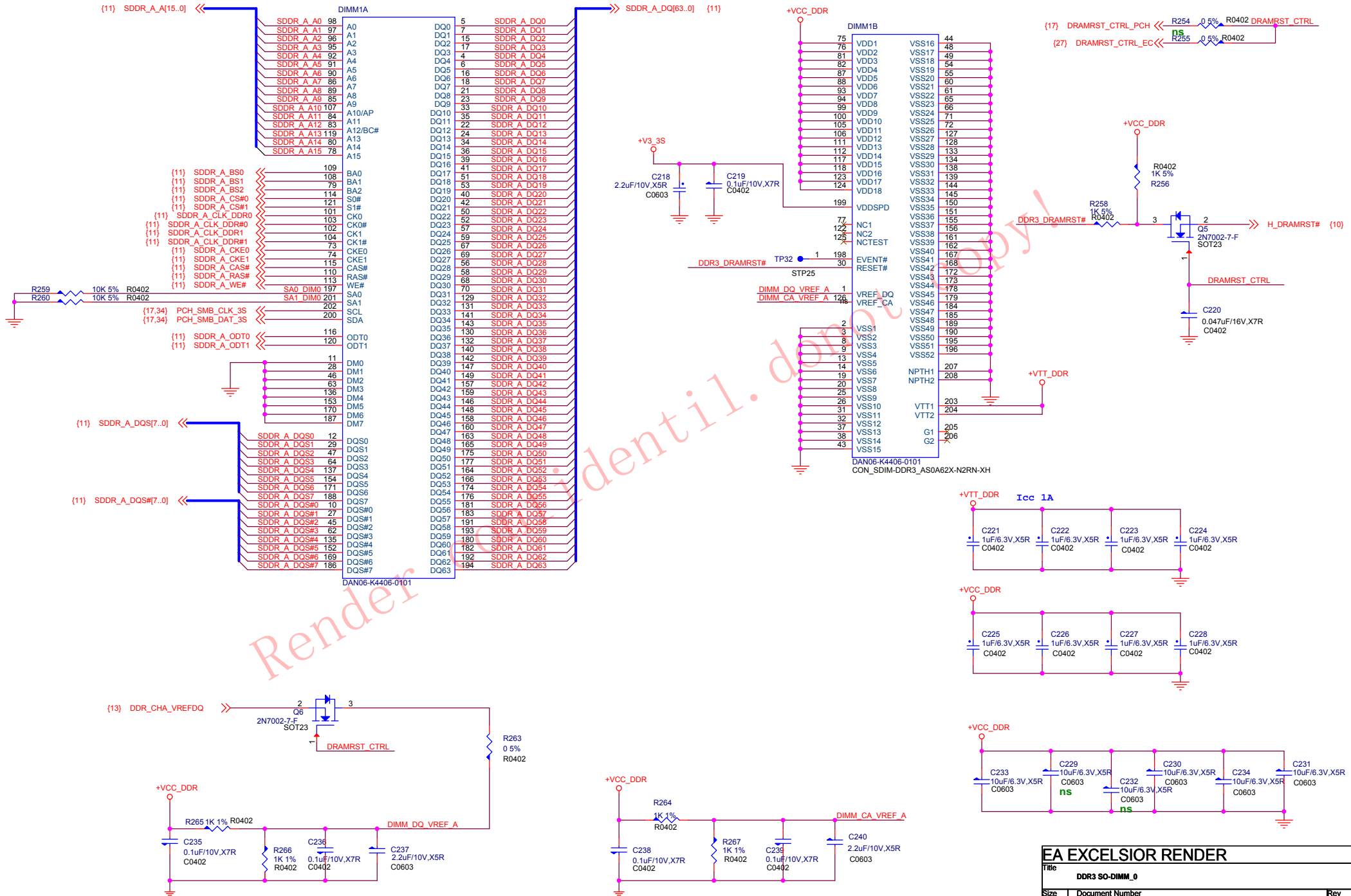
NM70

U5H



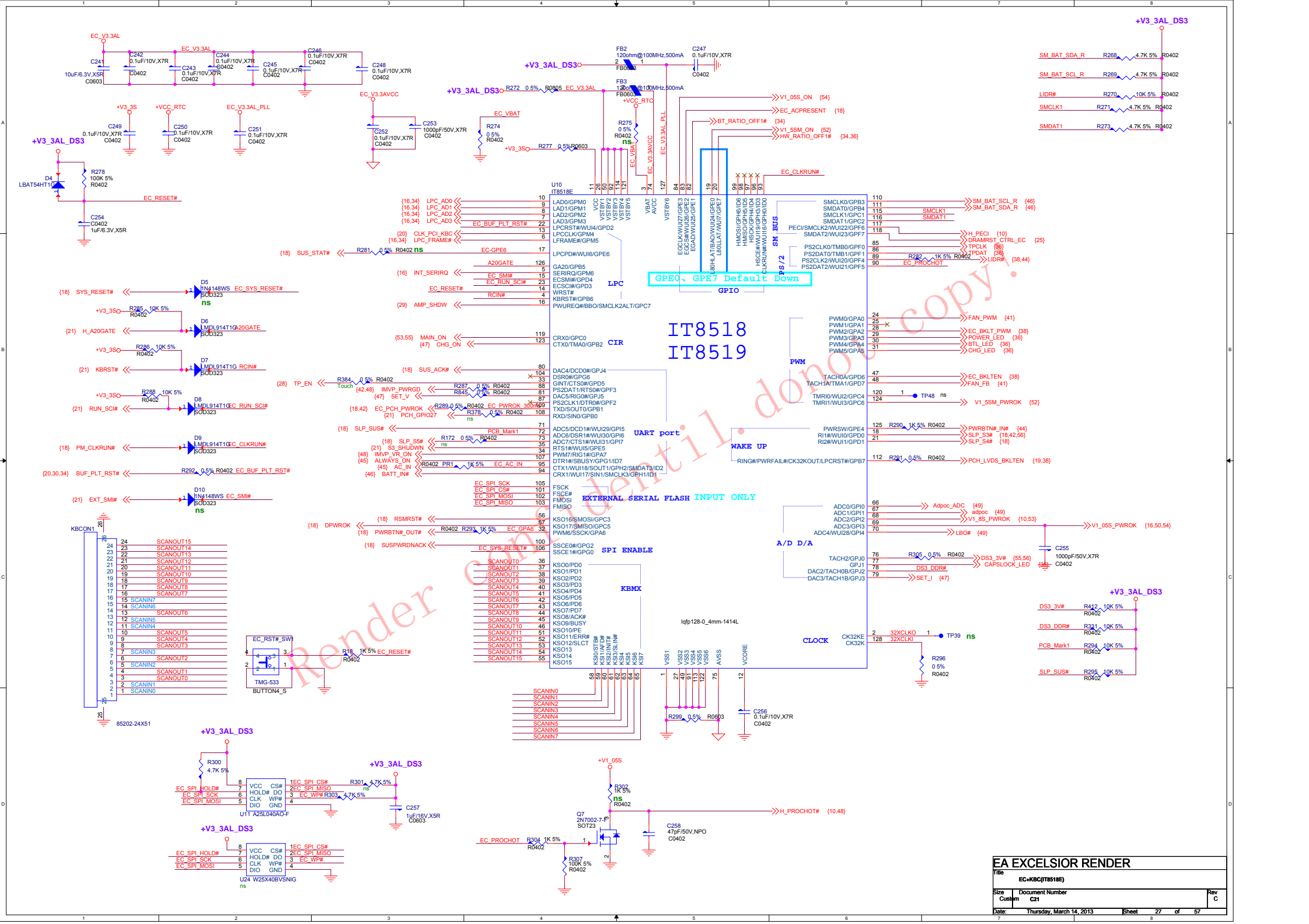
NM70

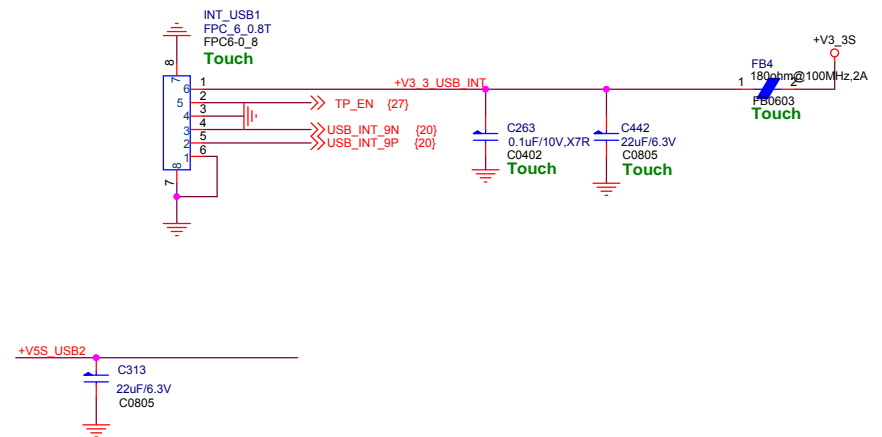
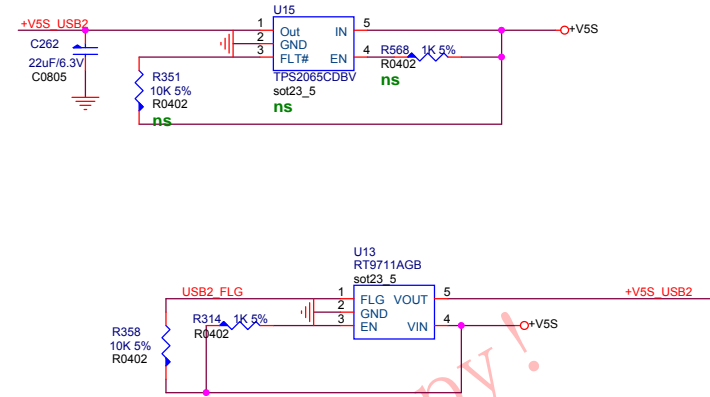
EA EXCELSIOR RENDER			
File		PCH (VSS)	
Size	Custom	Document Number	Rev
		C21	C
Date:	Thursday, March 14, 2013		Sheet 24 of 57

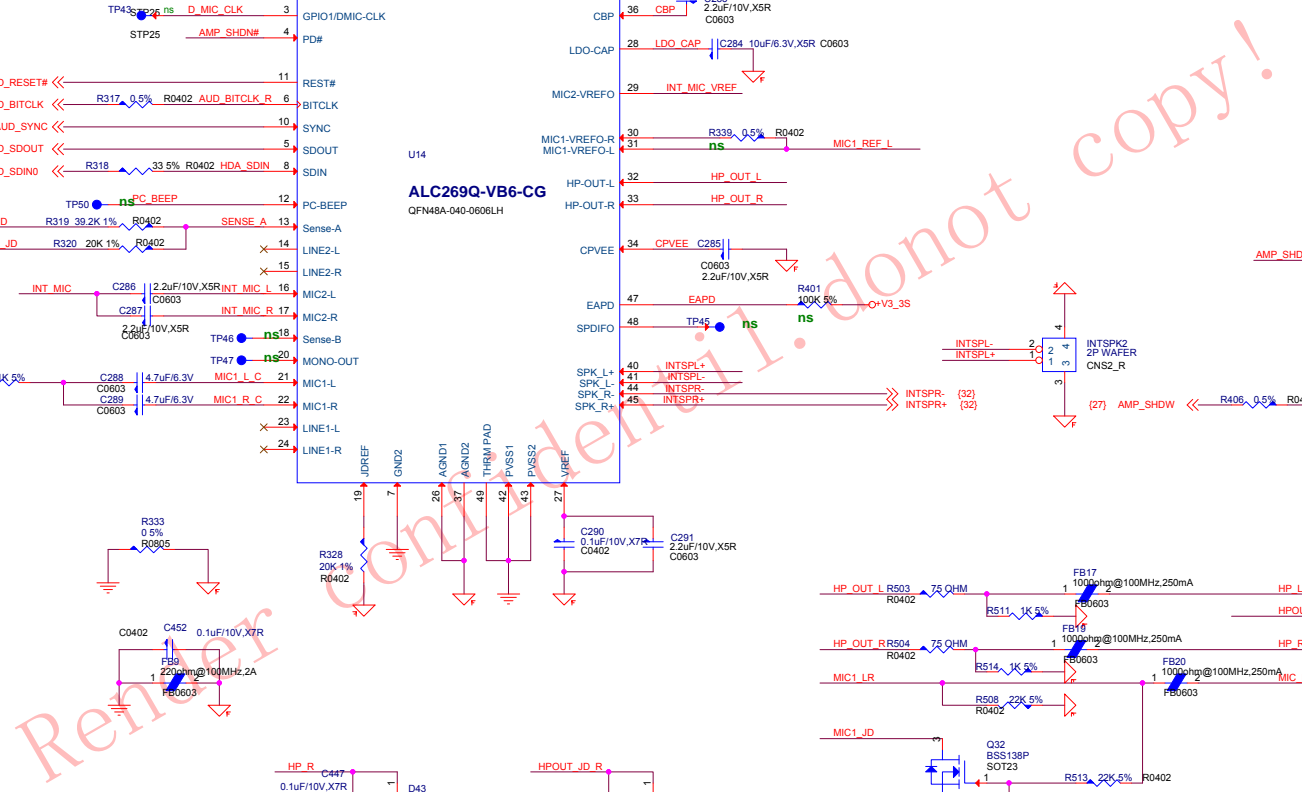


EA EXCELSIOR RENDER

Title			DDR3 SO-DIMM_0
Size	Document Number	Rev	
Custpm	C21	C	
Date:	Thursday, March 14, 2013	Sheet	25 of 57

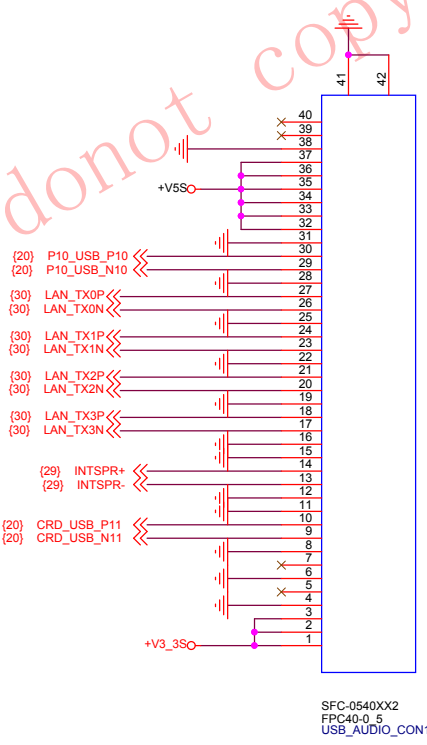




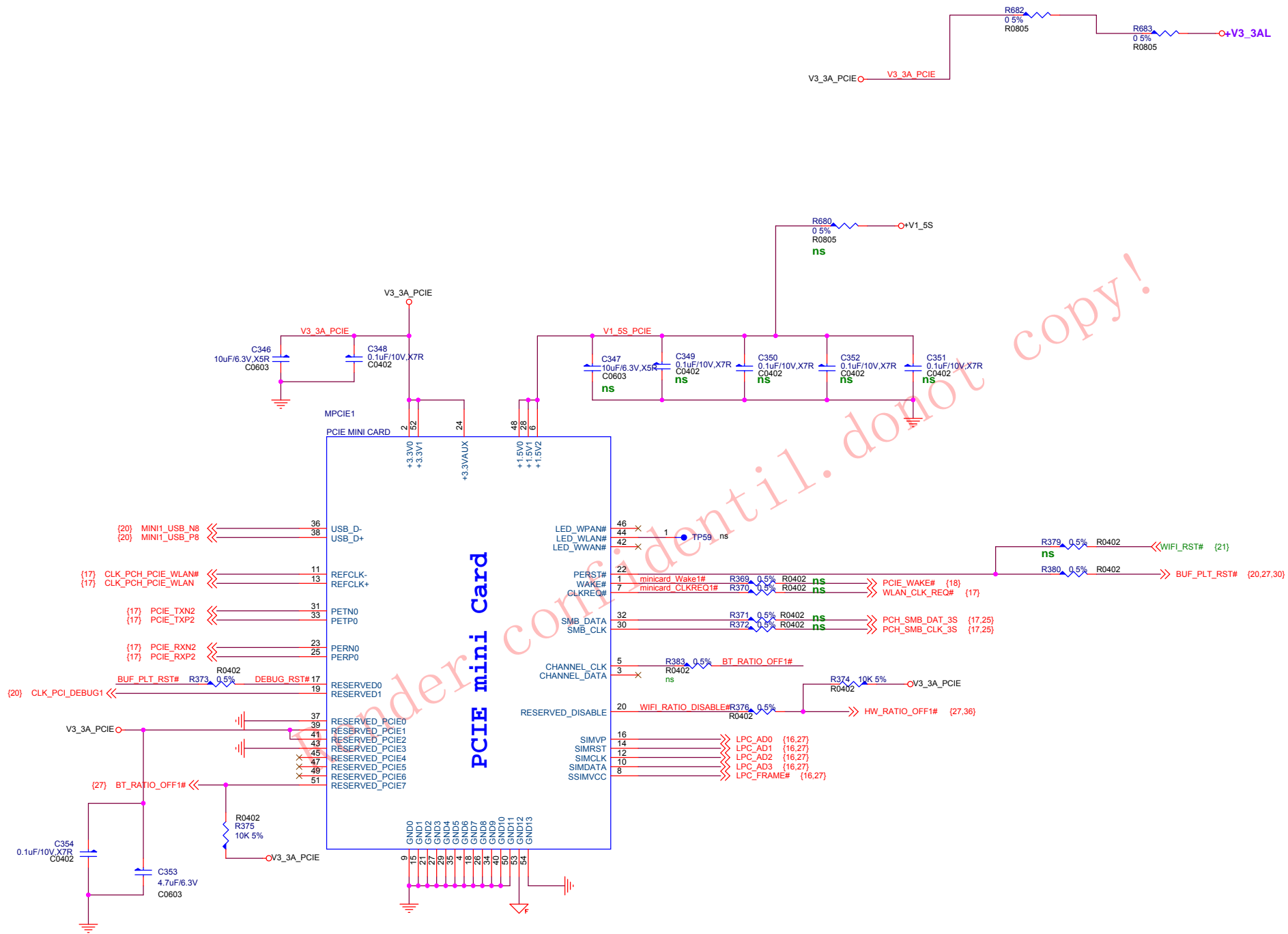




Render confidential! donot copy!



EA EXCELSIOR RENDER			
Title			
AUDIO_B_CONN,RSVD USB			
Size	Document Number		Rev
A3	C21		C
Date:	Thursday, March 14, 2013	Sheet	32 of 57

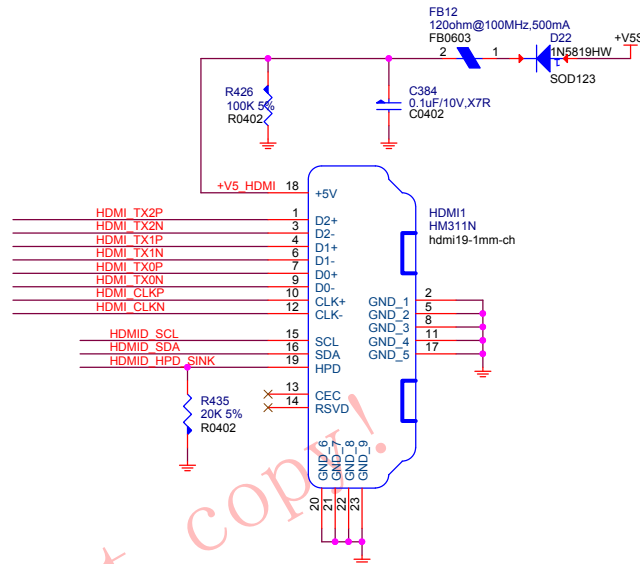
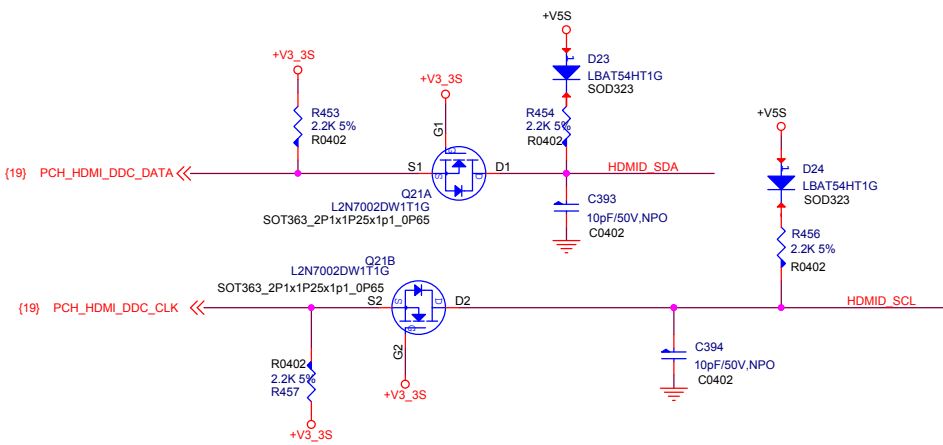
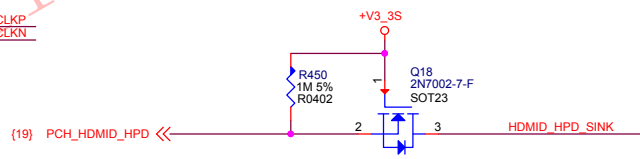
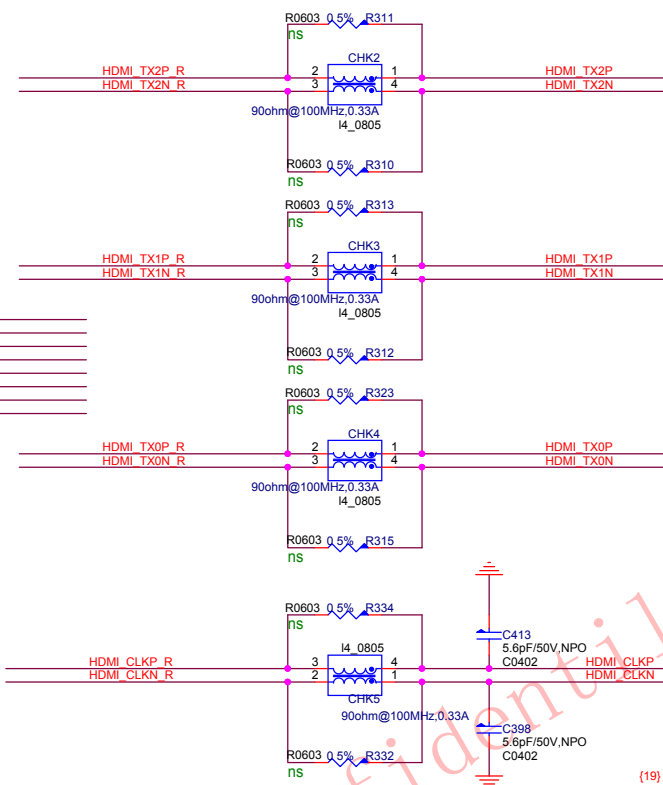
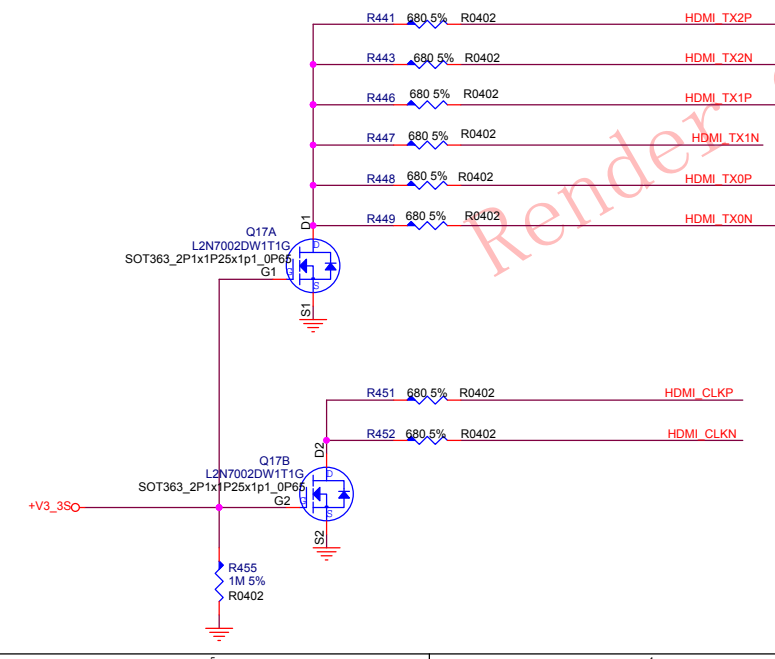


EA EXCELSIOR RENDER			
File: WLAN Mini-PCIE Card			
Size: A3	Document Number: C21		Rev: C
Date: Thursday, March 14, 2013	Sheet: 34	of: 57	

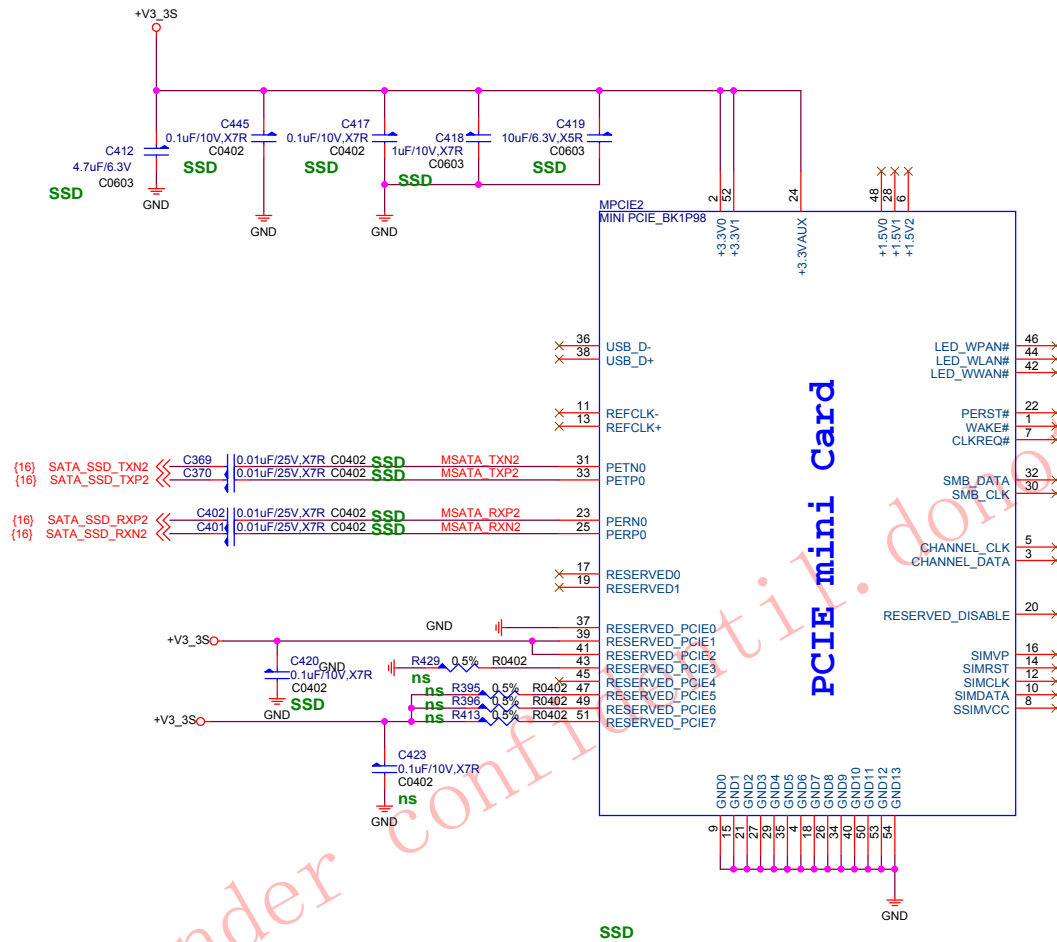




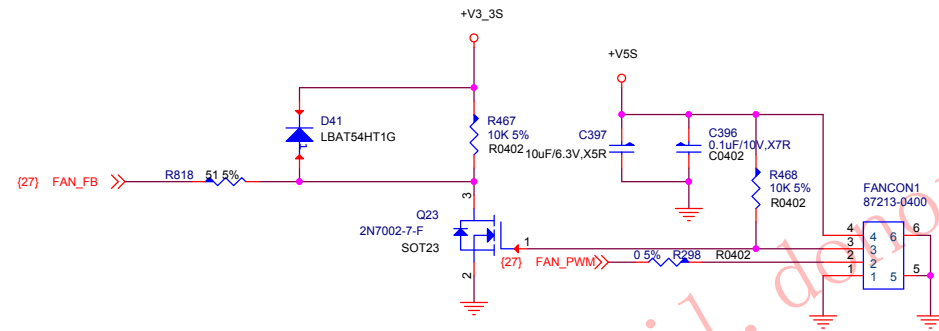
- (19) PCH\_HDMI\_DATA2\_P
- (19) PCH\_HDMI\_DATA2\_N
- (19) PCH\_HDMI\_DATA1\_P
- (19) PCH\_HDMI\_DATA1\_N
- (19) PCH\_HDMI\_DATA0\_P
- (19) PCH\_HDMI\_DATA0\_N
- (19) PCH\_HDMI\_CLK\_P
- (19) PCH\_HDMI\_CLK\_N

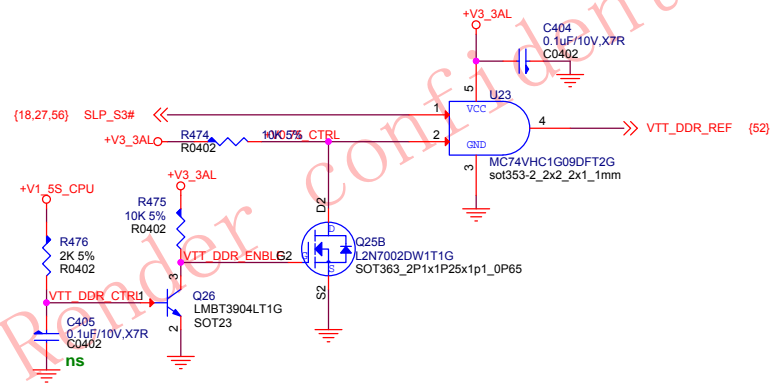
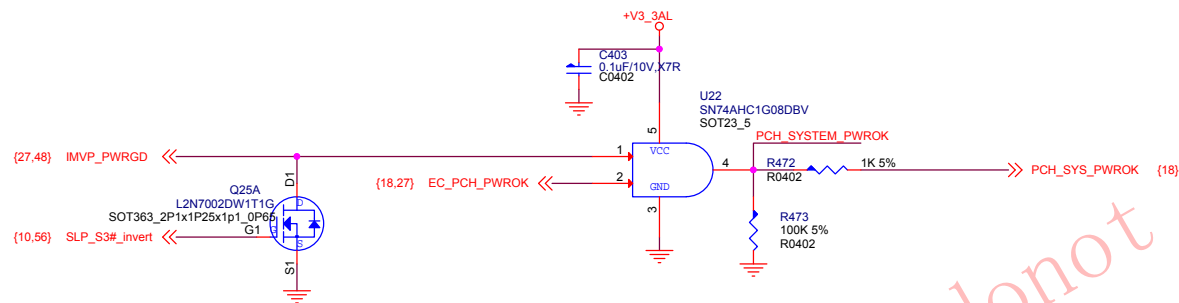


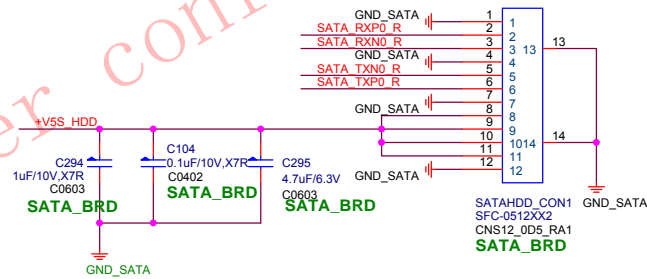
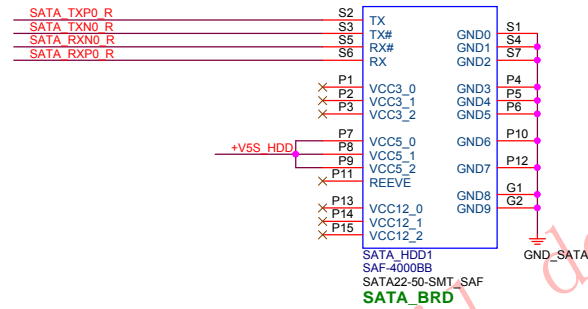
EA EXCELSIOR RENDER			
Title	HDMI		
Size	Document Number	Rev	
A3	C21	C	
Date:	Thursday, March 14, 2013	Sheet	39 of 57

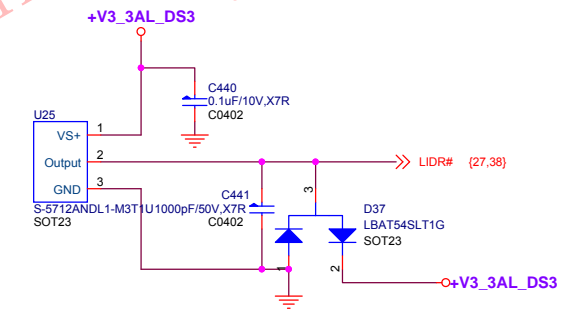
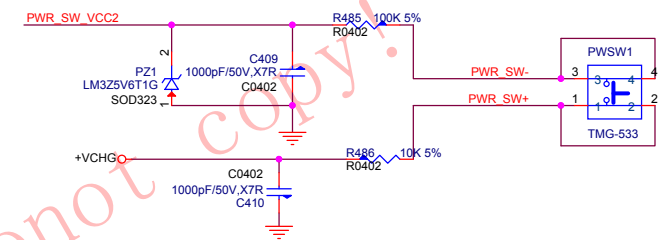
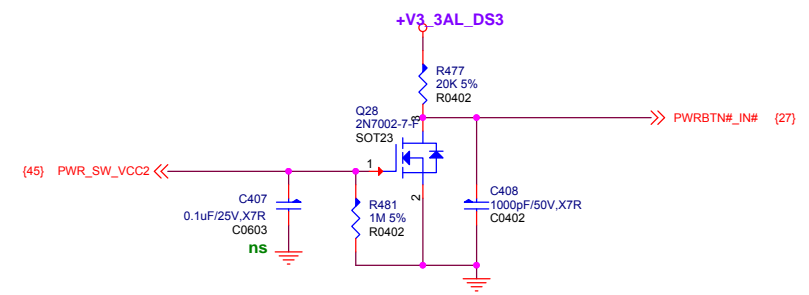


Title		
SATA BOARD		
Size	Document Number	Rev
A3	C21	C
Date:	Thursday, March 14, 2013	Sheet 40 of 57

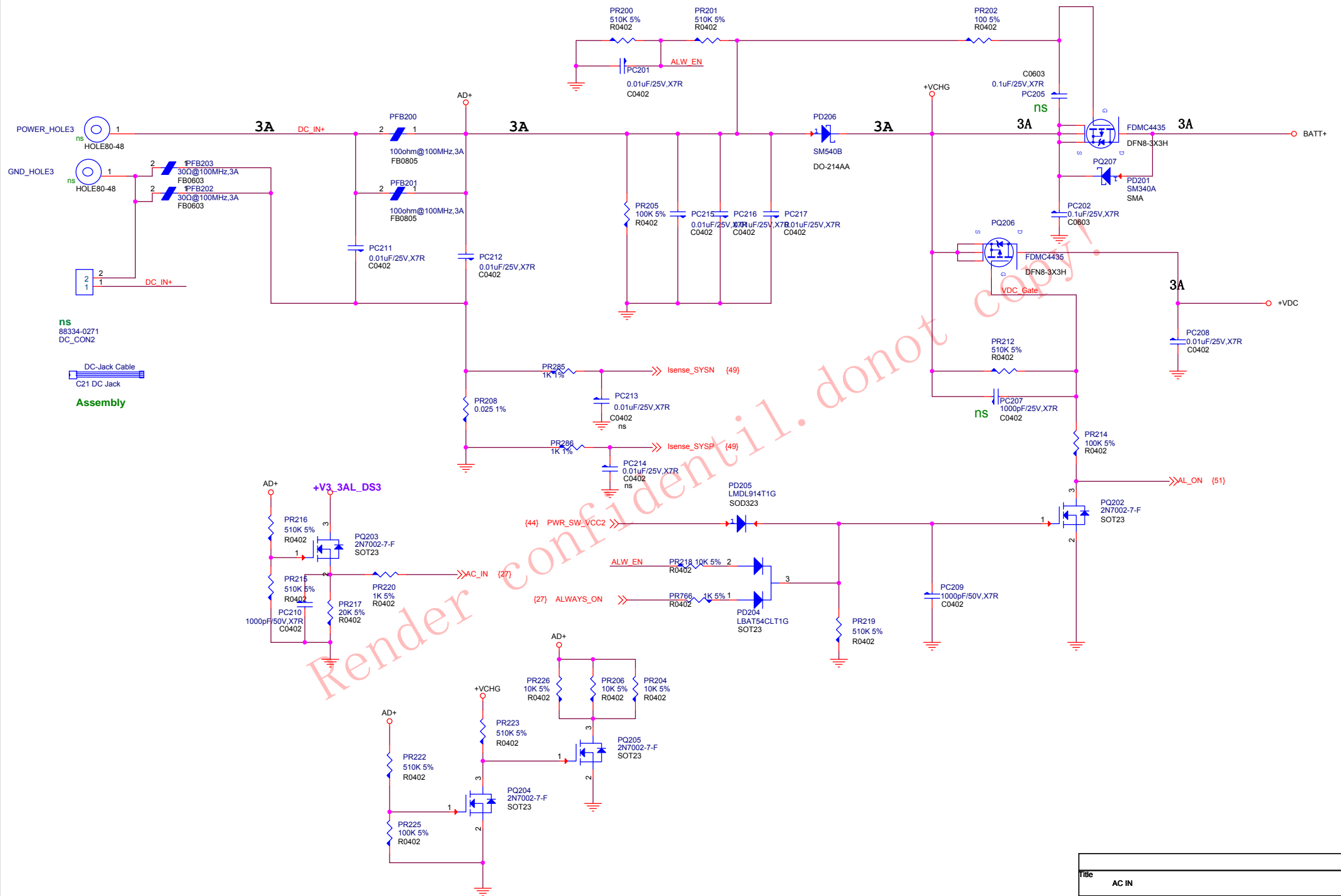






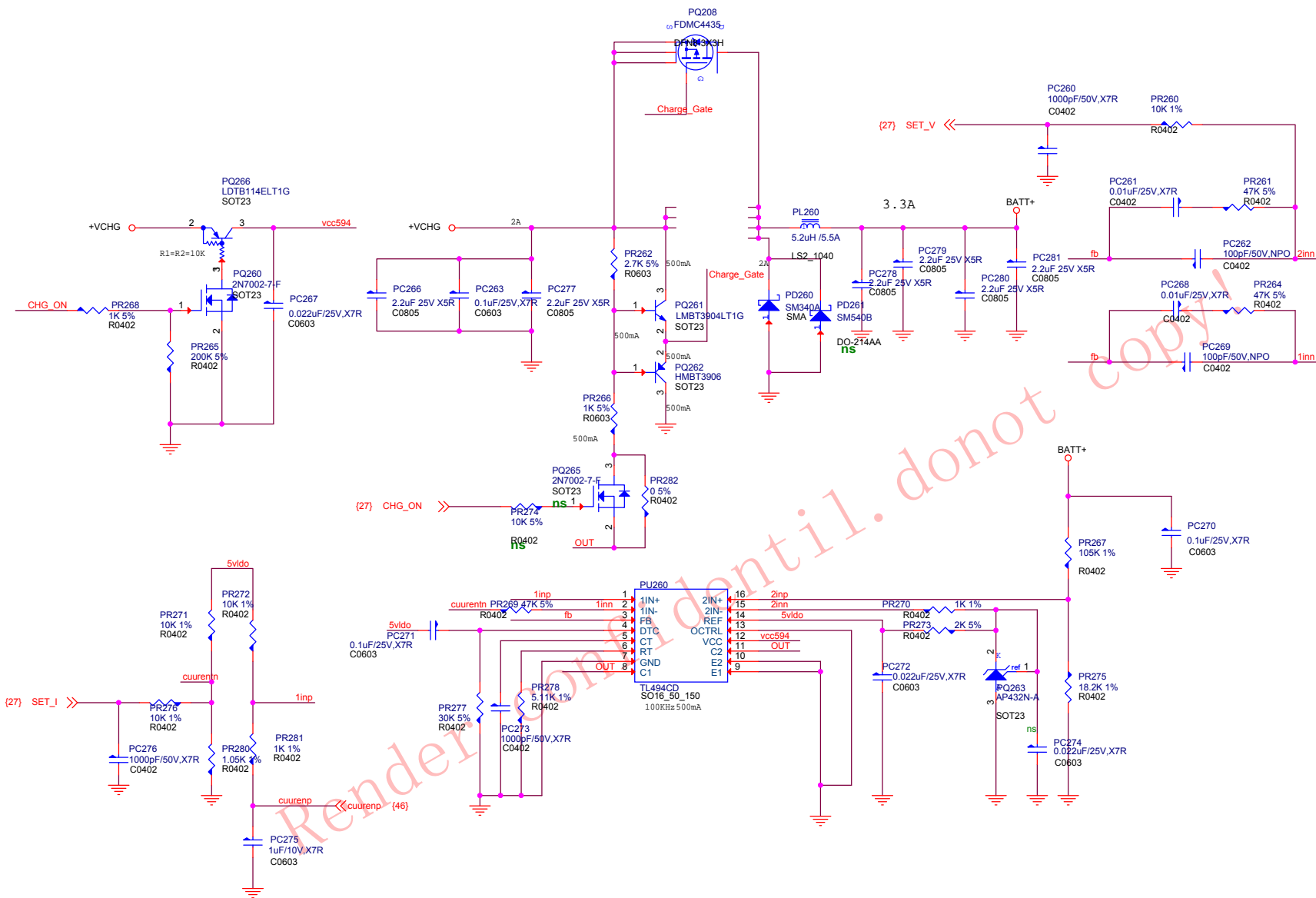


Render confidential. do not copy!



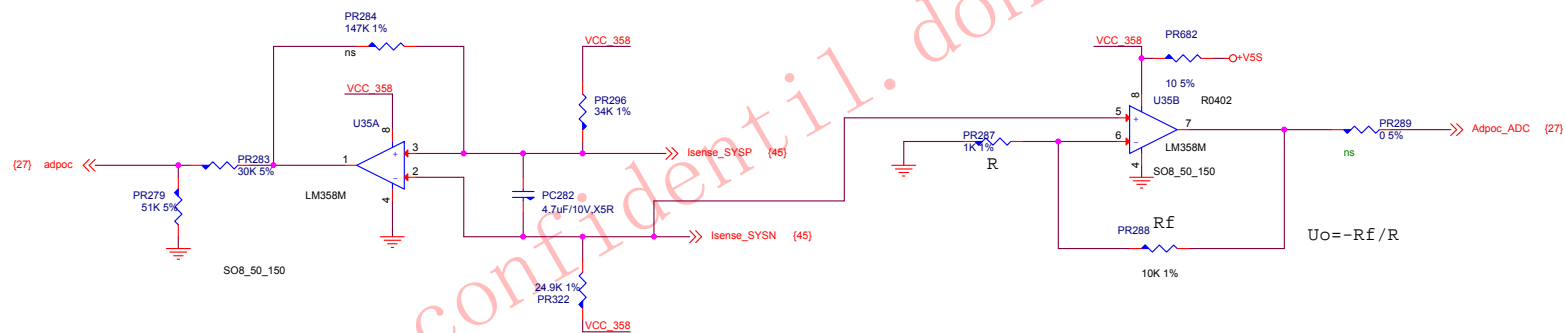
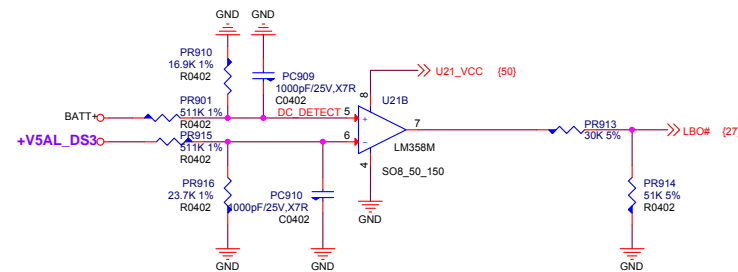
Title			AC IN
Size	A3	Document Number	C21
Date:	Thursday, March 14, 2013	Sheet	45 of 57





Title		
CHARGER		
Size	Document Number	Rev
A3	C21	C
Date:	Thursday, March 14, 2013	Sheet 47 of 57



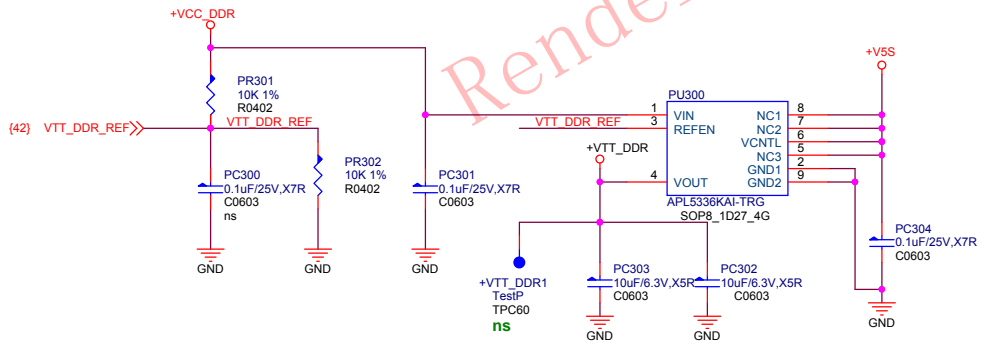
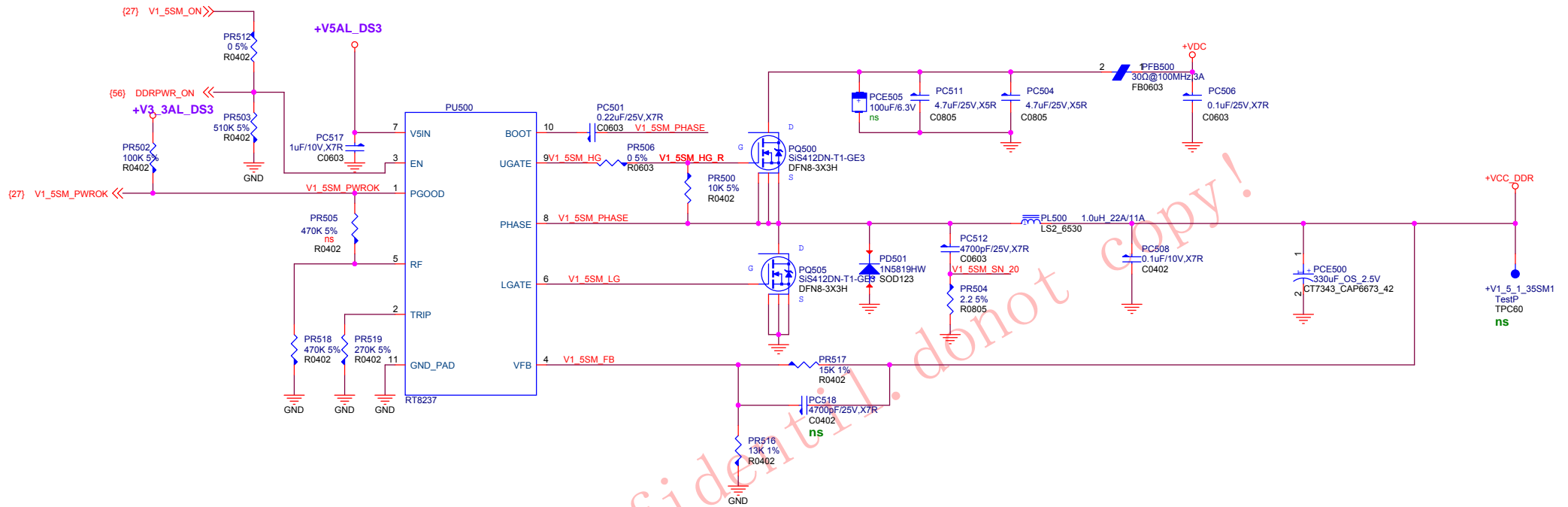


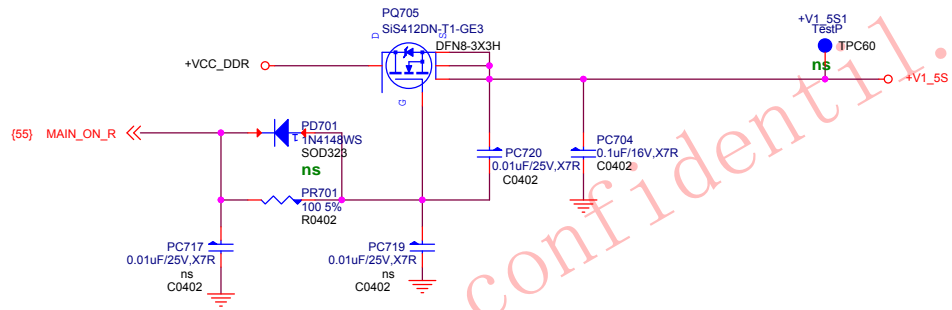
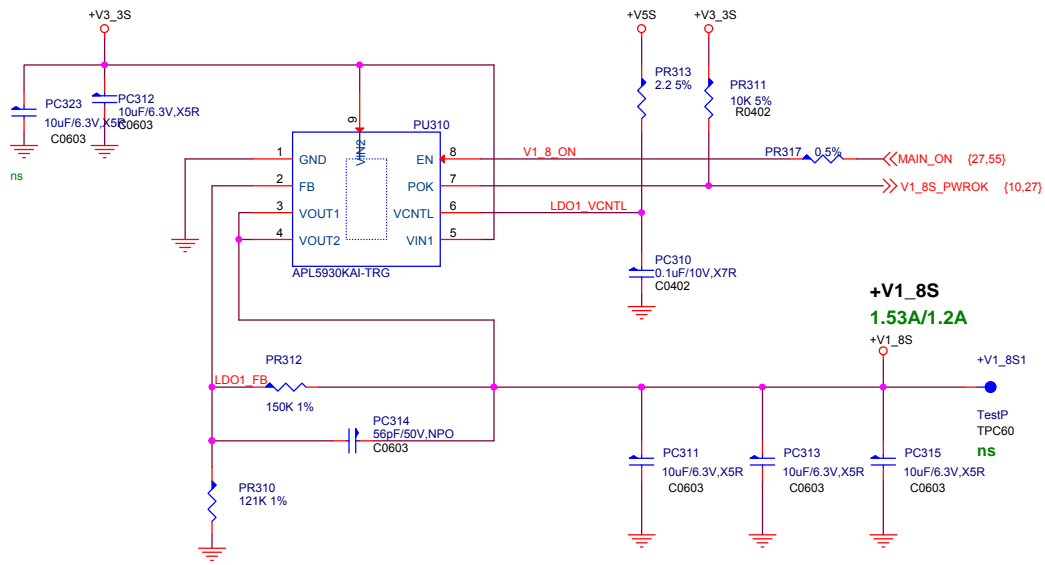
# EA EXCELSIOR RENDER

Title			VCCP_CORE(62882)
Size	Document Number	Rev	
CustomC21		C	
Date:	Thursday, March 14, 2013	Sheet	49 of 57

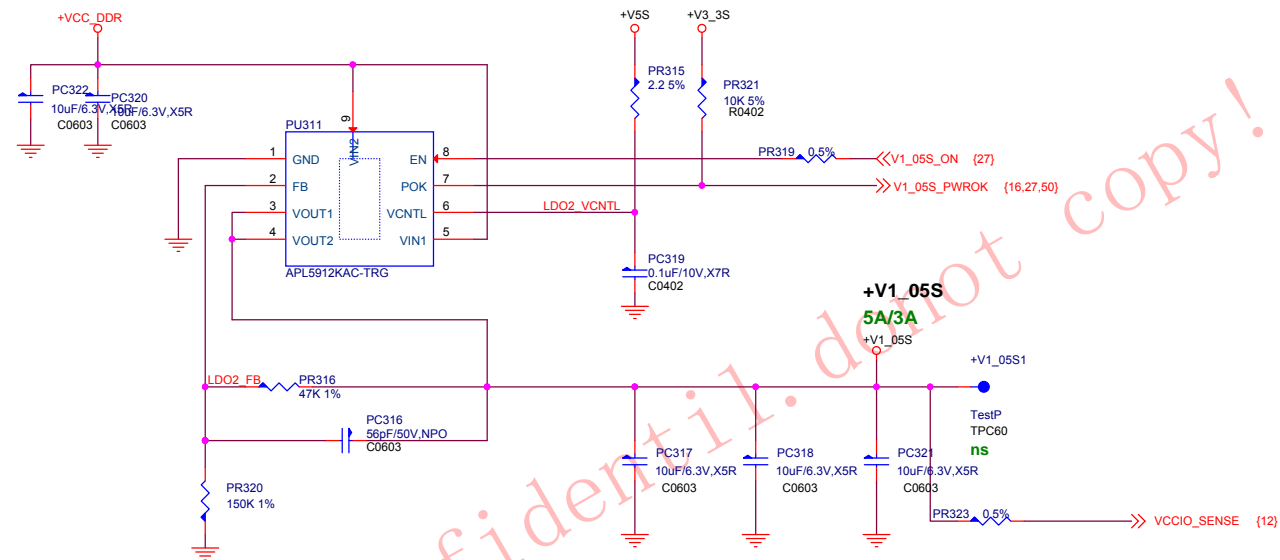




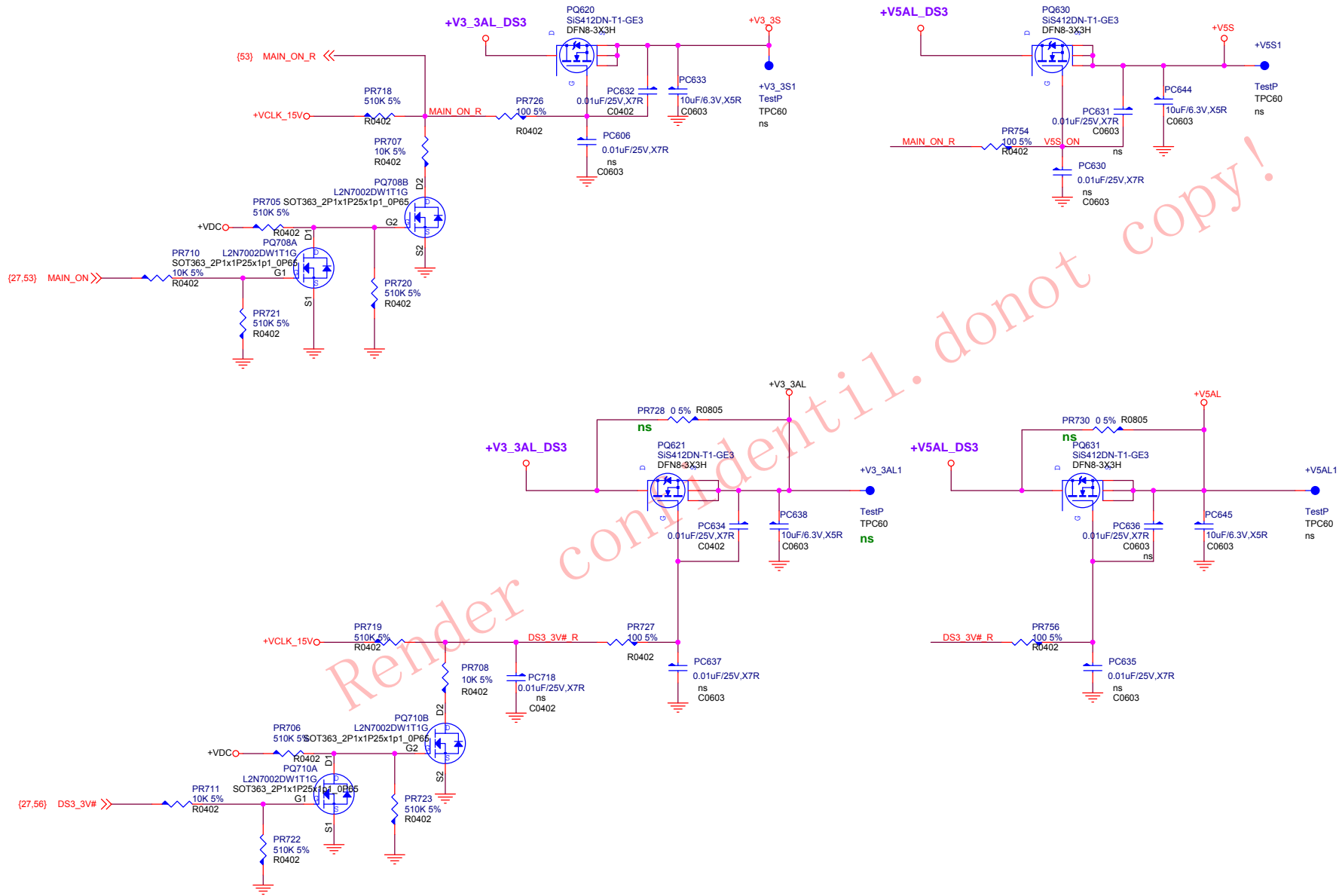




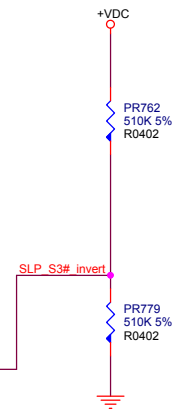
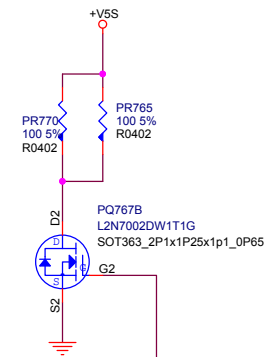
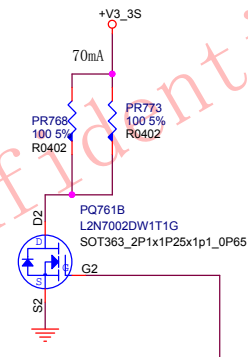
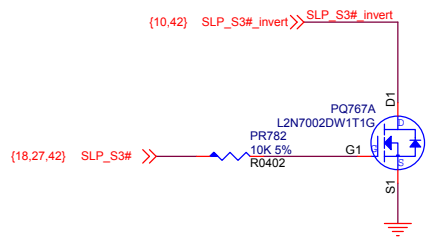
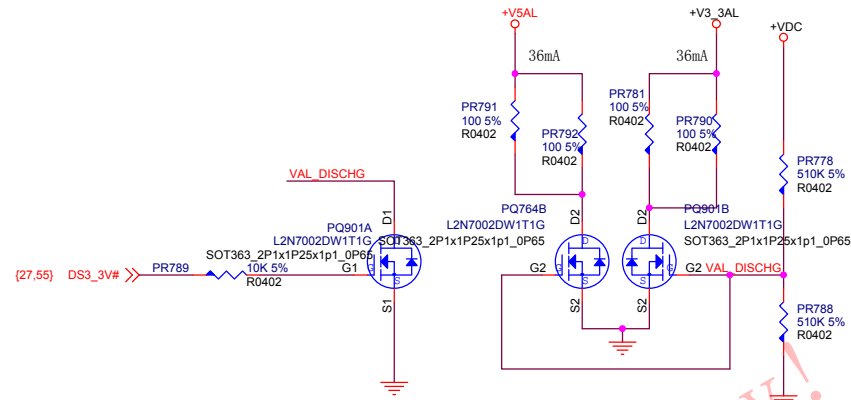
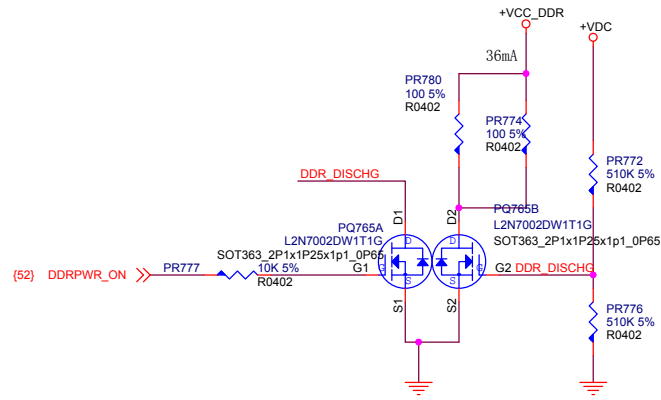
Title		
V1_8S		
Size	Document Number	Rev
A3	C21	C
Date:	Thursday, March 14, 2013	Sheet 53 of 57



Title		
<Title>		
Size	Document Number	
A3	C21	
Date:	Thursday, March 14, 2013	Sheet 54 of 57
		Rev C



Title		
SWITCH POWER		
Size	Document Number	Rev
A3	C21	C
Date:	Thursday, March 14, 2013	Sheet 55 of 57



Render confidential. donot copy!