MS-7366 Micro ATX

Version: 0B_070829B

CPU: **Intel Pentium 4 Cedar Mill / Prescott , Pentium D Smithfield / Presler and Conroe / Kentsfield family processors in LGA775 Package.**

System Chipset:

NVIDIA MCP73

On Board Device:

BIOS -- SPI Flash 8M
Azalia Codec -- ALC888
LPC Super I/O -- FINTEK F71882FG
LAN -- Realtek RTL8211BL-GR
CLOCK Gen -- Integated in MCP73
1394 Controller -- VT6308P

Main Memory:

Dual-channel DDR-II * 2 (Max 4GB)

Expansion Slots:

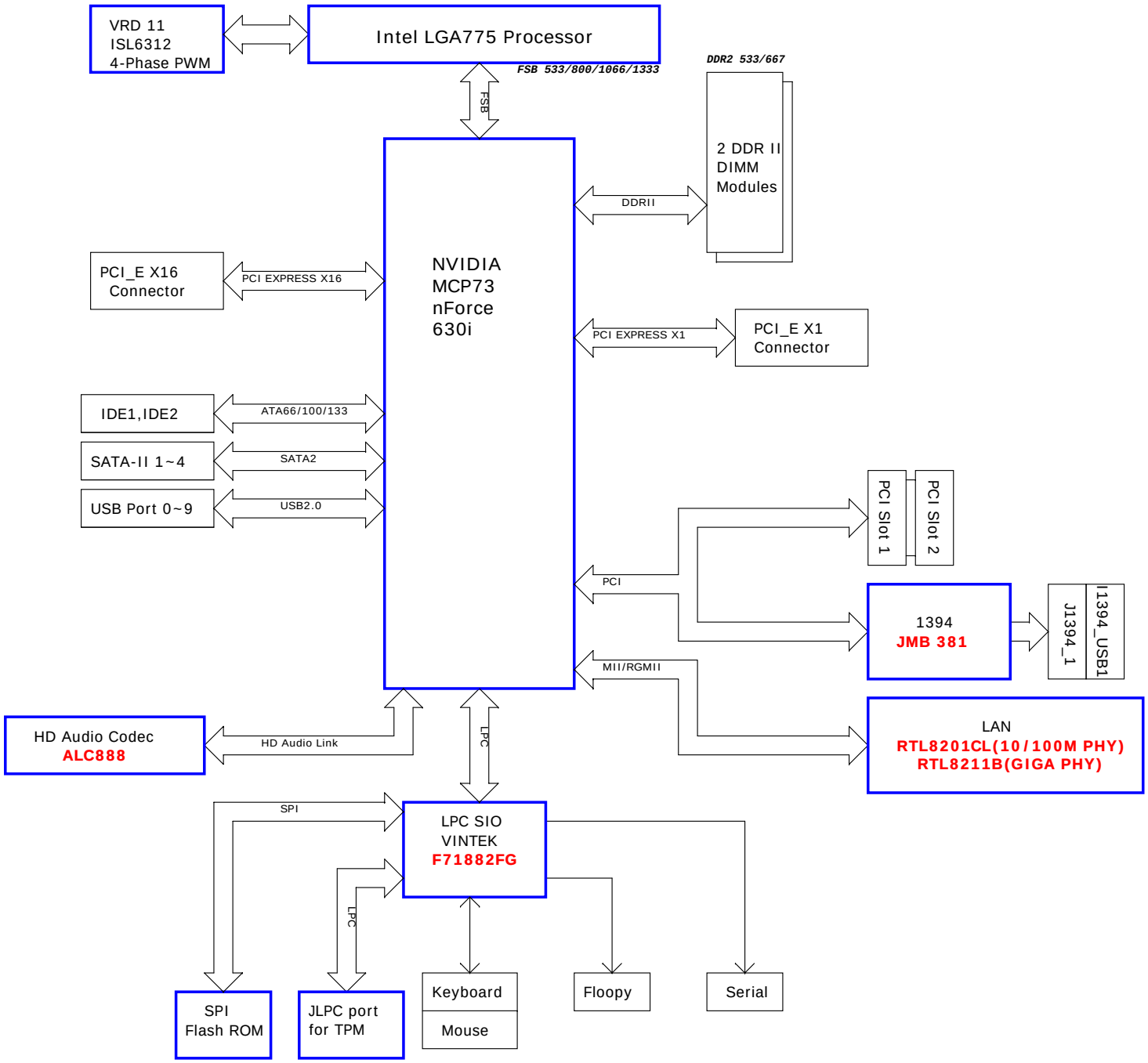
PCI EXPRESS X16 SLOT *1
PCI EXPRESS X1 SLOT * 1
PCI SLOT * 2

Intersil PWM:

Controller: ST L6703 (3 Phases)
Driver:ST L6703

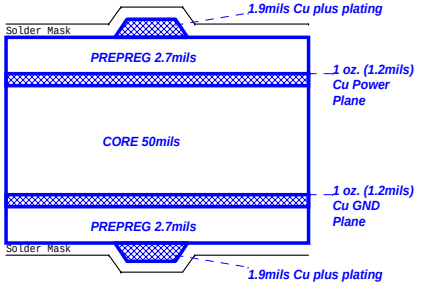
OPT	Function	Orcad Configure	BOM
A	MCP73U(HDMI,D-SUB)/F71882FG/ALC888/RTL8211BL/JMB381	Cfg-U	601-7366-B10
B	MCP73PV(DVI,D-SUB)/F71882FG/ALC888/RTL8211BL	Cfg-PV	
C	MCP73S(D-SUB)/F71882FG/ALC888/RTL8211BL	Cfg-S	
D	MCP73V(D-SUB)/F71882FG/ALC888/RTL8201CL	Cfg-V	

Block Diagram



Board Stack-up

(1080 Prepreg Considerations)



Single End 50ohm Top/Bottom : 4mils
USB2.0 - 100ohm : 20/4/8/4/20
HDMI - 100ohm : 20/4/8/4/20
SATA - 100ohm : 20/4/8/4/20
LAN - 100ohm : 20/4/8/4/20
PCIE - 100ohm : 20/4/8/4/20
IEEE1394 - 110ohm : 15/4/9/4/15
IDE : 15/4/8/4/15

MSI	MICRO-STAR INT'L CO.,LTD	
	MS-7366	
Size: Custom	Document Description: BLOCK DIAGRAM	Rev: 0B
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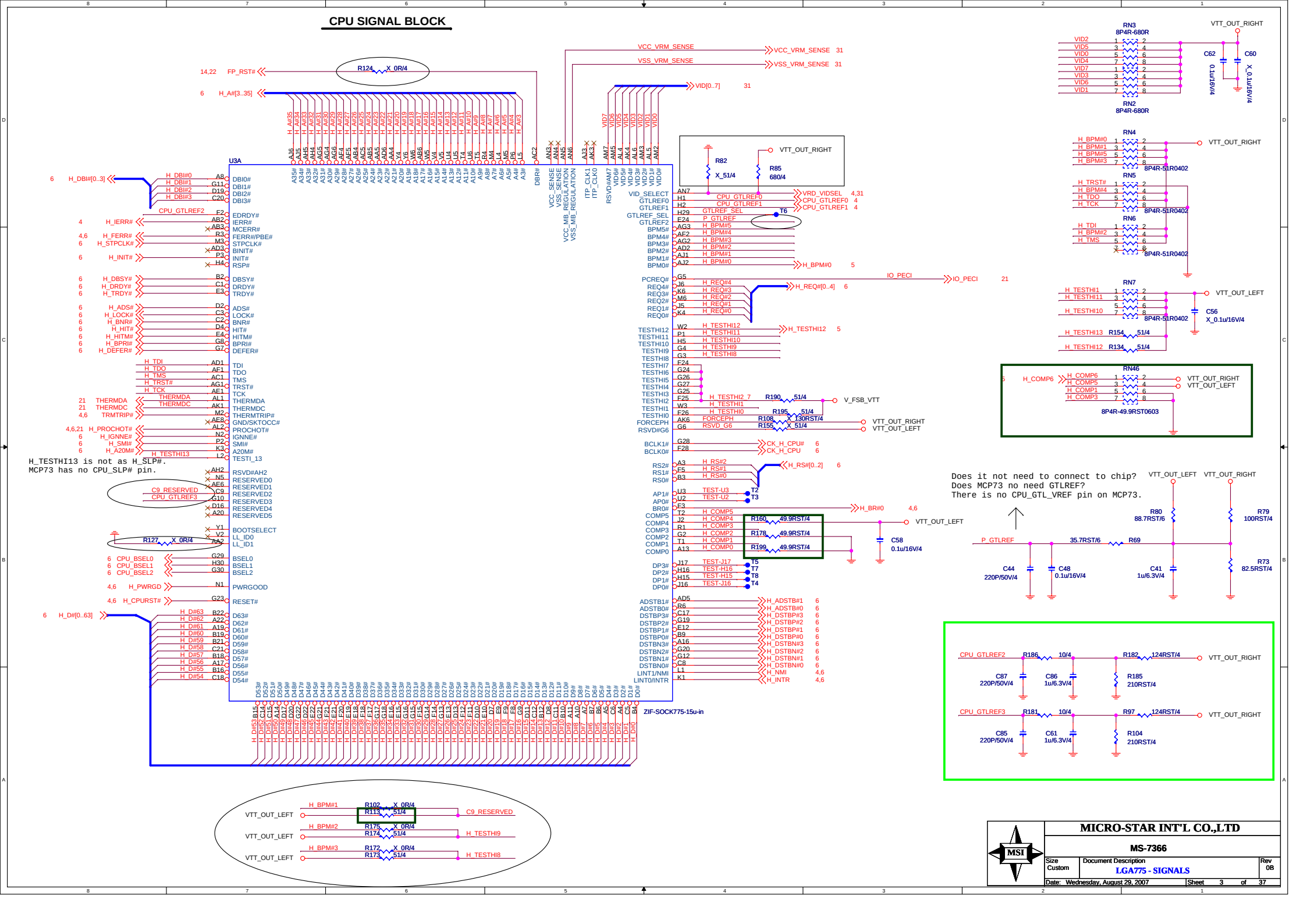
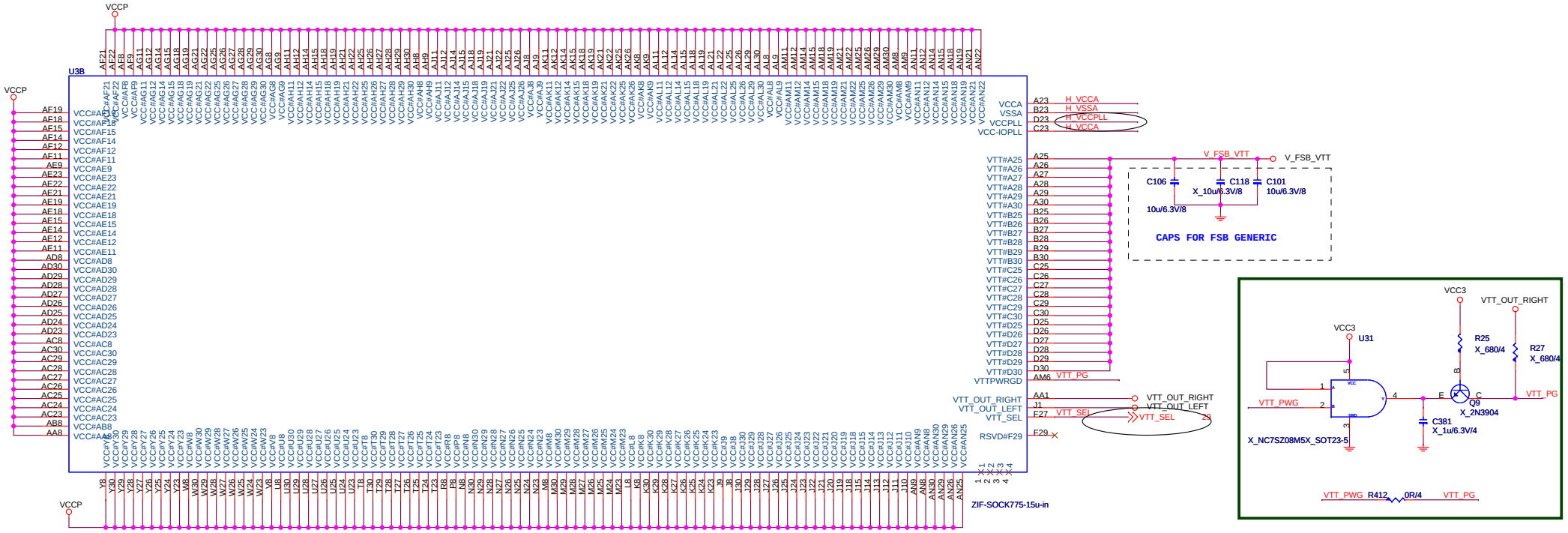
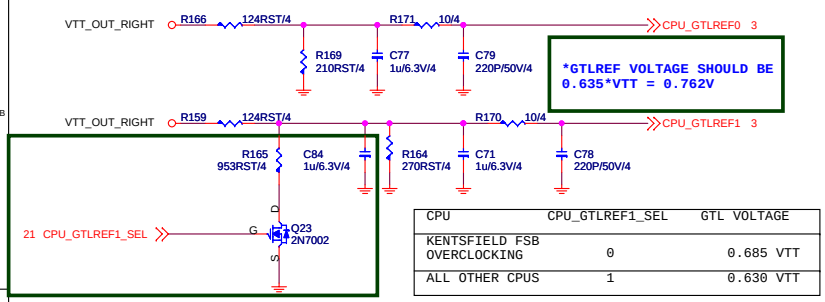
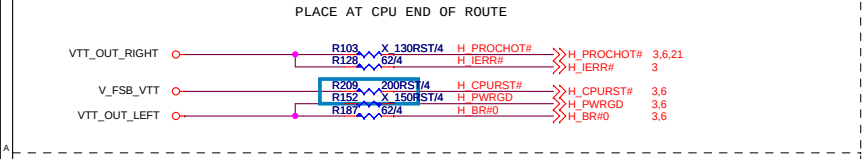
CPU SIGNAL BLOCK

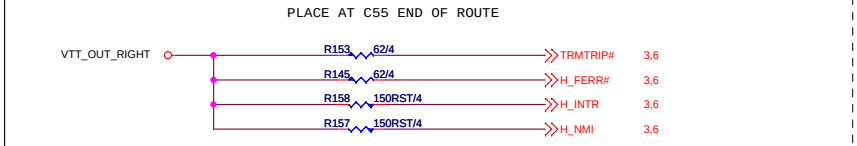


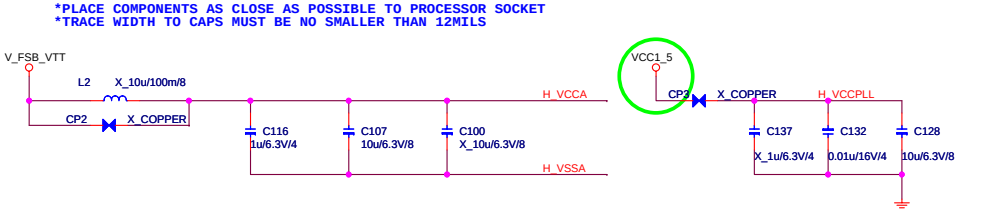




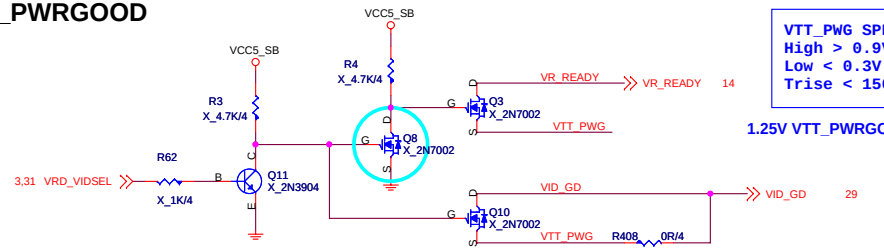
CPU	CPU_GTLREF1_SEL	GTL VOLTAGE
KENTSFIELD FSB OVERCLOCKING	0	0.685 VTT
ALL OTHER CPUS	1	0.630 VTT







VTT_PWRGOOD



VTT_PWG SPEC :
High > 0.9V
Low < 0.3V
Trise < 150ns

1.25V VTT_PWRGOOD

VID_SELECT	VTT_PWG	power on sequence
0 (VRM10)	VID_GD	VTT_PWG beforce VCCP
1 (VRM11)	VR_READY	VCCP beforce VTT_PWG

MICRO-STAR INT'L CO.,LTD

MS-7366

Size	Document Description	Rev
Custom	LGA775 - POWER	0B

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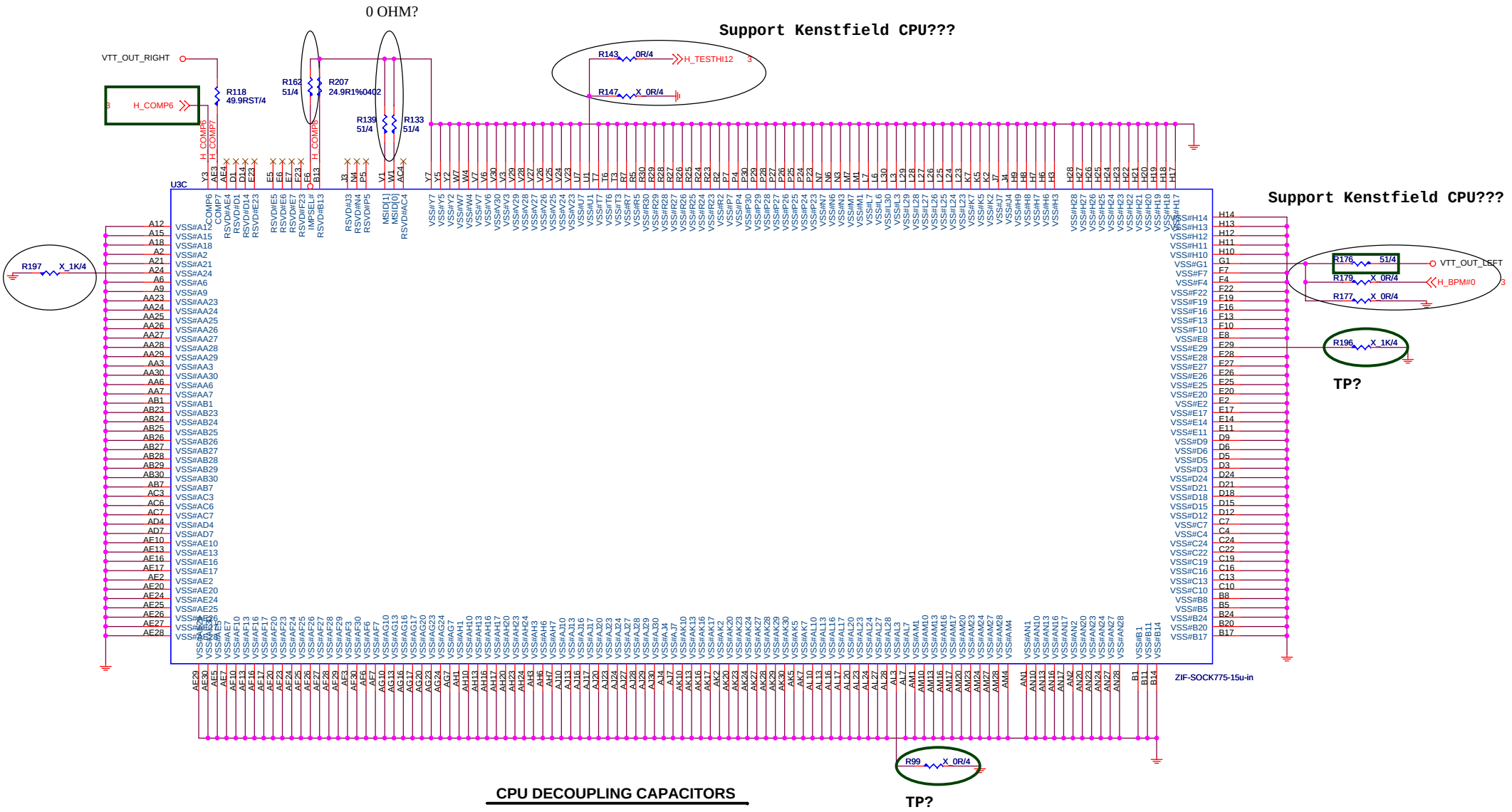

MICRO-STAR INT'L CO.,LTD		
MS-7366		
Size: Custom	Document Description: LGA775 - GND	Rev: 0B
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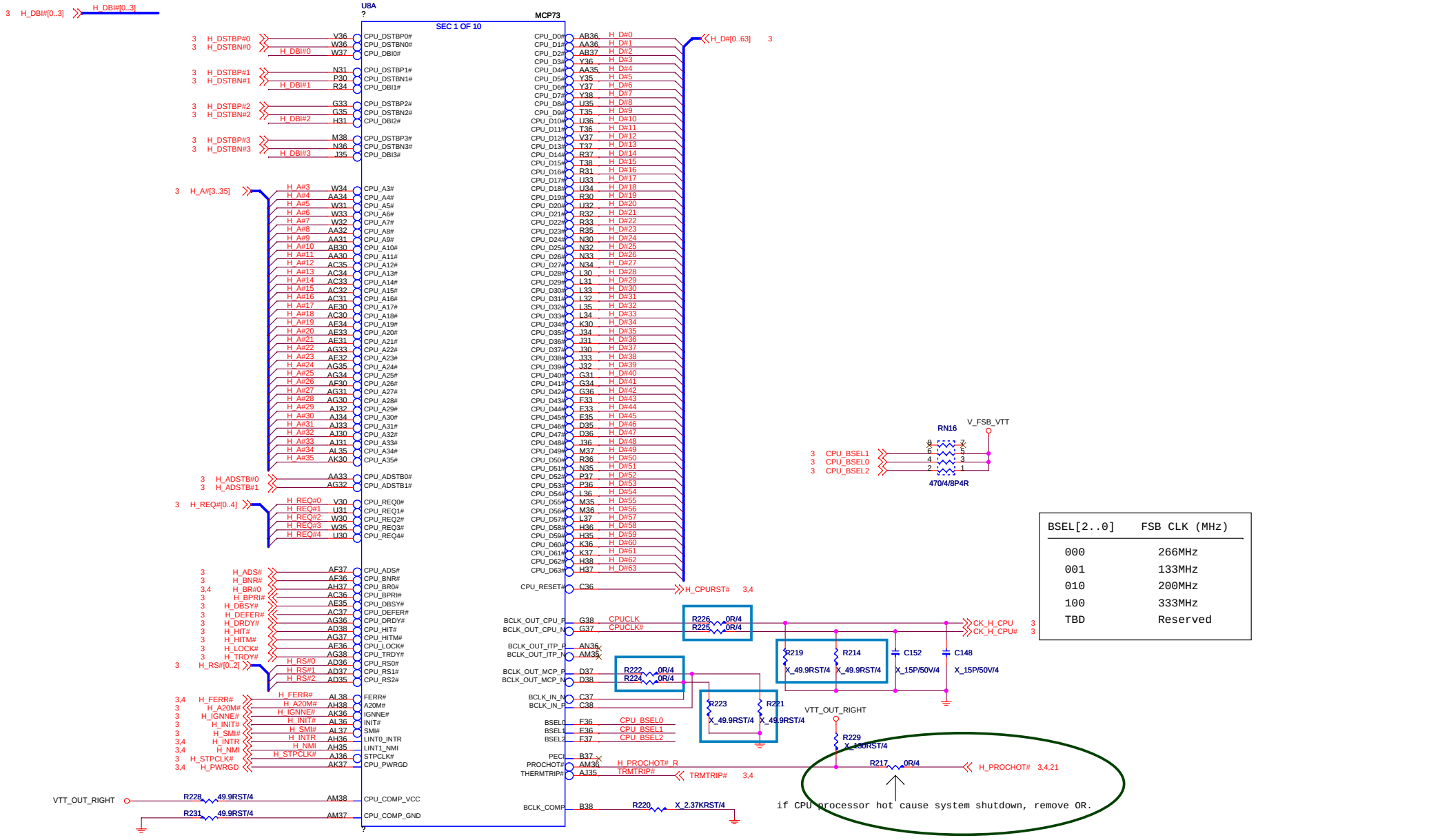

Company	MICRO-STAR INT'L CO.,LTD
Model	MS-7366
Size	Custom
Document Description	MCP73-CPU
Rev	0B
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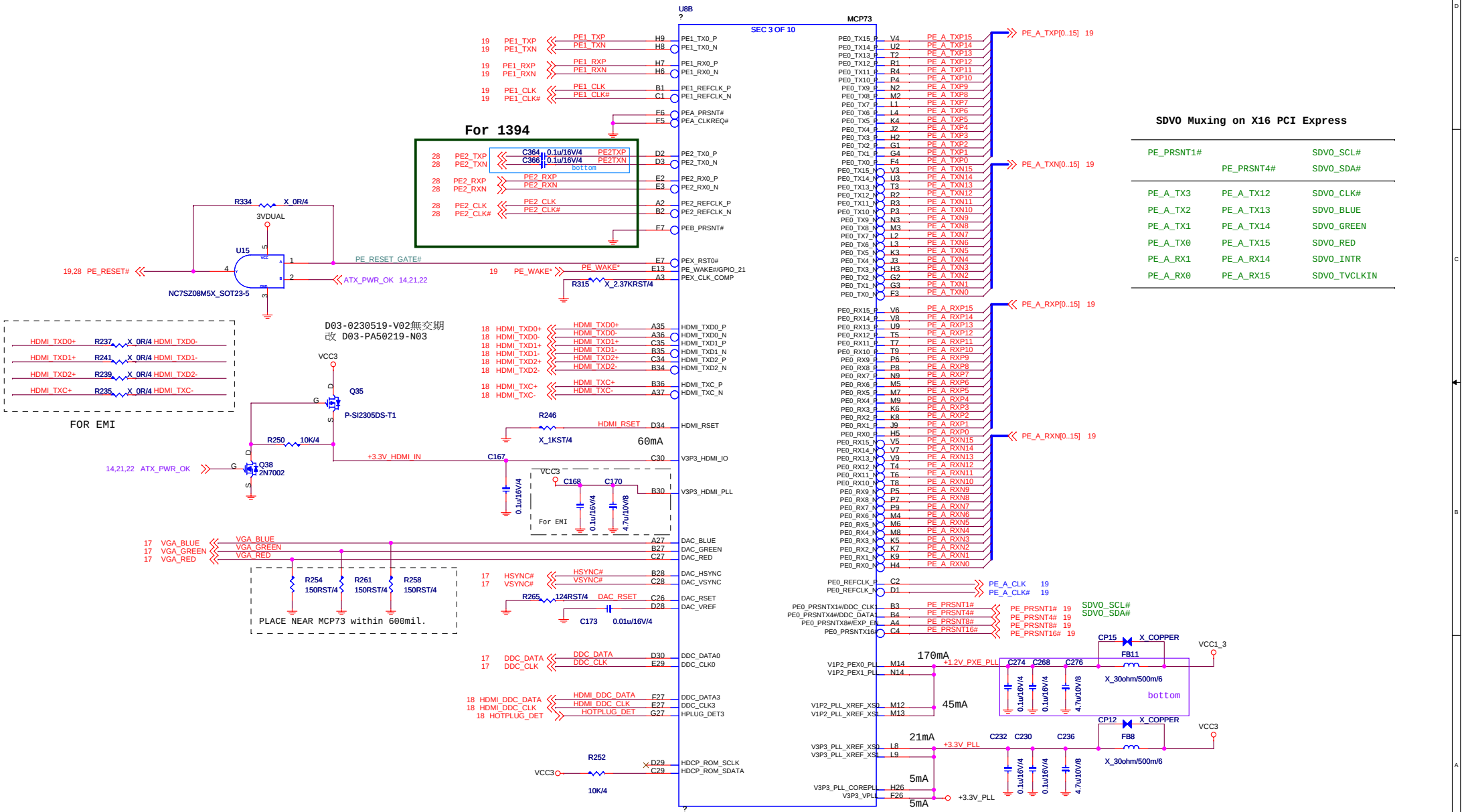
U8B
SEC 3 OF 10
MCP73
For 1394
PE1_TXP
PE1_TXN
PE1_RXP
PE1_RXN
PE1_CLK
PE1_CLK#
PEA_PRSNT#
PEA_CLKREQ#
PE2_TXP
PE2_TXN
PE2_RXP
PE2_RXN
PE2_CLK
PE2_CLK#
PEB_PRSNT#
C364 0.1u/16V/4 PE2TXP
C366 0.1u/16V/4 PE2TXN
bottom
R334 X_0R/4
3VDUAL
U15
NC7SZ08M5X_SOT23-5
PE_RESET#
PE_RESET_GATE#
ATX_PWR_OK 14,21,22
PE_WAKE*
R315 X_2.37KRST/4
PEX_RST0#
PE_WAKE#/GPIO_21
PEX_CLK_COMP
D03-0230519-V02無交期
改 D03-PA50219-N03
HDMI_TXD0+ R237 X_0R/4 HDMI_TXD0-
HDMI_TXD1+ R241 X_0R/4 HDMI_TXD1-
HDMI_TXD2+ R239 X_0R/4 HDMI_TXD2-
HDMI_TXC+ R235 X_0R/4 HDMI_TXC-
FOR EMI
HDMI_TXD0_P
HDMI_TXD0_N
HDMI_TXD1_P
HDMI_TXD1_N
HDMI_TXD2_P
HDMI_TXD2_N
HDMI_TXC_P
HDMI_TXC_N
VCC3
Q35
P-SI2305DS-T1
R246
X_1KST/4
HDMI_RSET
60mA
R250 10K/4
Q38
2N7002
+3.3V_HDMI_IN
C167
V3P3_HDMI_IO
C168
C170
V3P3_HDMI_PLL
For EMI
0.1u/16V/4
4.7u/10V/8
VGA_BLUE
VGA_GREEN
VGA_RED
DAC_BLUE
DAC_GREEN
DAC_RED
R254 150RST/4
R261 150RST/4
R258 150RST/4
PLACE NEAR MCP73 within 600mil.
HSYNC#
VSYNC#
DAC_HSYNC
DAC_VSYNC
R265 124RST/4 DAC_RSET
DAC_RSET
DAC_VREF
C173 0.01u/16V/4
DDC_DATA
DDC_CLK
DDC_DATA0
DDC_CLK0
HDMI_DDC_DATA
HDMI_DDC_CLK
HOTPLUG_DET
DDC_DATA3
DDC_CLK3
HPLUG_DET3
R252
10K/4
HDCP_ROM_SCLK
HDCP_ROM_SDATA
PE_A_TXP[0..15]
PE_A_TXN[0..15]
PE_A_RXP[0..15]
PE_A_RXN[0..15]
PE_A_CLK
PE_A_CLK#
PE_PRSNT1#
PE_PRSNT4#
PE_PRSNT8#
PE_PRSNT16#
SDVO_SCL#
SDVO_SDA#
170mA
+1.2V_PXE_PLL
C274 C268 C276
CP15 X_COPPER
FB11
X_30ohm/500m/6
VCC1_3
45mA
21mA
+3.3V_PLL
C232 C230 C236
CP12 X_COPPER
FB8
5mA
SDVO Muxing on X16 PCI Express
PE_PRSNT1# SDVO_SCL#
PE_PRSNT4# SDVO_SDA#
PE_A_TX3 PE_A_TX12 SDVO_CLK#
PE_A_TX2 PE_A_TX13 SDVO_BLUE
PE_A_TX1 PE_A_TX14 SDVO_GREEN
PE_A_TX0 PE_A_TX15 SDVO_RED
PE_A_RX1 PE_A_RX14 SDVO_INTR
PE_A_RX0 PE_A_RX15 SDVO_TVCLKIN
MICRO-STAR INT'L CO.,LTD
MS-7366
Size Custom
Document Description MCP73-PCIE/DAC/HDMI
Rev 08
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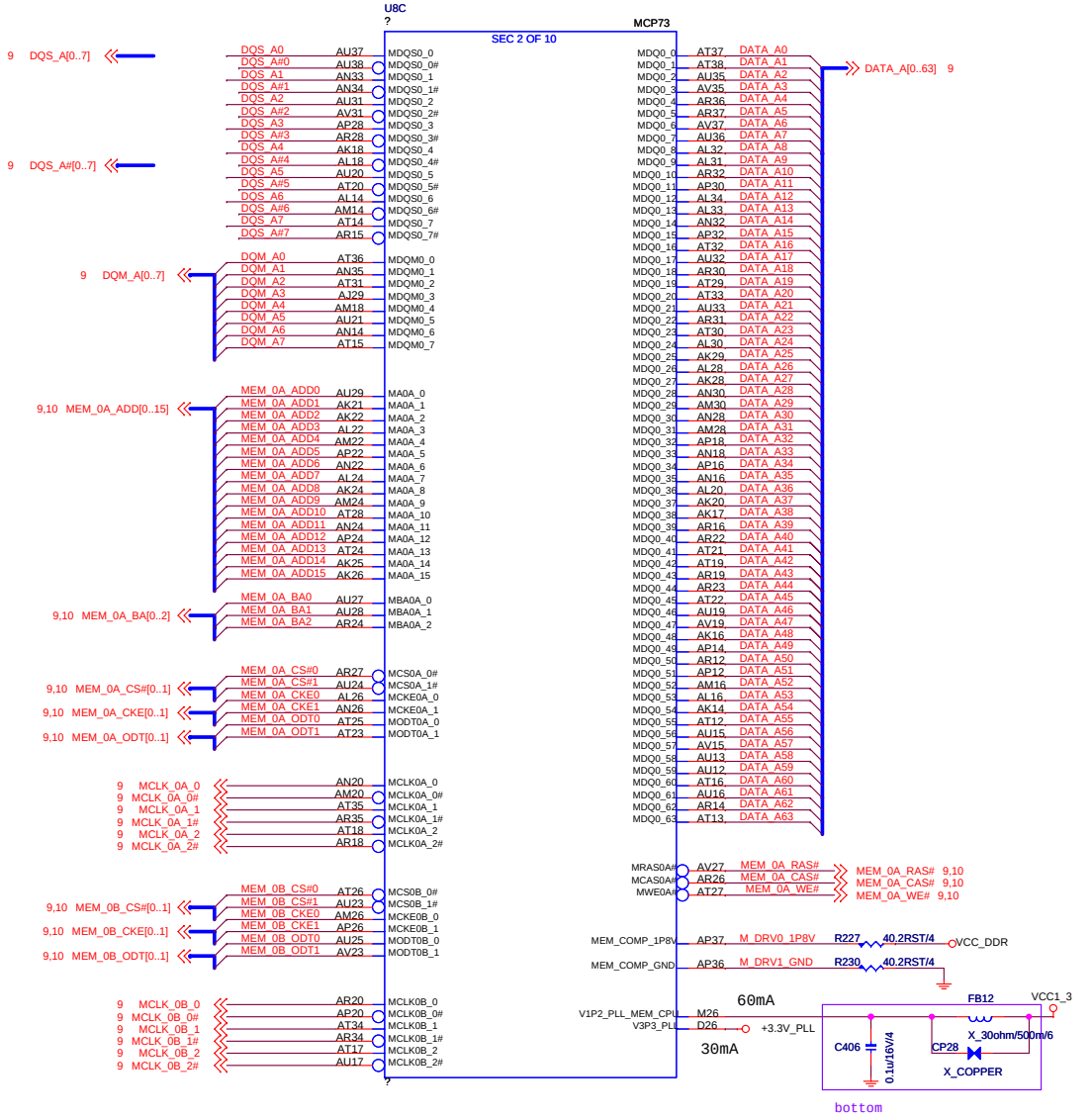
DATA 0

DIMM 1 ADDR 0A / CNTL 0A

DIMM 2 ADDR 0B / CNTL 0B

DIMM 0A



MICRO-STAR INT'L CO.,LTD			
MS-7366			
Size: Custom	Document Description: MCP73-MEM		Rev: 0B
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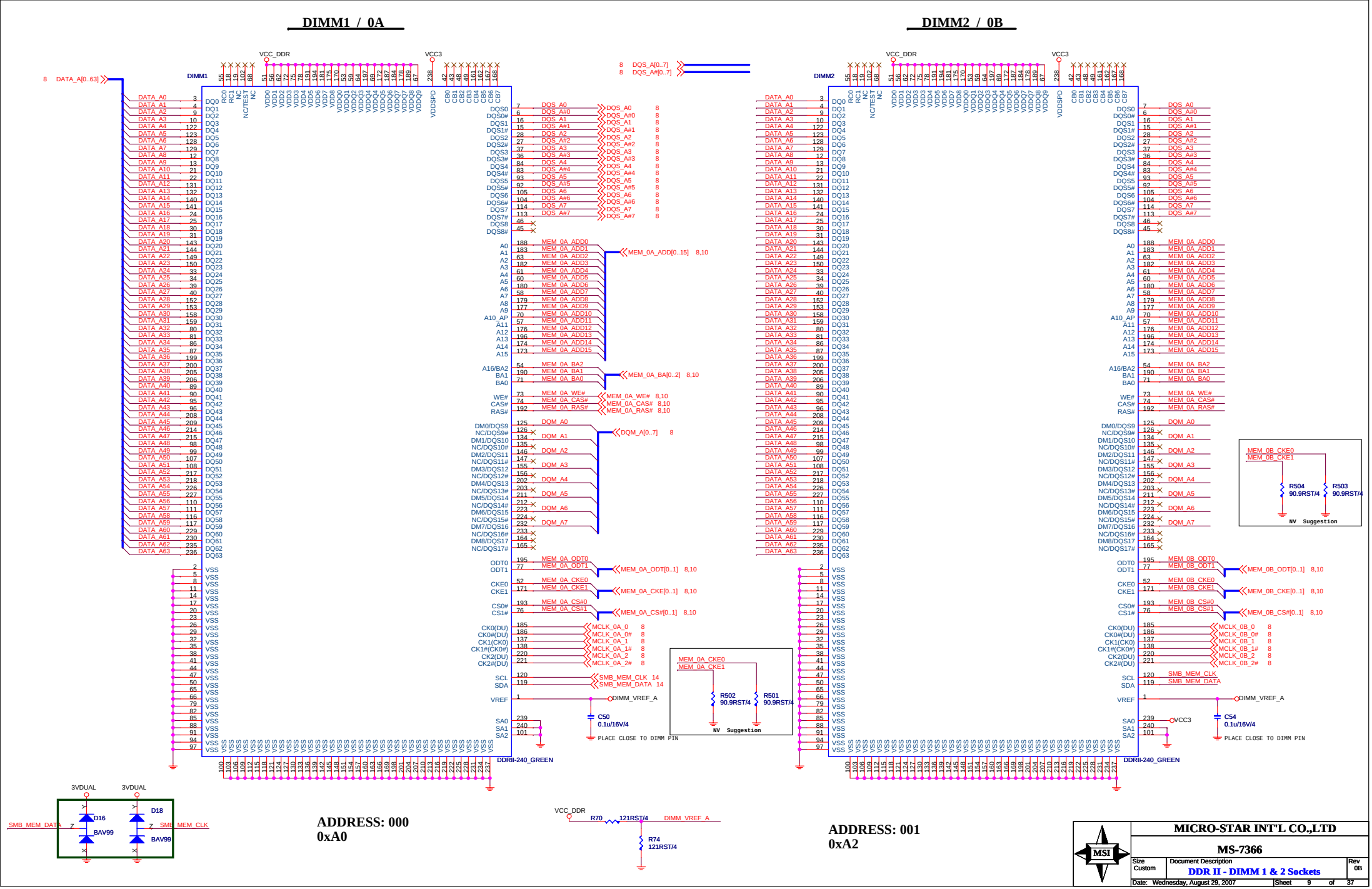

MICRO-STAR INT'L CO.,LTD	
MS-7366	
Size: Custom	Document Description: DDR II - DIMM 1 & 2 Sockets
Rev: 0B	
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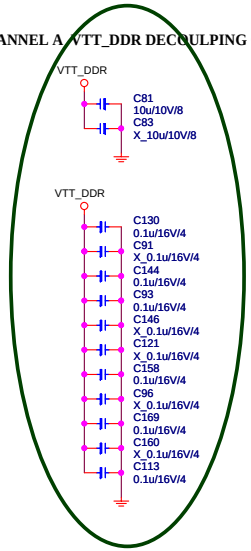
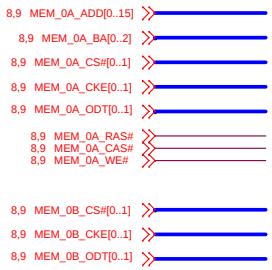
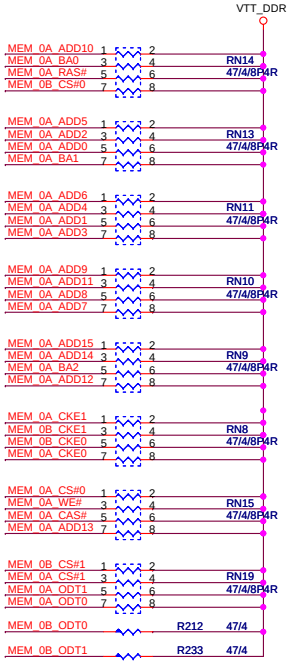
CHANNEL A VTT_DDR DECOULPING CAPS

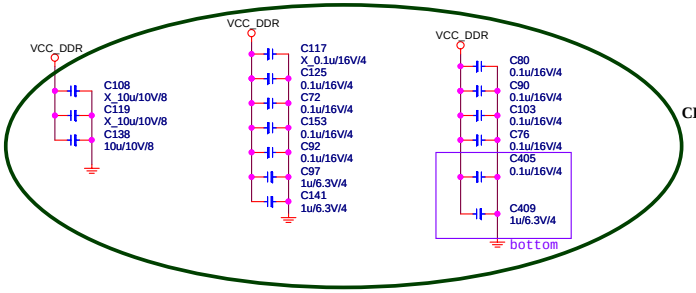


CHECK CAP

CHANNEL A ----- 0A , 0B







CHECK CAP

公板上0.1u X5, 1uX3, 10uX3
兩根再X2

MICRO-STAR INT'L CO.,LTD		
MS-7366		
Size: Custom	Document Description: DDR II VTT Termination & Decoupling	Rev: 0B
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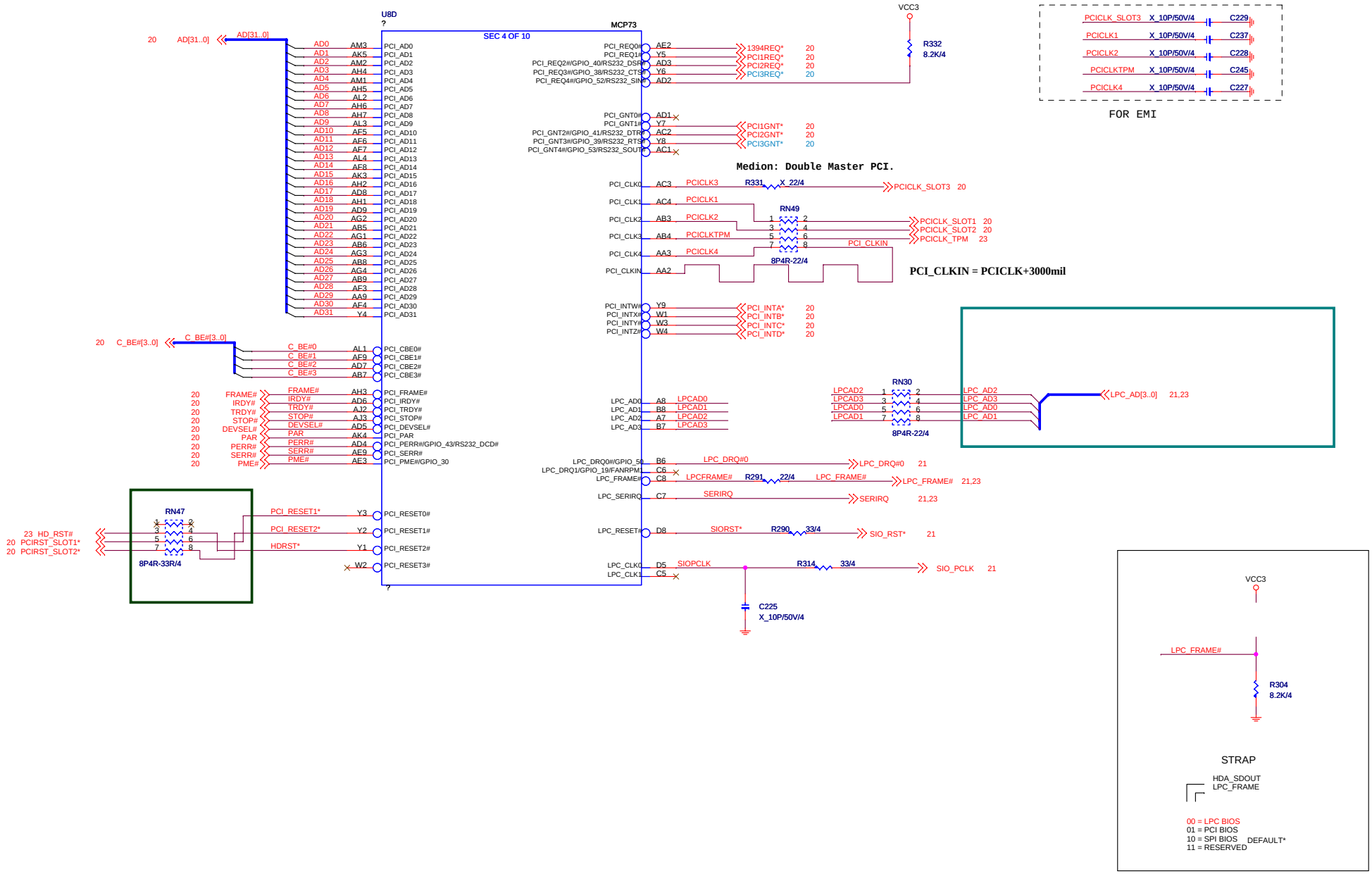
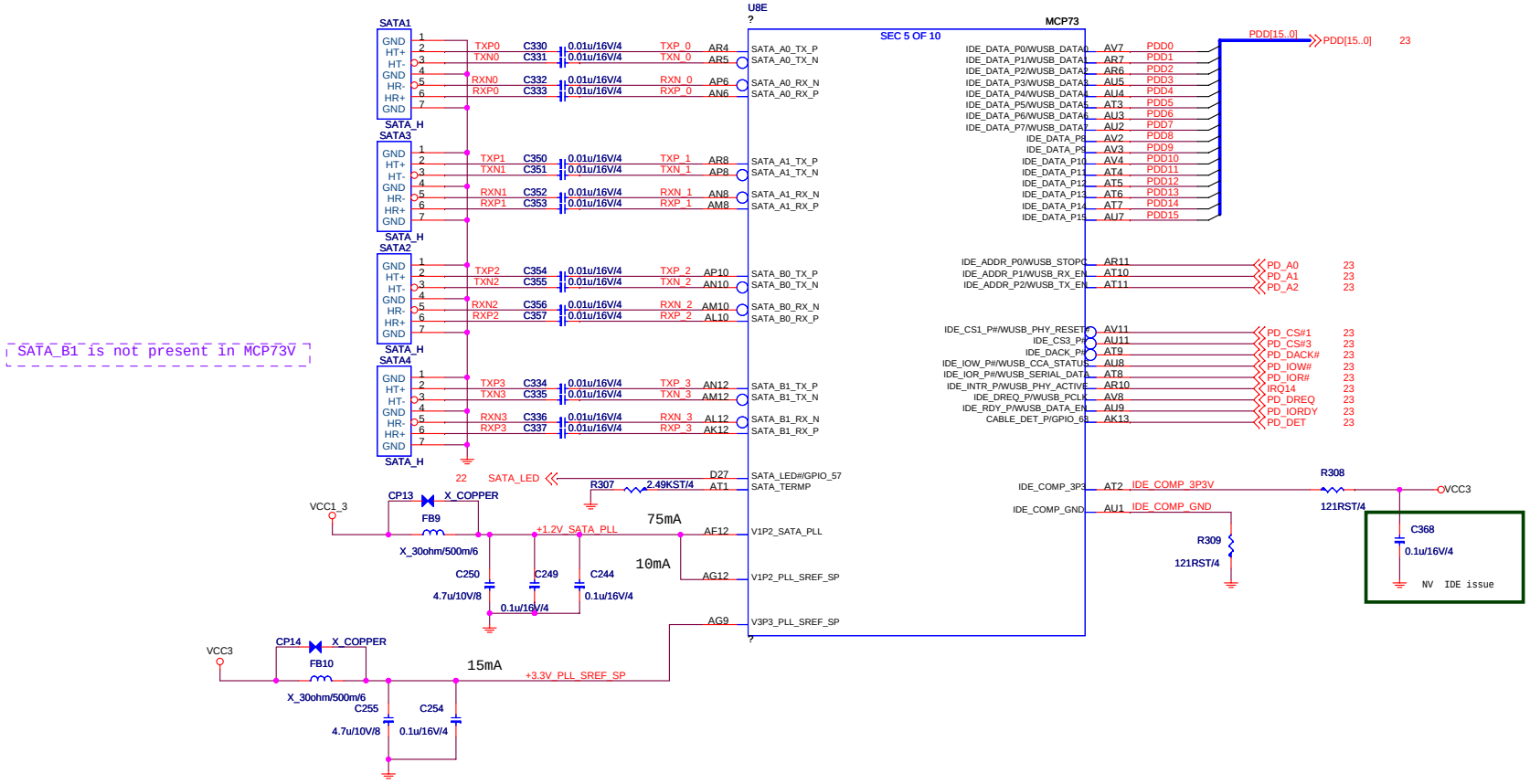


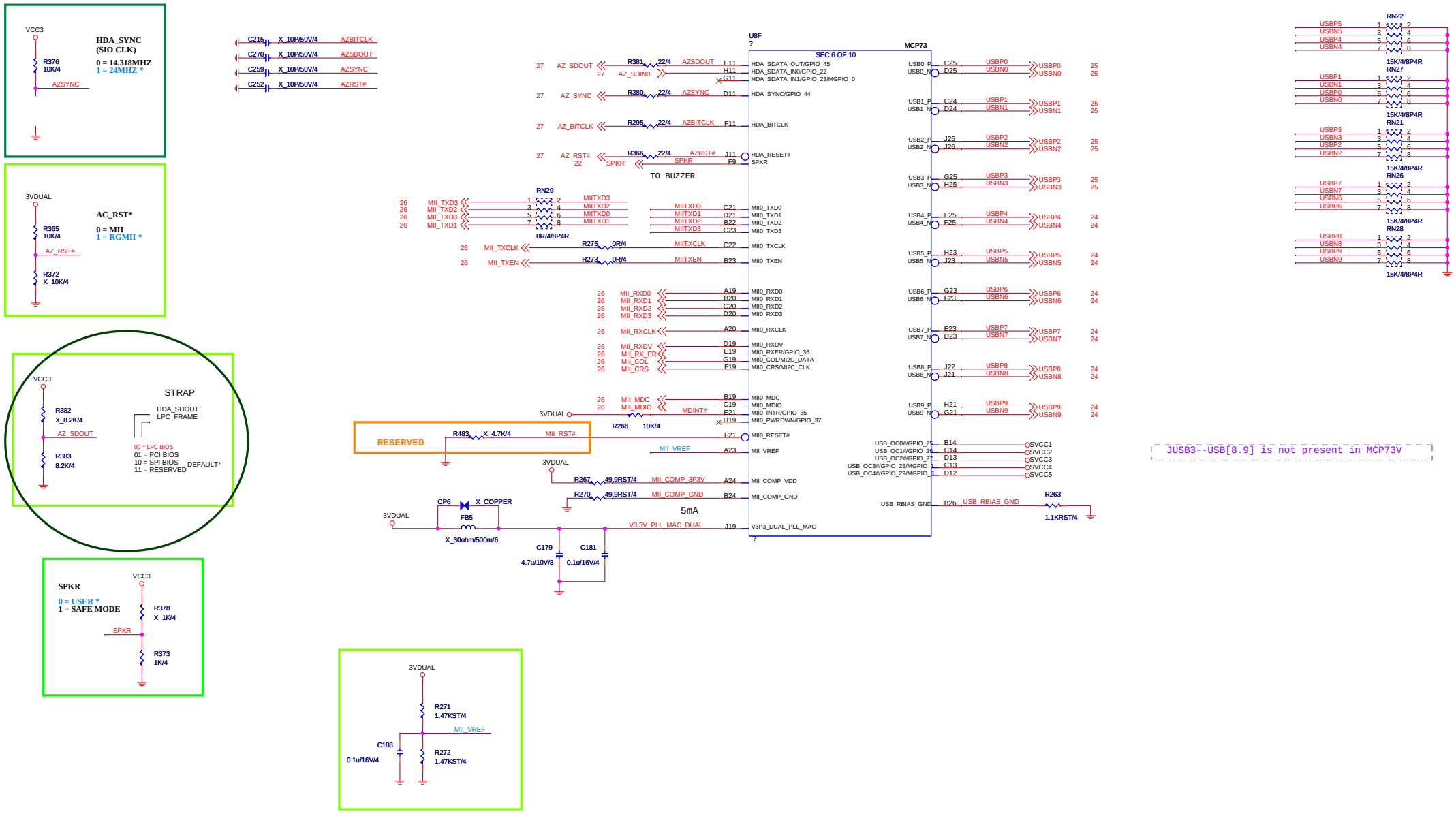
MICRO-STAR INT'L CO.,LTD	
MS-7366	
Size: Custom	Document Description: MCP73-PCI/LPC
Rev: 08	
Date: Wednesday, August 29, 2007	Sheet 11 of 37

PLACE CAPS AT CONNECTOR.

SATA_B1 is not present in MCP73V



MICRO-STAR INT'L CO.,LTD		
MS-7366		
Size: Custom	Document Description: MCP73-SATA/IDE	Rev: 08
Date: Wednesday, August 29, 2007	Sheet 12 of 37	


HDA_SYNC
(SIO CLK)
0 = 14.318MHZ
1 = 24MHZ *
AC_RST*
0 = MII
1 = RGMII *
STRAP
HDA_SDOUT
LPC_FRAME
00 = LPC BIOS
01 = PCI BIOS
10 = SPI BIOS
11 = RESERVED
DEFAULT*
SPKR
0 = USER *
1 = SAFE MODE
U8F
SEC 6 OF 10
MCP73
TO BUZZER
RESERVED
5mA
JUSB3--USB[8.9] is not present in MCP73V
MICRO-STAR INT'L CO.,LTD
MS-7366
MCP51-Audio/USB/GPIO
Date: Wednesday, August 29, 2007
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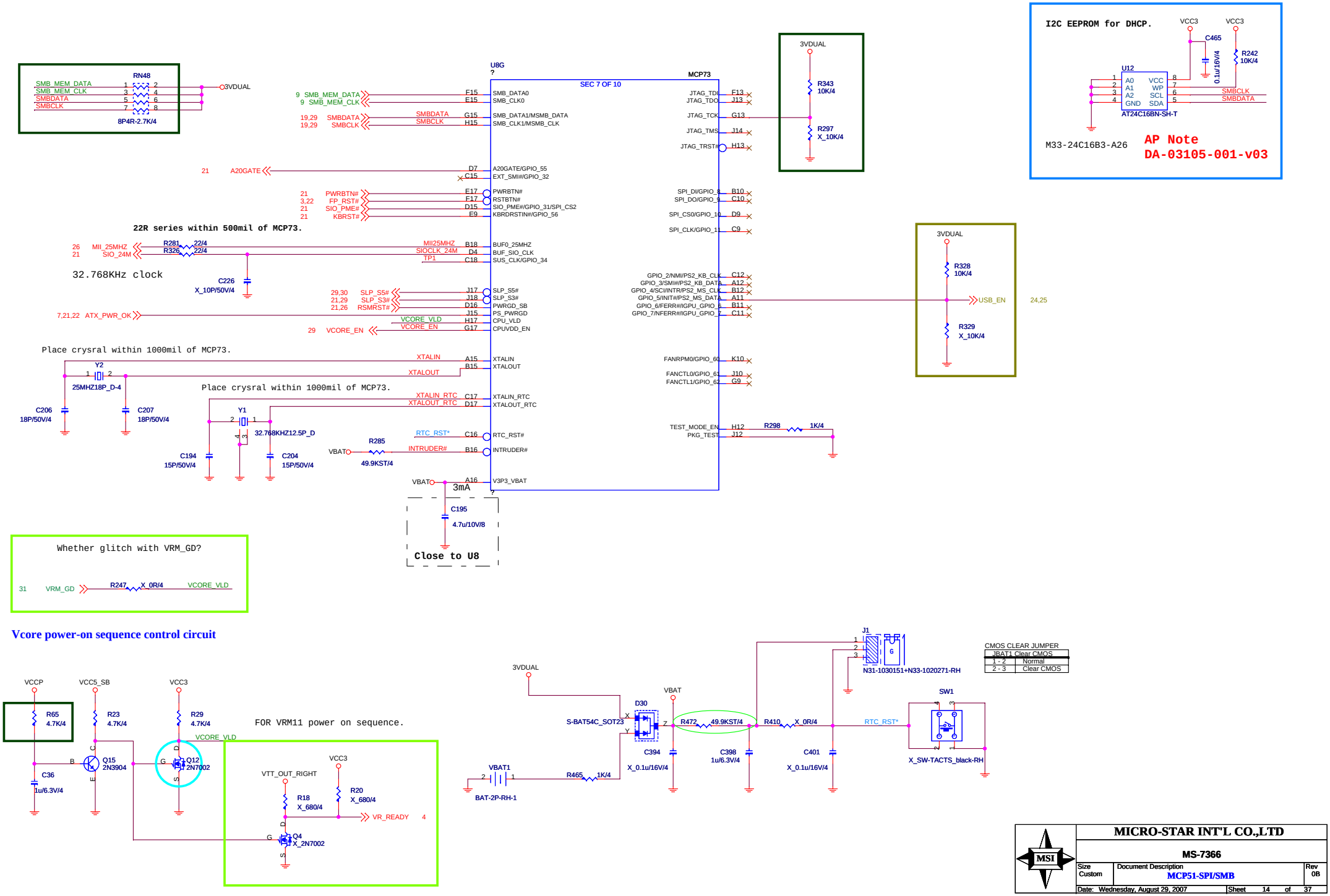
I2C EEPROM for DHCP.
M33-24C16B3-A26
AP Note
DA-03105-001-v03
22R series within 500mil of MCP73.
32.768KHz clock
Place crysral within 1000mil of MCP73.
Place crysral within 1000mil of MCP73.
Close to U8
Whether glitch with VRM_GD?
Vcore power-on sequence control circuit
FOR VRM11 power on sequence.
CMOS CLEAR JUMPER
JBAT1 Clear CMOS
1 - 2 Normal
2 - 3 Clear CMOS
MICRO-STAR INT'L CO.,LTD
MS-7366
MCP51-SPI/SMB
Date: Wednesday, August 29, 2007
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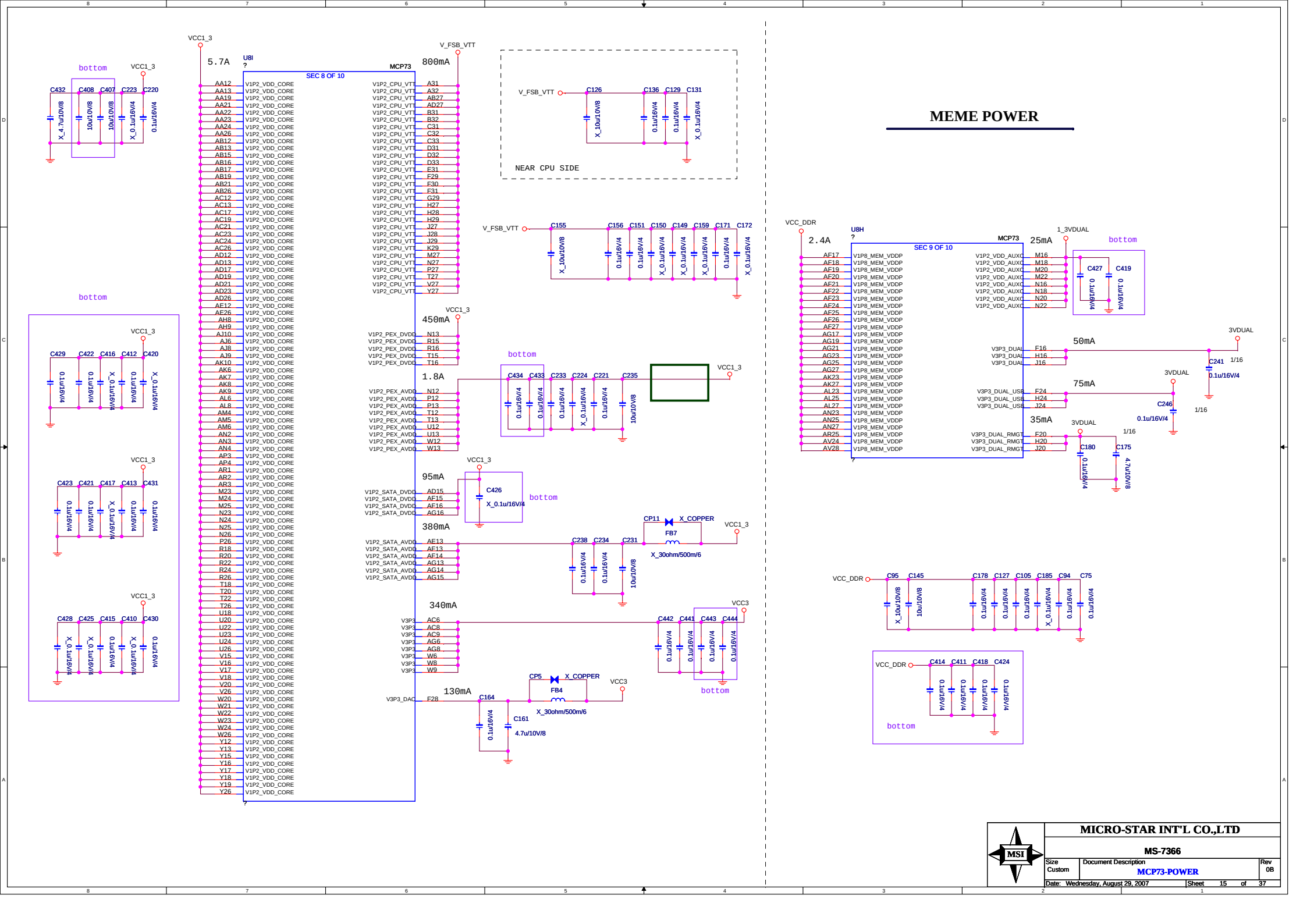
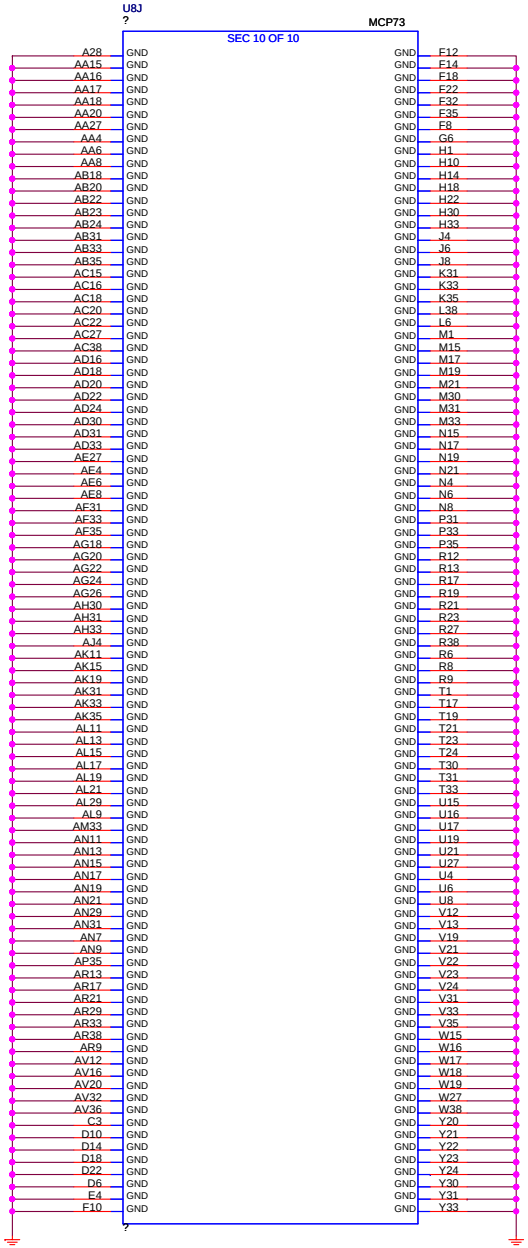

MEME POWER
U8I
SEC 8 OF 10
MCP73
5.7A
800mA
450mA
1.8A
95mA
380mA
340mA
130mA
NEAR CPU SIDE
U8H
SEC 9 OF 10
2.4A
25mA
50mA
75mA
35mA
MICRO-STAR INT'L CO.,LTD
MS-7366
MCP73-POWER
Wednesday, August 29, 2007



MICRO-STAR INT'L CO.,LTD

MS-7366

Size	Document Description	Rev
Custom	MCP73-GND	08

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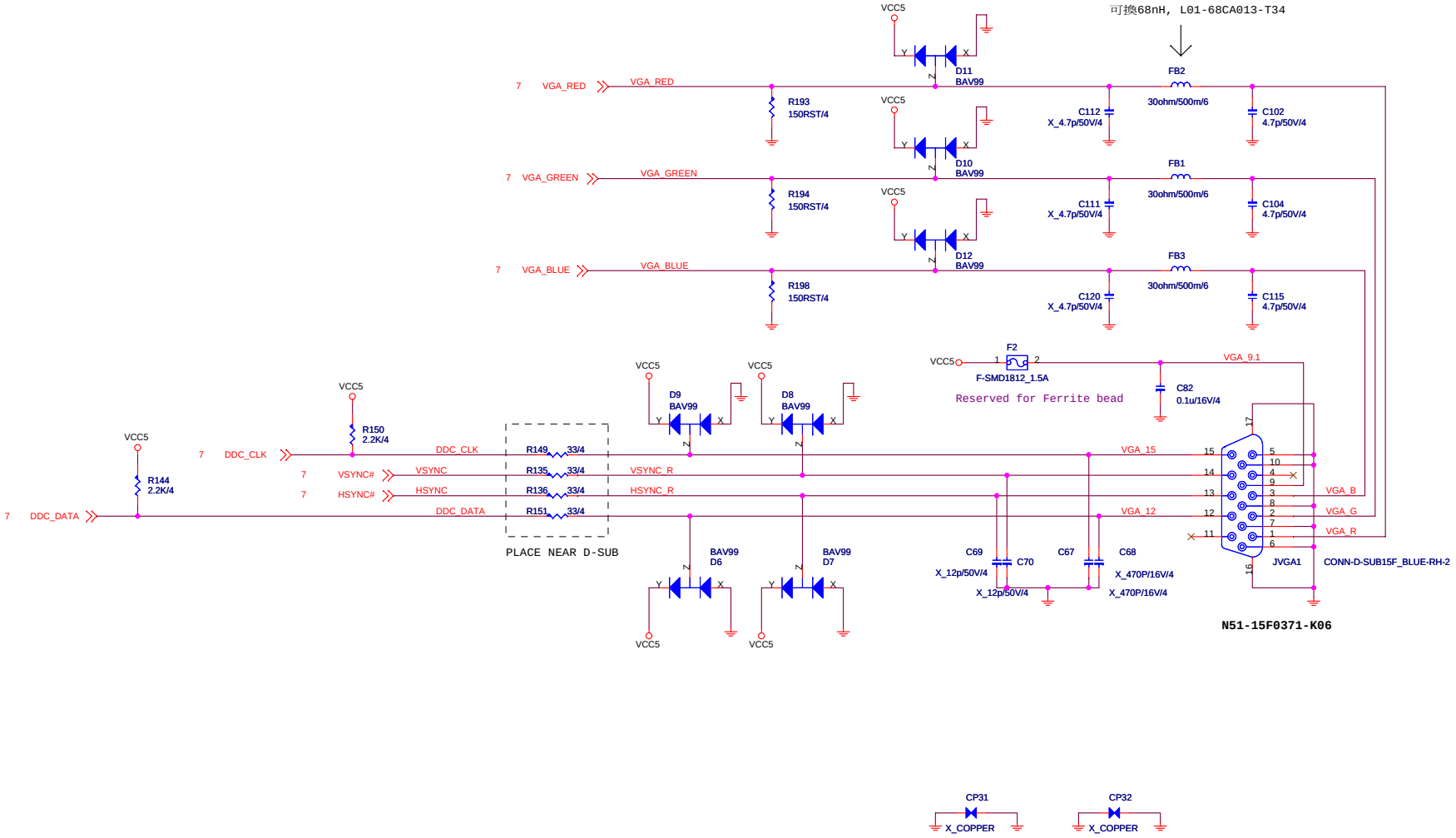
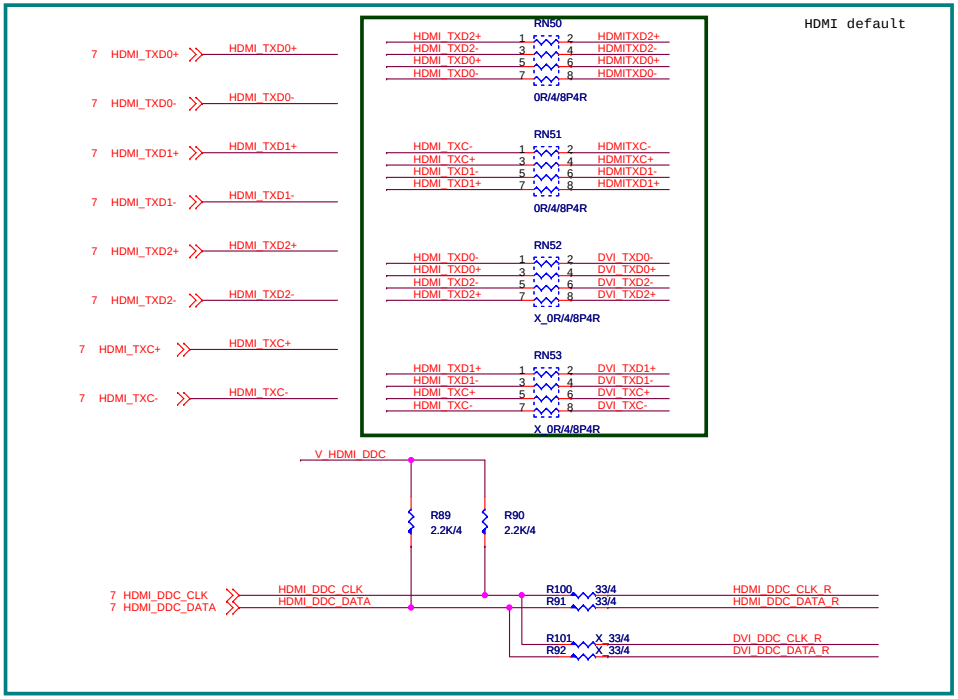
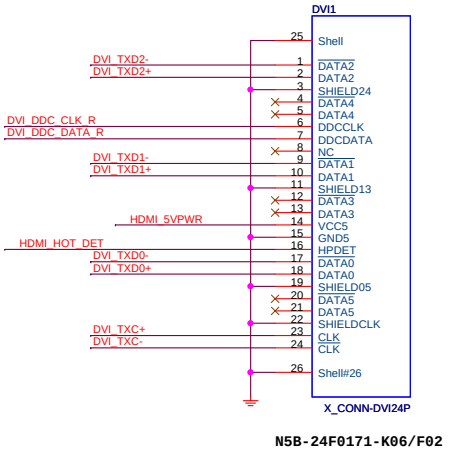
PLACE NEAR VGA CONNECTOR



MICRO-STAR INT'L CO.,LTD		
MS-7366		
Size: Custom	Document Description: D-SUB	Rev: 08
Date: Wednesday, August 29, 2007	Sheet 17 of 37	

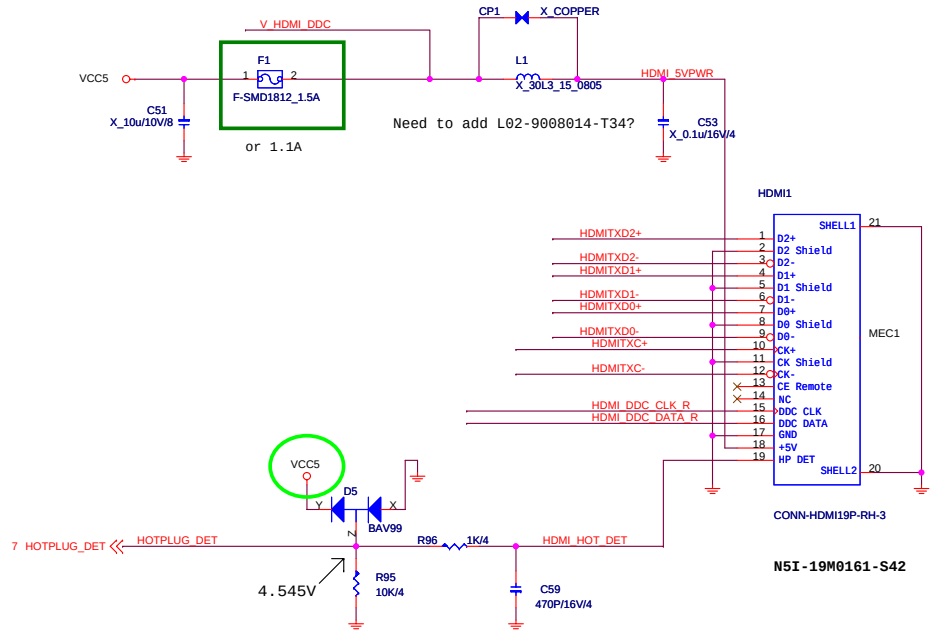


DVI CONNECTOR



N5B-24F0171-K06/F02

HDMI CONNECTOR



N5I-19M0161-S42

MICRO-STAR INT'L CO.,LTD			
MS-7366			
Size: Custom	Document Description: HDMI/DVI		Rev: 08
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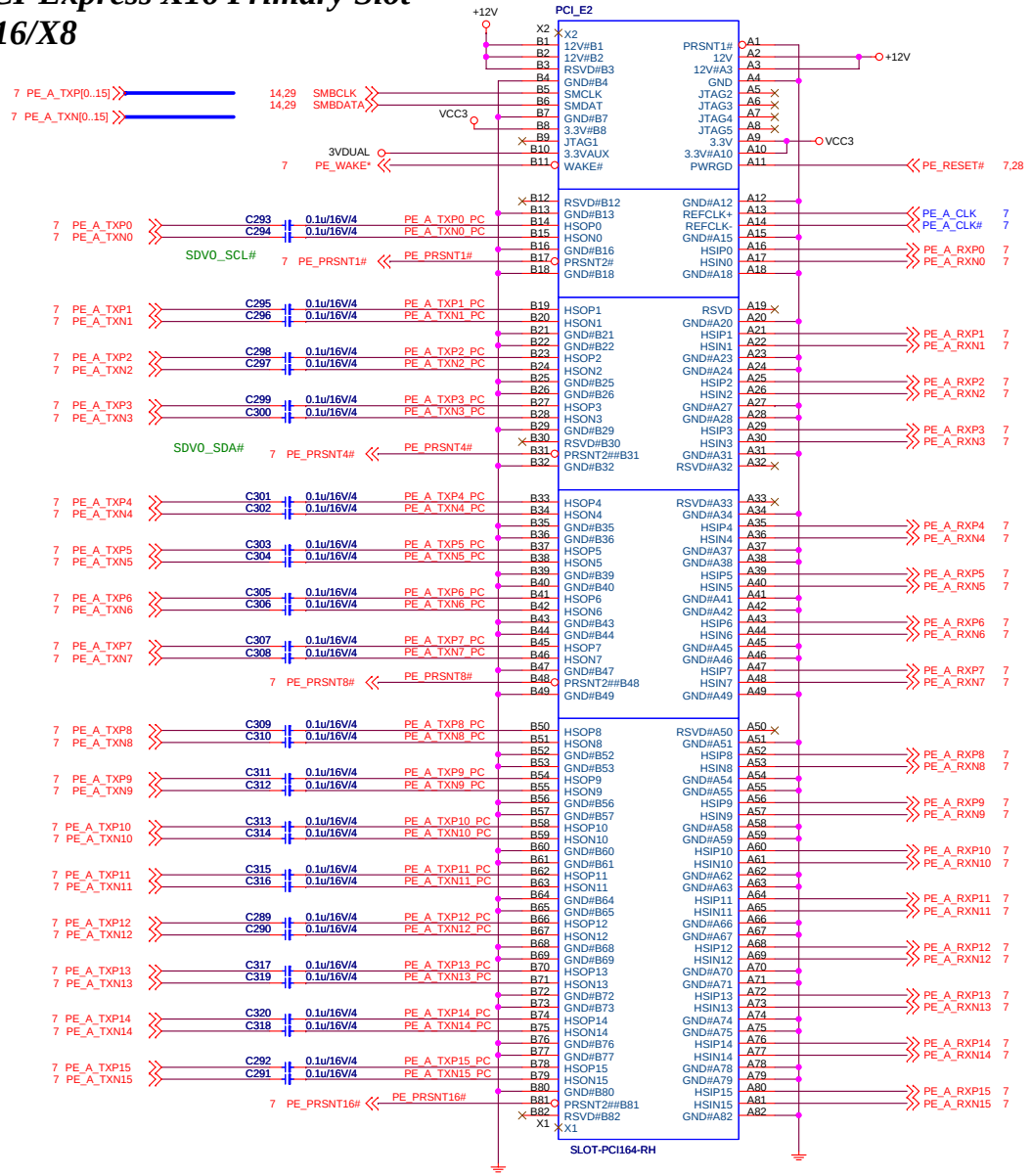
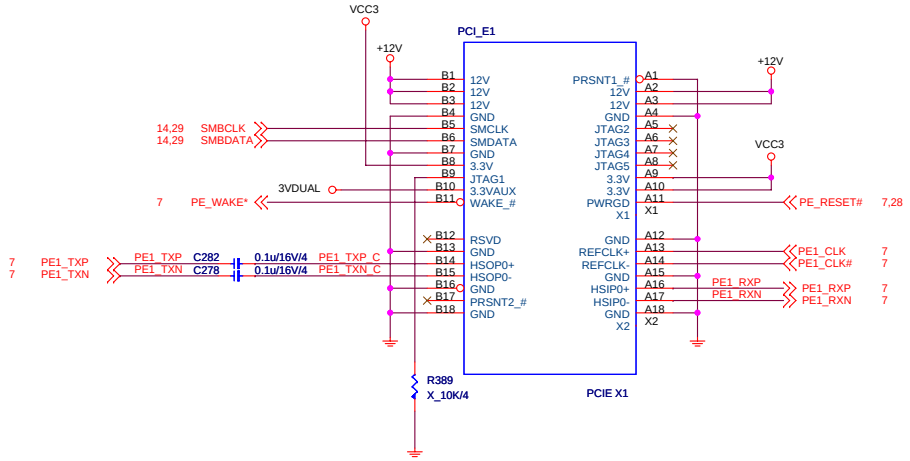
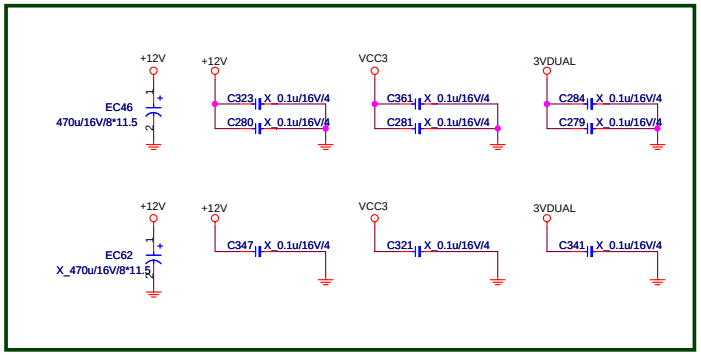
PCI-Express X16 Primary Slot
X16/X8



PCI-Express x1 SLOT 1





MICRO-STAR INT'L CO.,LTD		
MS-7366		
Size: Custom	Document Description: PCI-E X16/X1 Slot	Rev: 08
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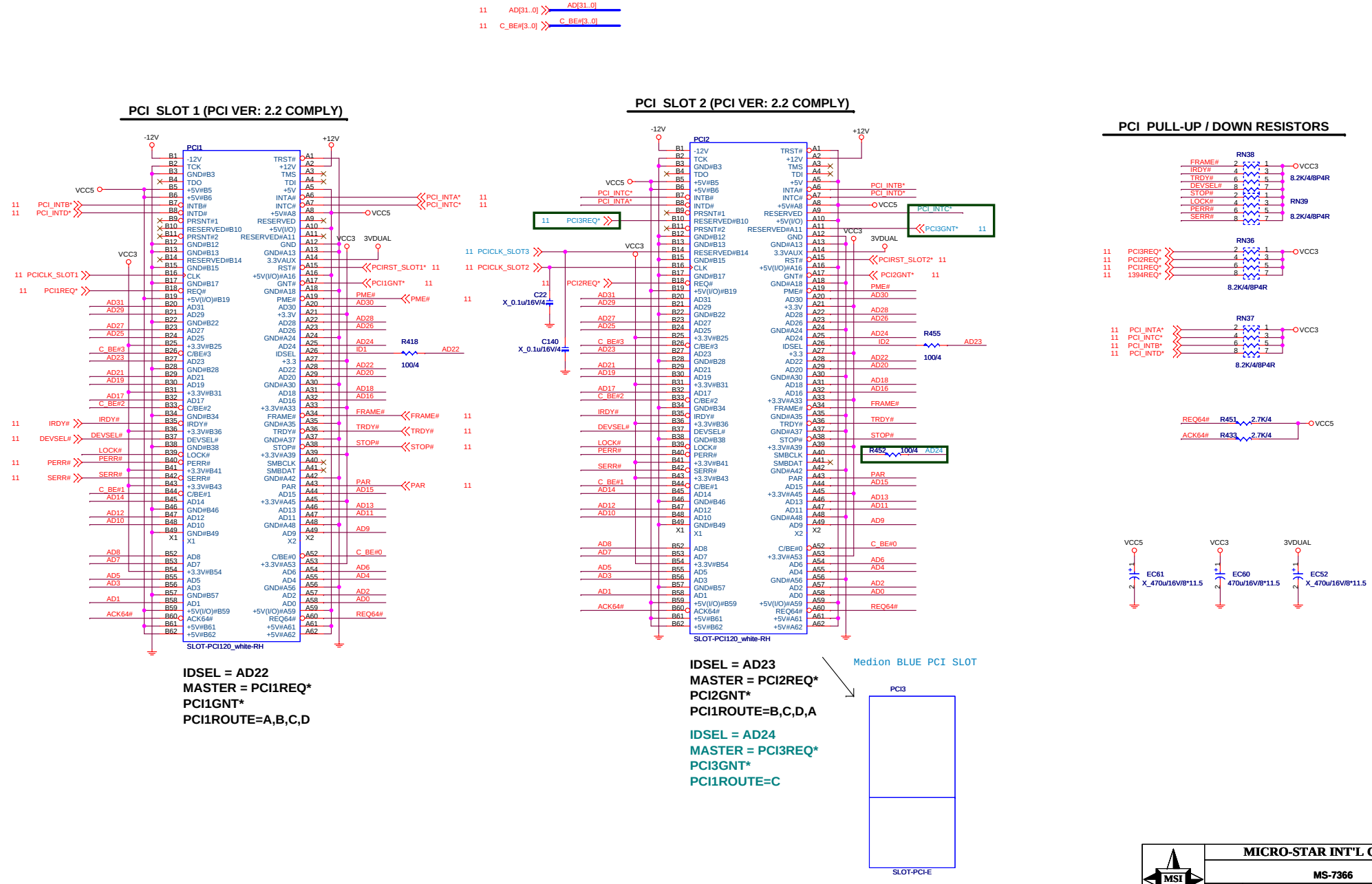

MICRO-STAR INT'L CO.,LTD

MS-7366

Size	Document Description	Rev
Custom	PCI Slot 1 & 2	0B

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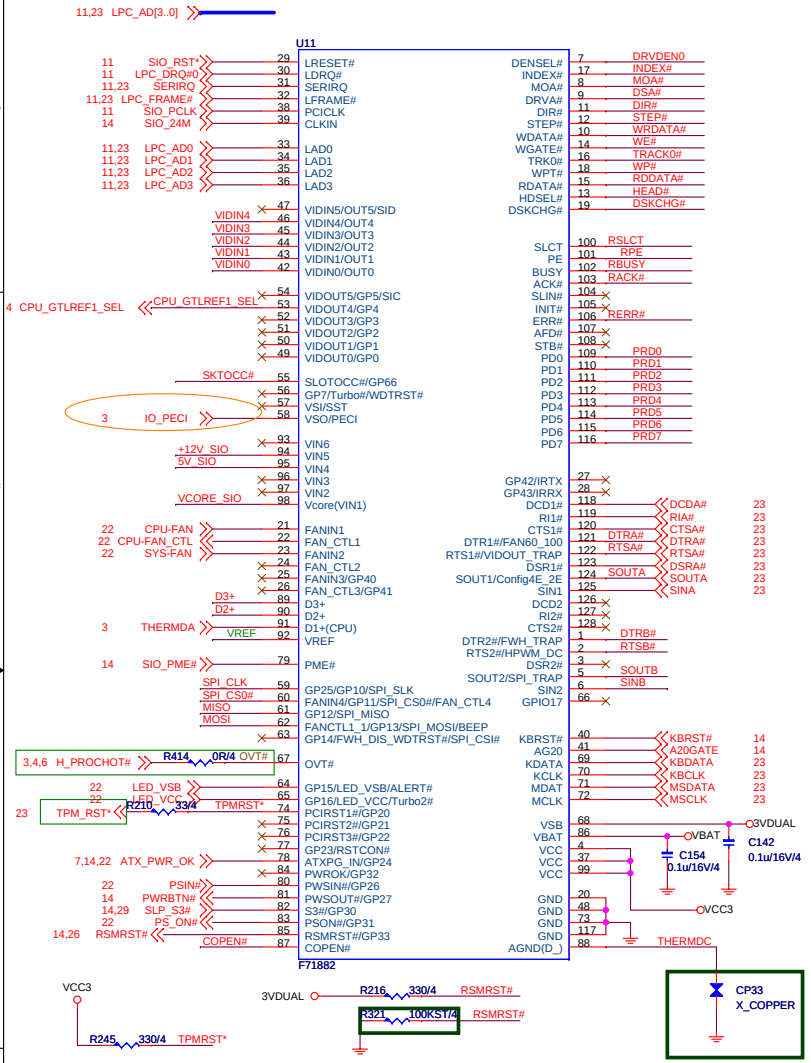
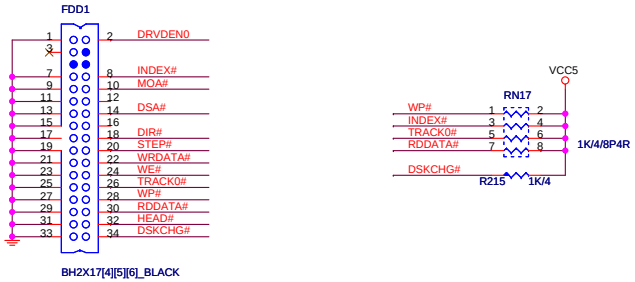
Super I/O

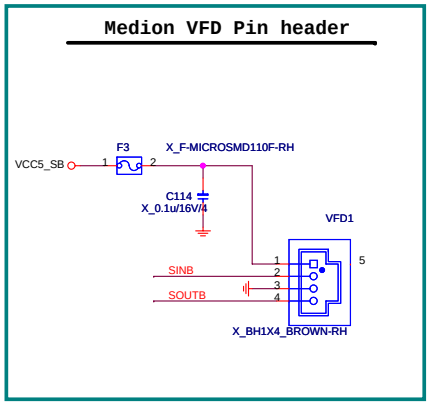
LPC SUPER I/O F71882



FLOPPY CONN BOLCK

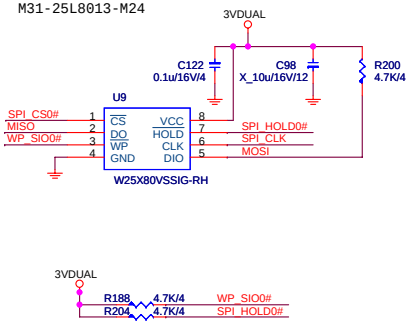


Medion VFD Pin header

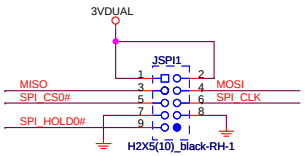


SPI 8M FLASH ROM

M31-25X8003-W03
M31-25L8013-M24

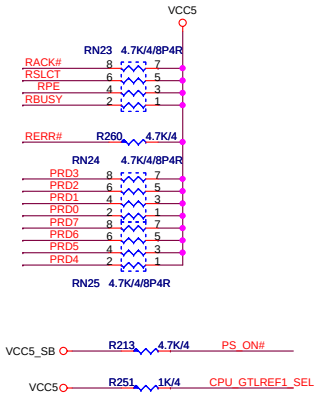


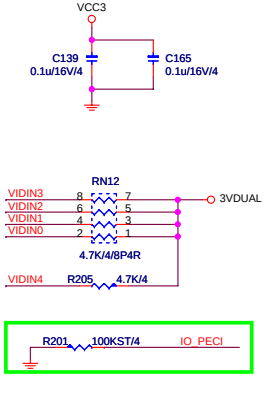
SPI DEBUG PORT



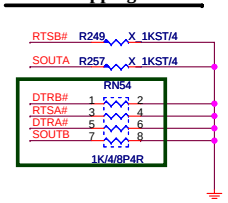
Part Number : N31-2051451-H06

Place close to SPI ROM





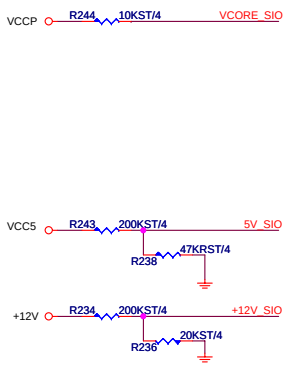
Strapping



	Don't STUFF	STUFF
RTSB#	PWM FAN	LINEAR FAN
RTSA#	PIN49-54=VID_OUT	PIN49-54=GPIO
	PIN42-47=VIDIN	PIN42-47=VIDIN/OUT
SOUTA	4E	2E
DTRB#, SOUTB	SPI_DISABLE	SPI_ENABLE
DTRA#	FAN START DUTY 60%	FAN START DUTY 100%

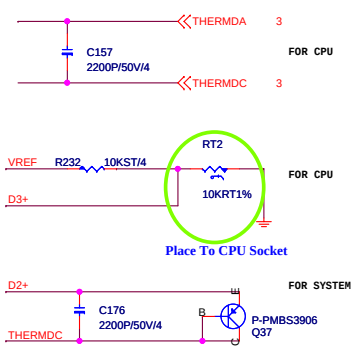
VOLTAGE SENSING(H/W Monitor).

The best voltage input level is about 1V.



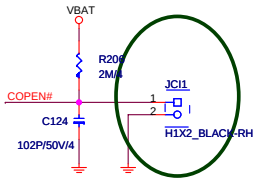
Temperature Sensing

DIODE SENSING CIRCUIT



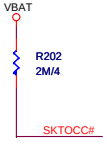
CASE OPEN CIRCUIT

Change to reserve???



CPU VID reset

SLOT_OCC#



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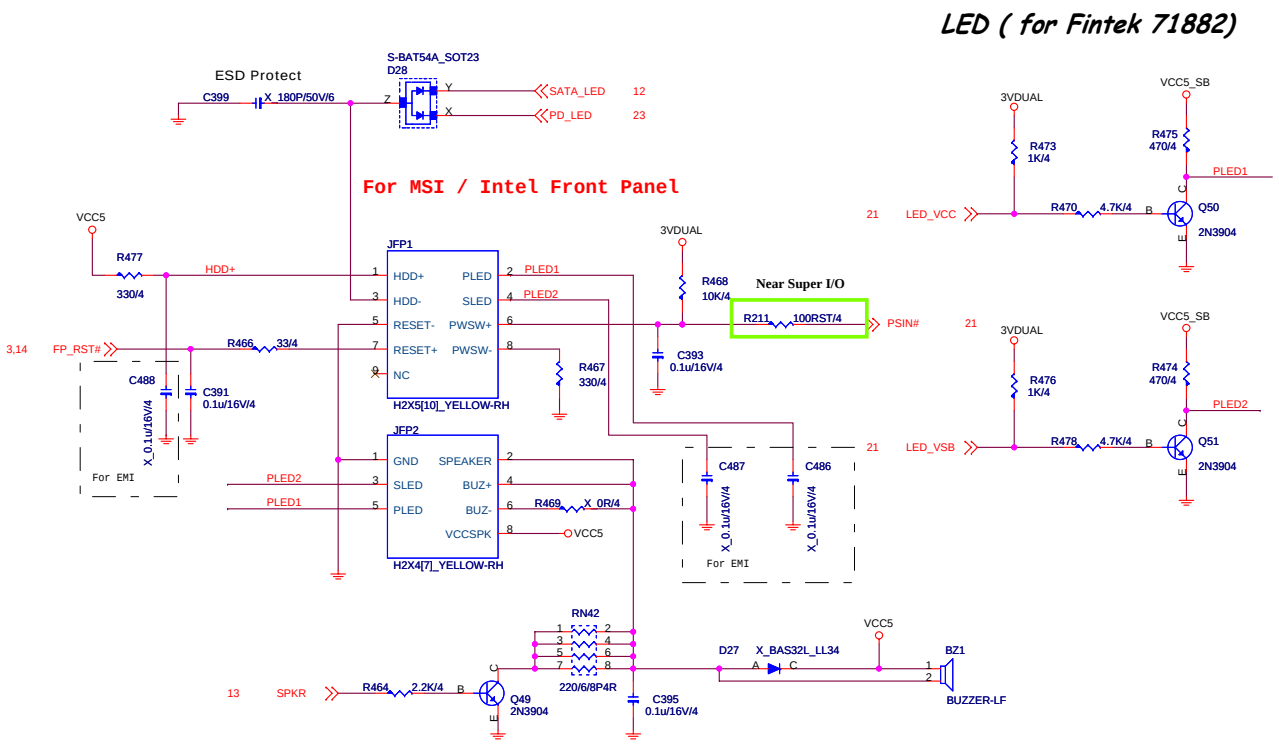
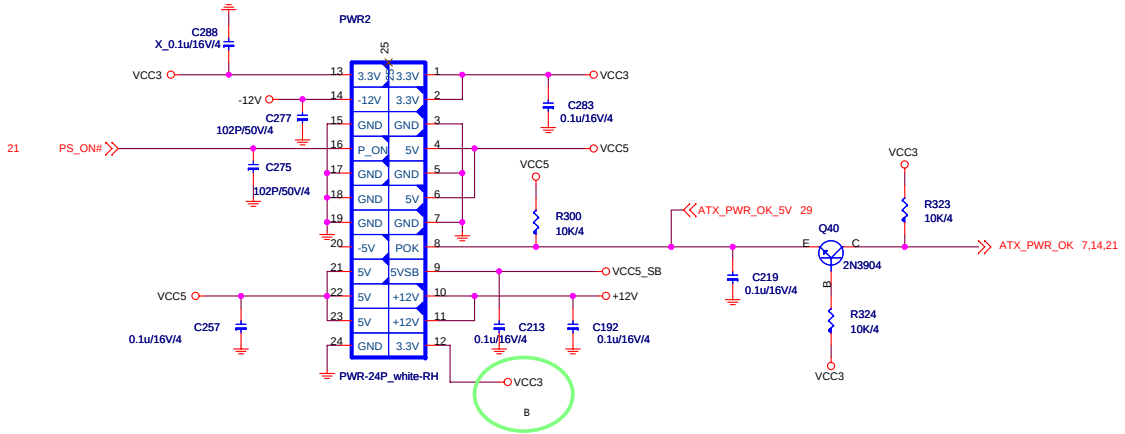
ATX connector / Front Panel

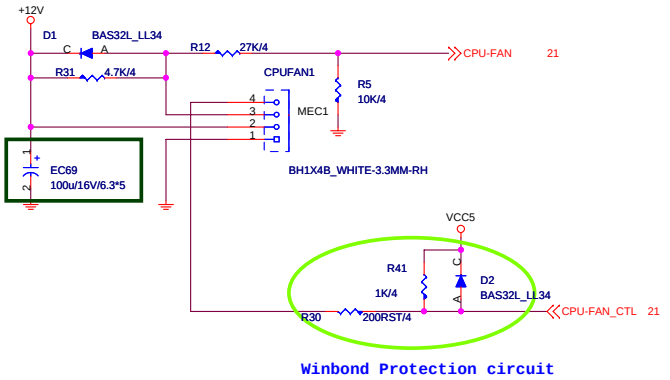
Intel Front Panel



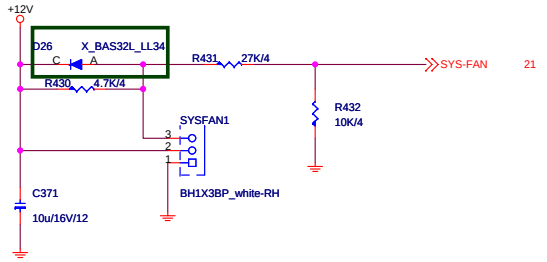
ATX Connector



CPU FAN

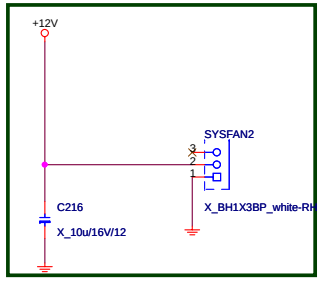


SYSTEM FAN



NB FAN

Reserve for NB_FAN, Near MCP73
MP Remove



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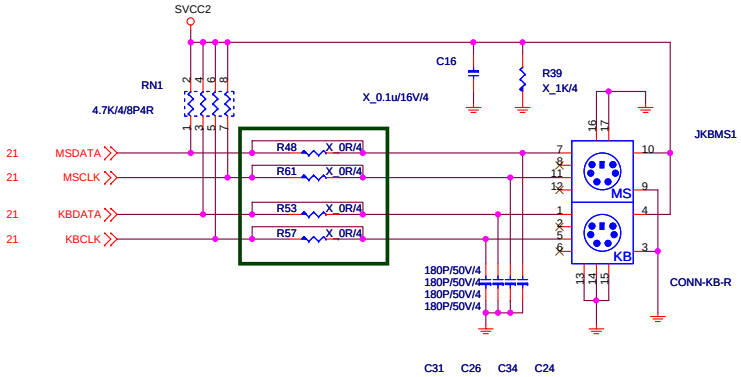
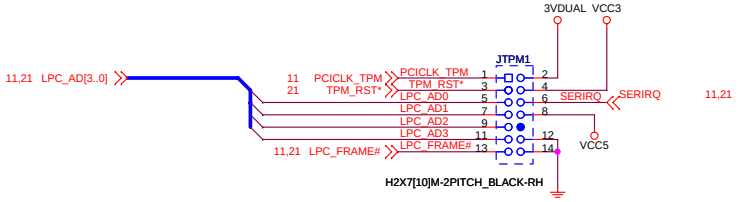
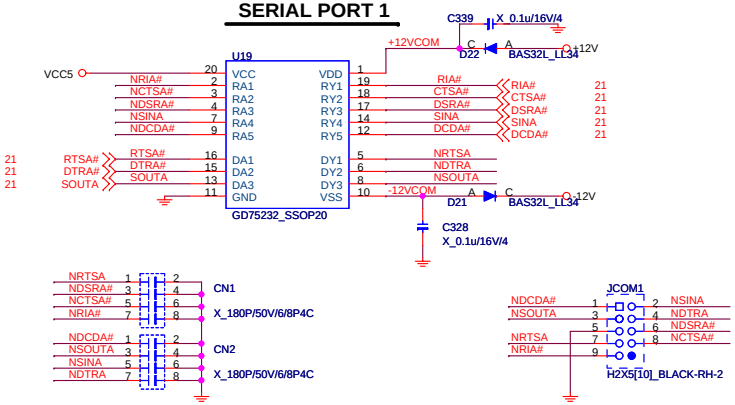
PS2 KEYBOARD & MOUSE CONNECTOR



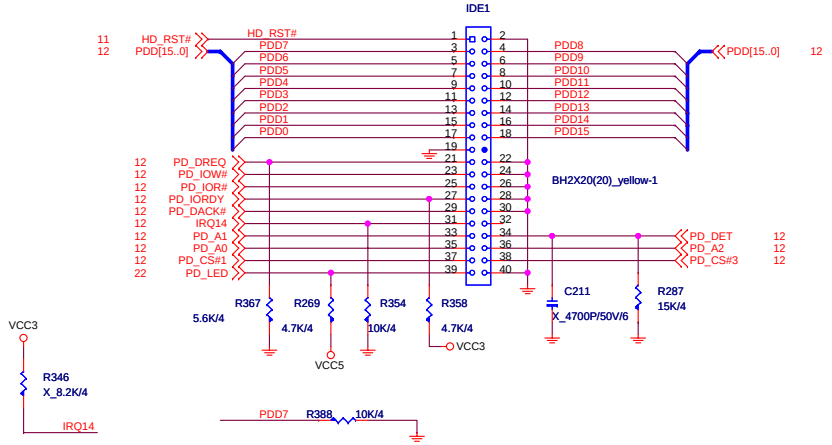
JLPC port for TPM



SERIAL PORT 1



PRIMARY IDE BLOCK



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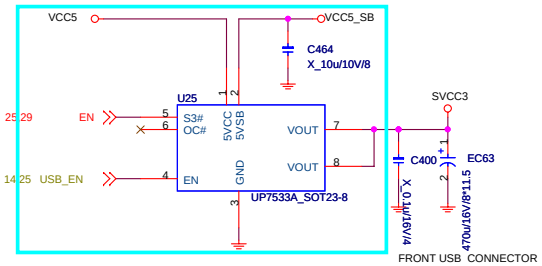
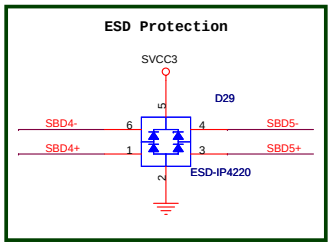
FRONT PANEL USB CONNECTOR

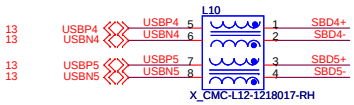
POWER CIRCUIT FOR USB PORT 4,5

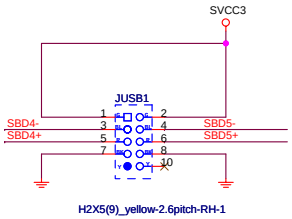


ESD Protection



FRONT PANEL USB CONNECTOR FOR USB PORT 4,5

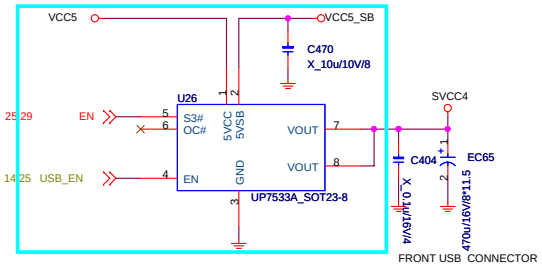


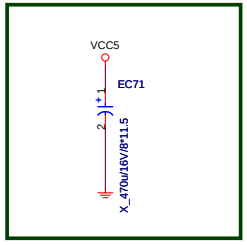


NEAR USB CONNECTOR

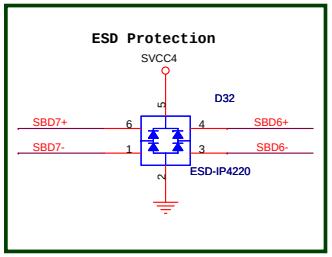
22 / 7.5 / 7.5 / 7.5 / 22 / 7.5 / 7.5 / 7.5 / 22

POWER CIRCUIT FOR USB PORT 6,7

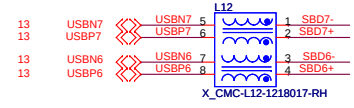


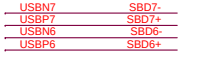


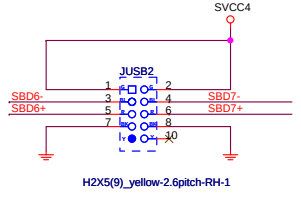
ESD Protection



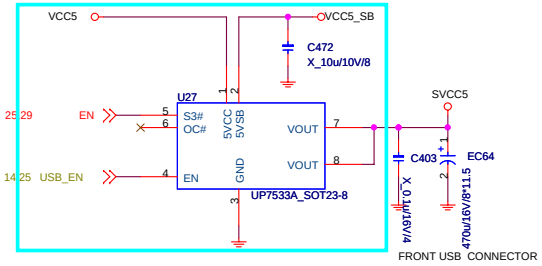
FRONT PANEL USB CONNECTOR FOR USB PORT 6,7





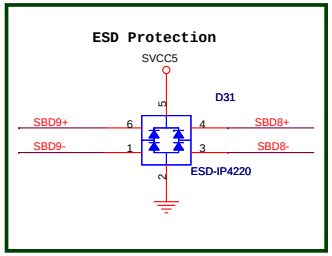


POWER CIRCUIT FOR USB PORT 8,9



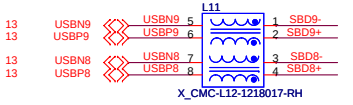
JUSB3--USB[8.9] is not present in MCP73V

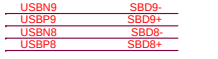
ESD Protection

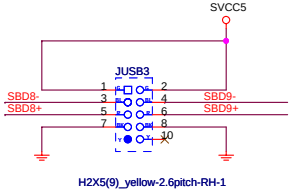


FRONT PANEL USB CONNECTOR FOR USB PORT 8,9

JUSB3 is not present in MCP73S







JUSB3--USB[8.9] is not present in MCP73V

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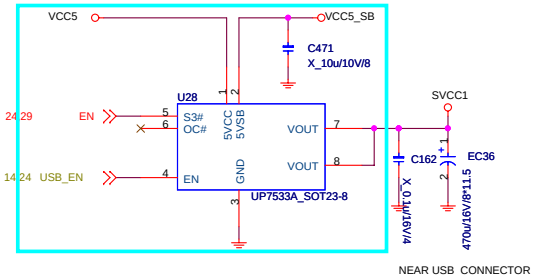
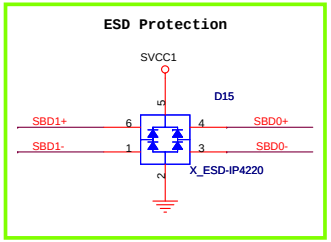
REAR PANEL USB CONNECTOR

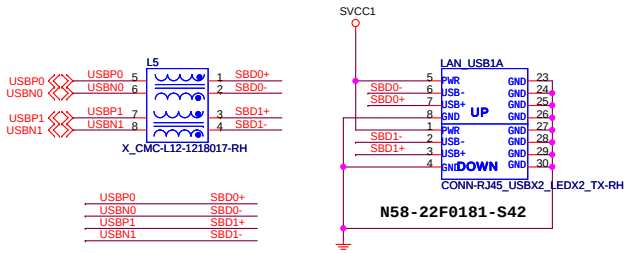
POWER CIRCUIT FOR USB PORT 0,1



NEAR USB CONNECTOR



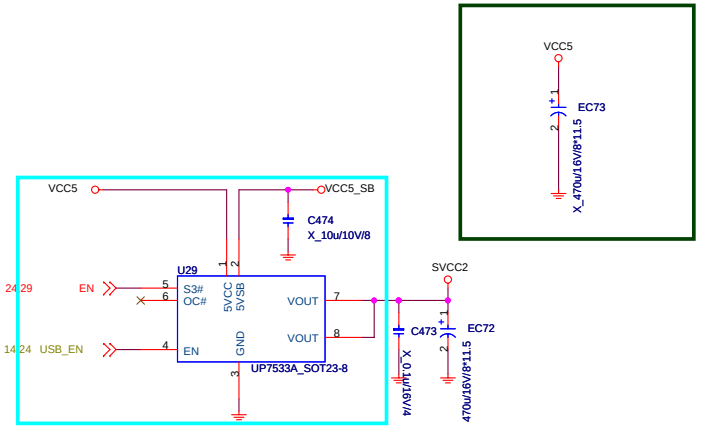
REAR PANEL USB CONNECTOR FOR USB PORT 0,1



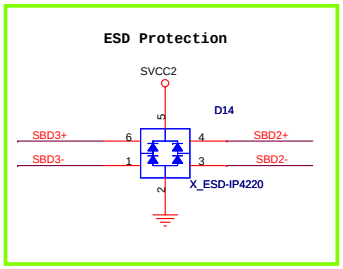
NEAR USB CONNECTOR

22 / 7.5 / 7.5 / 7.5 / 22 / 7.5 / 7.5 / 7.5 / 22

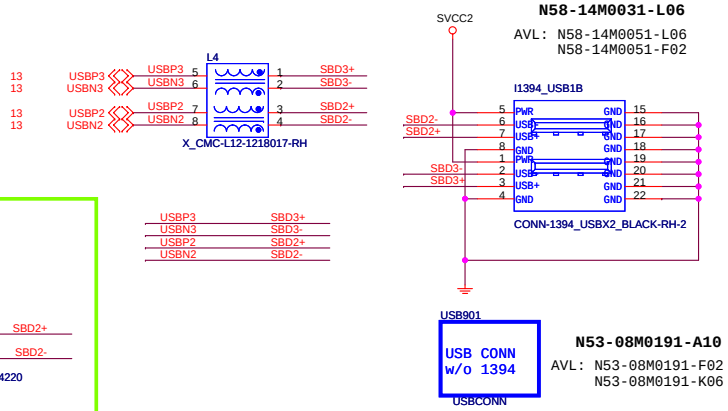
POWER CIRCUIT FOR USB PORT 2,3



NEAR USB CONNECTOR



REAR PANEL USB CONNECTOR FOR USB PORT 2,3



NEAR USB CONNECTOR

22 / 7.5 / 7.5 / 7.5 / 22 / 7.5 / 7.5 / 7.5 / 22

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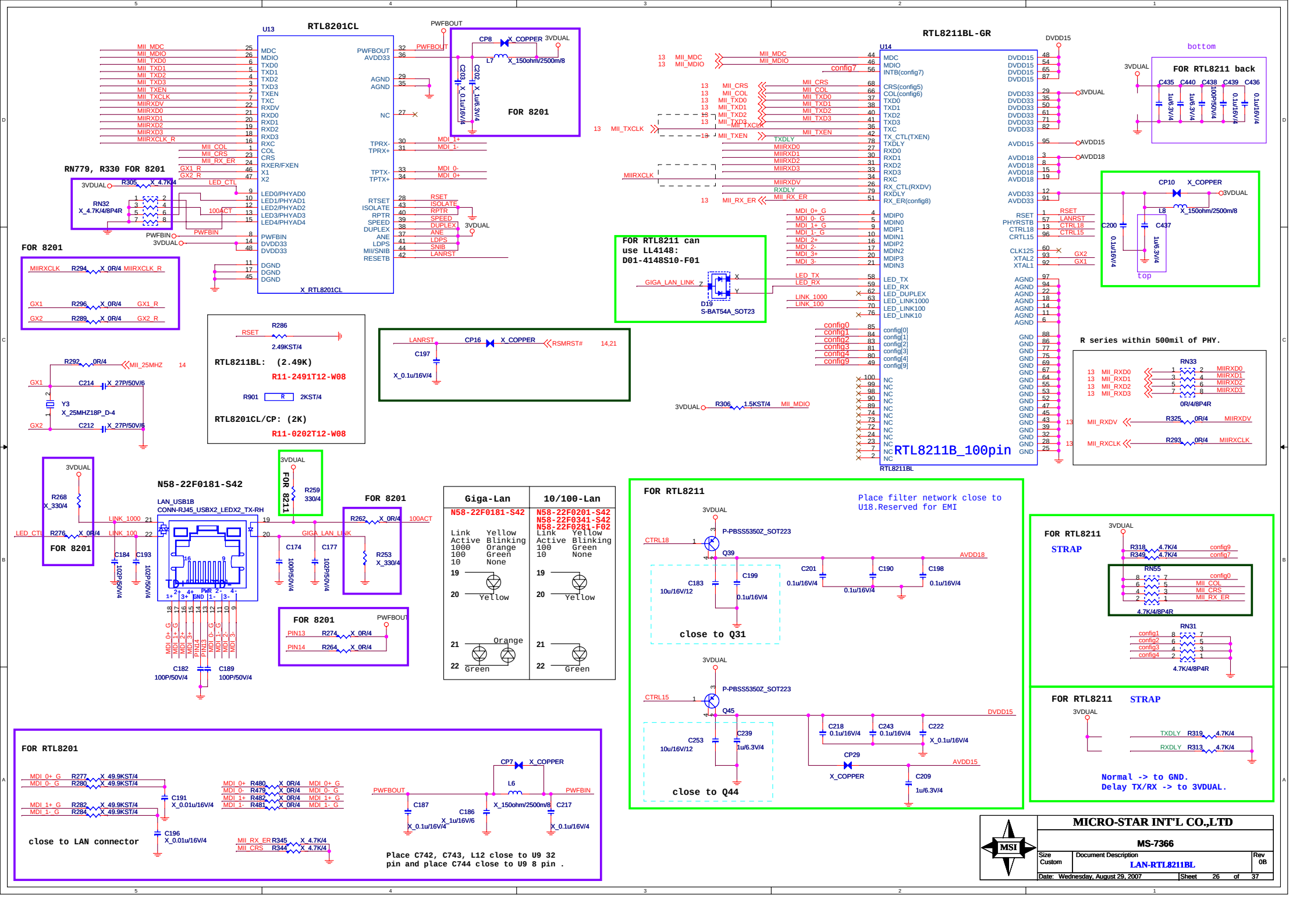
U13
RTL8201CL
X_RTL8201CL
RN779, R330 FOR 8201
FOR 8201
PWFBOUT
CP8
X_COPPER
3VDUAL
L7
X_150ohm/2500m/8
C203
C202
X_1u/6.3V/4
X_0.1u/16V/4
FOR 8201
MII_MDC
MII_MDIO
MII_TXD0
MII_TXD1
MII_TXD2
MII_TXD3
MII_TXEN
MII_TXCLK
MIIRXDV
MIIRXD0
MIIRXD1
MIIRXD2
MIIRXD3
MIIRXCLK_R
MII_COL
MII_CRS
MII_RX_ER
GX1_R
GX2_R
LED_CTL
100ACT
PWFBIN
MDI_1+
MDI_1-
MDI_0-
MDI_0+
RSET
ISOLATE
RPTR
SPEED
DUPLEX
ANE
LDPS
SNIB
LANRST
R305
X_4.7K/4
RN32
X_4.7K/4/8P4R
MIIRXCLK
R294
X_0R/4
MIIRXCLK_R
GX1
R296
GX1_R
GX2
R289
GX2_R
R286
2.49KST/4
RTL8211BL: (2.49K)
R11-2491T12-W08
R901
2KST/4
RTL8201CL/CP: (2K)
R11-0202T12-W08
LANRST
CP16
RSMRST#
14,21
C197
X_0.1u/16V/4
R292
0R/4
MII_25MHZ
14
C214
X_27P/50V/6
Y3
X_25MHZ18P_D-4
C212
N58-22F0181-S42
LAN_USB1B
CONN-RJ45_USBX2_LEDX2_TX-RH
FOR 8211
R259
330/4
FOR 8201
R262
X_0R/4
100ACT
R253
X_330/4
R268
X_330/4
LINK_1000
LINK_100
LED_CTL
R276
X_0R/4
GIGA_LAN_LINK
C184
C193
C174
C177
102P/50V/4
100P/50V/4
C182
C189
100P/50V/4
FOR 8201
PIN13
PIN14
R274
R264
X_0R/4
Giga-Lan
N58-22F0181-S42
Link Yellow
Active Blinking
1000 Orange
100 Green
10 None
10/100-Lan
N58-22F0201-S42
N58-22F0341-S42
N58-22F0281-F02
Link Yellow
Active Blinking
100 Green
10 None
Yellow
Orange
Green
FOR RTL8201
MDI_0+_G
MDI_0-_G
MDI_1+_G
MDI_1-_G
R277
R280
R284
X_49.9KST/4
C191
C196
X_0.01u/16V/4
close to LAN connector
MDI_0+
MDI_0-
MDI_1+
MDI_1-
R480
R479
R482
R481
X_0R/4
MII_RX_ER
MII_CRS
R345
R344
X_4.7K/4
CP7
L6
C187
C186
X_1u/16V/6
C217
PWFBIN
Place C742, C743, L12 close to U9 32 pin and place C744 close to U9 8 pin .
RTL8211BL-GR
U14
RTL8211B_100pin
RTL8211BL
FOR RTL8211 can use LL4148: D01-4148S10-F01
GIGA_LAN_LINK
D19
S-BAT54A_SOT23
config7
config0
config1
config2
config3
config4
config9
R306
1.5KST/4
DVDD15
AVDD15
AVDD18
AVDD33
GND
bottom
FOR RTL8211 back
C435
C440
C438
C439
C436
top
CP10
L8
C200
C437
R series within 500mil of PHY.
RN33
0R/4/8P4R
R325
R293
FOR RTL8211
Place filter network close to U18.Reserved for EMI
P-PBSS5350Z_SOT223
Q39
Q45
CTRL18
CTRL15
C183
C253
10u/16V/12
C199
C239
1u/6.3V/4
C201
C190
C198
C218
C243
C222
C209
AVDD18
DVDD15
close to Q31
close to Q44
FOR RTL8211
STRAP
R318
R349
4.7K/4
RN55
4.7K/4/8P4R
RN31
FOR RTL8211
STRAP
TXDLY
RXDLY
R319
R313
Normal -> to GND.
Delay TX/RX -> to 3VDUAL.
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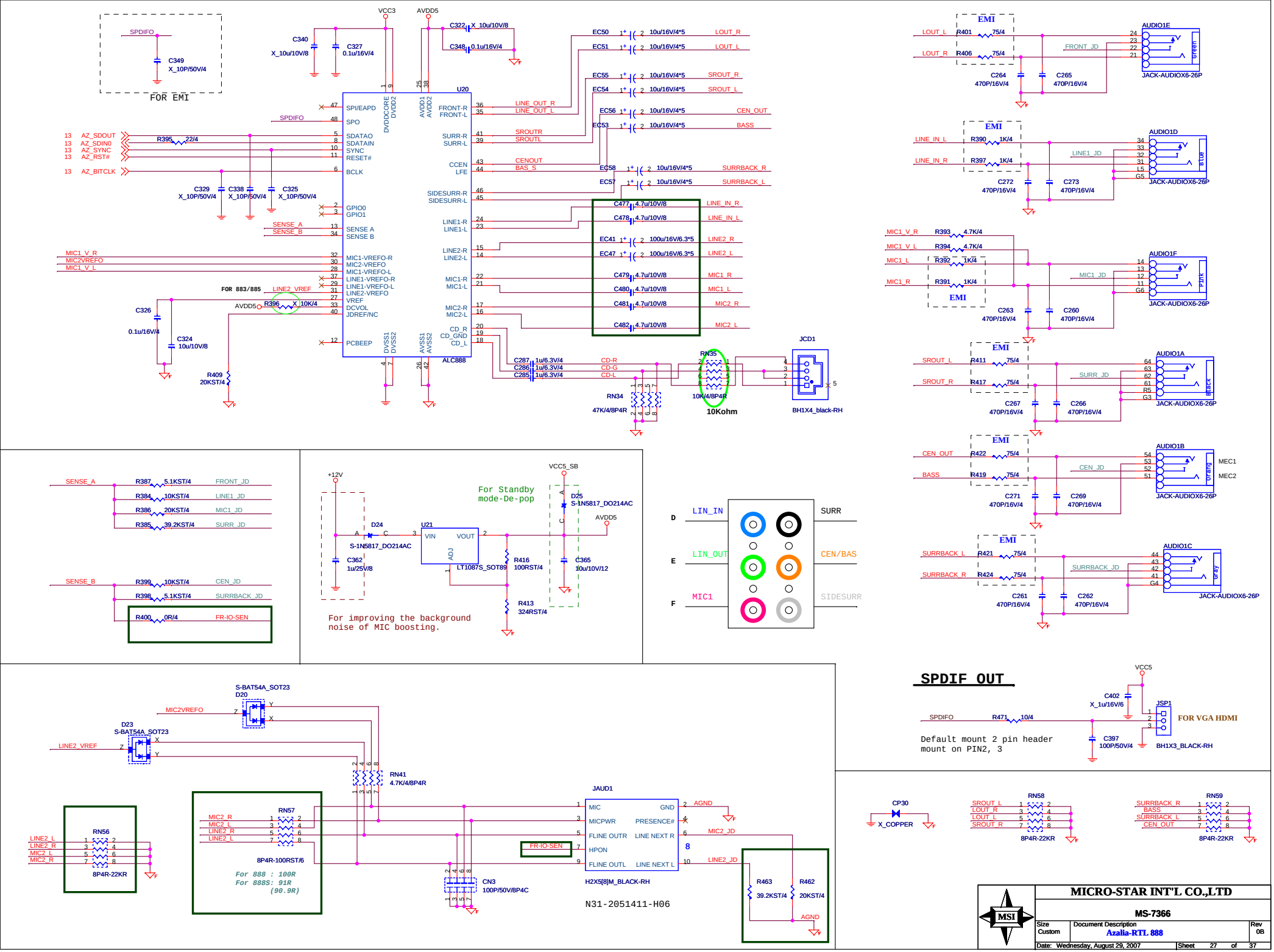

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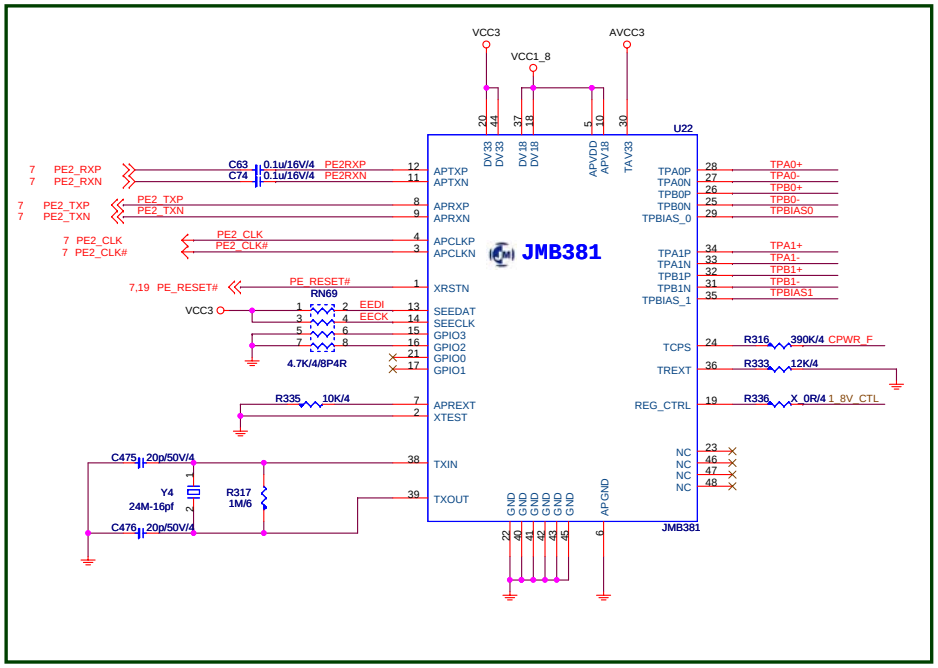
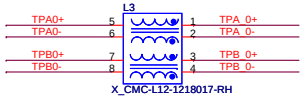
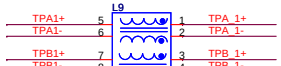
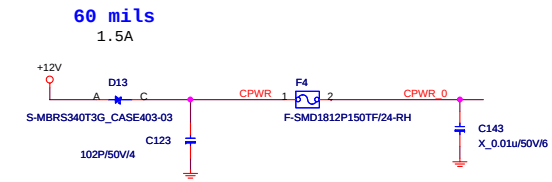
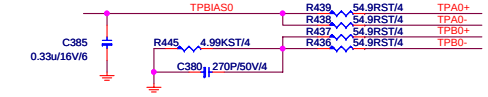
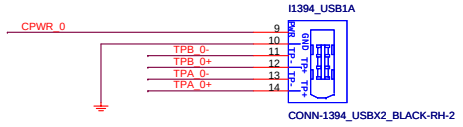


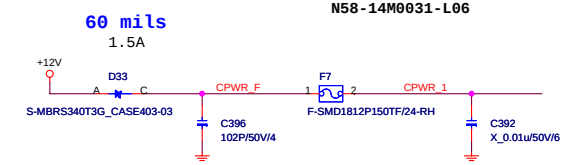


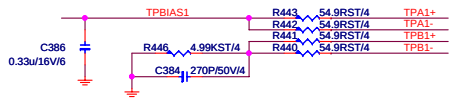


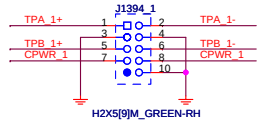




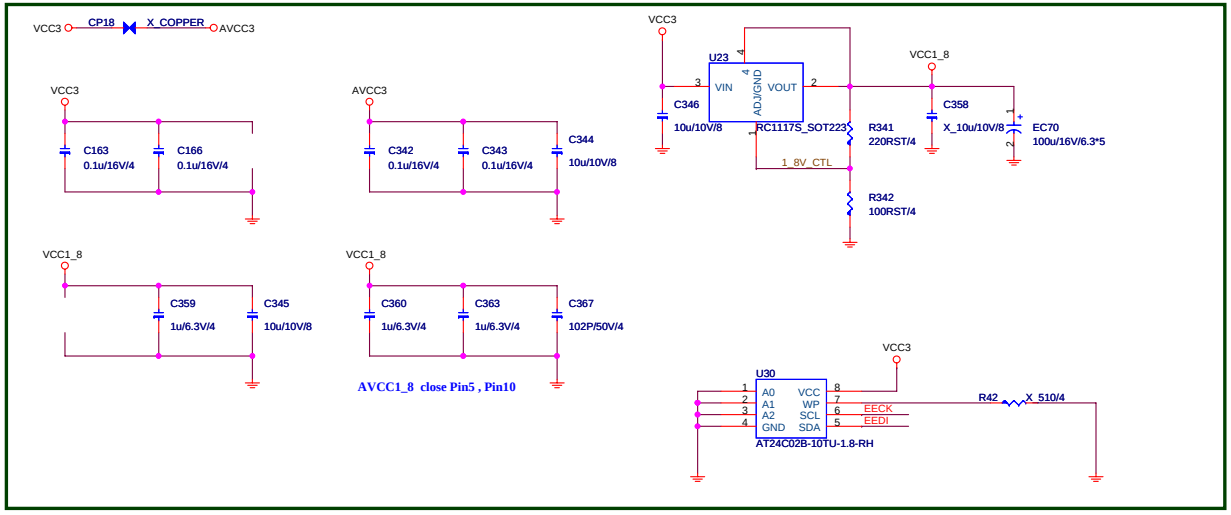




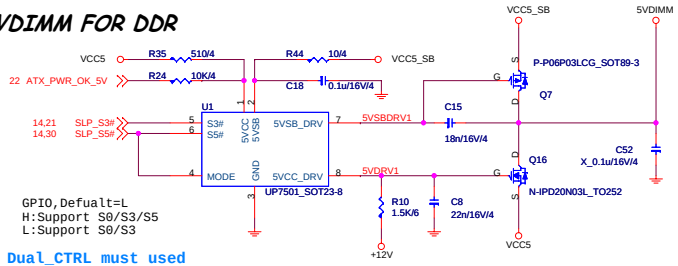




For Intel 1394 pinheader



5VDIMM FOR DDR

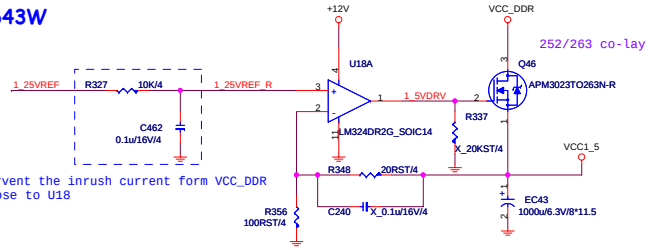


GPIO,Defualt=L
H:Support S0/S3/S5
L:Support S0/S3

The Dual_CTRL must used
default "Output- Low"

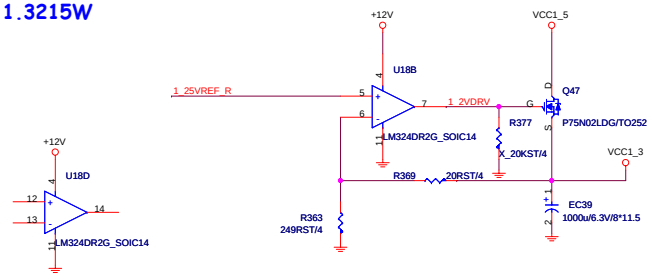
VCC1_5, 8.81A
2.643W

H_VCCPLL=>100mA
MCP73 =>7A



pervent the inrush current form VCC_DDR
close to U18

VCC1_3, 8.81A
1.3215W

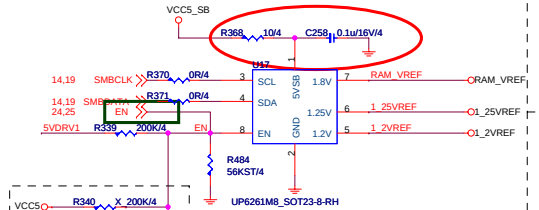


Reference Voltage

up6261: High Precision Voltage Console

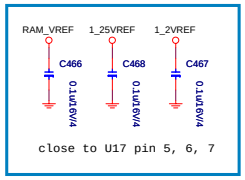
ONLY OVER DDR Voltage to 2V

VCC5_SB to UP6261 pin1 path keep the same.



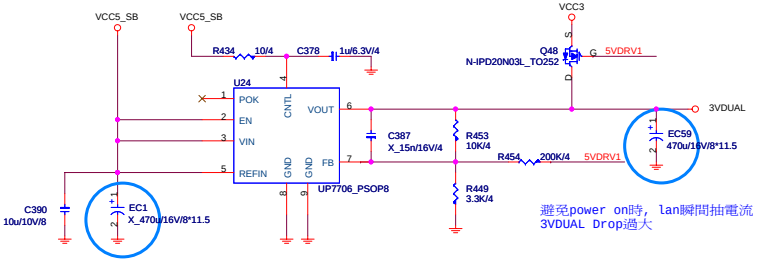
EN : 0.4~1.4V

I32-0626109-U33 , delay 20 ms
避免EN比 5VDRV1早, MCP73 core power
抽到VCC5_SB



close to U17 pin 5, 6, 7

3VDUAL, 1.7A

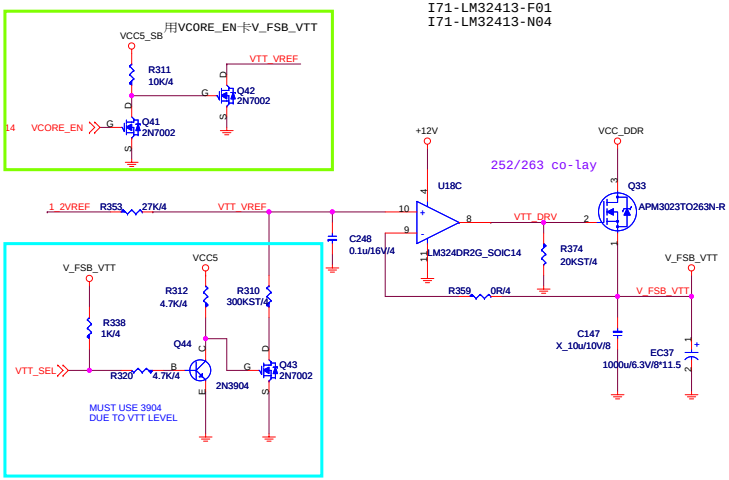


避免power on時, lan瞬間抽電流
3VDUAL Drop過大

避免power on時, lan瞬間抽電流
VCC5_SBL Drop過大

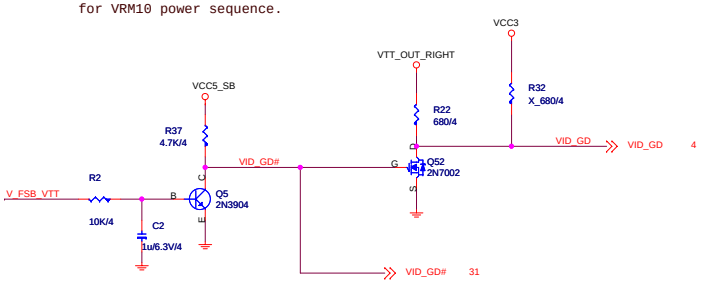
FSB_VTT, 6.1A
3.66W

U18:
I71-LM32403-T07
I71-LM32413-005
I71-LM32413-F01
I71-LM32413-N04



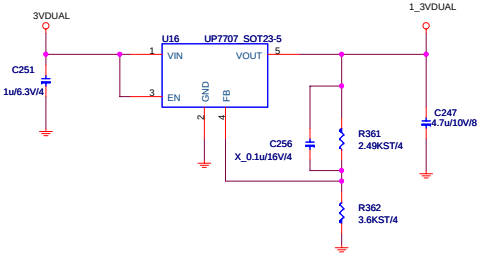
VTT_SEL = L	V_FSB_VTT=1.1V	For future KENTSFIELD processor. (FSB1333, Quad-Core)
VTT_SEL = H	V_FSB_VTT=1.2V	For normal processors.

VID_GD# to PWM and VID_GD to CPU
for VRM10 power sequence.

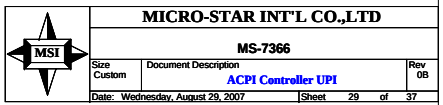


1_3VDUAL, 25mA

up7707: 600mA Low Dropout Linear Regulator

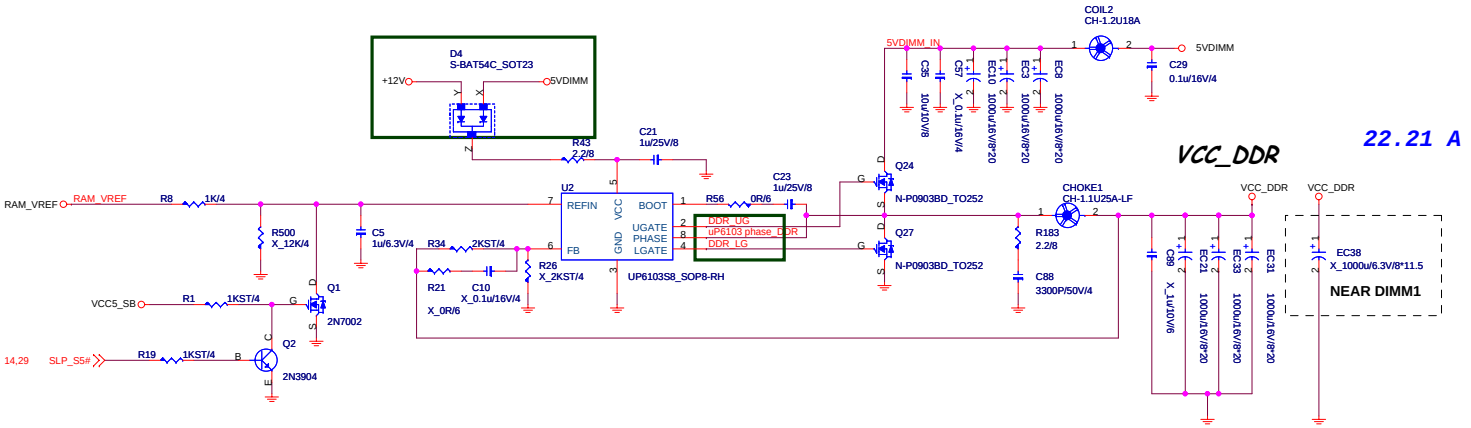


Vout=0.8* (R1+R2)/R1



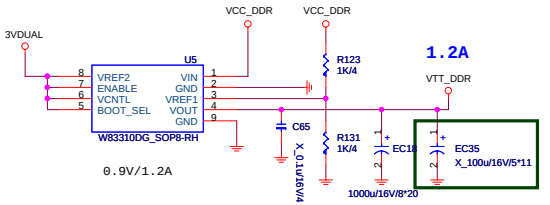
DDR II 1.8V POWER

Iripple=22.21*0.6*0.8/1=10.66A
2.35*3*1.7=11.985A>10.08A

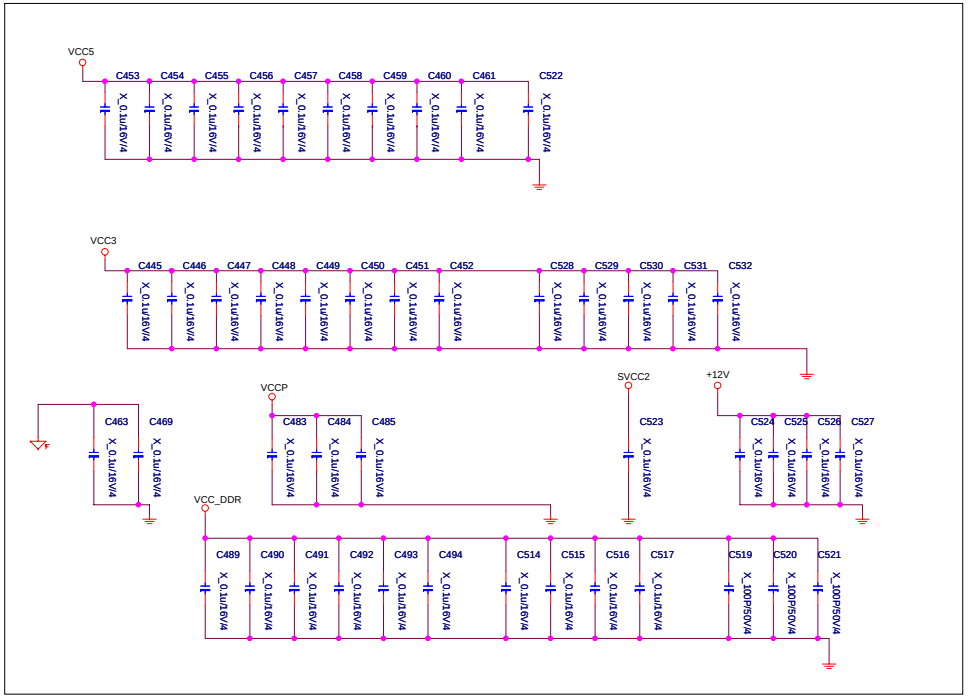


DDR VTT Power

To CPU Copper trace width > 200mils



EMI



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MS-7366		
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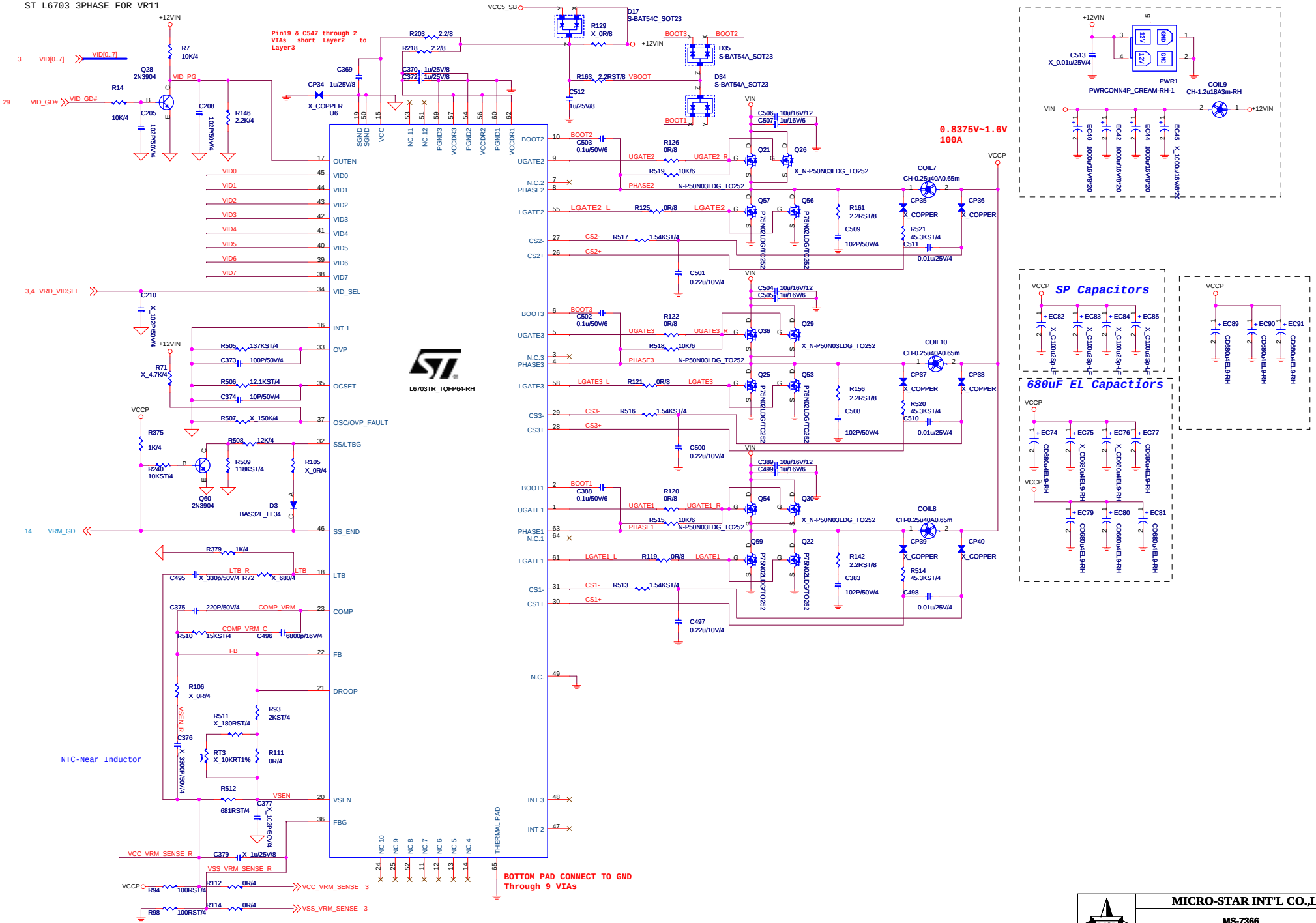
ST L6703 3PHASE FOR VR11



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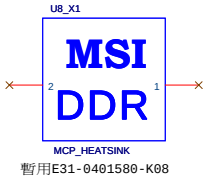
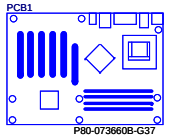
MS-7366			
Size: Custom	Document Description: VRM11-ST L6703		Rev: 0B
Date: Wednesday, August 29, 2007		Sheet 31 of 37	



暫用E31-0401580-K08



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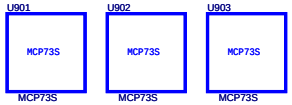
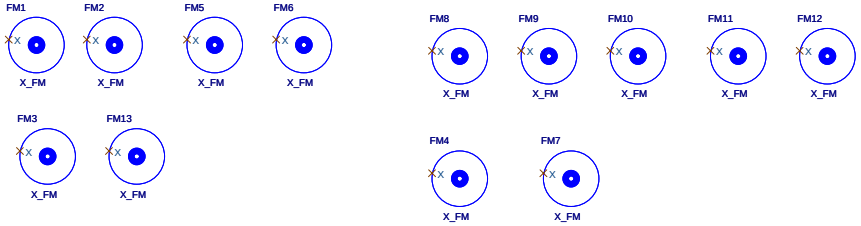


Table 1-4. Comparison of Different MCP73 Models

Features	MCP73D	MCP73PV	MCP73O	MCP73S	MCP73V
iGPU	No	DX9 SM3.0	DX9 SM3.0	DX9 SM3.0	DX9 SM3.0
Display Interface	N/A	HDMI, DVI, RGB, sDVO	DVI, RGB, sDVO	DVI, RGB, sDVO	RGB
Integrated HDCP	N/A	Yes	Yes	Yes	No
FSB	1333	1333	1333	1066	1066
Memory	DDR2-667 64-bit	DDR2-667 64-bit	DDR2-667 64-bit	DDR2-667 64-bit	DDR2-667 64-bit
PCI Express	1 ×16, 2 ×1	1 ×16, 2 ×1	1 ×16, 2 ×1	1 ×16, 2 ×1	1 ×16, 2 ×1
USB Ports	8	10	10	10	8
Networking	10/100/1000	10/100/1000	10/100/1000	10/100/1000	10/100
SATA II Ports	4	4	4	4	4
RAID	0, 1	0, 1, 0+1, 5	0, 1, 0+1, 5	0, 1, 0+1, 5	0, 1
PATA-133	Two devices	Two devices	Two devices	Two devices	Two devices
iGPU Dev-ID	N/A	0x7E0	0x7E1	0x7E2	0x7E3
Marketing Brand Name	NVIDIA nForce 630i	NVIDIA nForce 630i GeForce 7050	NVIDIA nForce 630i GeForce 7050	NVIDIA nForce 630i GeForce 7025	NVIDIA nForce 610i GeForce 7025

Optics Orientation Holes



Mounting Holes

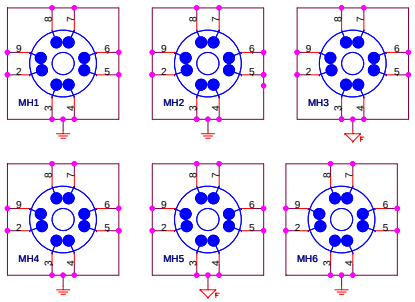
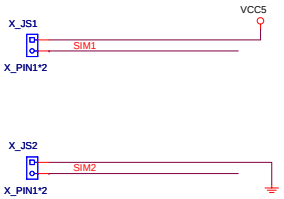


Table 1. MCP73 SKU Definition

Features	MCP73PV	MCP73S	MCP73V
FSB	1333	1333	1066
Memory	DDR2-800 64 bit	DDR2-667 64 bit	DDR2-667 64 bit
Display	HDMI, DVI, RGB, sDVO	DVI, RGB, sDVO	RGB
Integrated HDCP	Yes	Yes	No
Integrated Networking	10/100/1000	10/100/1000	10/100
Vista Premium	Yes	Yes	Yes
PCI-E	1 x16, 2 x1	1 x16, 2 x1	1 x16, 2 x1
USB Ports	10	10	8
SATA II Ports	4	4	4
RAID	0,1,0+1,5	0,1,0+1,5	0, 1
PATA Drives	2	2	2

Simulation



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