


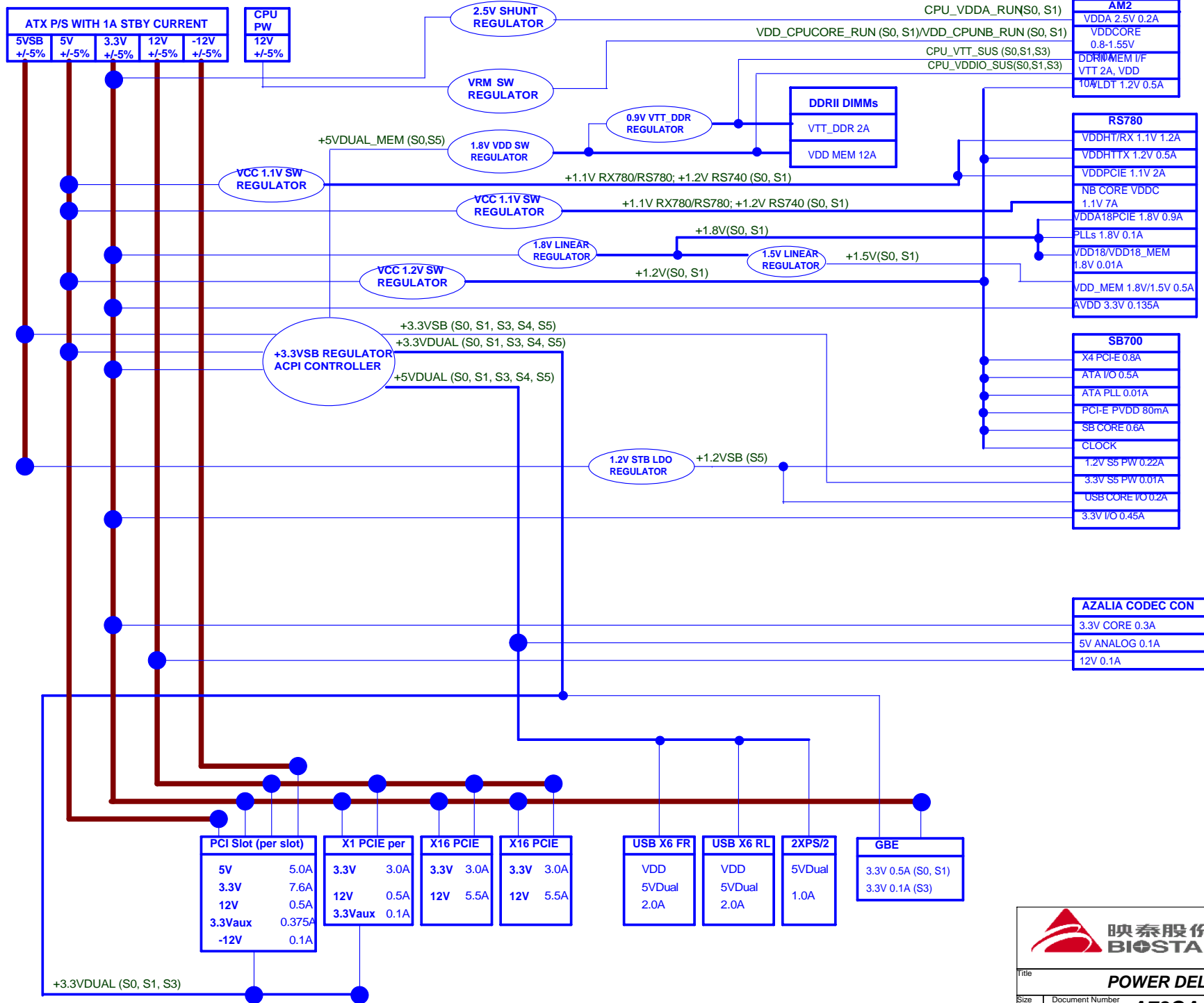
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14	DDR2 DIMM B1
15	DDR2 DIMM A2
16	DDR2 DIMM B2
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18	DDR2 DECOUPLING
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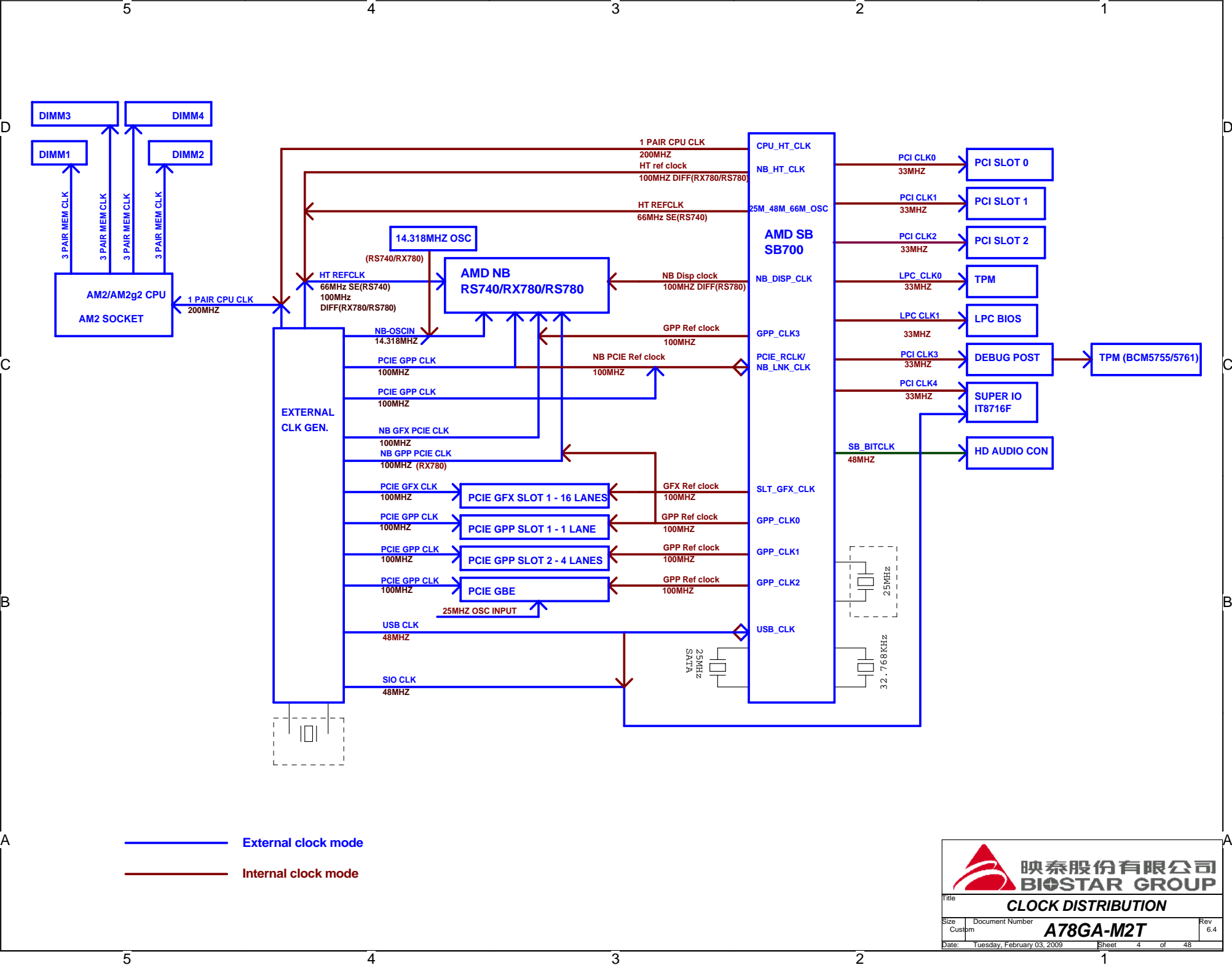
A78GA-M2T (RS780&SB700)

REV 6.4

DDR2 X 4 Dual channel , PCI-Ex16 X 1 , PCI-Ex1 X 1 , PCI X 2 , Realtek 10/100/1000 PCI-E Lan , AMD K8-940

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Title COVER			
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5


4

3

2

1

REV	DATA	DESCRIPTION
6.3	16/09/08	1. Add U6- --->解決3850顯卡有時點不亮問題 Page:30
6.4	02/02/09	1.修改EMI問題及螺絲孔(使用A76GA-M2T V6.0 PCB修改) 2.Add D14,R307,R323 3.U12,U13用料修改FP6321A 4.R320 NI



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BIOSSTAR GROUP

Title

REVISION HISTORY

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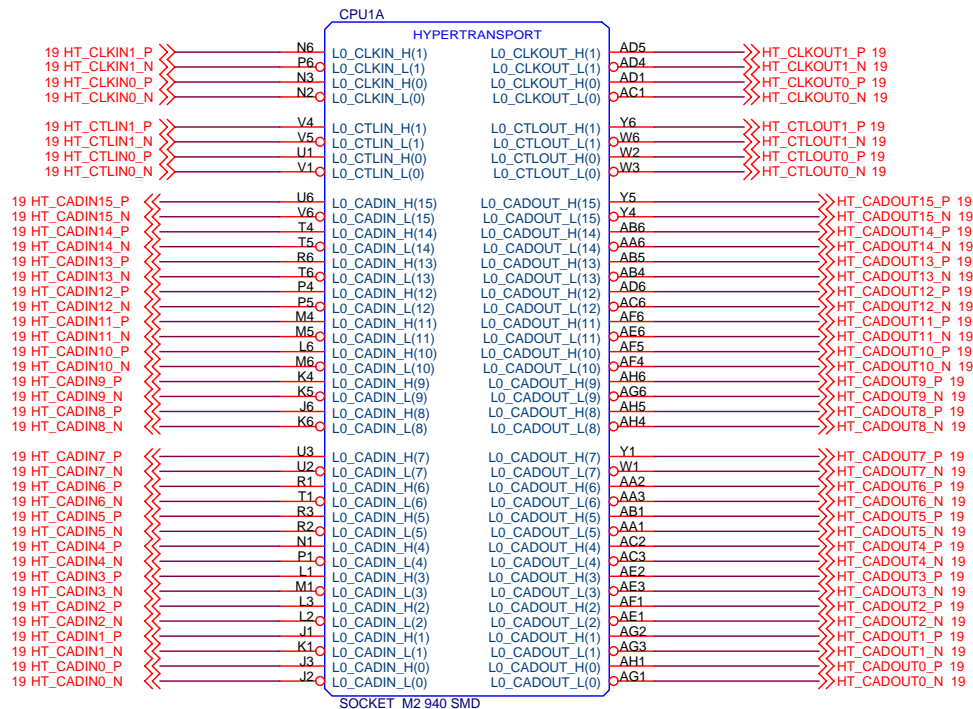
4

3

2

1

HyperTransport



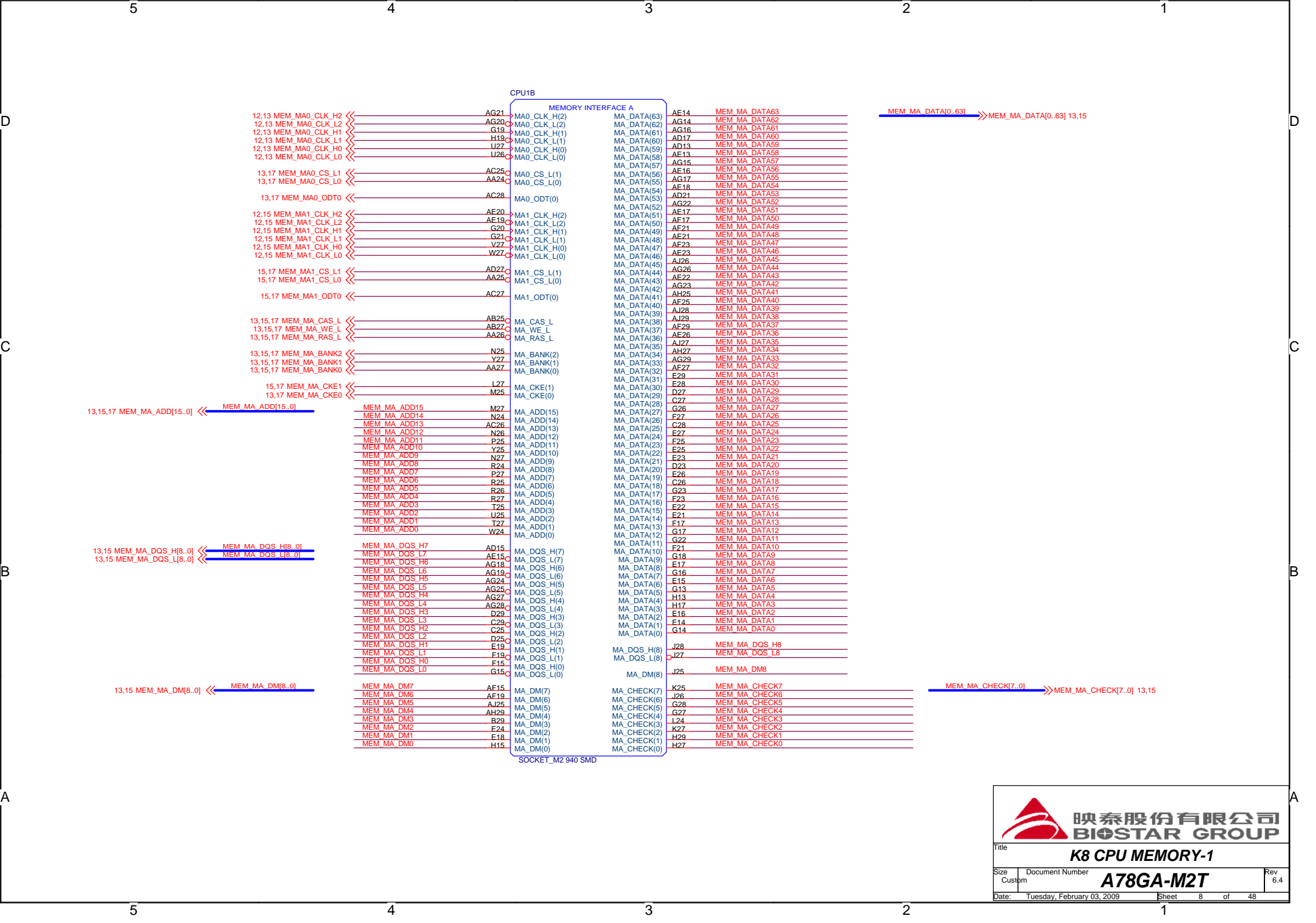
映泰股份有限公司
BIOTAR GROUP

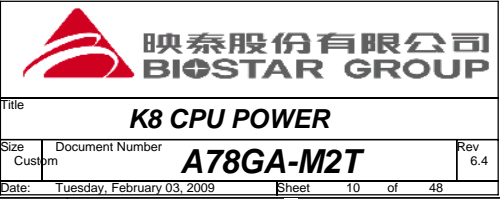
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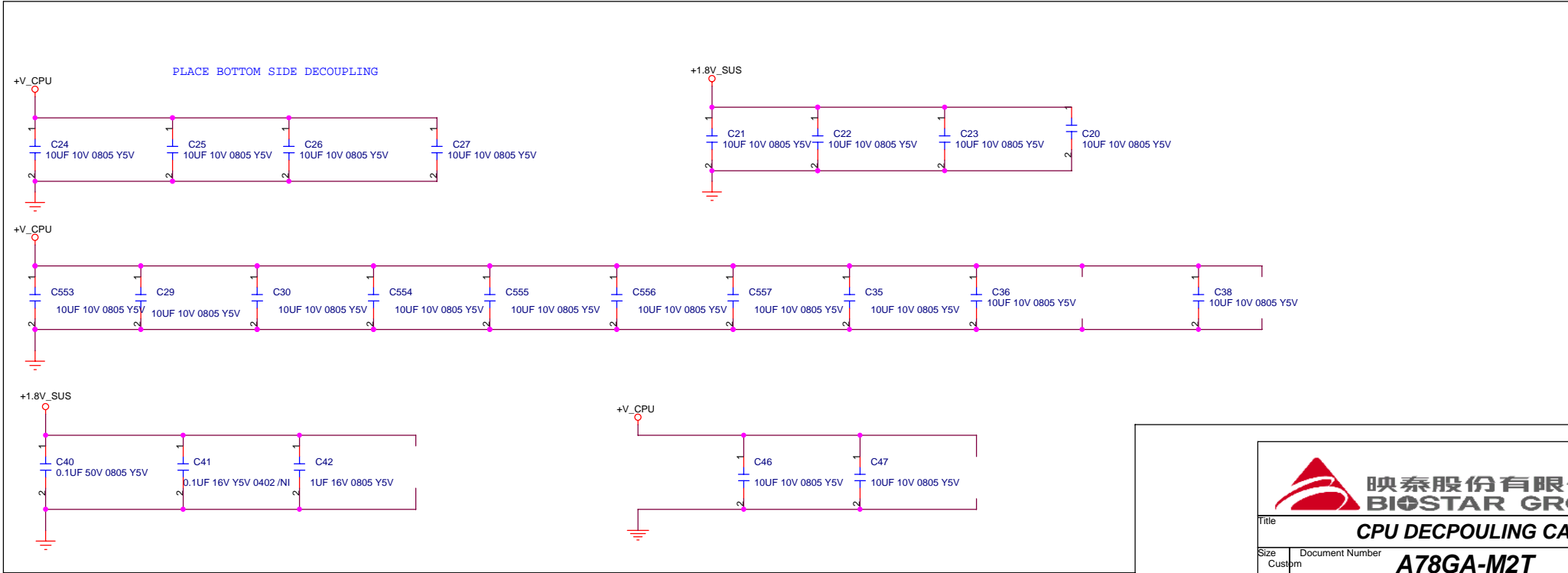
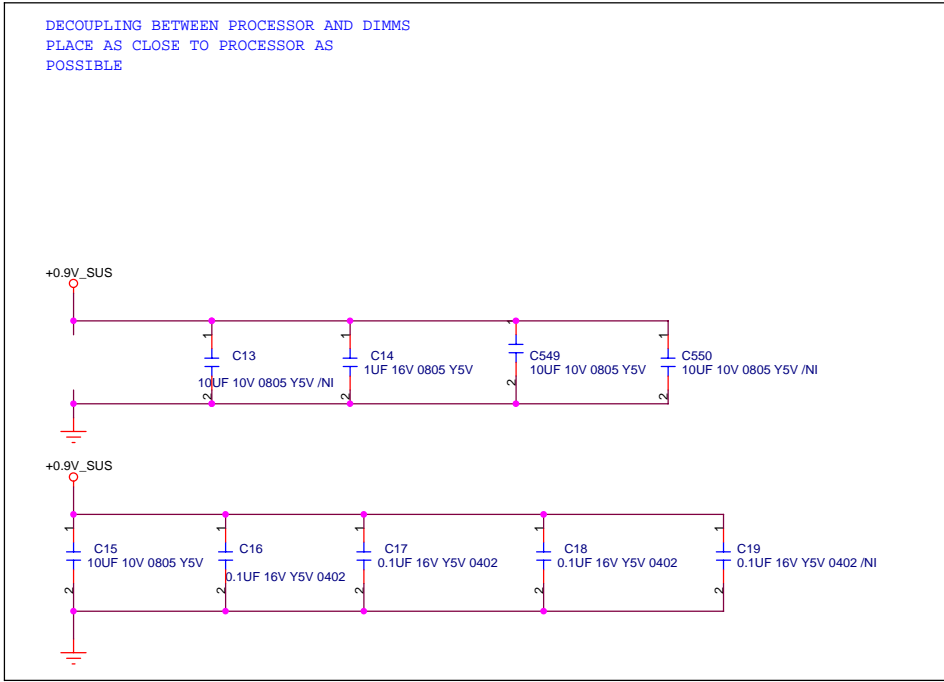
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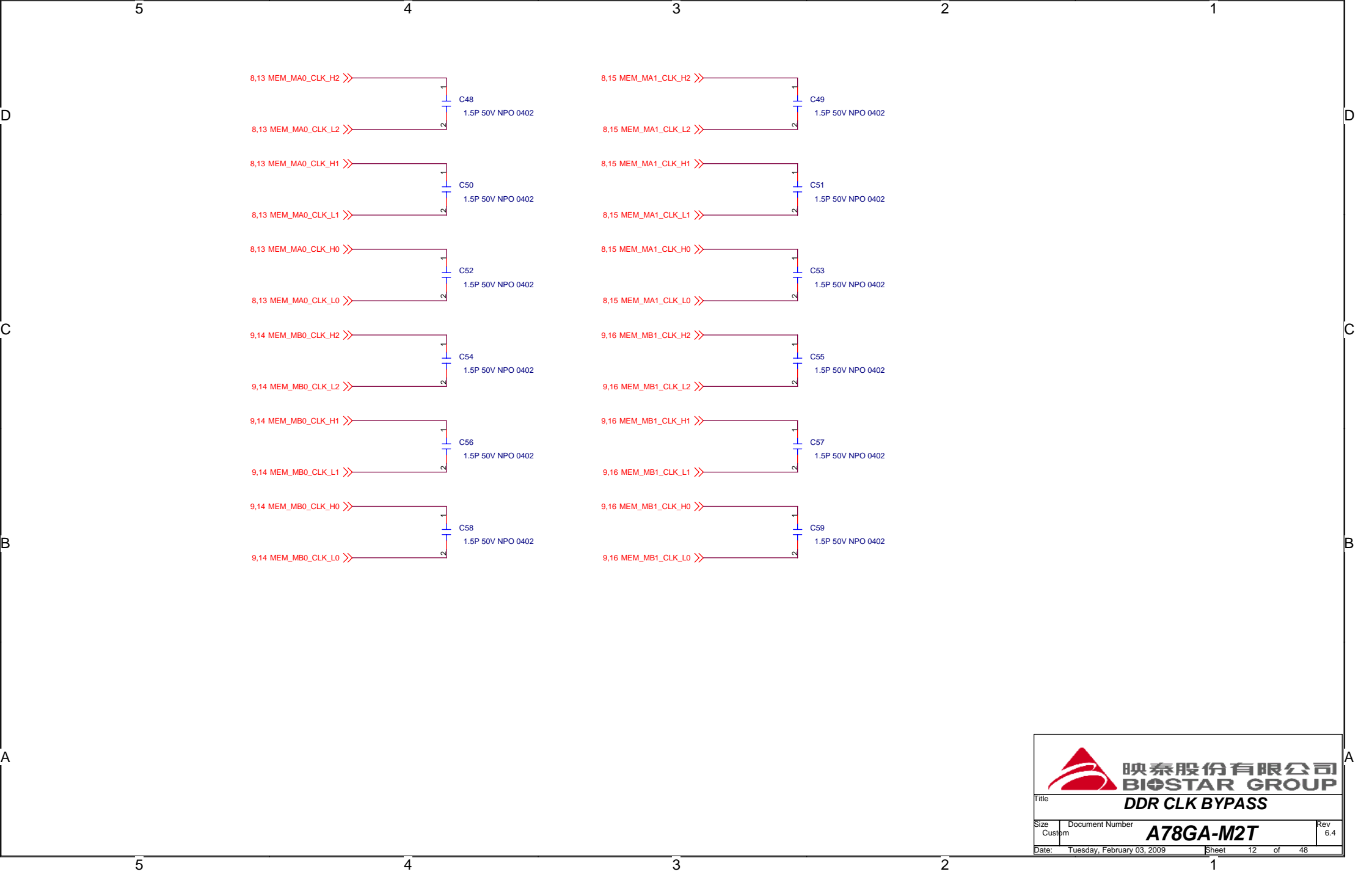


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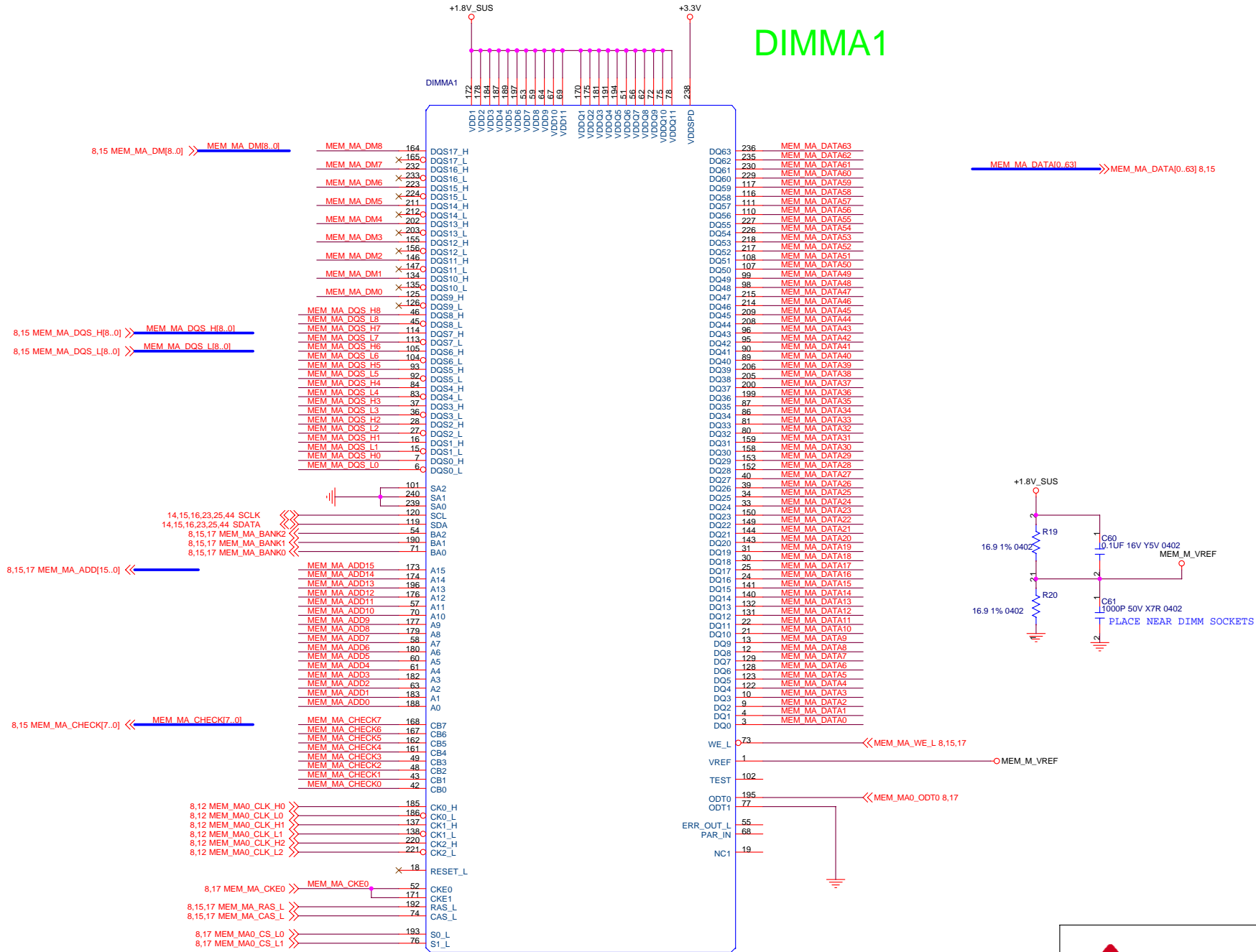




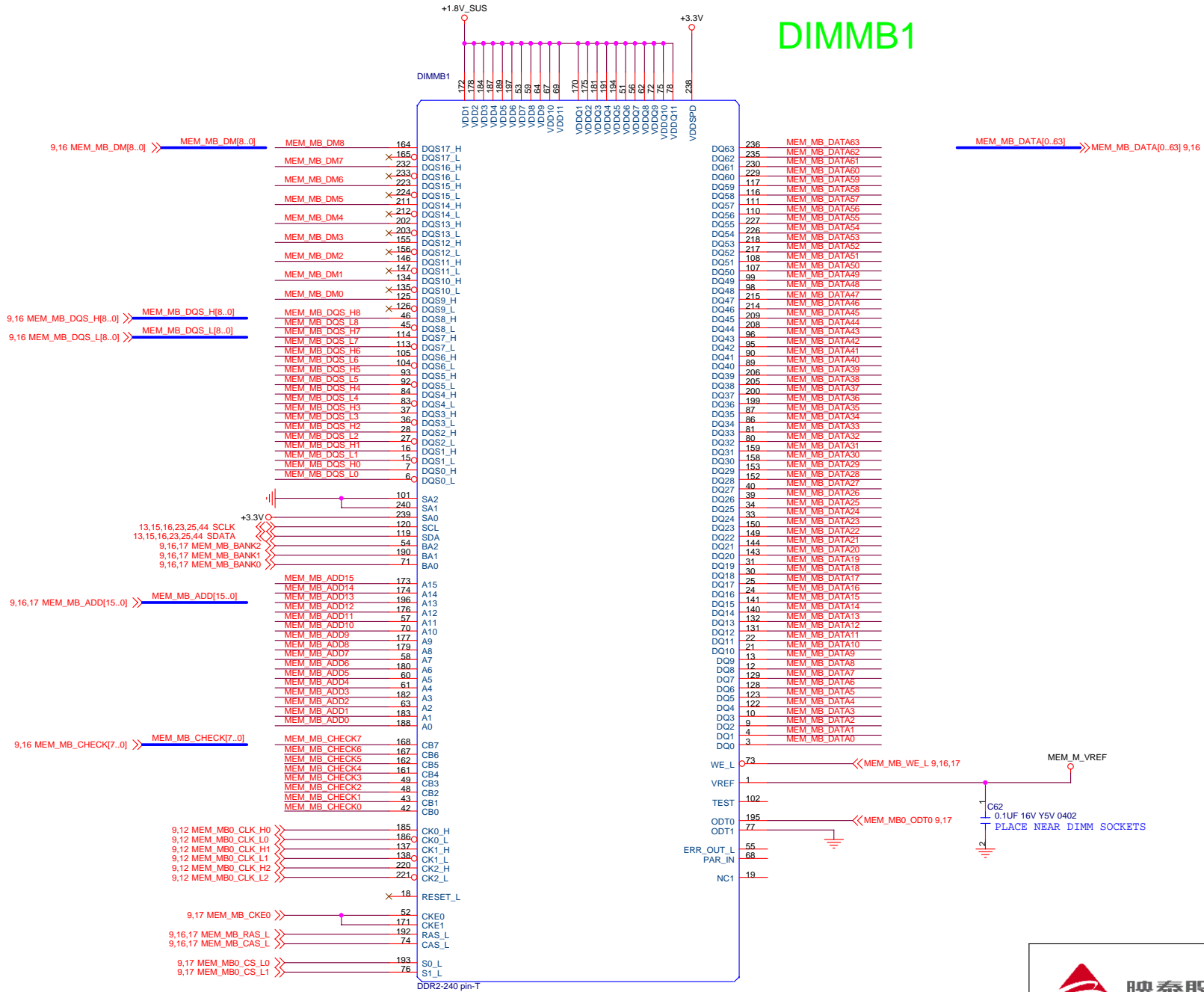




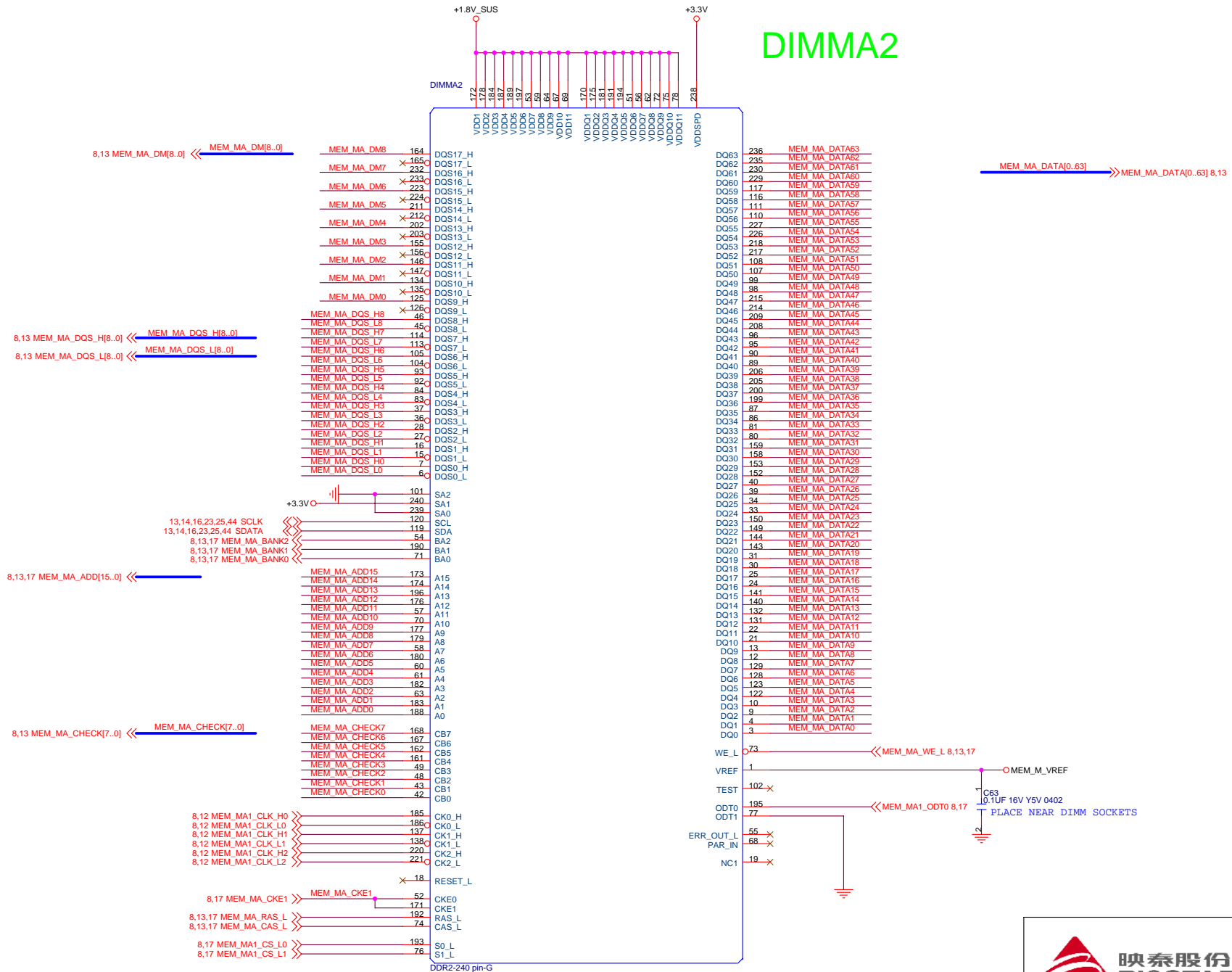
DIMMA1



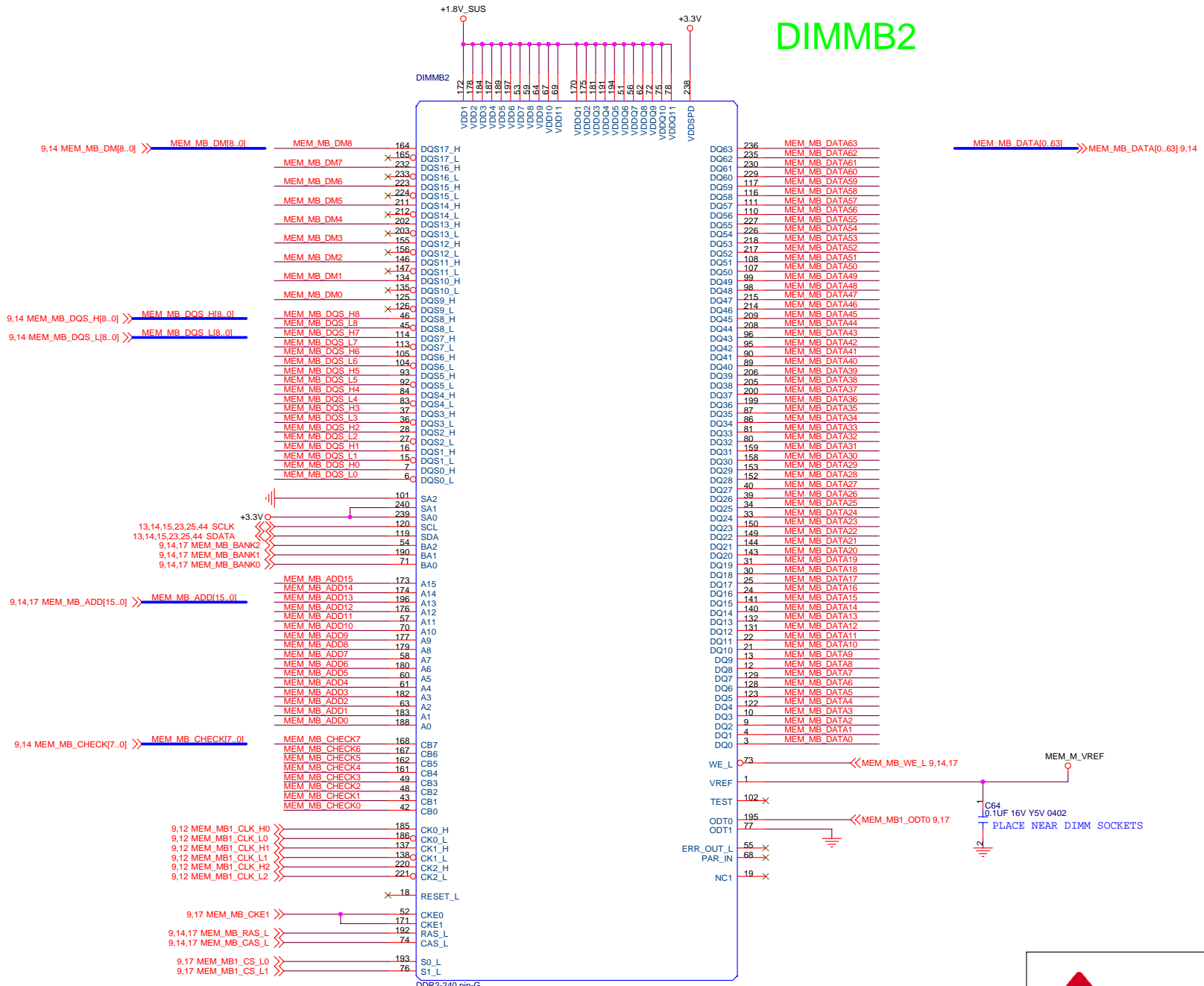
DIMMB1



DIMMA2



DIMMB2



Title
DDR DIMM-4

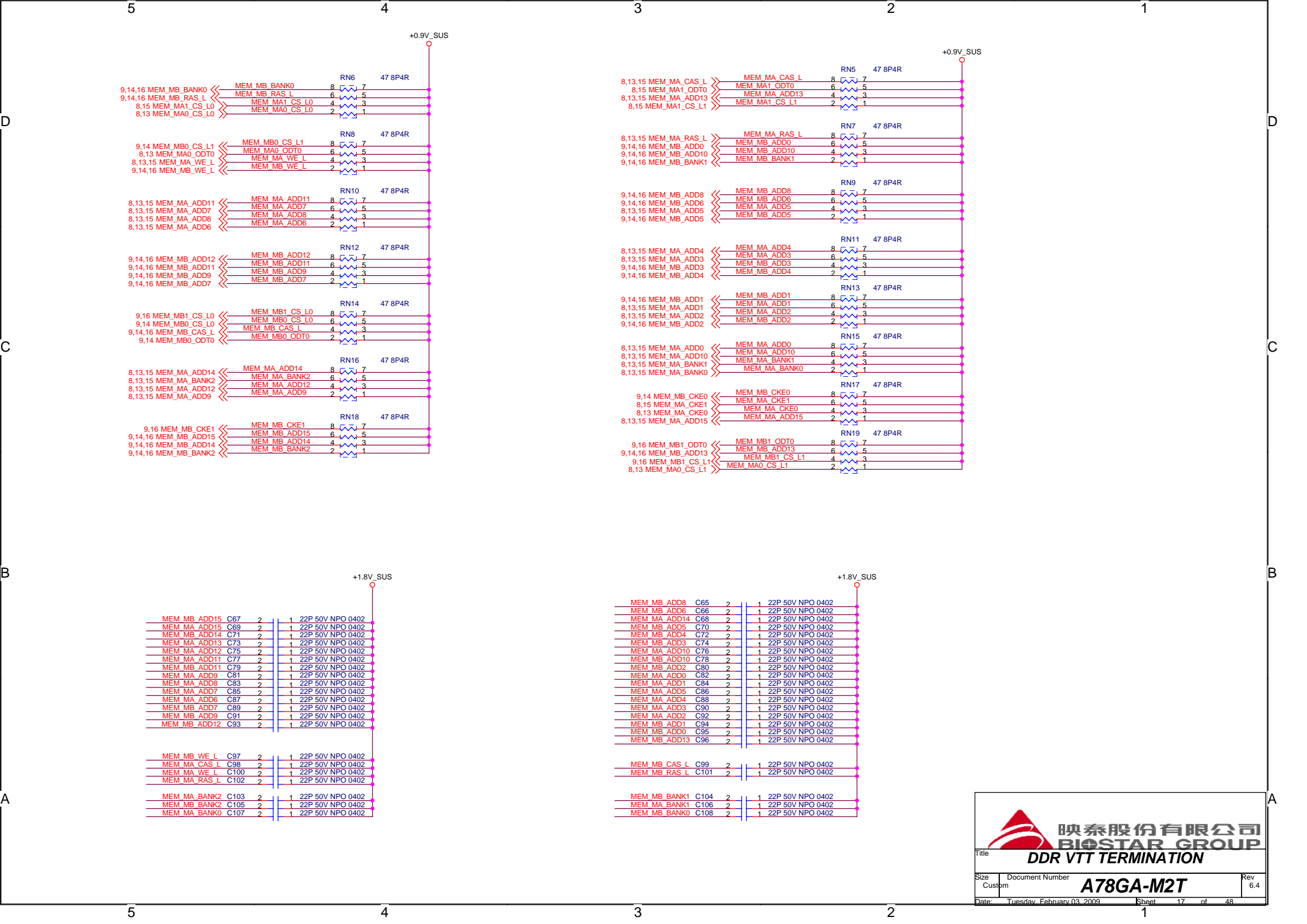
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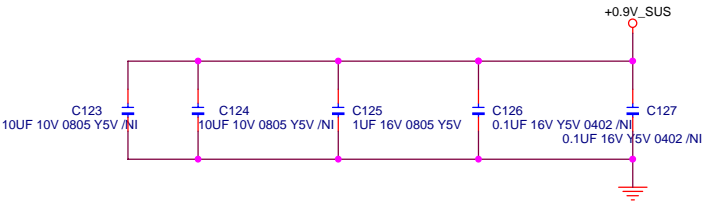
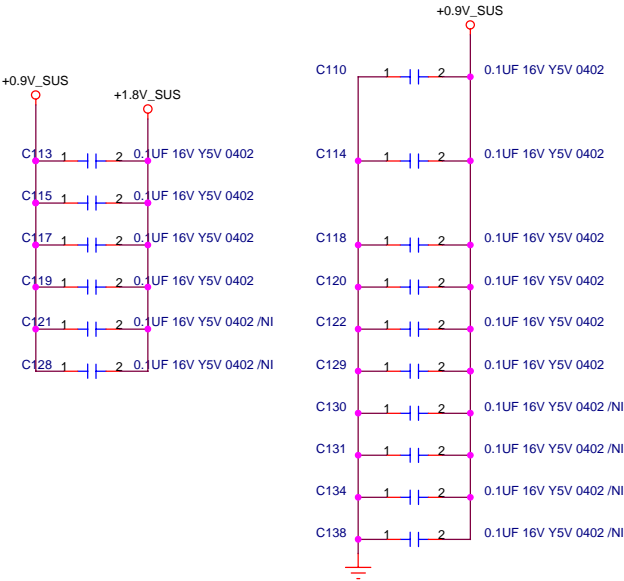
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Tuesday, February 03, 2009

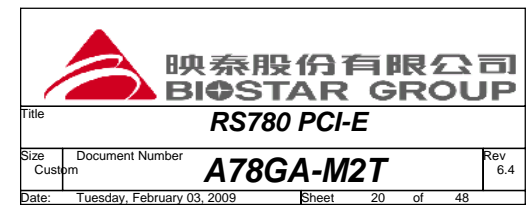
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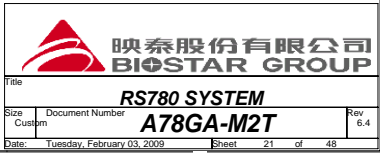
Rev
6.4

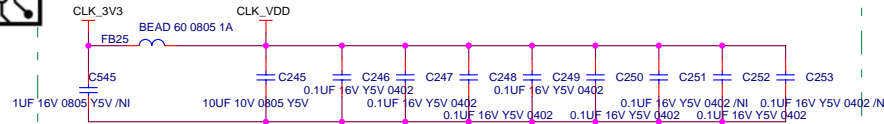


LAYOUT: FRONT SIDE PLACE ALTERNATING GND AND 1.8V
ALONG 0.9V VTT FILL





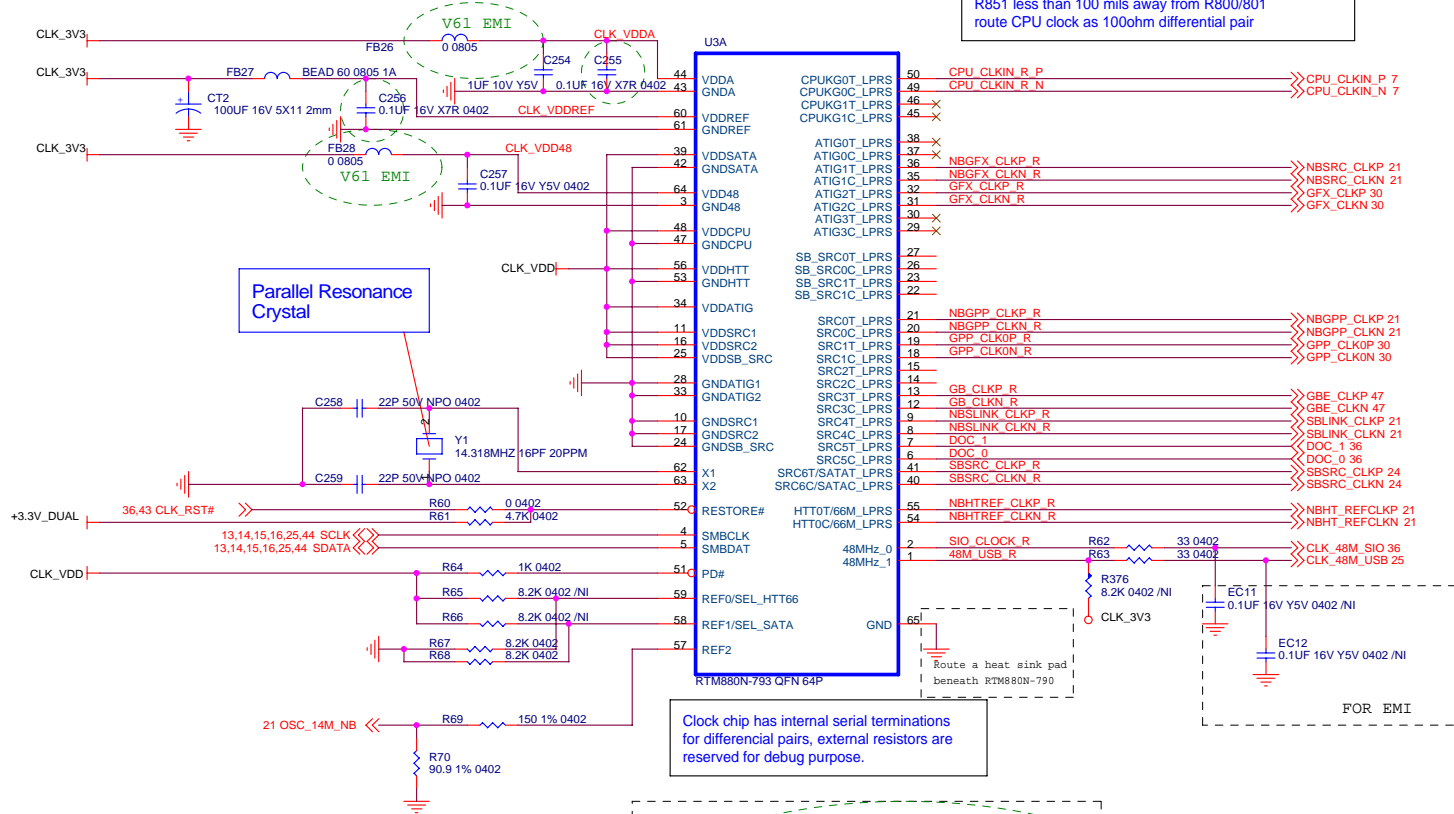




- 1- PLACE ALL THE SERIES TERMINATION RESISTORS AS CLOSE TO U800 AS POSSIBLE
- 2- ROUTE ALL SRCCLKTx AND SRCCLKCx AS DIFFERENT PAIR RULE
- 3- PUT DECOUPLING CAPS CLOSE TO U800 POWER PIN

AS DIFFERENT PAIR RULE

Place R800/801 less than 500 mils away from U800
R851 less than 100 mils away from R800/801
route CPU clock as 100ohm differential pair



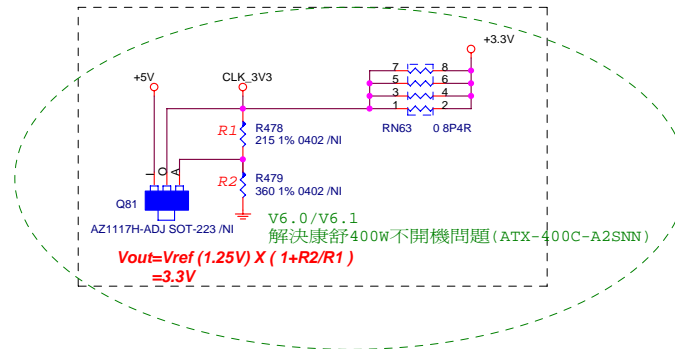
Parallel Resonance Crystal

Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.

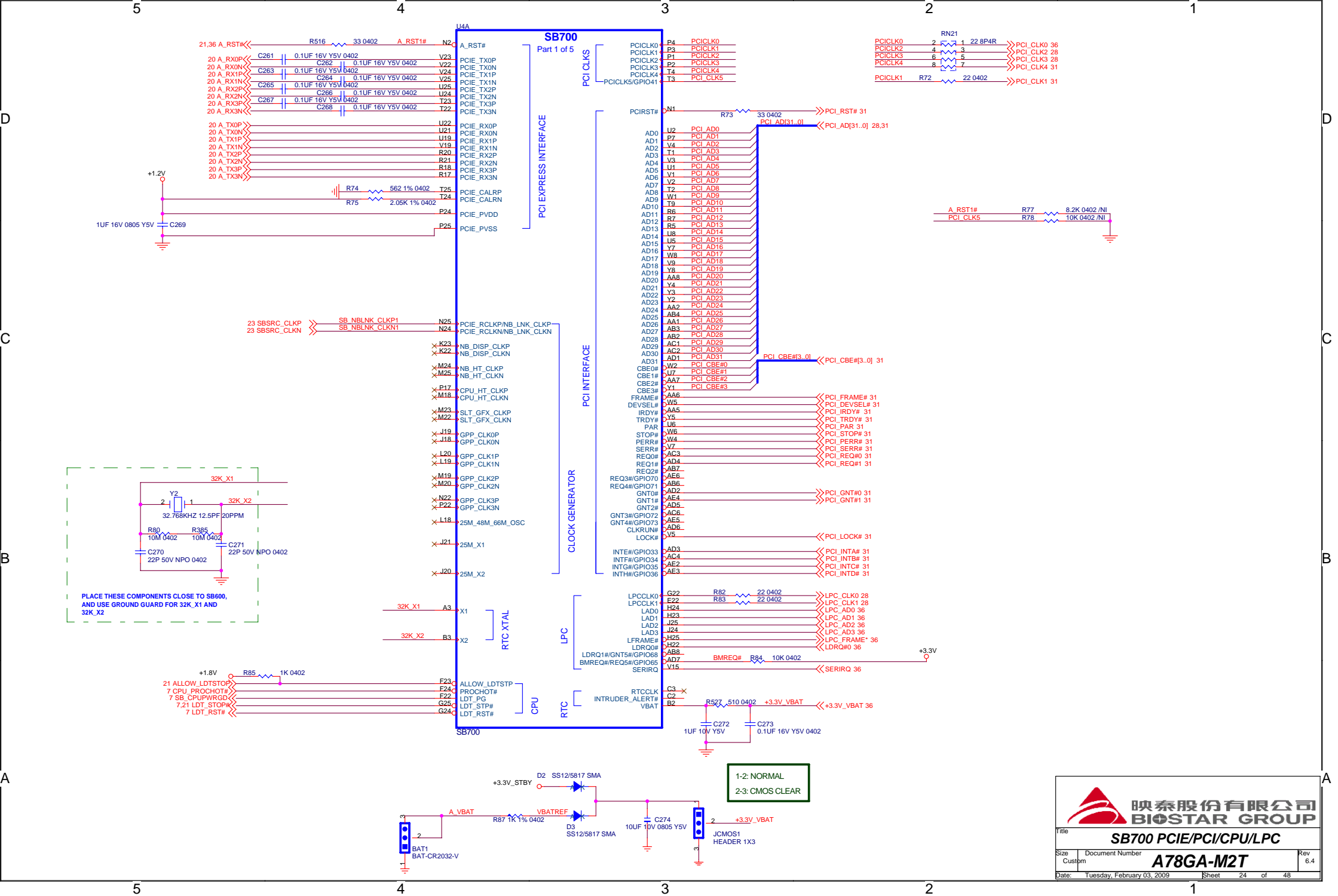
FOR EMI

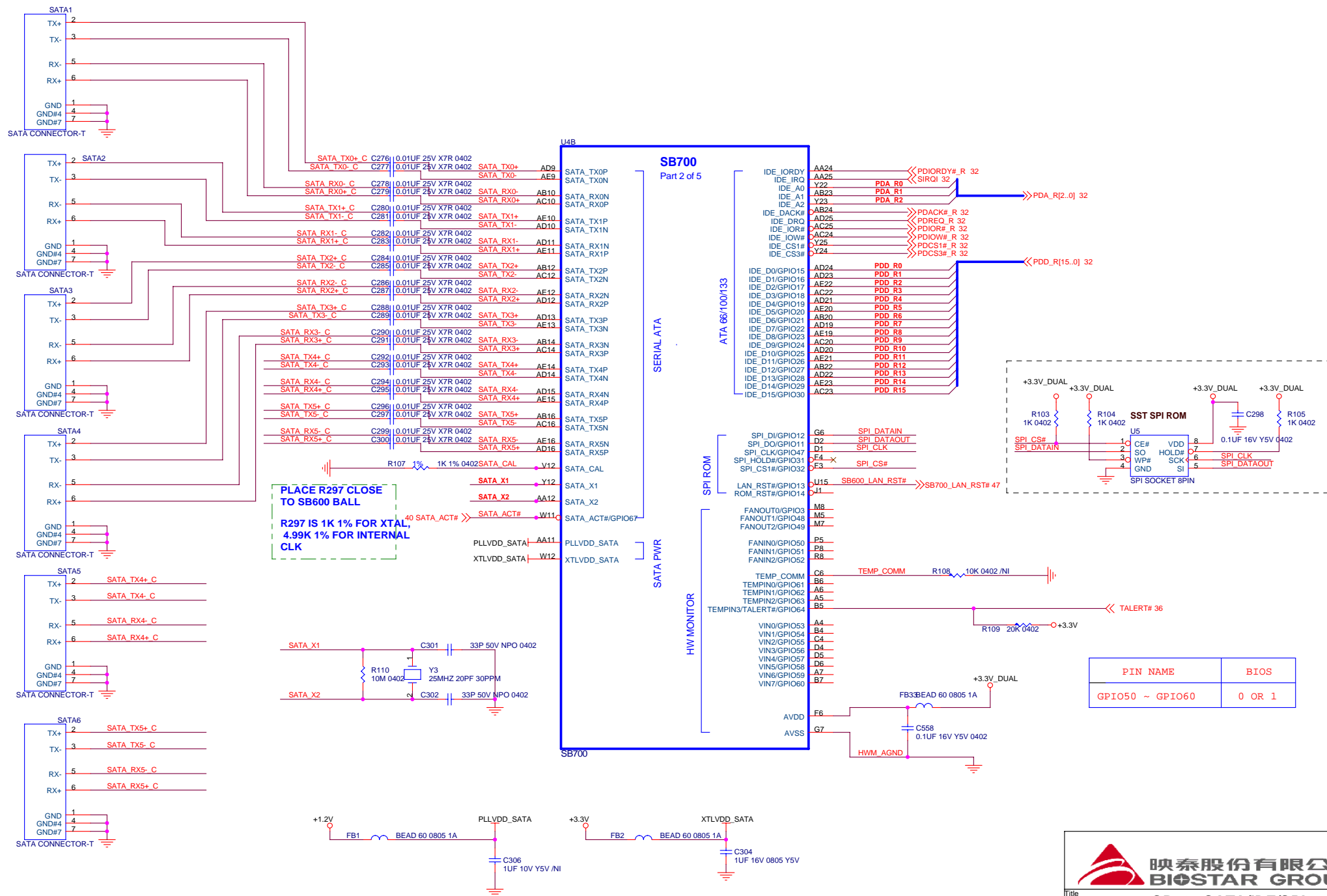
SEL_HTT66	1	66MHz 3.3V single ended HTT clock
	0*	100MHz differential HTT clock
SEL_SATA	1	100MHz non-spreading differential SATA clock
	0*	100MHz differential spreading SRC clock

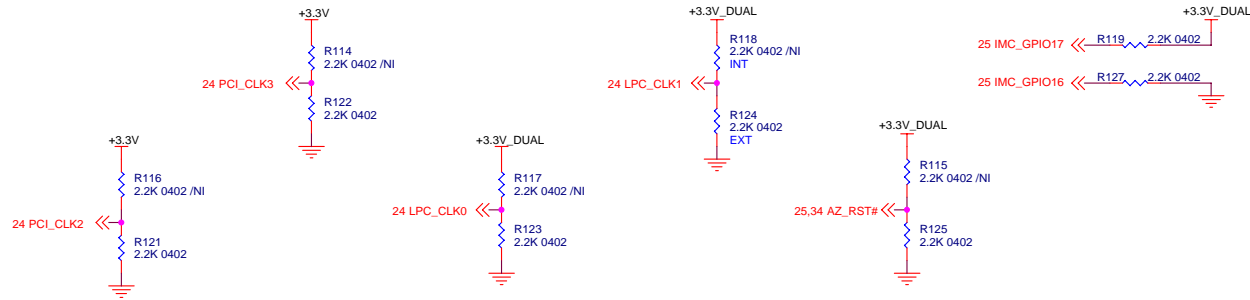
* default



AZ1117H-ADJ SOT-223 INI
解決康舒400W不開機問題 (ATX-400C-A2SNN)
 $V_{out} = V_{ref} (1.25V) \times (1 + R2/R1) = 3.3V$







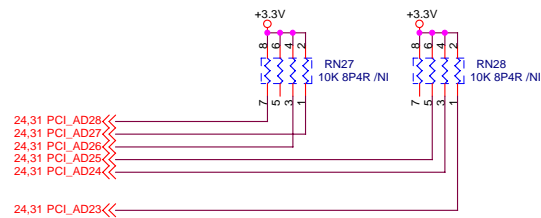
REQUIRED STRAPS

NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

	PCI_CLK2	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0	LPC_CLK1	AZ_RST#	IMC_GPIO17 IMC_GPIO16
PULL HIGH	WATCHDOG TIMER ON NB_PWRGD ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	IMC ENABLED	ROM TYPE: H, L = SPI ROM DEFAULT
PULL LOW	WATCHDOG TIMER ON NB_PWRGD DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	IMC DISABLED DEFAULT	



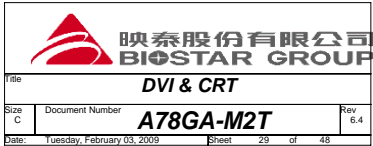
OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

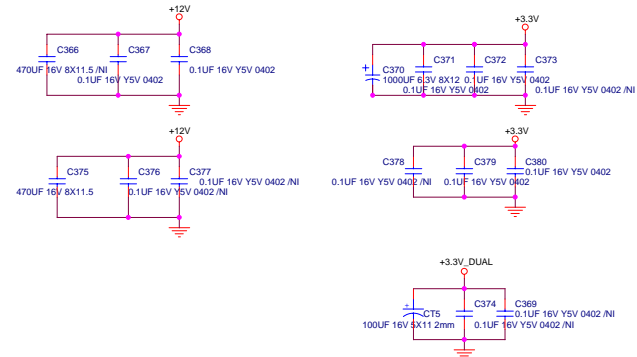
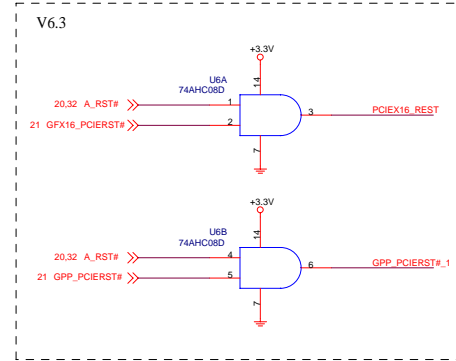
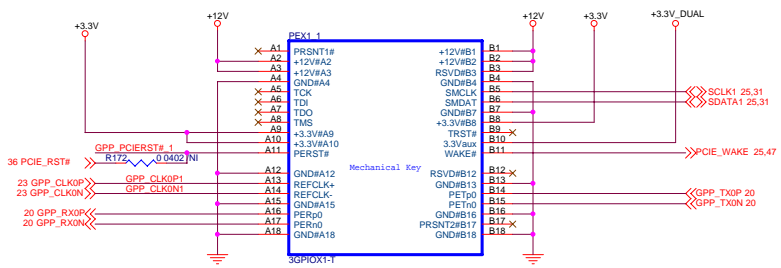
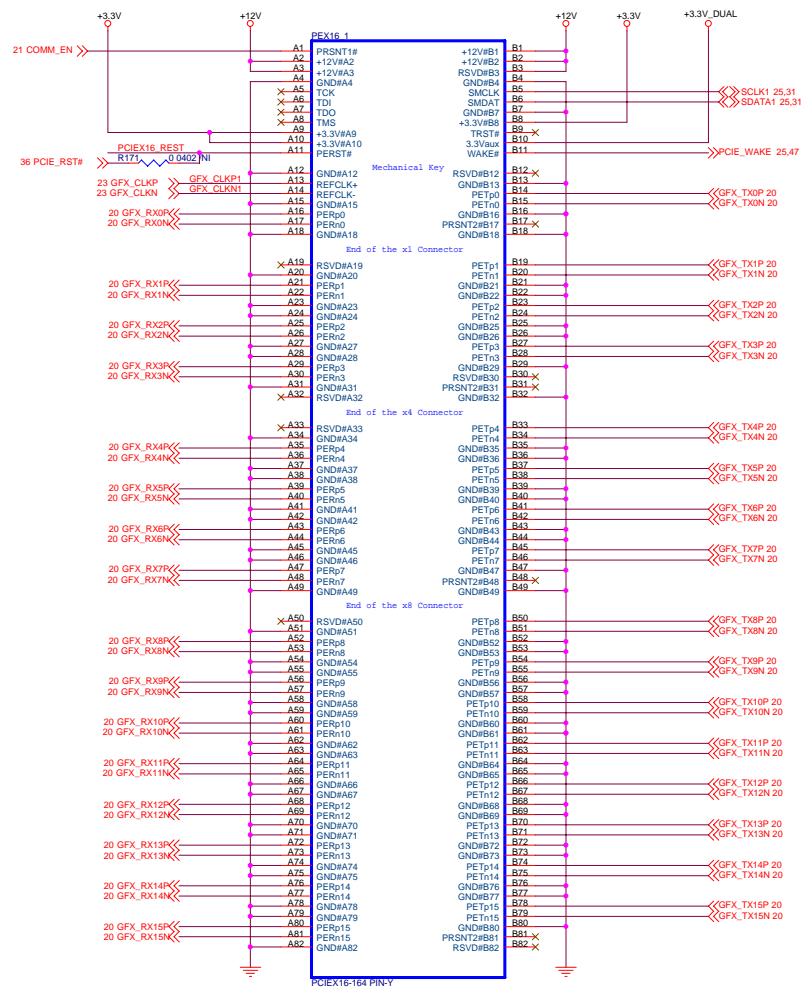


DEBUG STRAPS

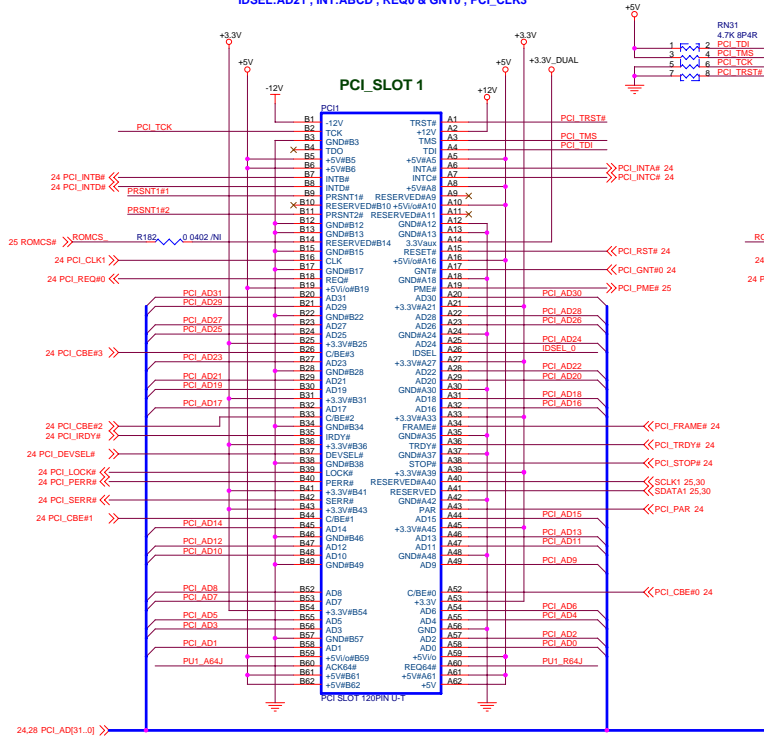
SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

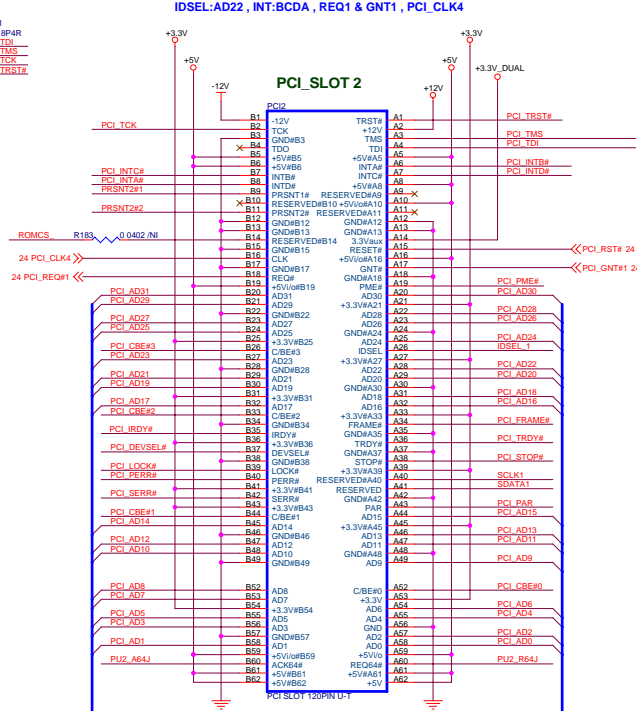




IDSEL:AD21, INT:ABCD, REQ0 & GNT0, PCI_CLK3



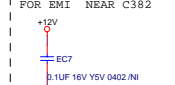
IDSEL:AD22, INT:BCDA, REQ1 & GNT1, PCI_CLK4



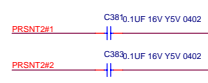
IDSEL_0 R185 100 0402 PCI_AD21



FOR EMI NEAR C382



IDSEL_1 R184 100 0402 PCI_AD22

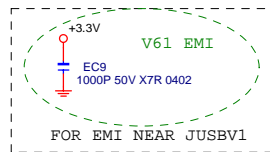
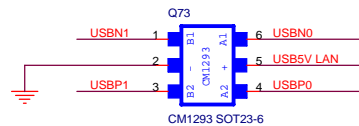
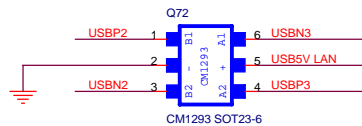


PCI PULL UPS



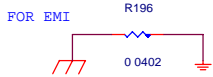
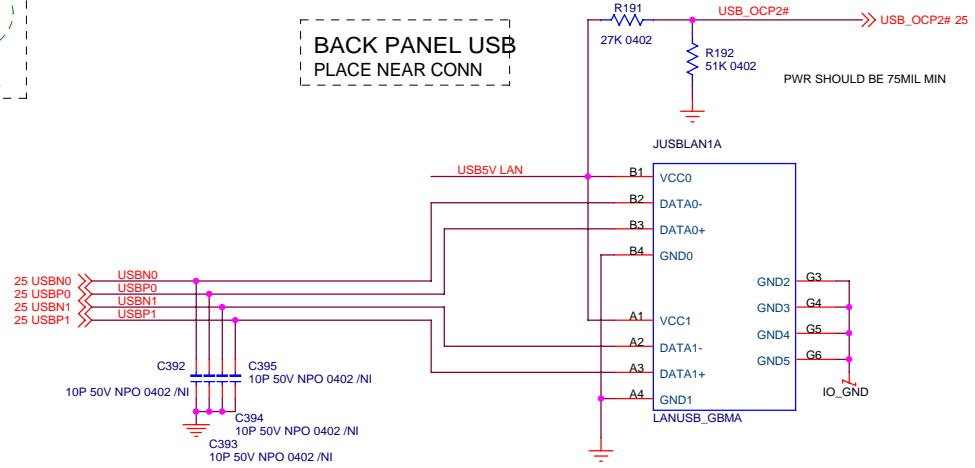
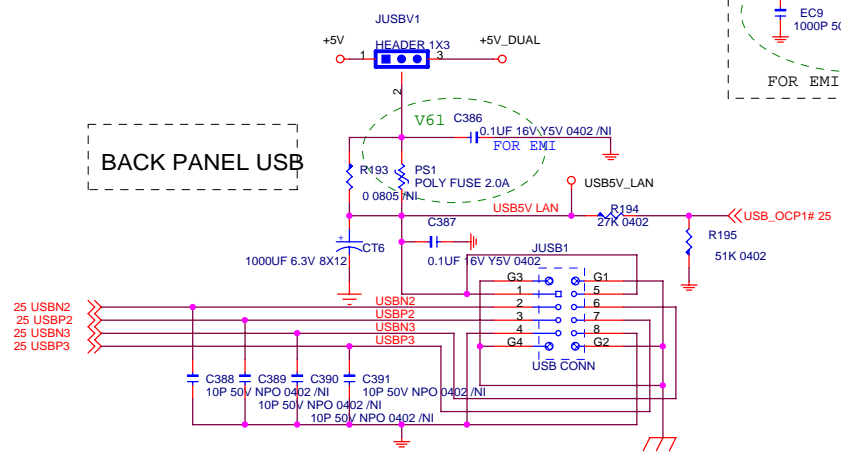
FOR EMI NEAR RN32



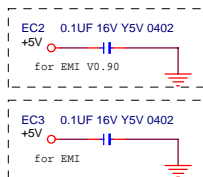


BACK PANEL USB
PLACE NEAR CONN

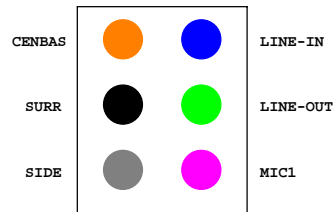
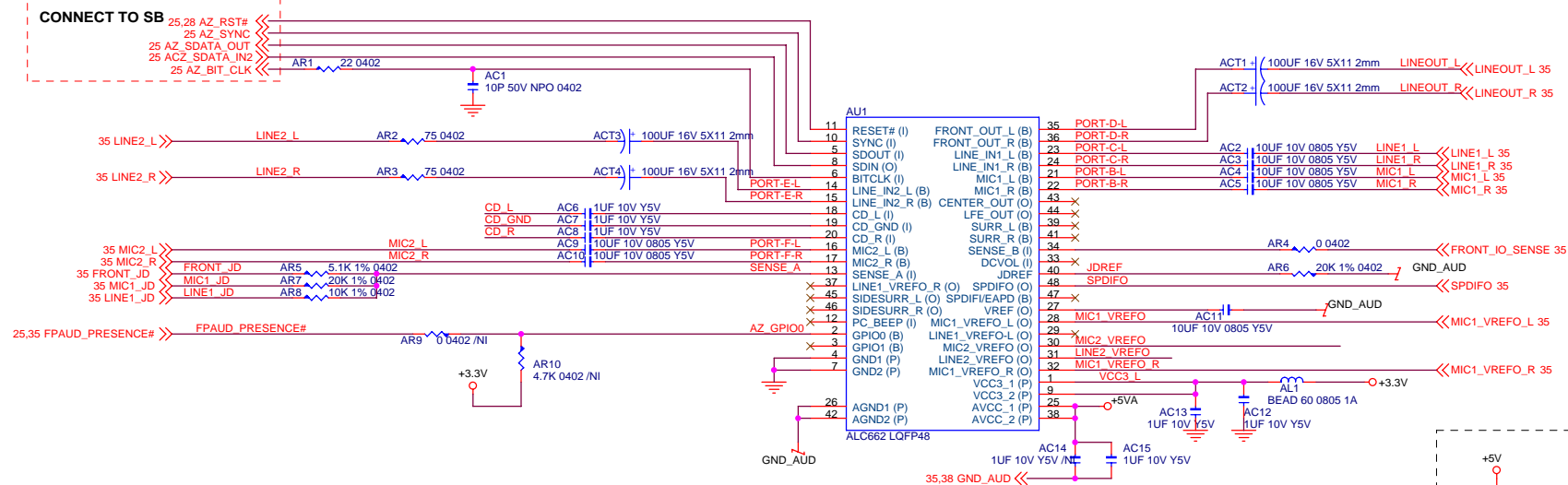
BACK PANEL USB



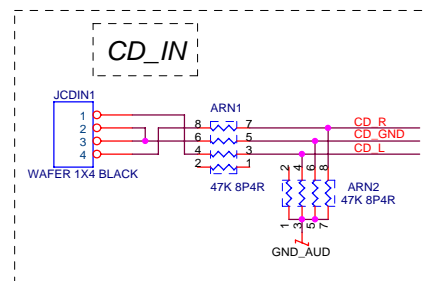
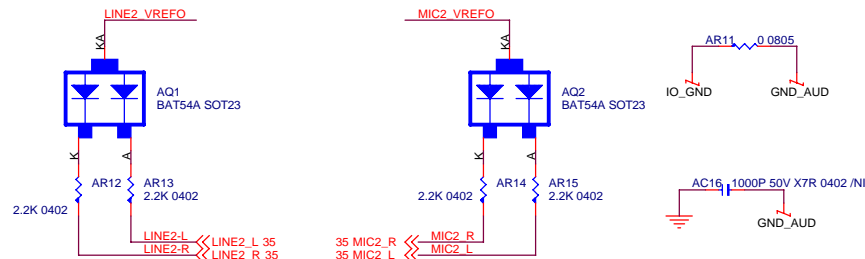
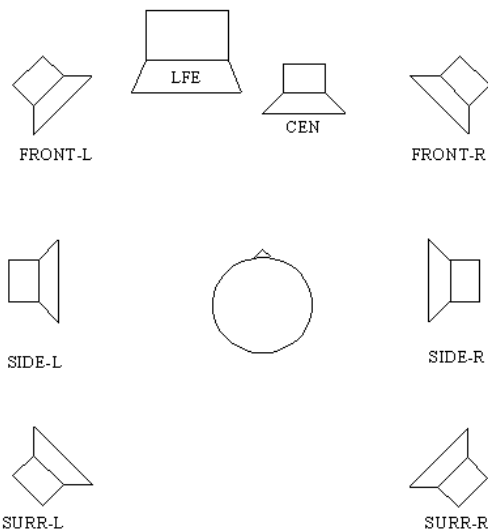
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Size	Document Number	A78GA-M2T	
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CONNECT TO SB



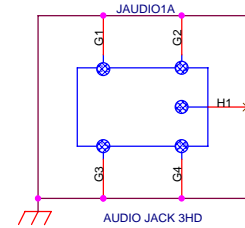
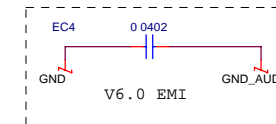
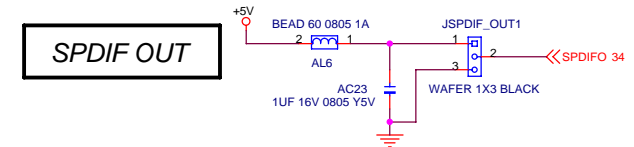
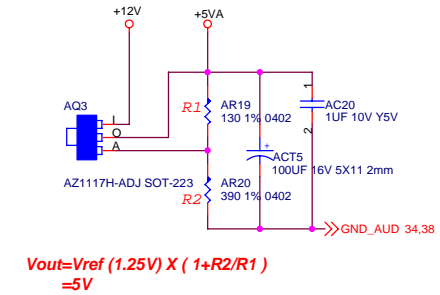
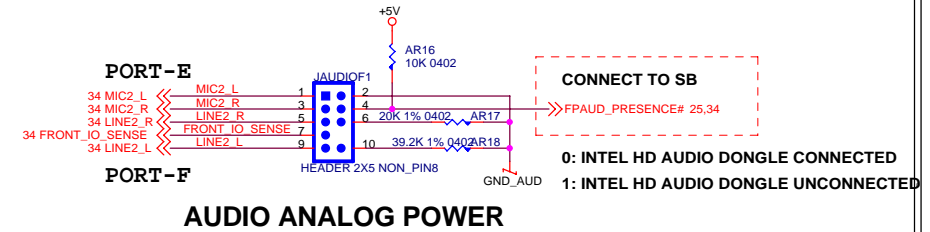
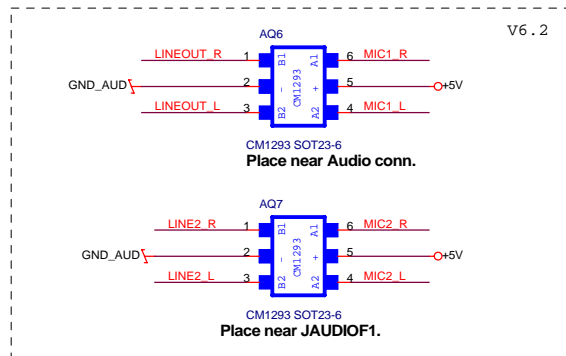
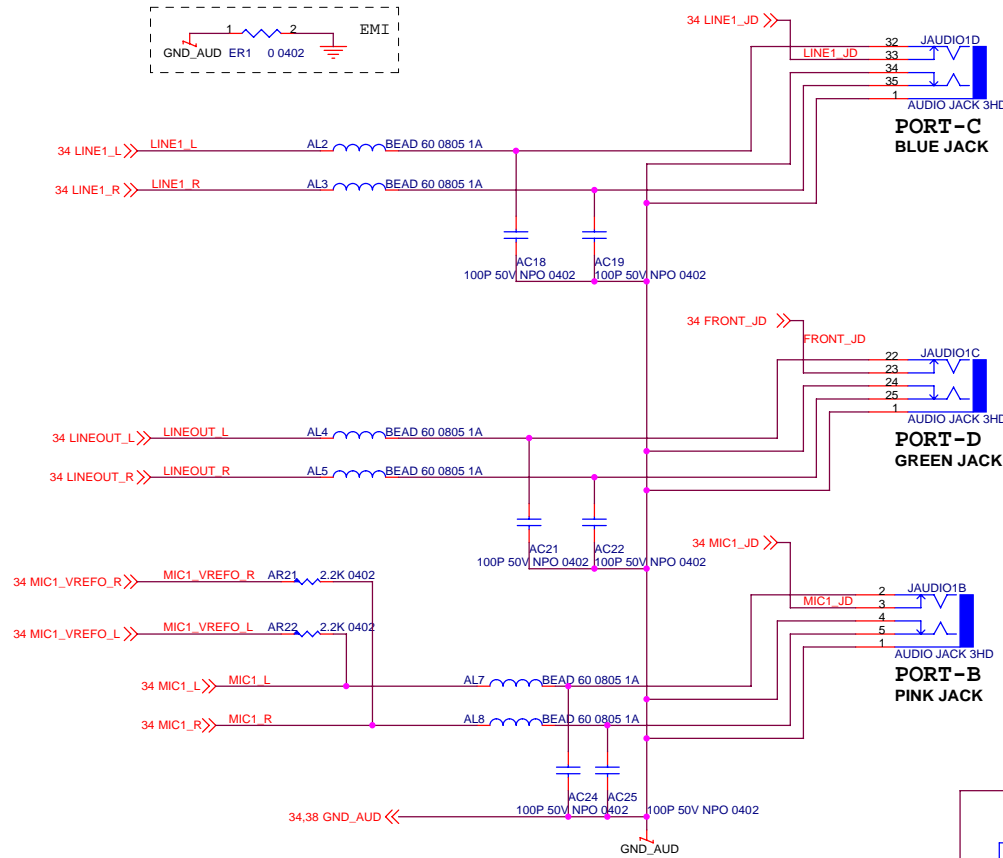
7.1 Speaker Configuration



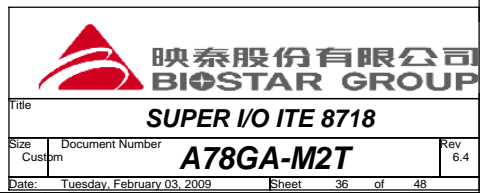
Configuration

	PORT-A	PORT-B	PORT-C	PORT-D	PORT-E	PORT-F	PORT-G	PORT-H
Function	SURR	MIC1	LINE1	LINEOUT	LINE2	MIC2	CEN/LFE	SIDE
Location	Rear	Rear	Rear	Rear	Front	Front	Rear	Rear

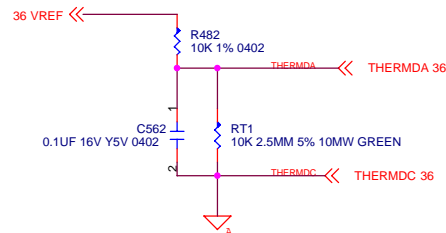
Rear Panel Onboard Analog I/O



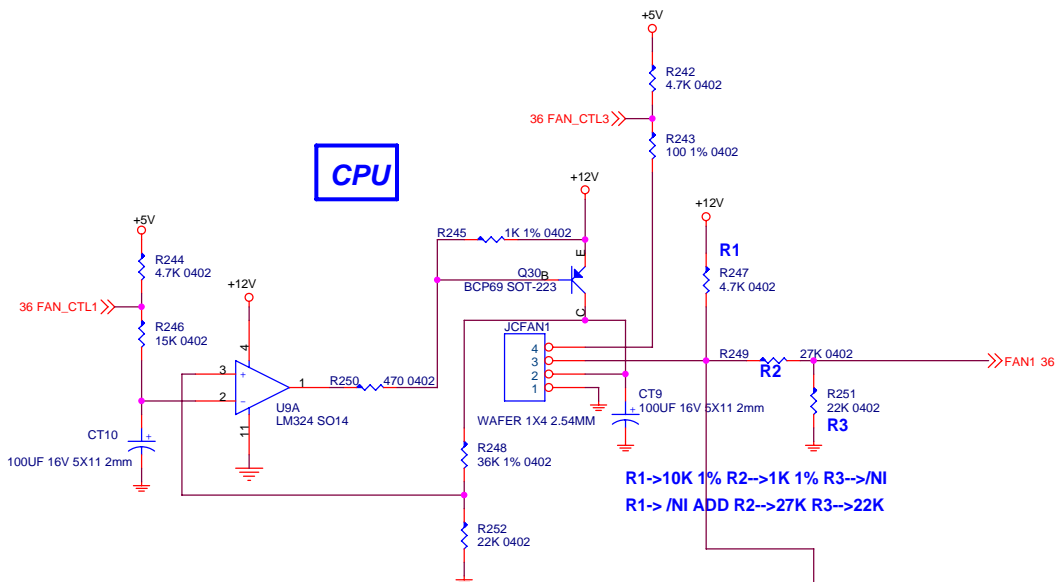
1. IT8718F PIN(28,48) VIDO6 &VIDO7 need pull high.
2. PIN (119,127)RI1&RI2 need pull high.
3. Use GPIO need pull high 4.7K.
4. Pin78 & Pin 30 & Pin 71 不要拿來當GPIO.
5. IT8718F/CX pin 28 & pin 48 default 為VIDO6 & VIDO7 不要當GPIO使用
6. GPIO use:IT8712F/KX, /IT8716,IT8718F ALL GPIO need pull high , pure On Den Prian(pull 1.2V or 3.3V or 5V皆可),不會有 10-20 ns 自己 pull high.



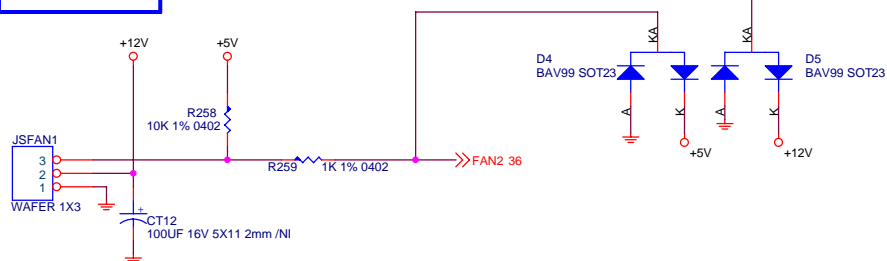
Temperature Sensing



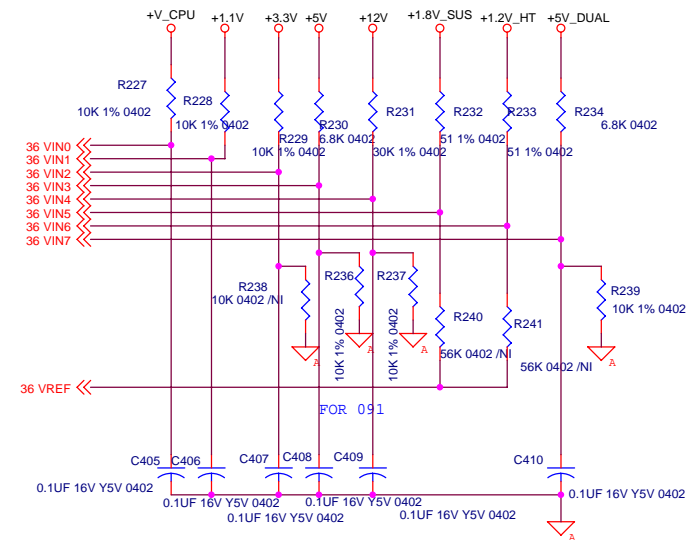
CPU

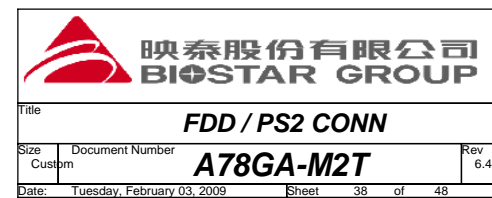
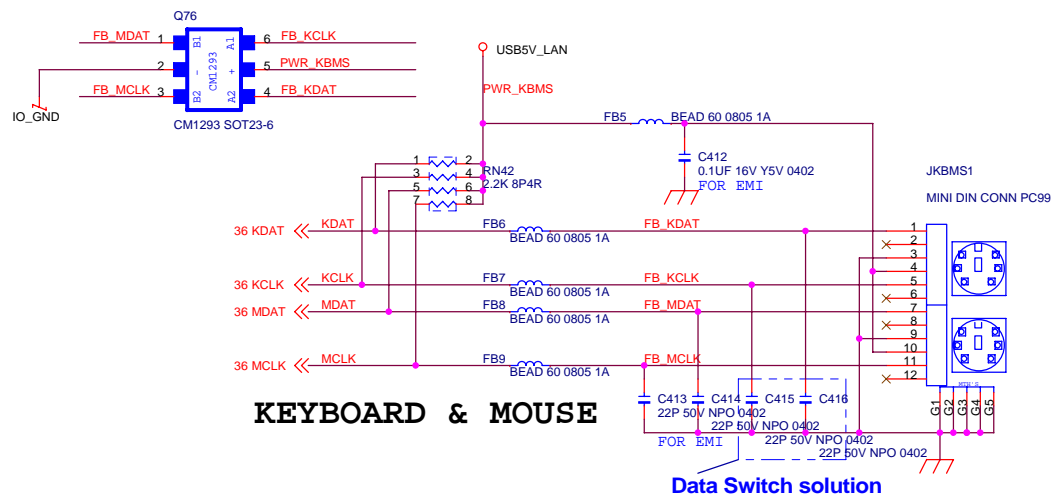


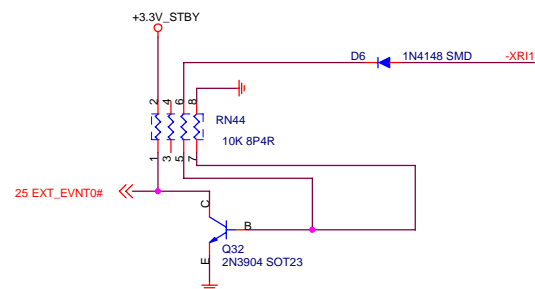
SYSTEM



Voltage Sensing







JCOM1

RIN1 1 2 RIN3

DOUT2 3 4 DOUT3

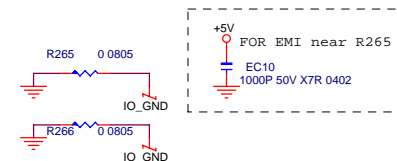
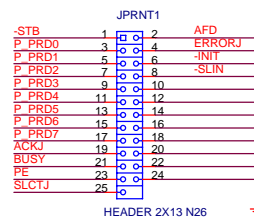
DOUT1 5 6 RIN2

-XRI1 7 8 RIN4

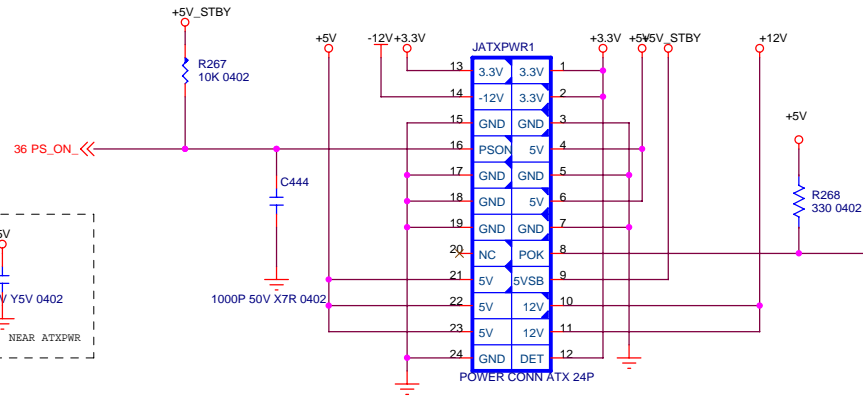
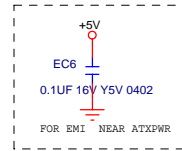
9 10 X

HEADER 2X5 N10 G

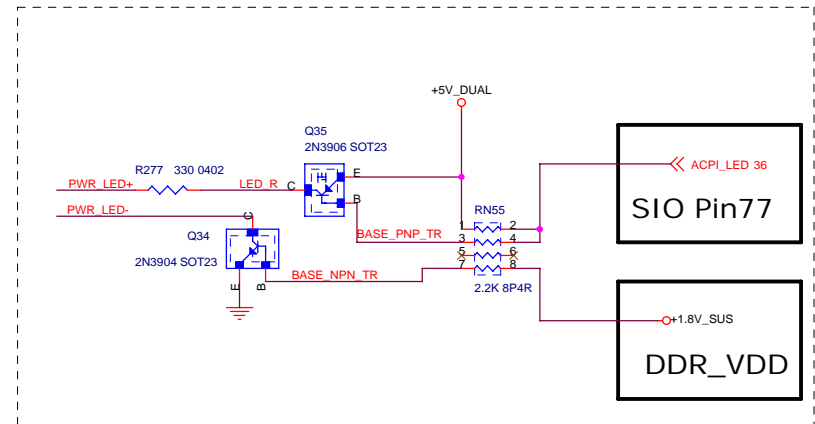
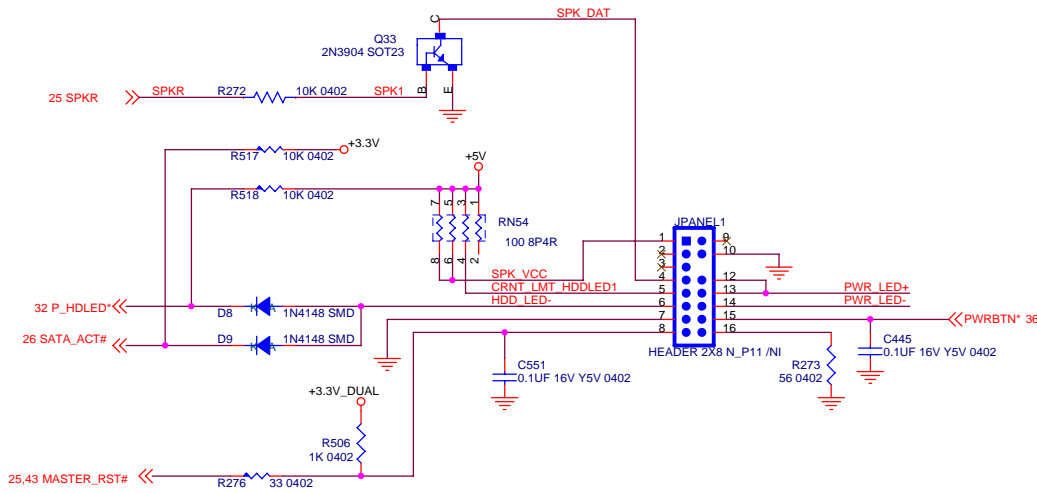
1	2
●	●
●	●
●	●
9	●



Title			
COM & PRINTER CONNECTOR			
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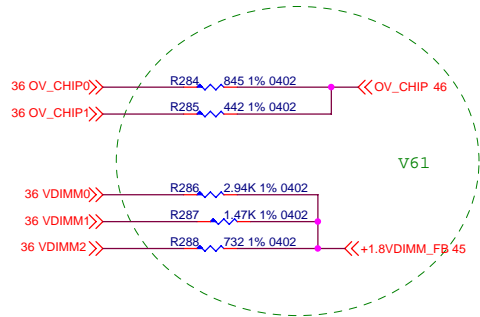


LED_D2	LED_D1	MESSAGE
OFF	OFF	ABNORMAL
OFF	ON	MEMORY ERROR
ON	OFF	VGA ERROR
ON	ON	NORMAL

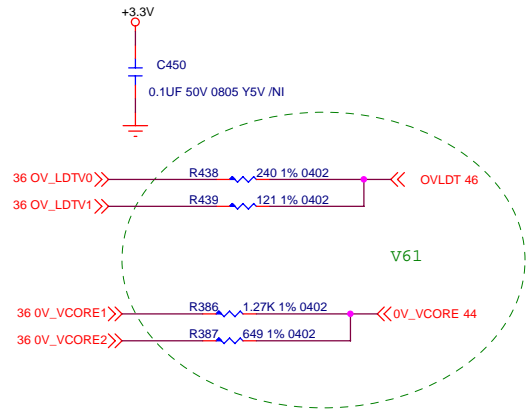


7.44 K8_VID5 >>> VID_OUT5 7.44
7.44 K8_VID4 >>> VID_OUT4 7.44

7.44 K8_VID3 >>> VID_OUT3 7.44
7.44 K8_VID2 >>> VID_OUT2 7.44
7.44 K8_VID1 >>> VID_OUT1 7.44
7.44 K8_VID0 >>> VID_OUT0 7.44



DUAL +3.3V

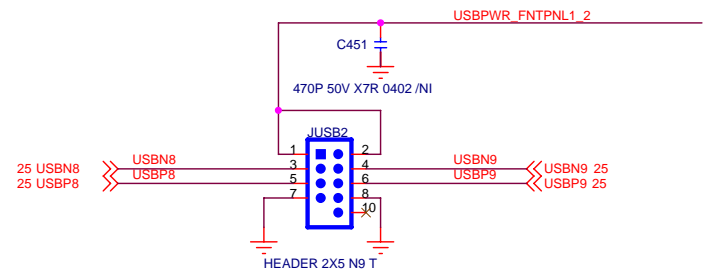
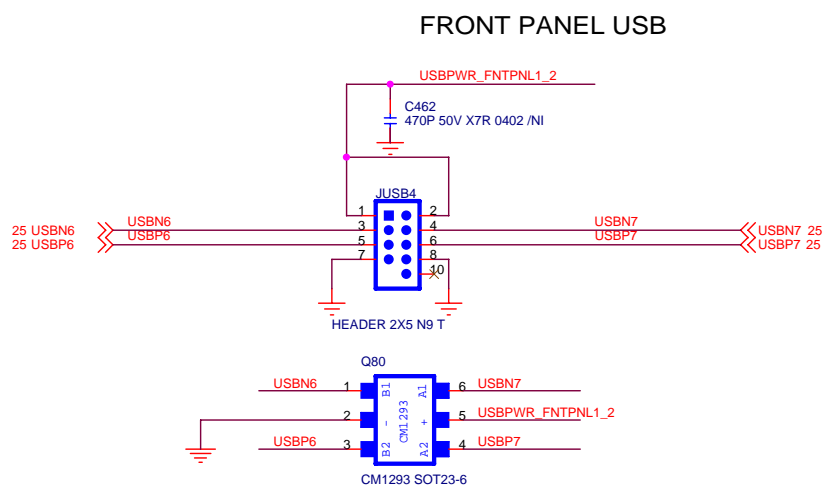
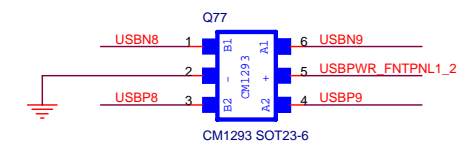
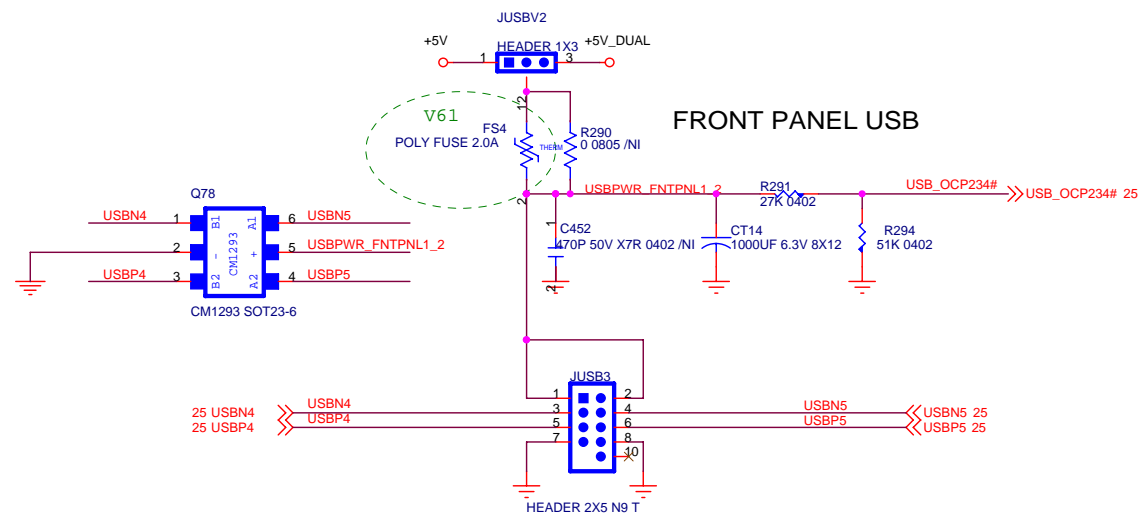


+1.1V	OV_CHIP1	OV_CHIP0
+1.15V	1	1
+1.25V	0	1
+1.35V	1	0
+1.45V	0	0

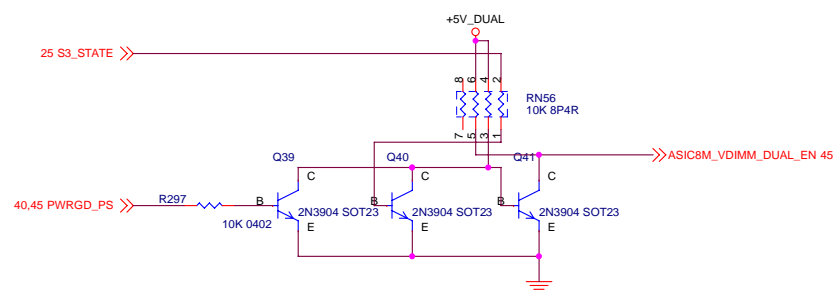
+1.8VDIMM_FB	VDIMM0	VDIMM1	VDIMM2
Default 1.95V	1	1	1
2.05V	0	1	1
2.15V	1	0	1
2.25V	0	0	1
2.35V	1	1	0
2.45V	0	1	0
2.55V	1	0	0
2.65V	0	0	0

1.2V_HT	OV_LDT0	OV_LDT1
Default 1.2V	1	1
1.3V	0	1
1.4V	1	0
1.5V	0	0

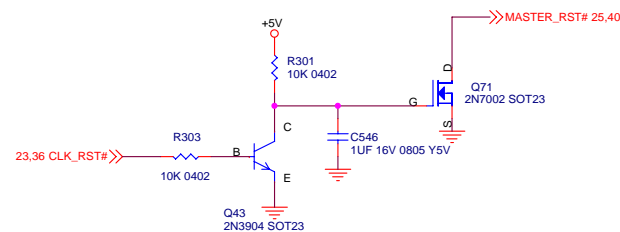
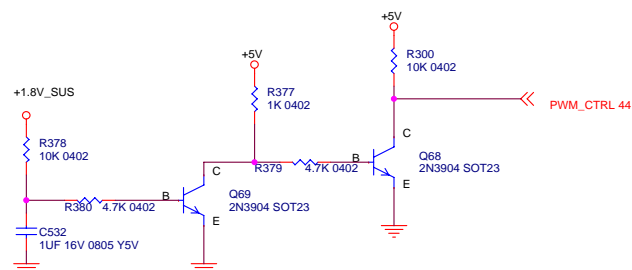
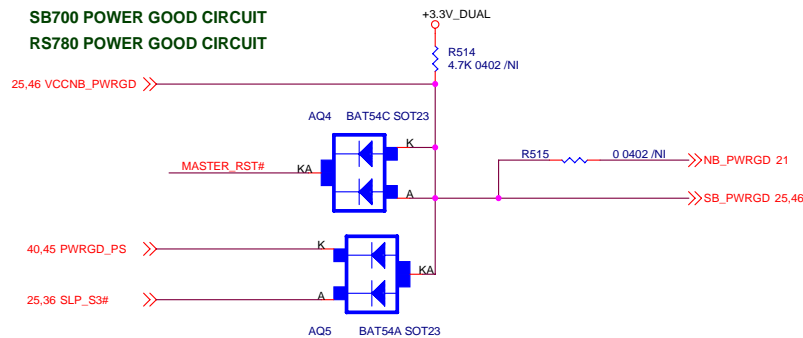
OV_VCORE	OV_VCORE1	OV_LDT1
Default V_CPU	1	1
+0.1V	0	1
+0.2V	1	0
+0.3V	0	0

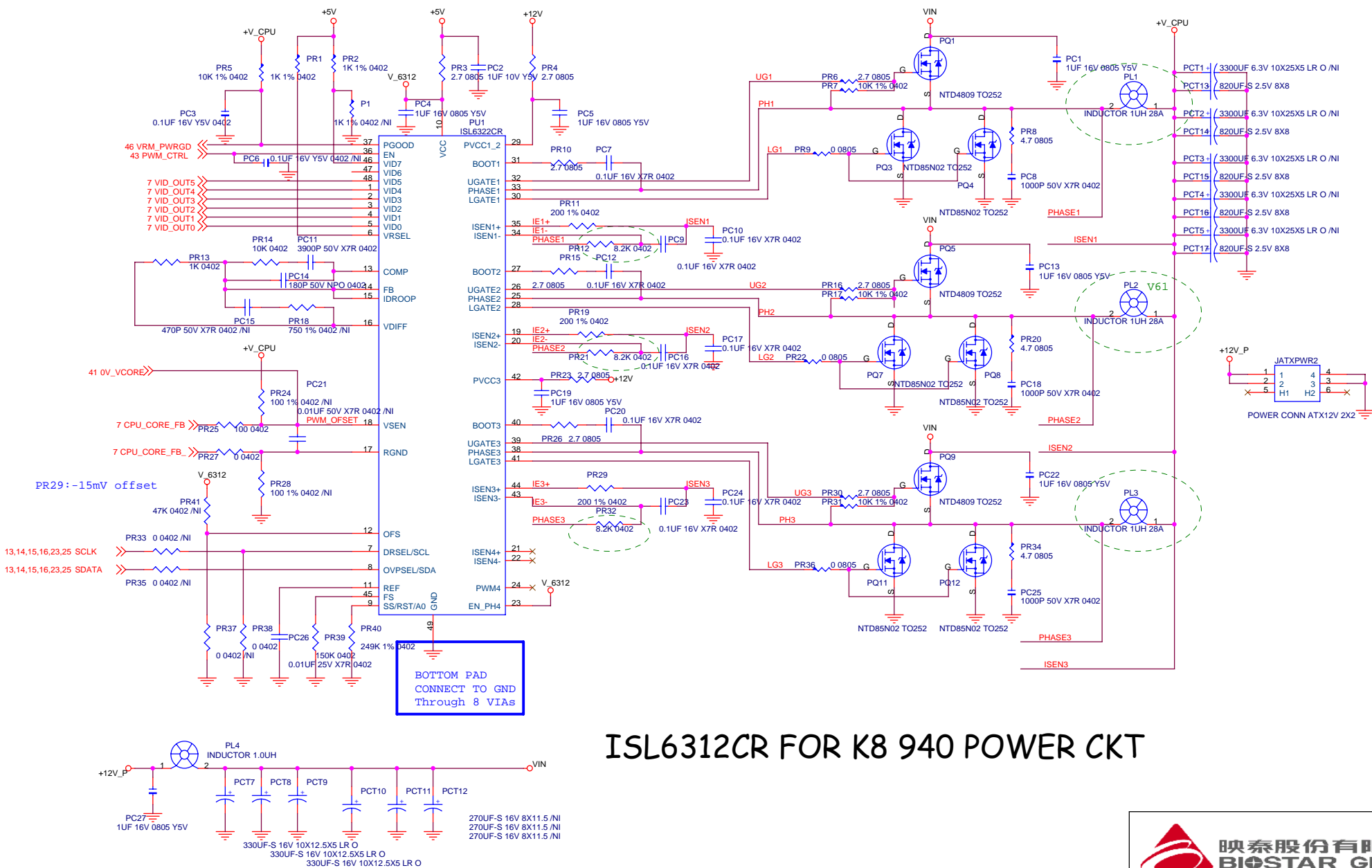


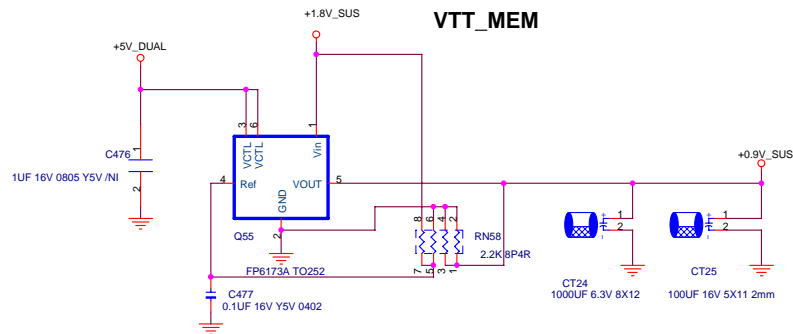
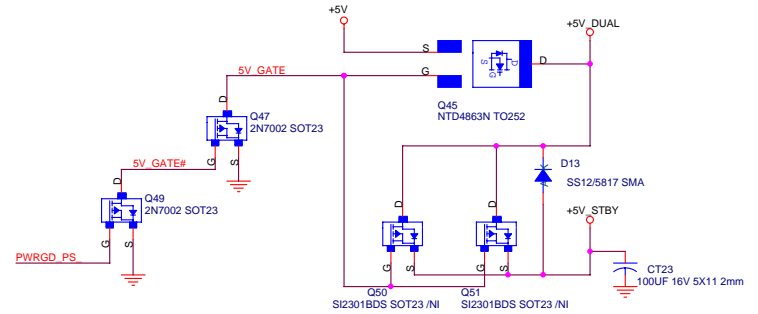
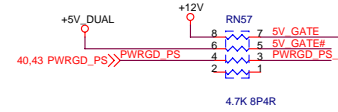
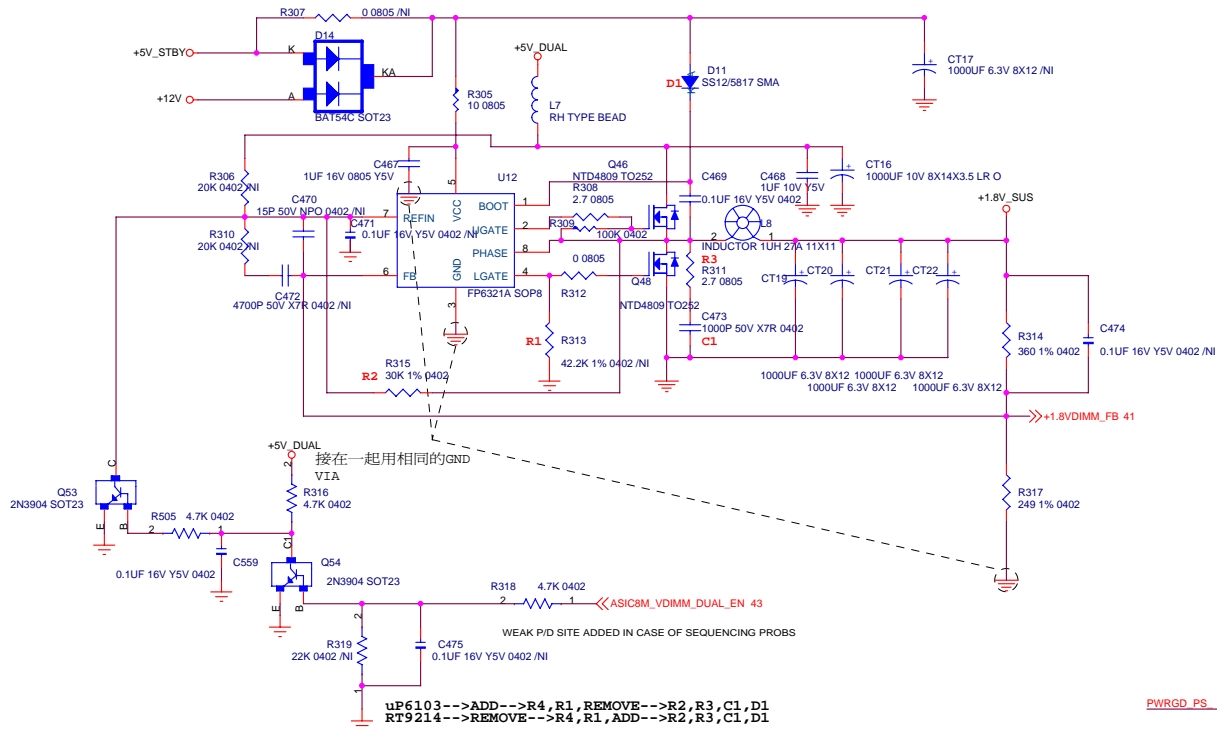
ATHLON64 POWER GOOD & ENABLES CIRCUIT



SB700 POWER GOOD CIRCUIT RS780 POWER GOOD CIRCUIT







COLAY

NB
+1.1V @ 8A AMPS MAX



映泰股份有限公司
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