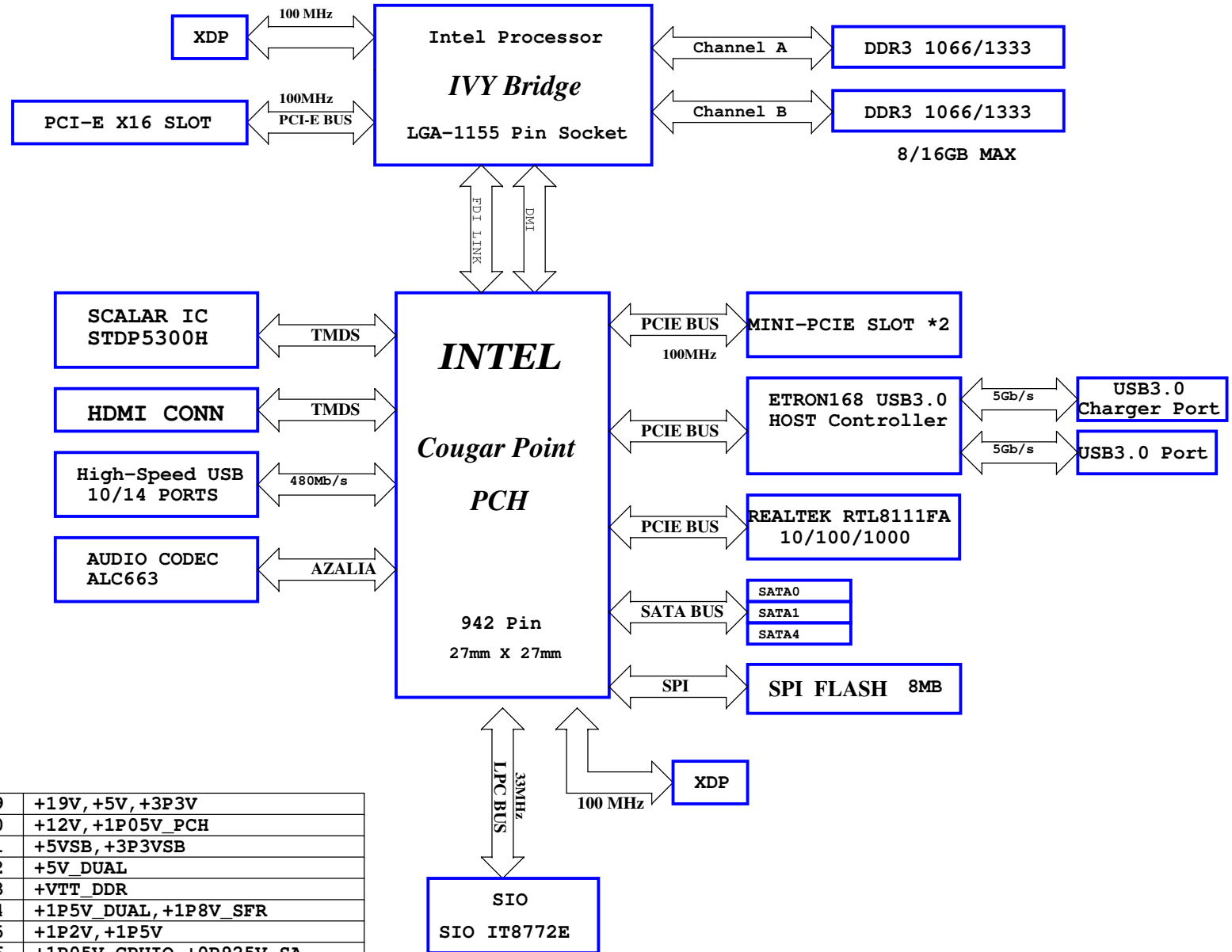


IPISB-AG

Revision: 1.04

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02	POWER SEQUENCE
03~08	INTEL CPU SOCKET1155(1~6)
09	PLTRST CPU#
10~11	DDR3 CHANNEL A&B
12	DDR3 TERMINATION A&B
13	TEMP SENSOR
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23	PCH_DPWROK & SLP_SUS
24	RSMRST# / PCIE_RST#
25	SM BUS & SPI ROM
26	PCI EXPRESS X16 SLOT
27	MINI-PCIE SLOT
28	MINI-PCIE SLOT
29	REALTEK RTL8111FA CONTROLLER
30	RJ45 CONNECTOR
31	REAR USB2.0 PORT
32	USB 3.0 CONTROLLER
33	USB POWER
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38	USB Charger SLG55583
39	+5VA FOR CHARGE
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41	AUDIO AMP
42	AUDIO CONNECTOR
43	AUDIO SW
44	SUPER I/O IT8772E
45	FRONT PANEL CIRCUIT
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47	INTEGRATED VGA PORT
48	IR / SPKR
49	RTC/COMS/SCREW
50	DEBUG HEADER
51~52	SCALAR STDP5300H
53	I2S AUDIO DAC
54	LVDS CONNECTOR
55	DVI MUX
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57	DVI CONNECTOR
58	HDMI-IN CONNECTOR
59	HDMI-OUT CONNECTOR
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62	CARD READER RTS5209-GR
63	CARD READER CONNECTOR
64	+19V OCP - SYS_IN
65	VCORE CONTROLLER
66	VCORE DRIVER 1-2
67	VCORE DRIVER 2-2
68	AXG DRIVER



69	+19V, +5V, +3P3V
70	+12V, +1P05V_PCH
71	+5VSB, +3P3VSB
72	+5V_DUAL
73	+VTT_DDR
74	+1P5V_DUAL, +1P8V_SFR
75	+1P2V, +1P5V
76	+1P05V_CPUIO, +0P925V_SA
77	+1P05V_CPUIO CAP
78	DISCHARGE
79	CURRENT SENSE
80	CPU XDP DEBUG CONNECTOR
81	PCH XDP DEBUG CONNECTOR
82	EMI CAP

PEGATRON DT-MB RESTRICTED SECRET

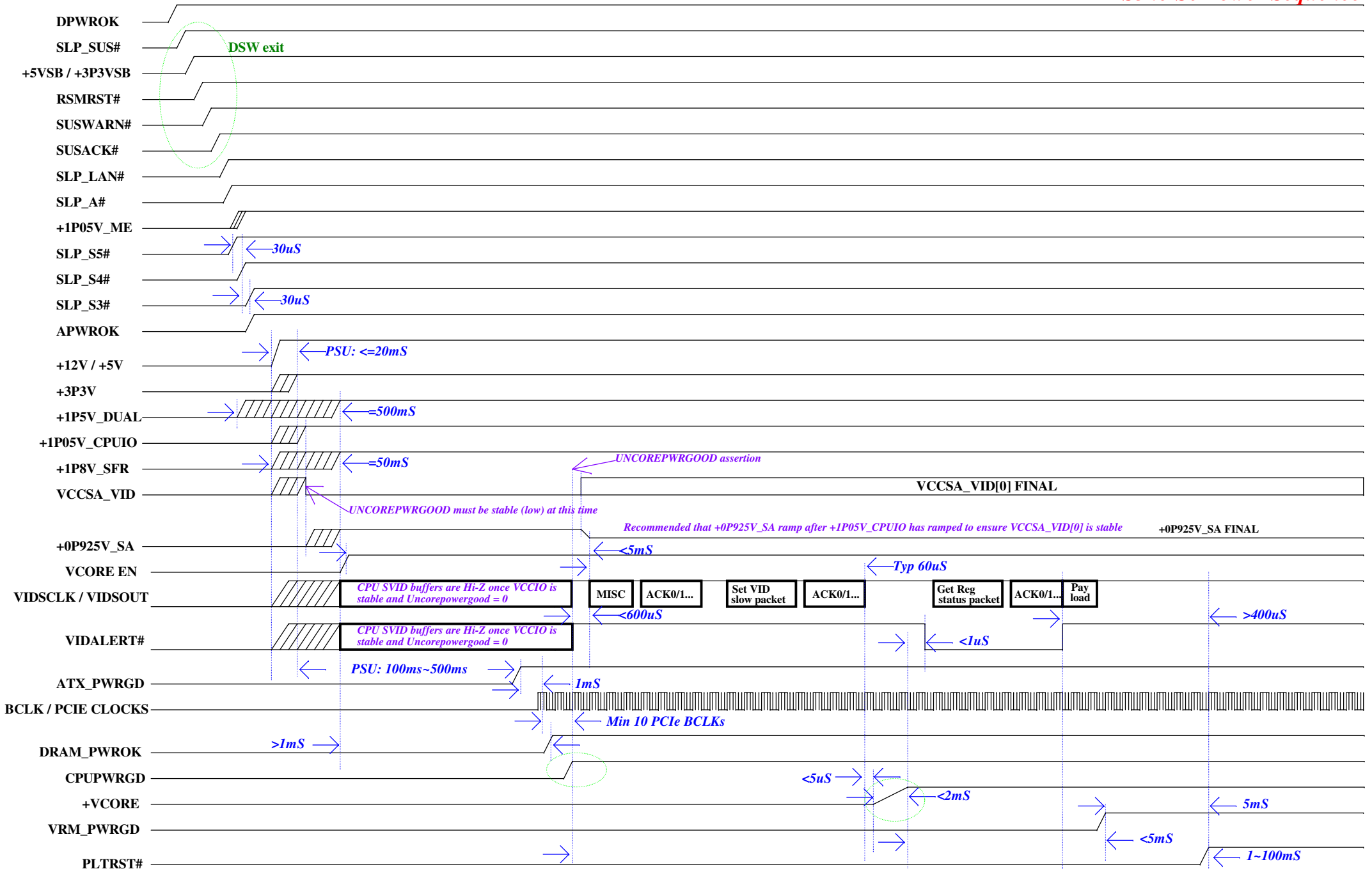
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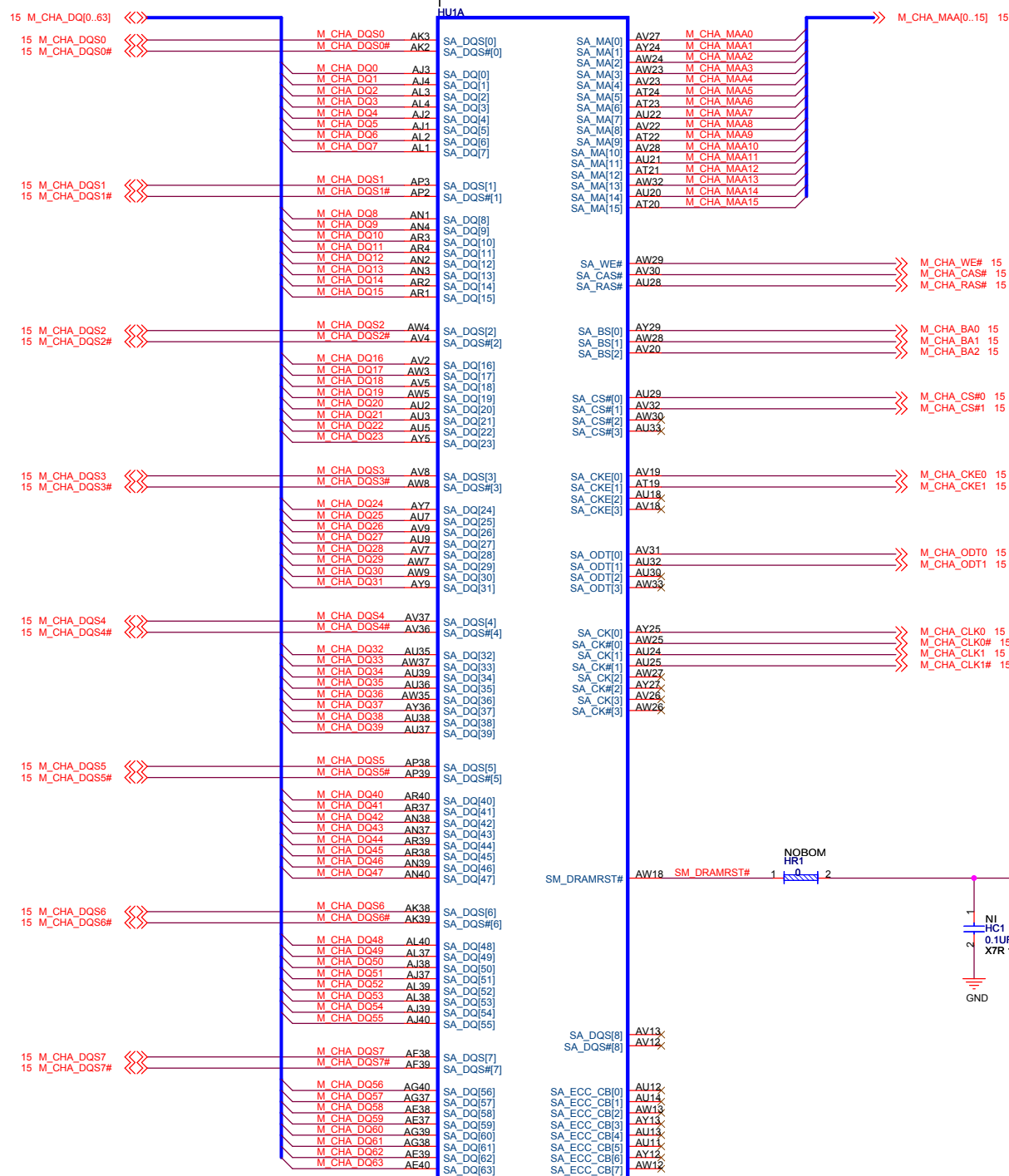
Pegatron Corp. Engineer: **ShunJing_Yang**

Size A3 Project Name **IPISB-AG** Rev 1.04

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S5 to S0 Power Sequence





DDR3_A

SOCKET_1155P

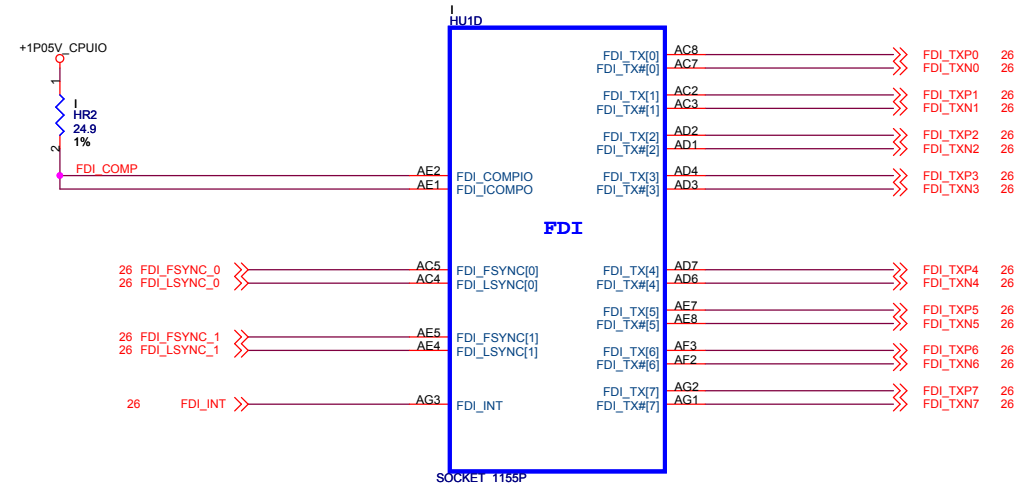
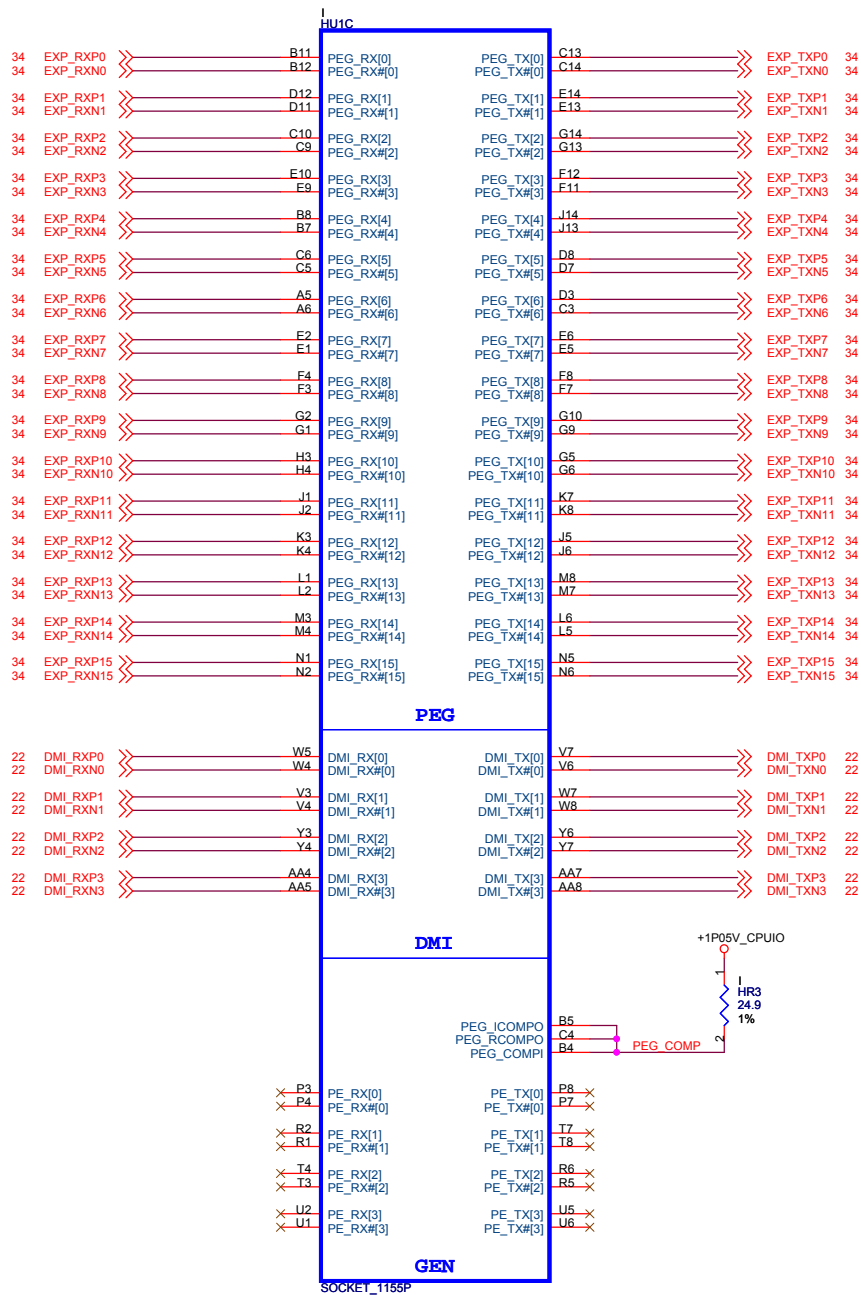
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : DDR3_A 1-6

Pegatron Corp. Engineer: Haoche_Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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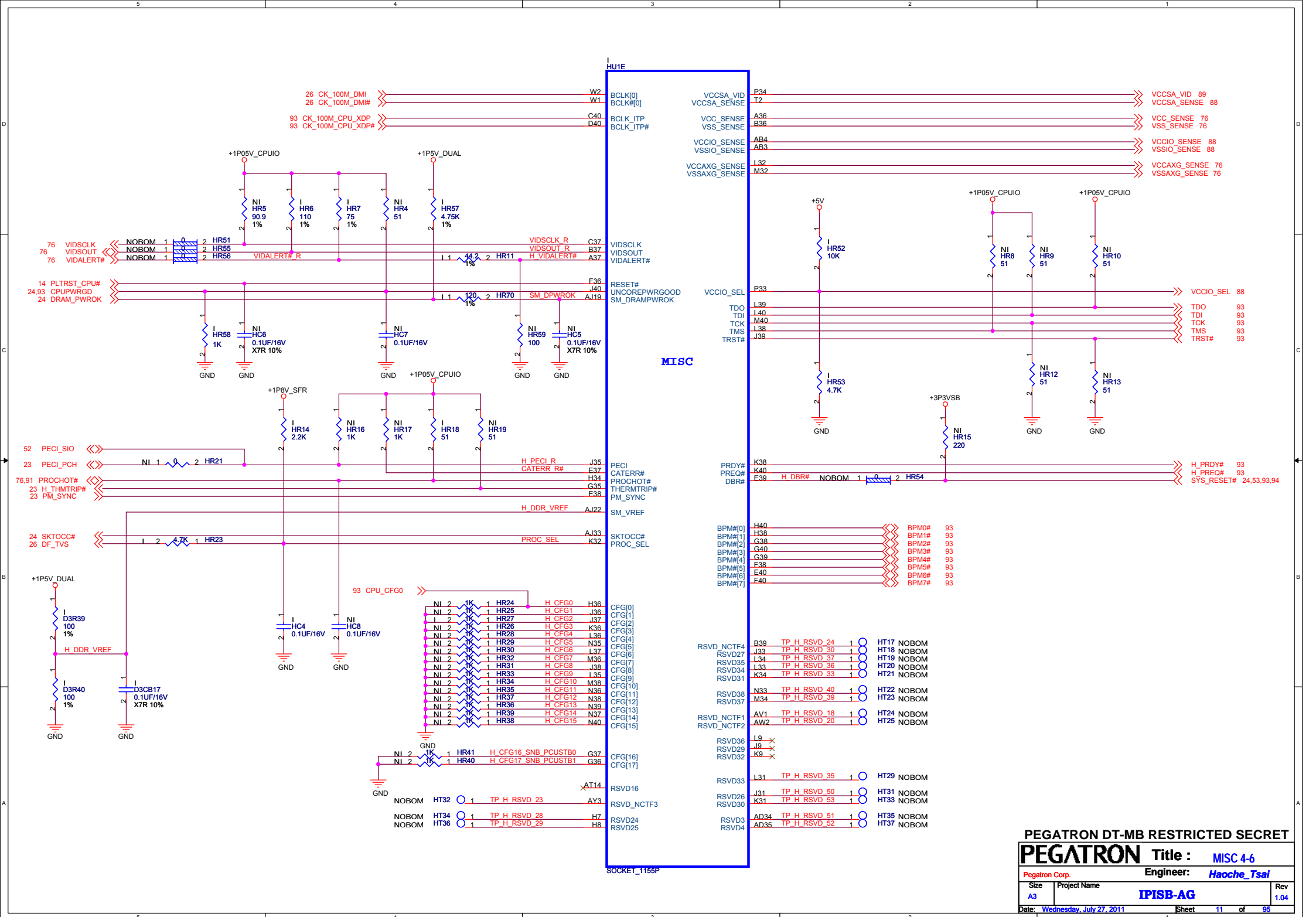
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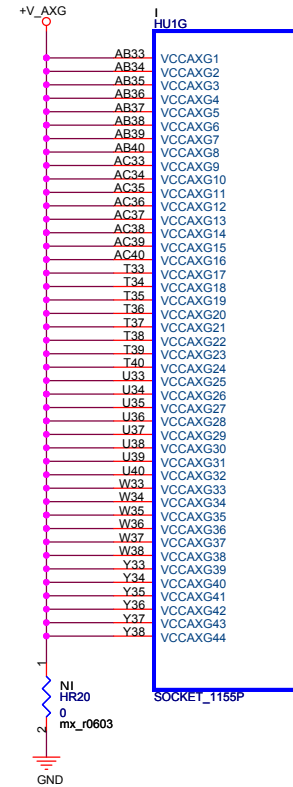
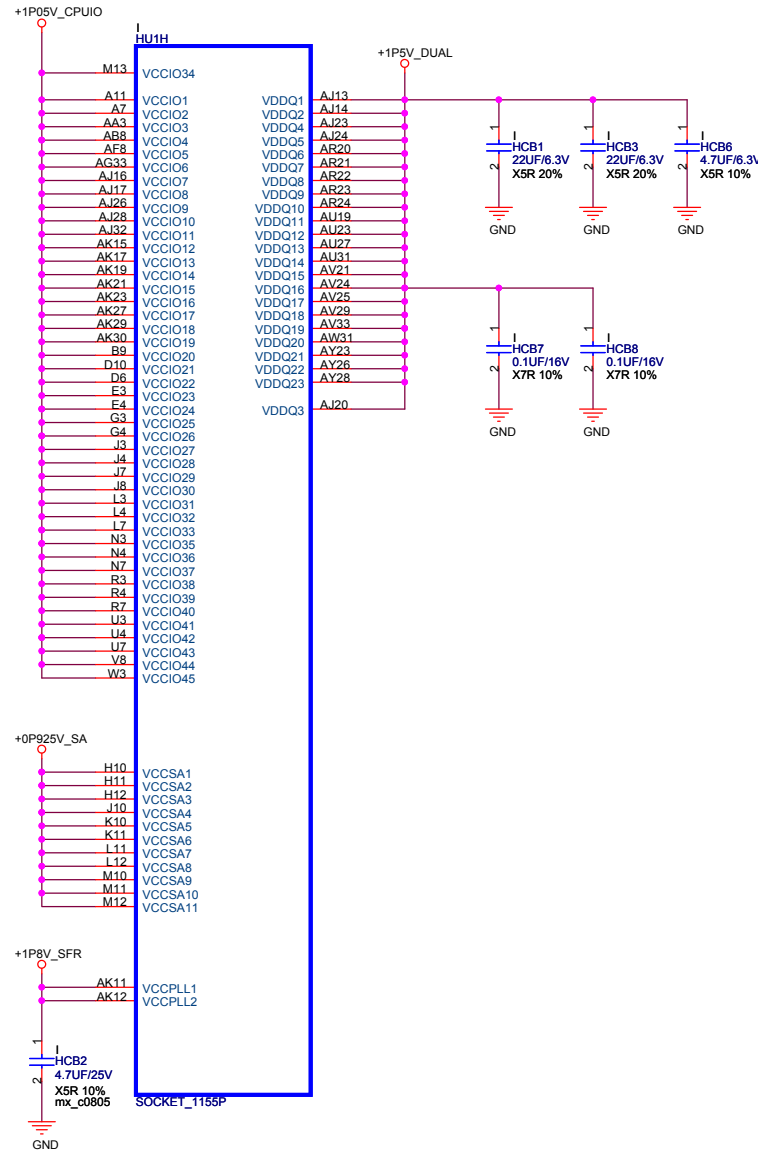
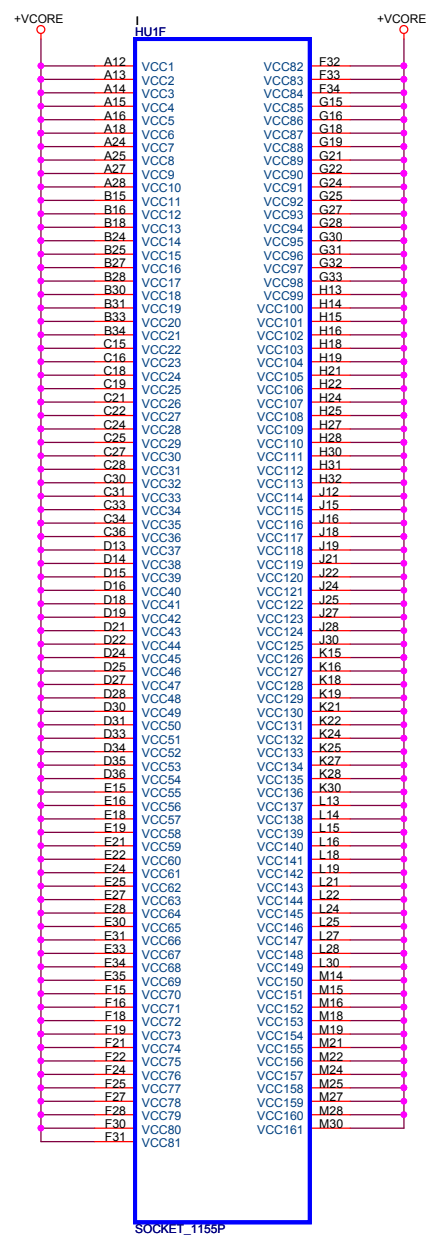
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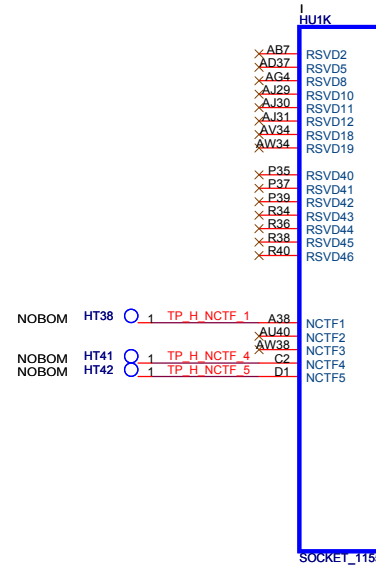
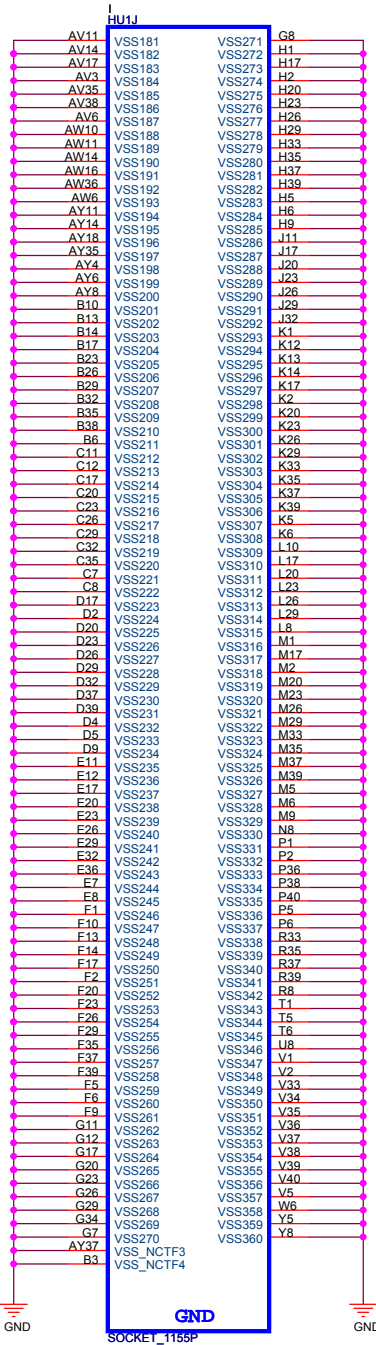
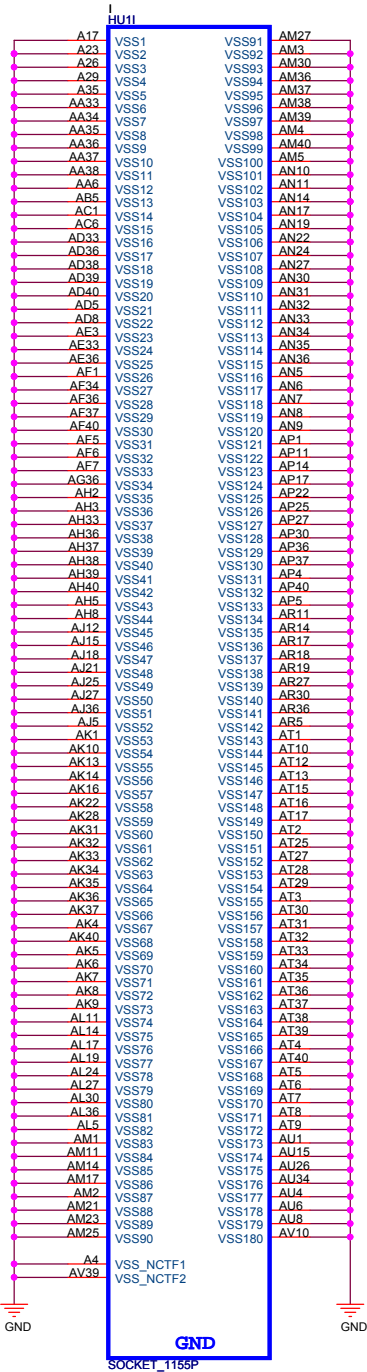
Pegatron Corp. Engineer: Haoche Tsai

Size A3 Project Name IPISB-AG Rev 1.04

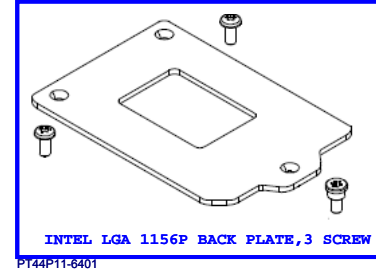
Date: Wednesday, July 27, 2011 Sheet 10 of 95



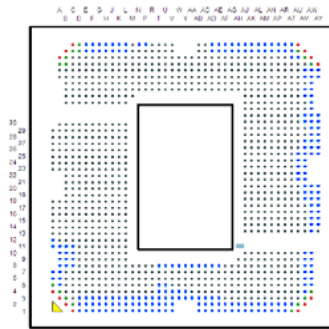
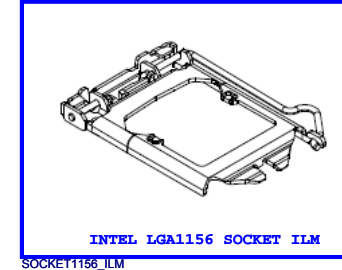




BACKPLATE1



ILM1



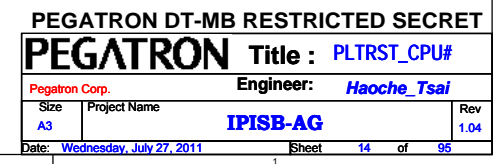
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : VSS 6 - 6

Pegatron Corp. Engineer: Haoche_Tsai

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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **DDR3 CHANNEL A**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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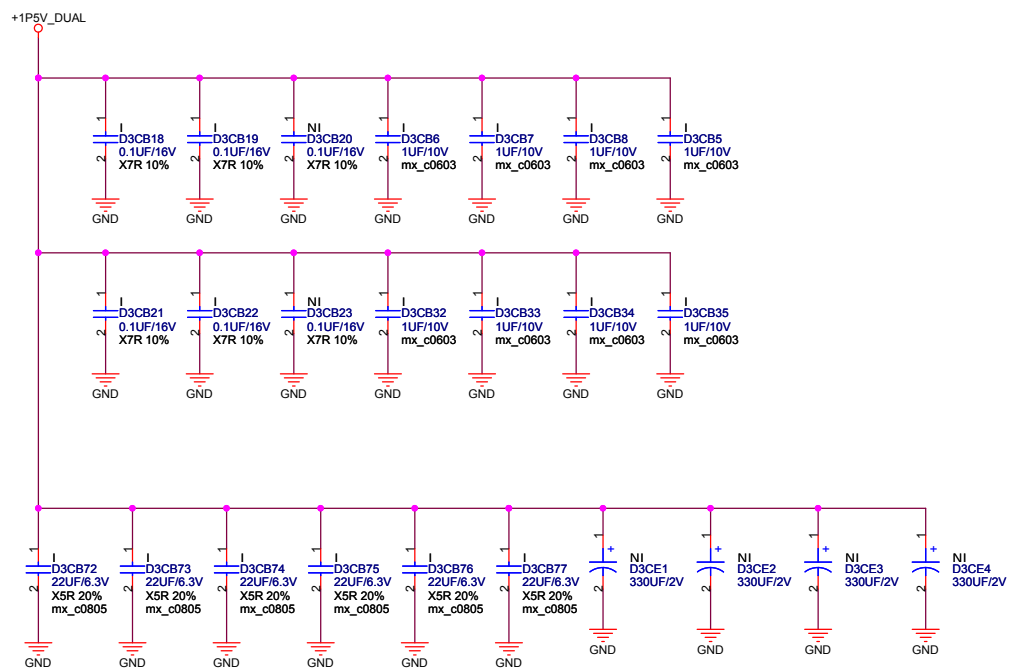
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : DDR3 CHANNEL B

Pegatron Corp. Engineer: Haoche_Tsai

Size A3	Project Name IPISB-AG	Rev 1.04
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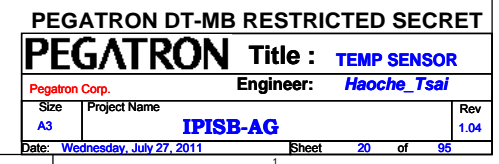
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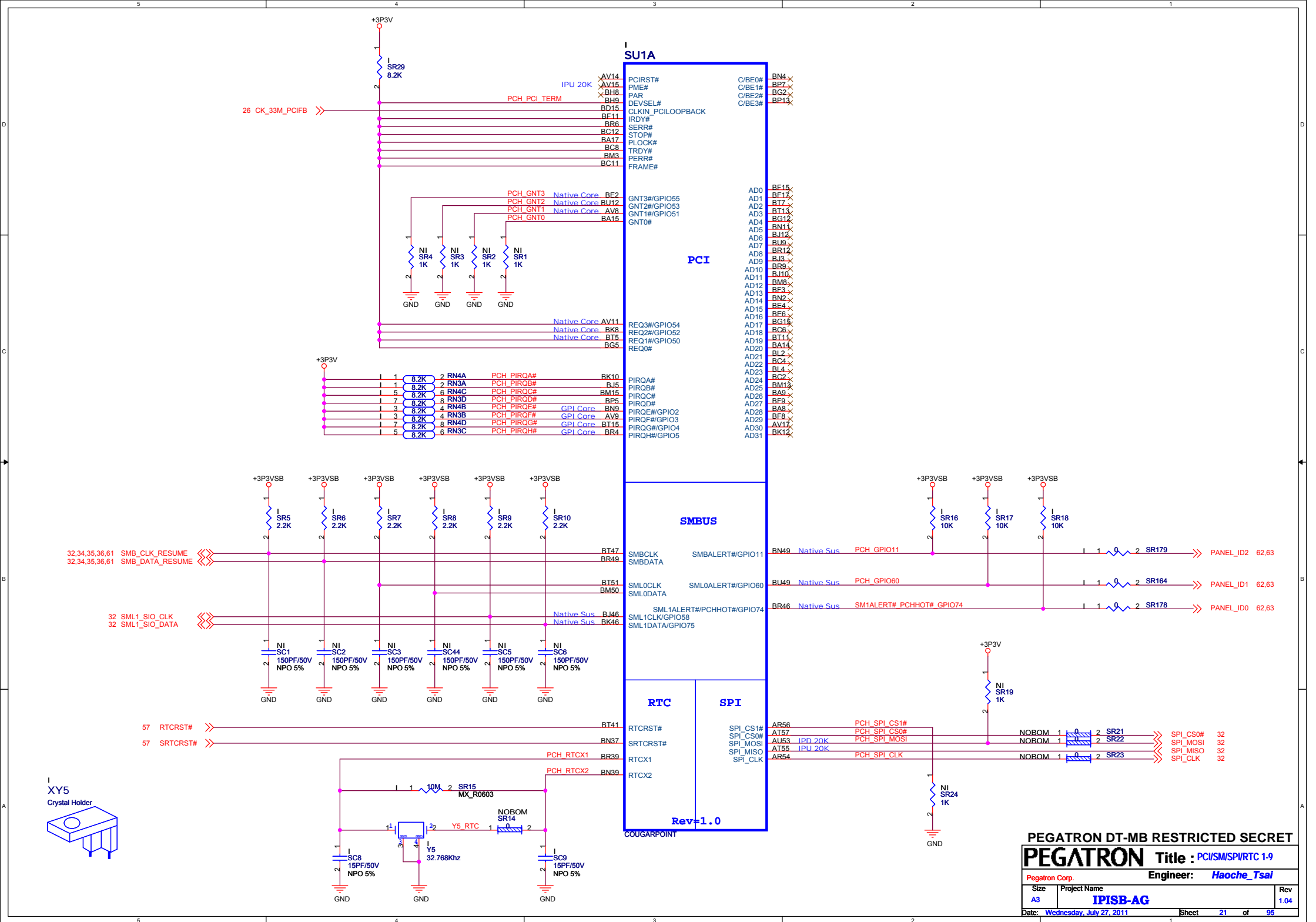
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Pegatron Corp. Engineer: Haoche Tsai

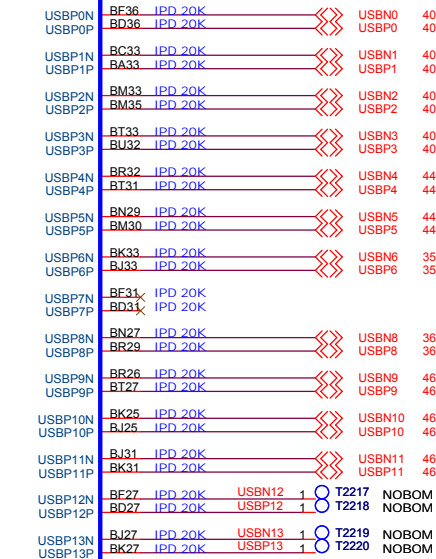
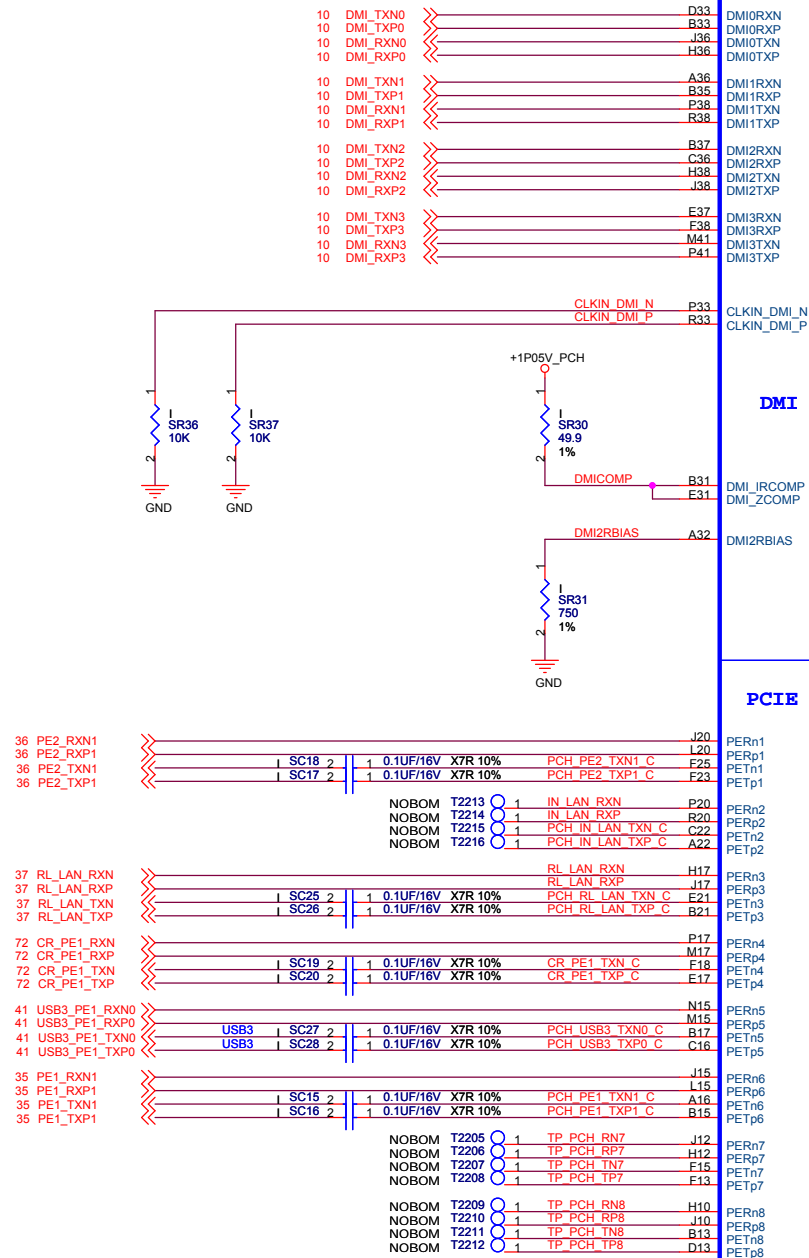
Size A3	Project Name IPISB-AG	Rev 1.04
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Date: Wednesday, July 27, 2011 Sheet 19 of 95

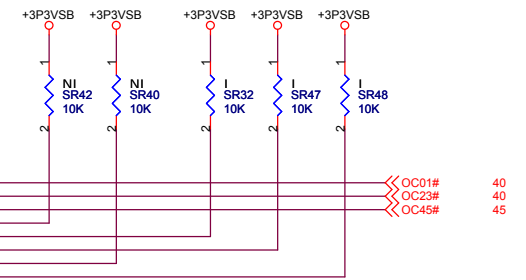
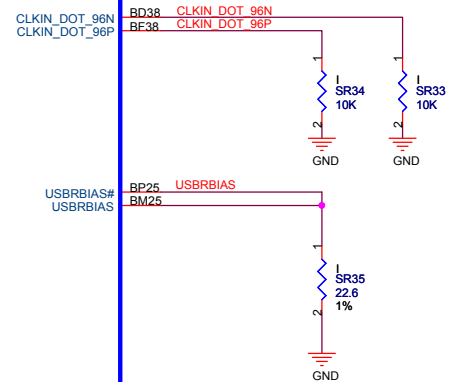
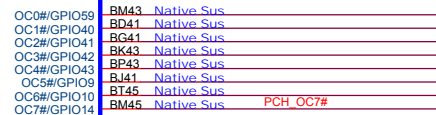




SU1B



USB



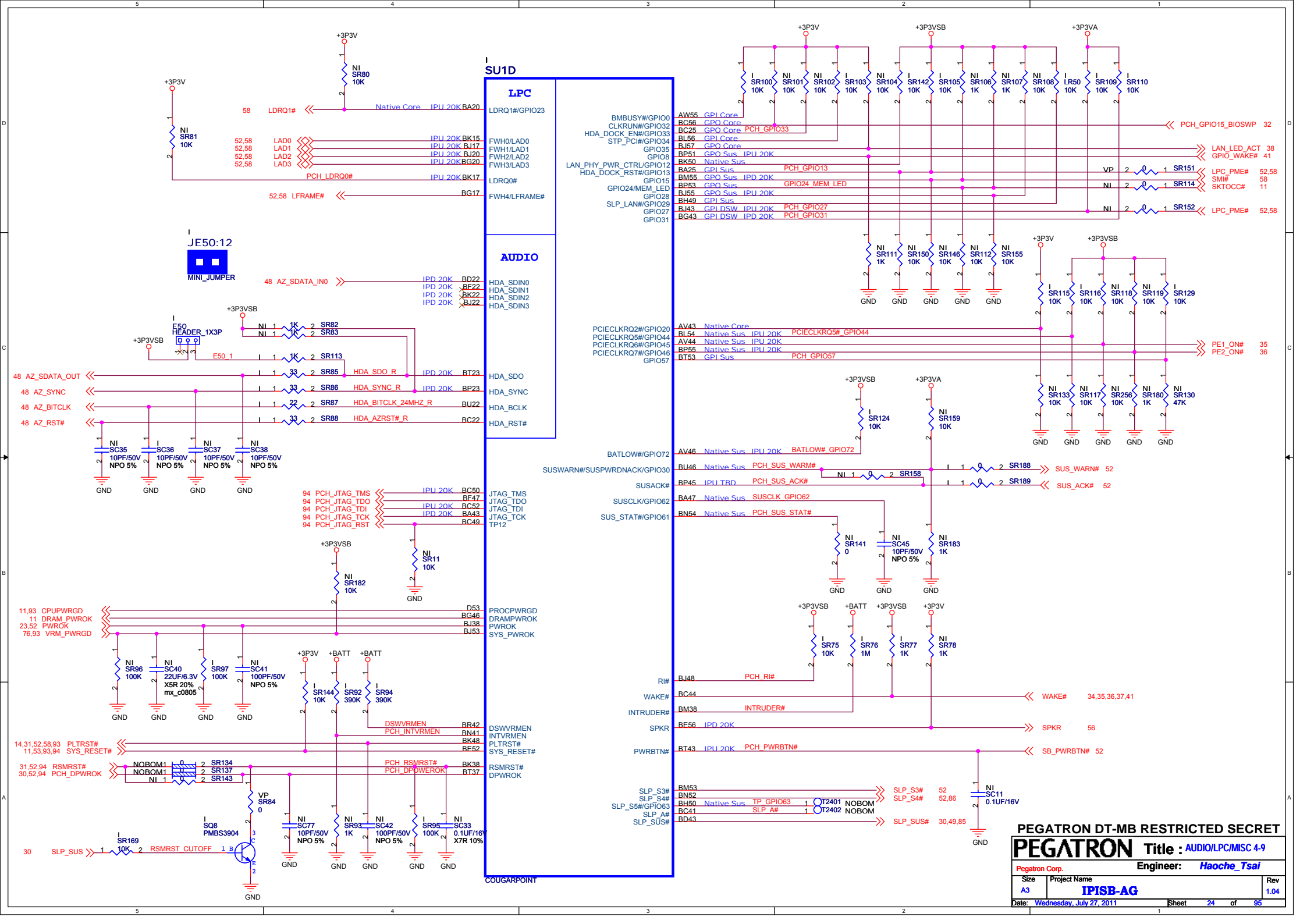
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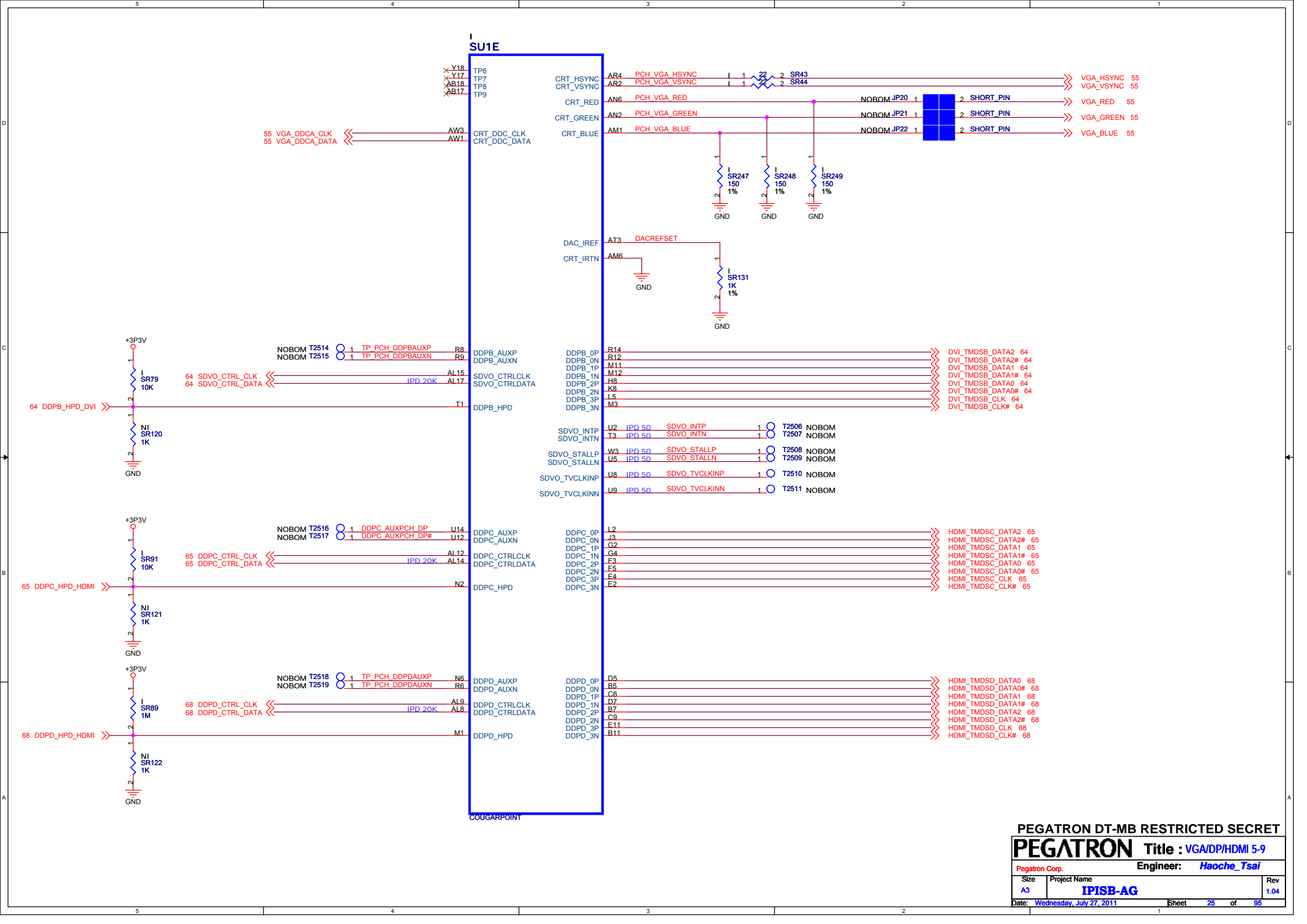
PEGATRON Title : PCIE/USB/DMI 2-9

Pegatron Corp. Engineer: Haoche_Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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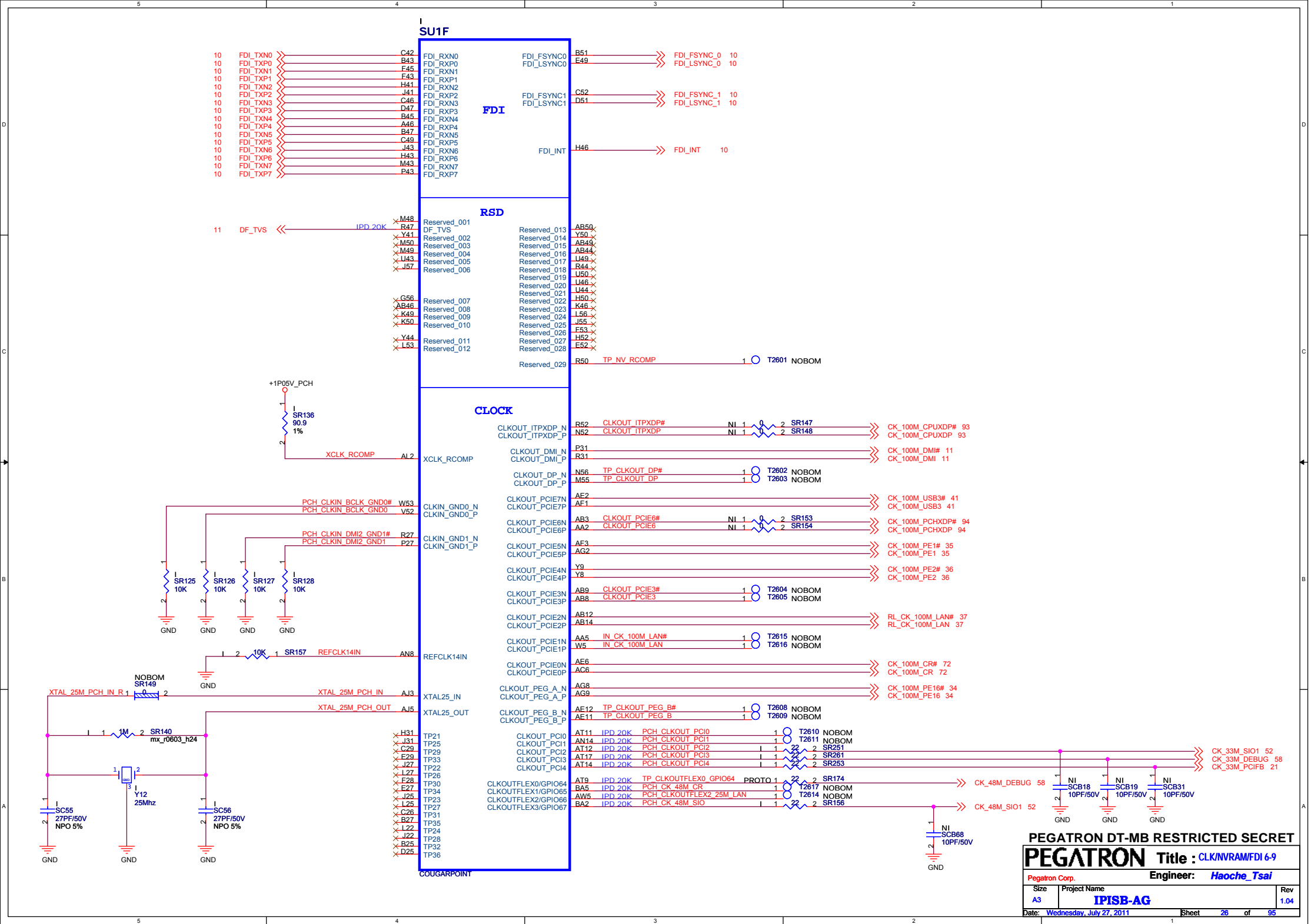
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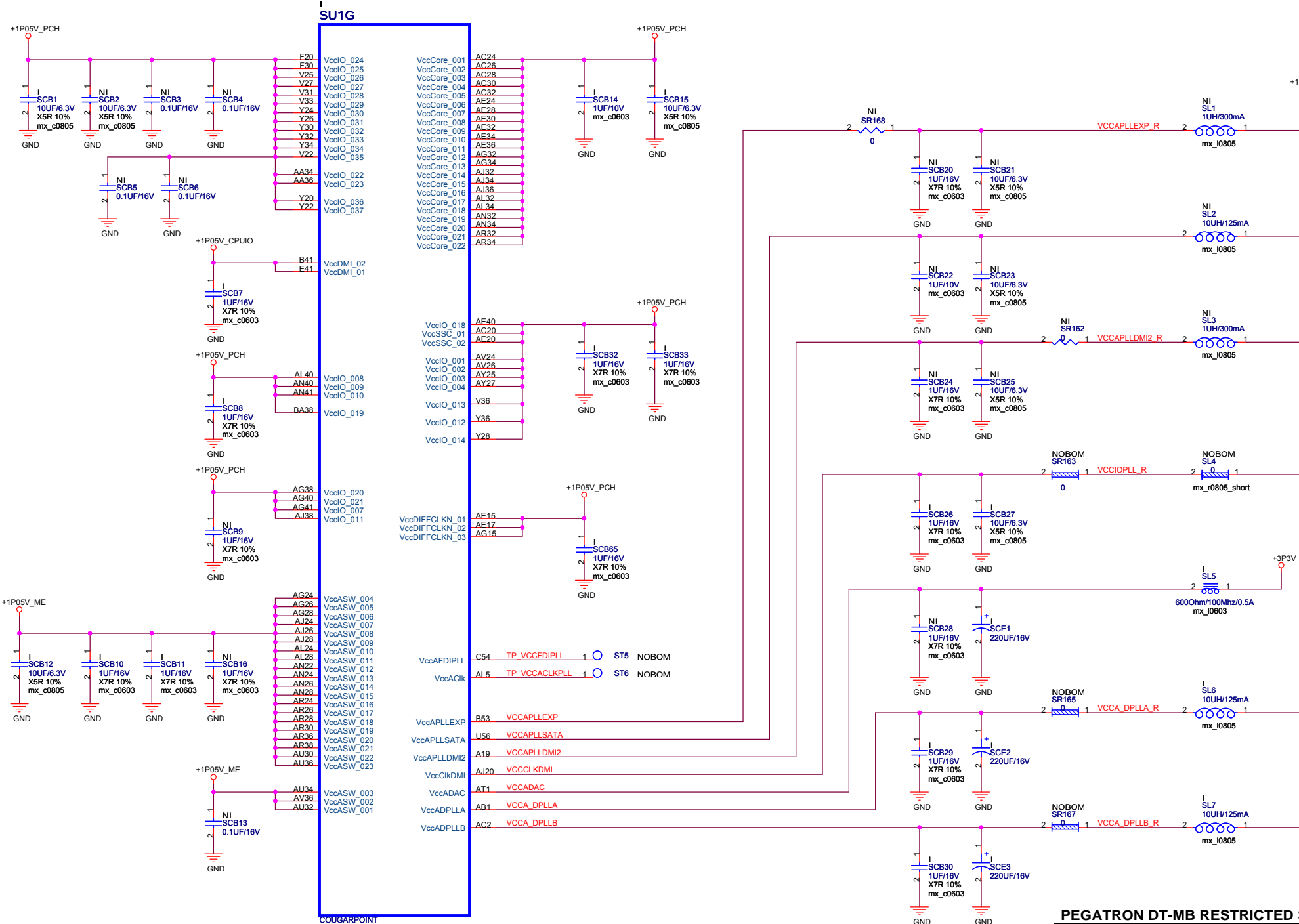
PEGATRON Title : VGA/DP/HDMI 5-9

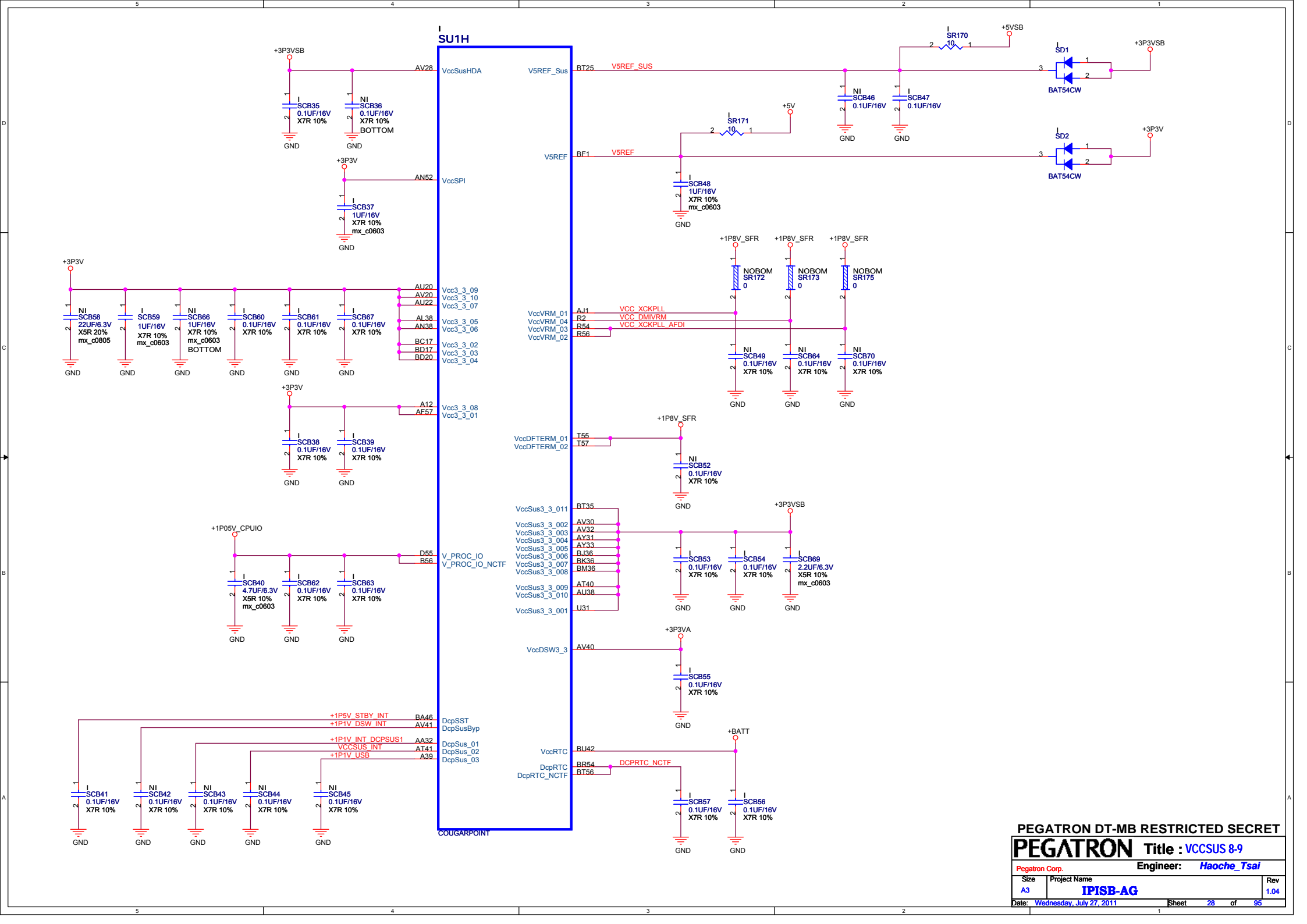
Pegatron Corp. Engineer: Haoche Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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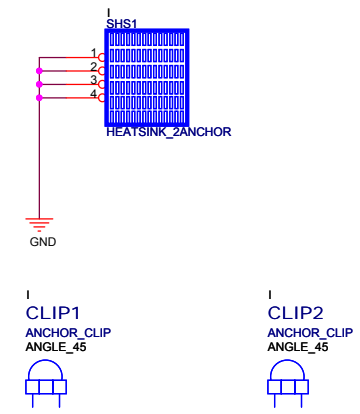
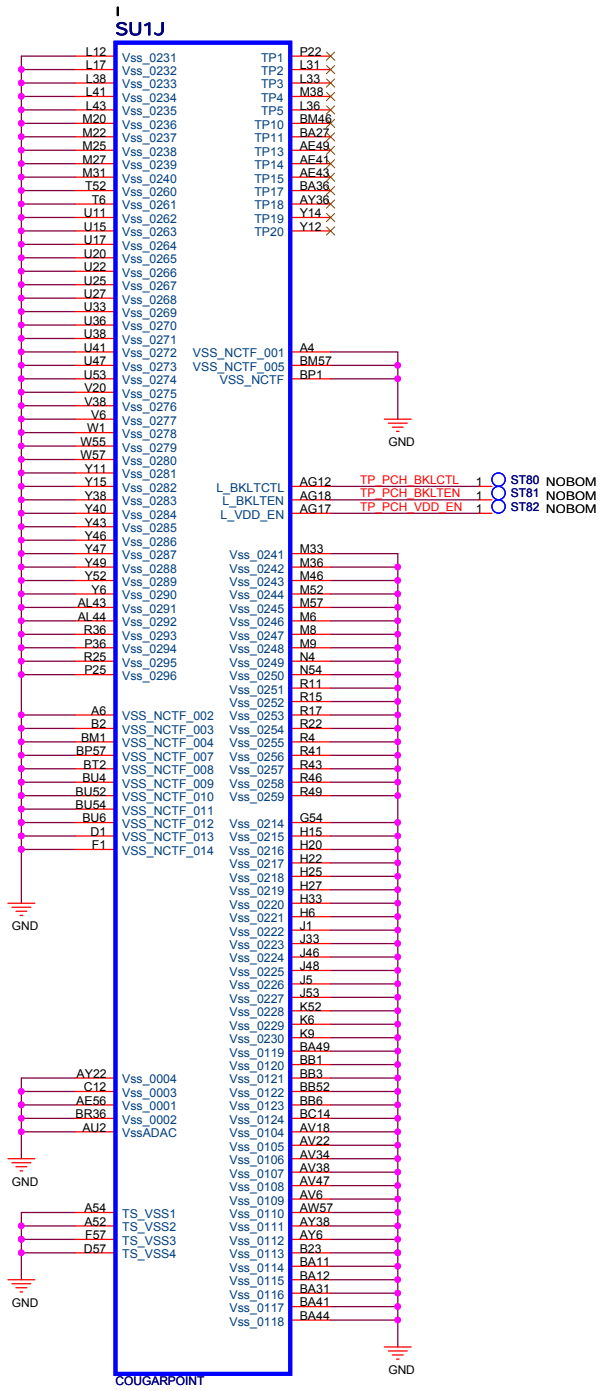
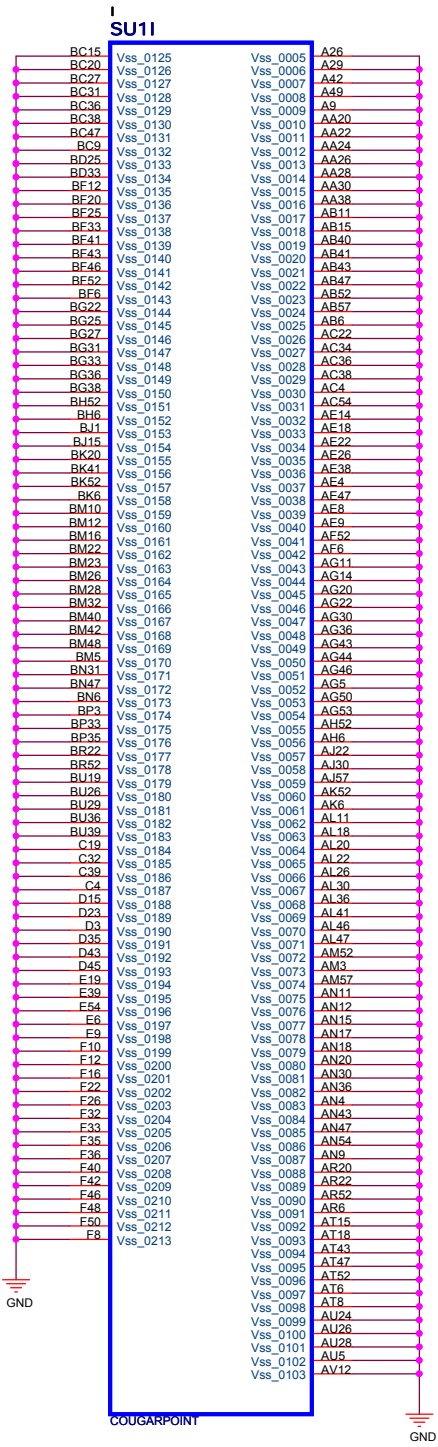
PEGATRON DT-MB RESTRICTED SECRET

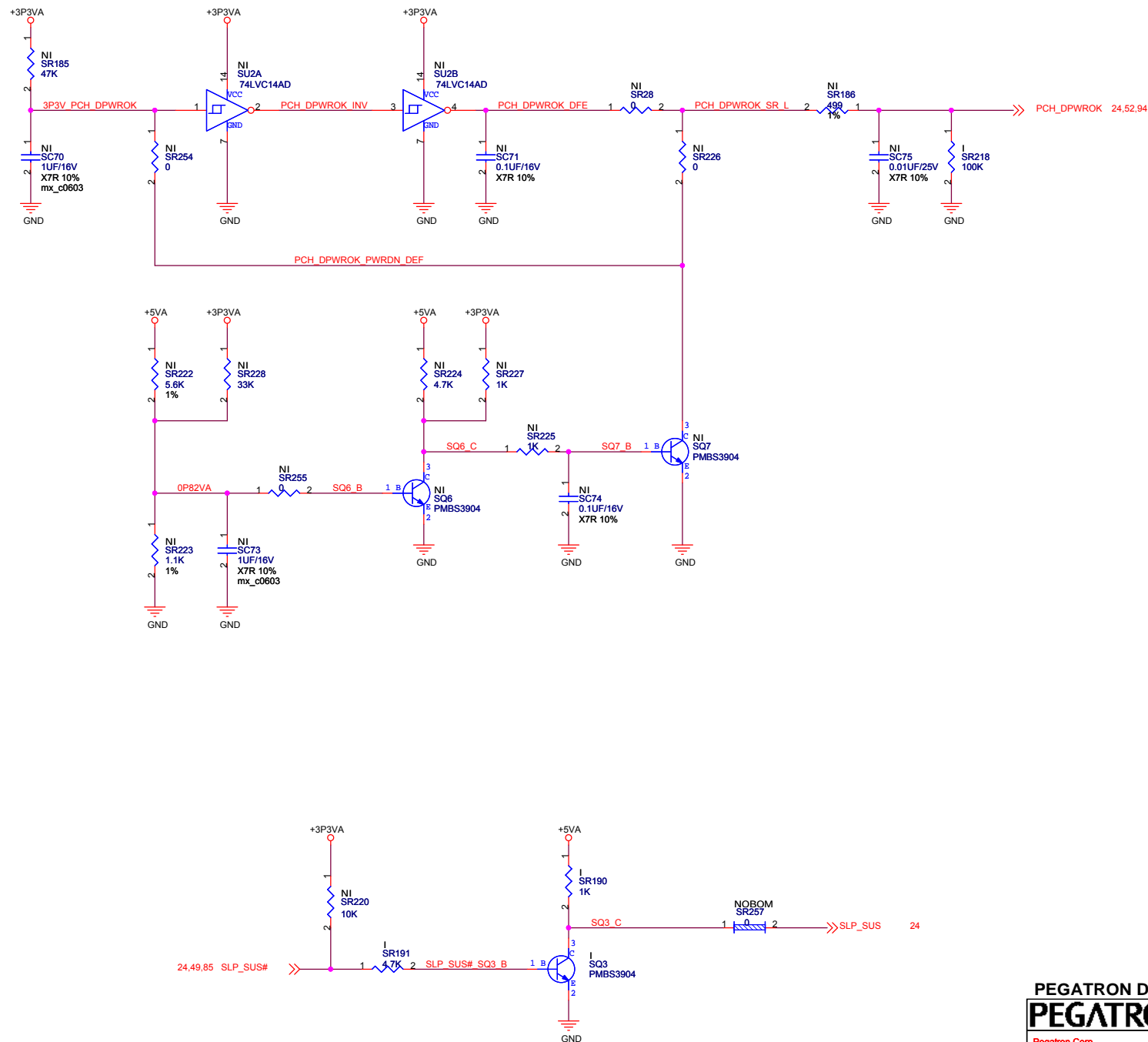
PEGATRON Title : VCCSUS 8-9

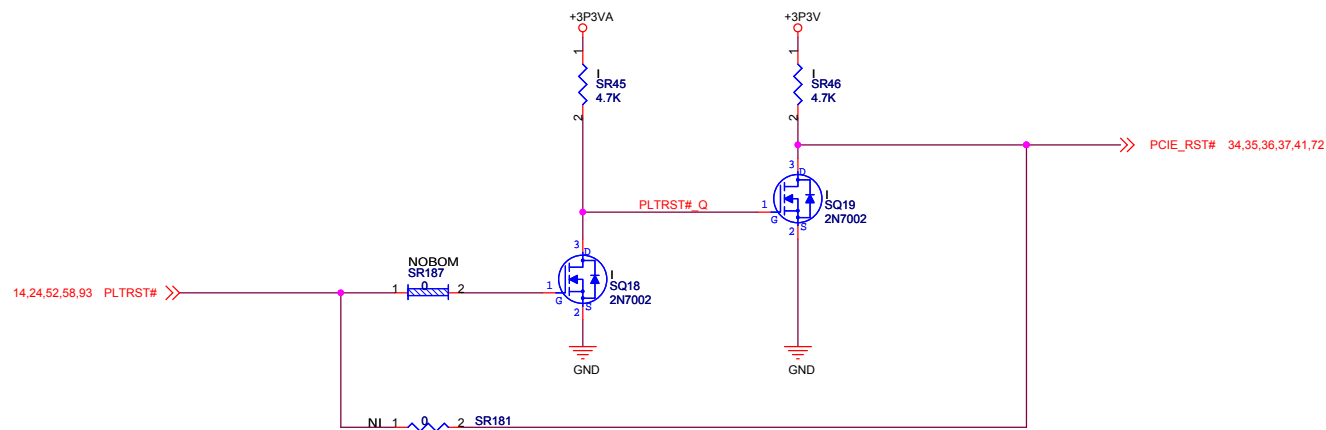
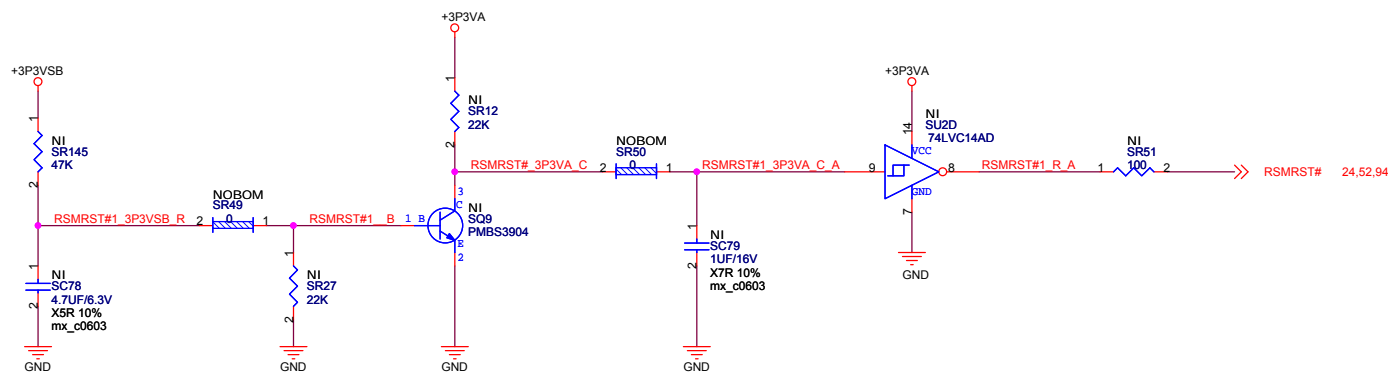
Pegatron Corp. Engineer: Haoche Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

Date: Wednesday, July 27, 2011 Sheet 28 of 95







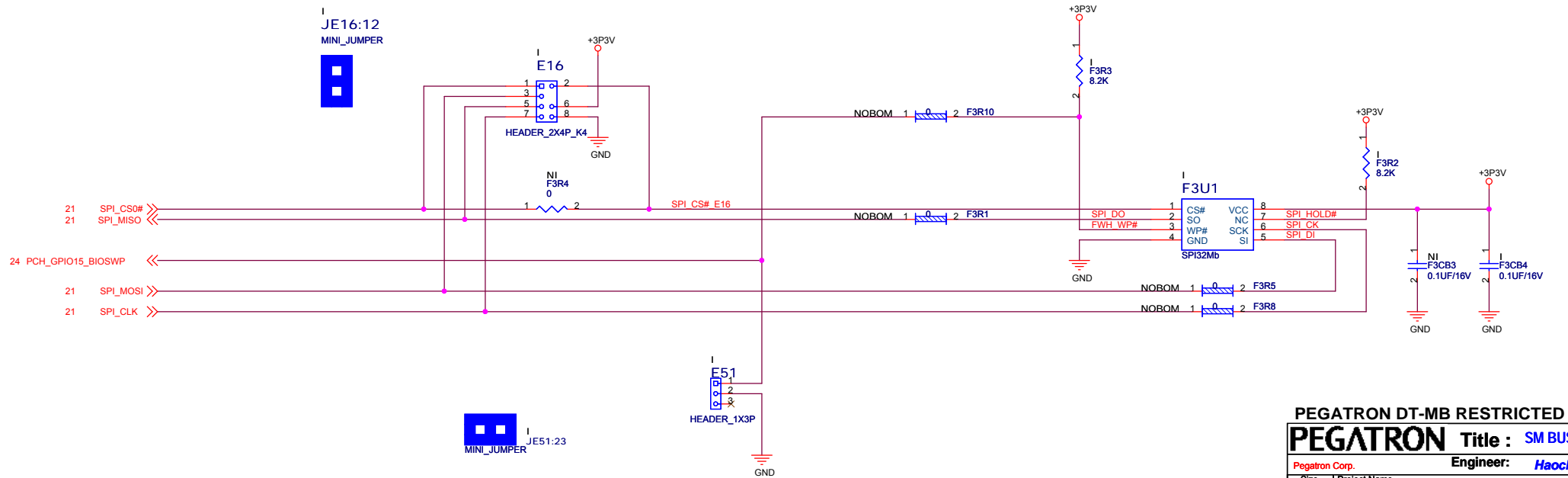
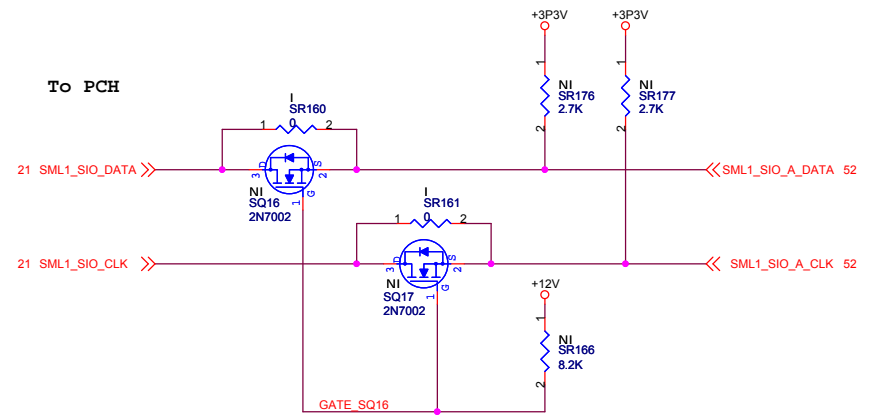
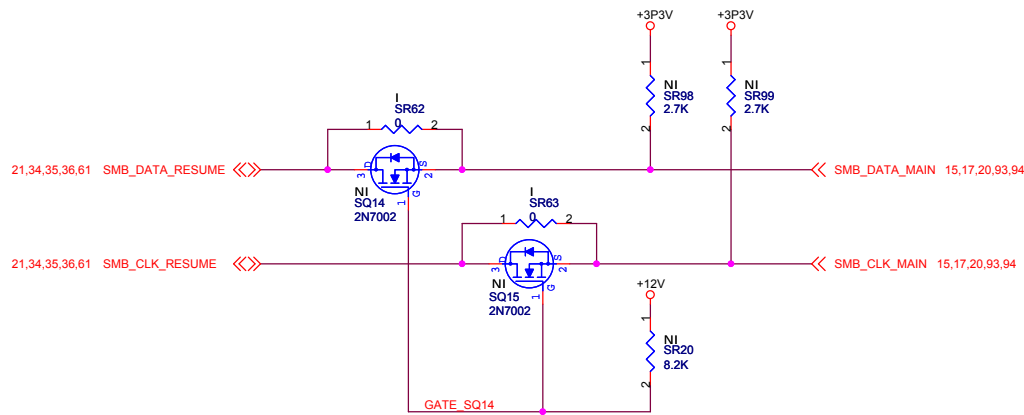
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : RSMRST# / PCIE_RST#

Pegatron Corp. Engineer: Haoche_Tsai

Size A3 Project Name IPISB-AG Rev 1.04

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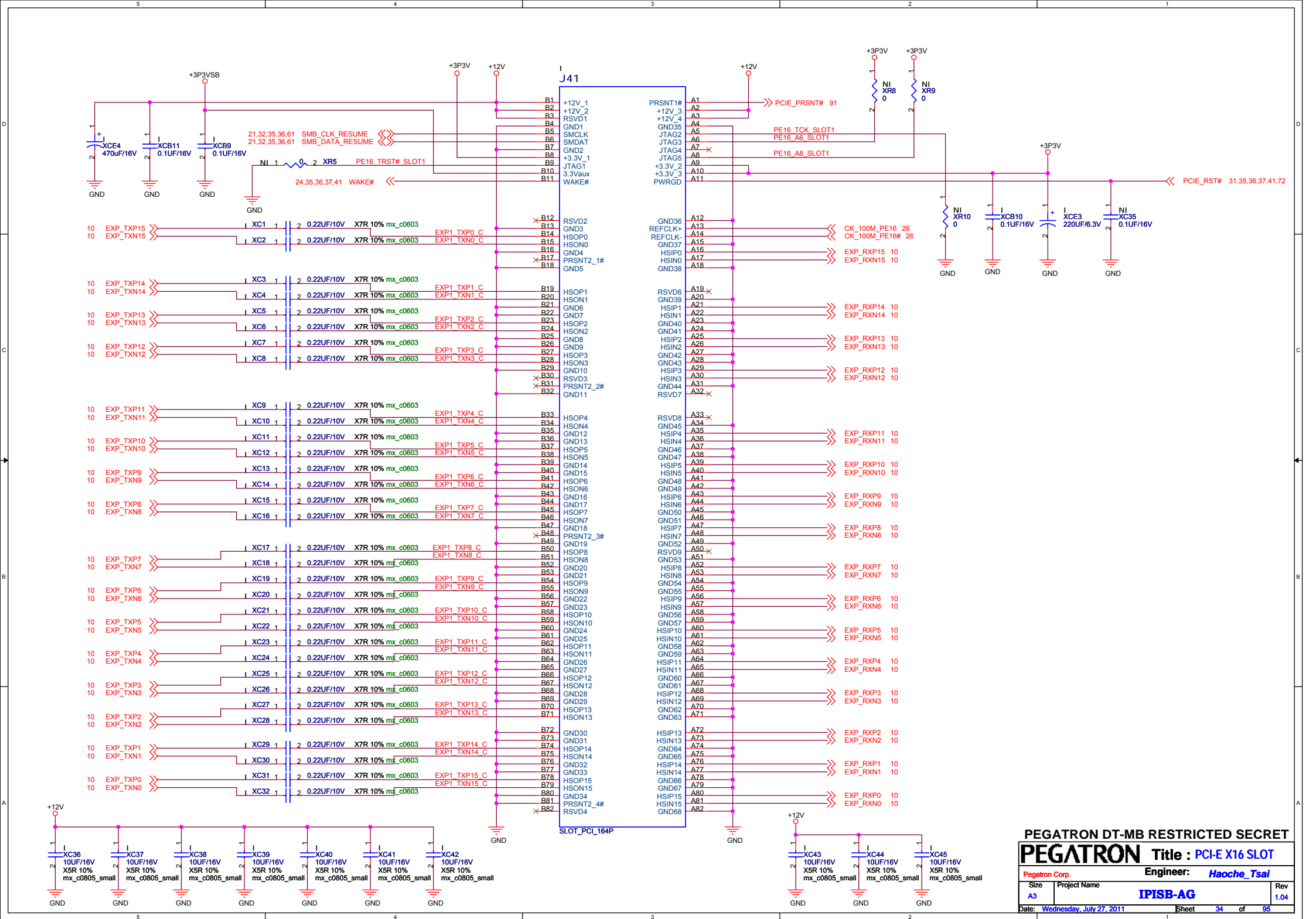


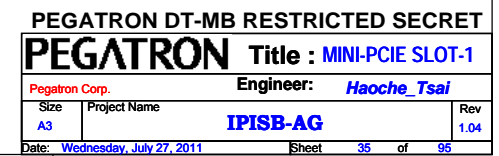
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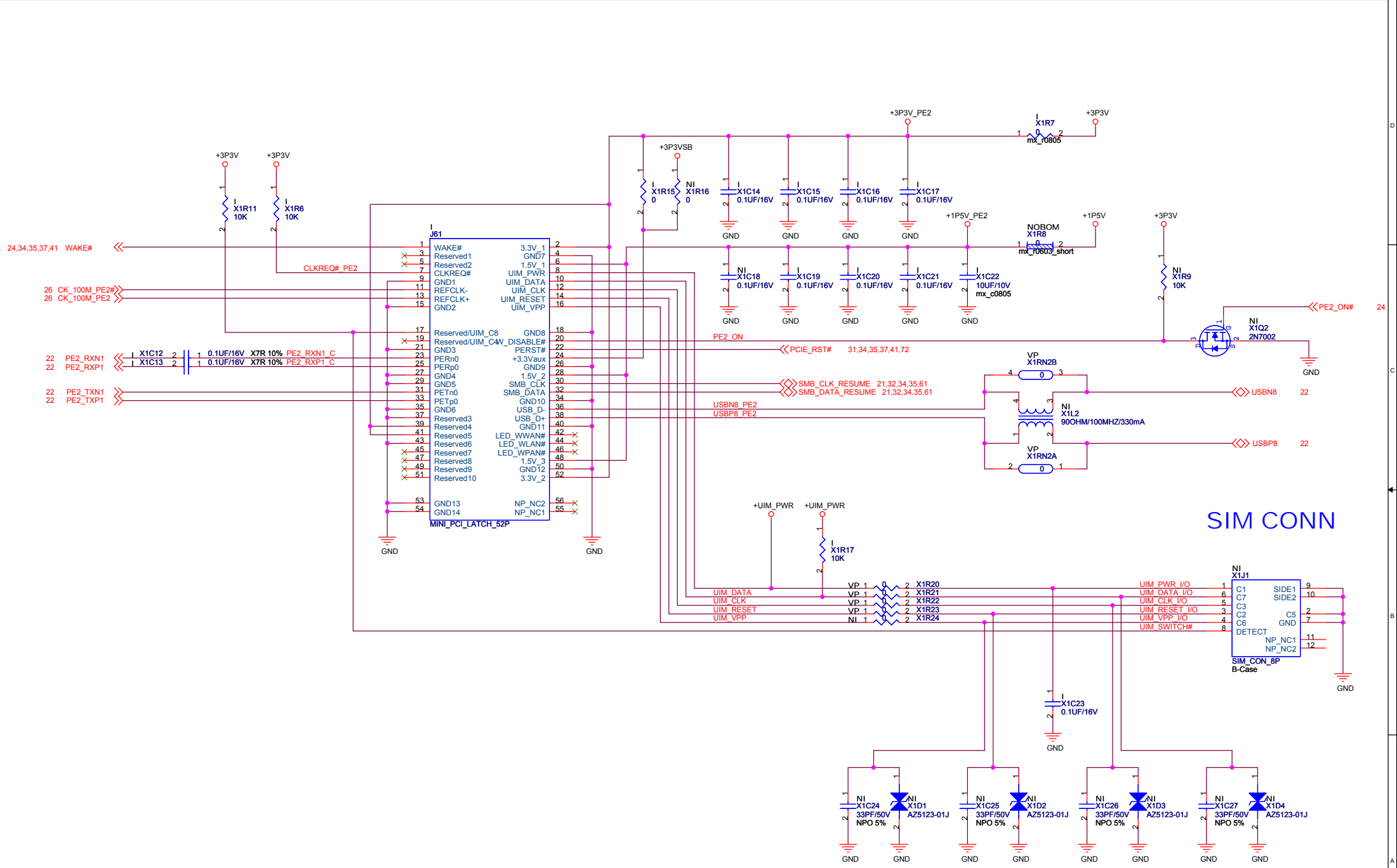
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Pegatron Corp. Engineer: **Haoche_Tsai**

Size	Project Name	Rev
A3	IPISB-AG	1.04







SIM CONN

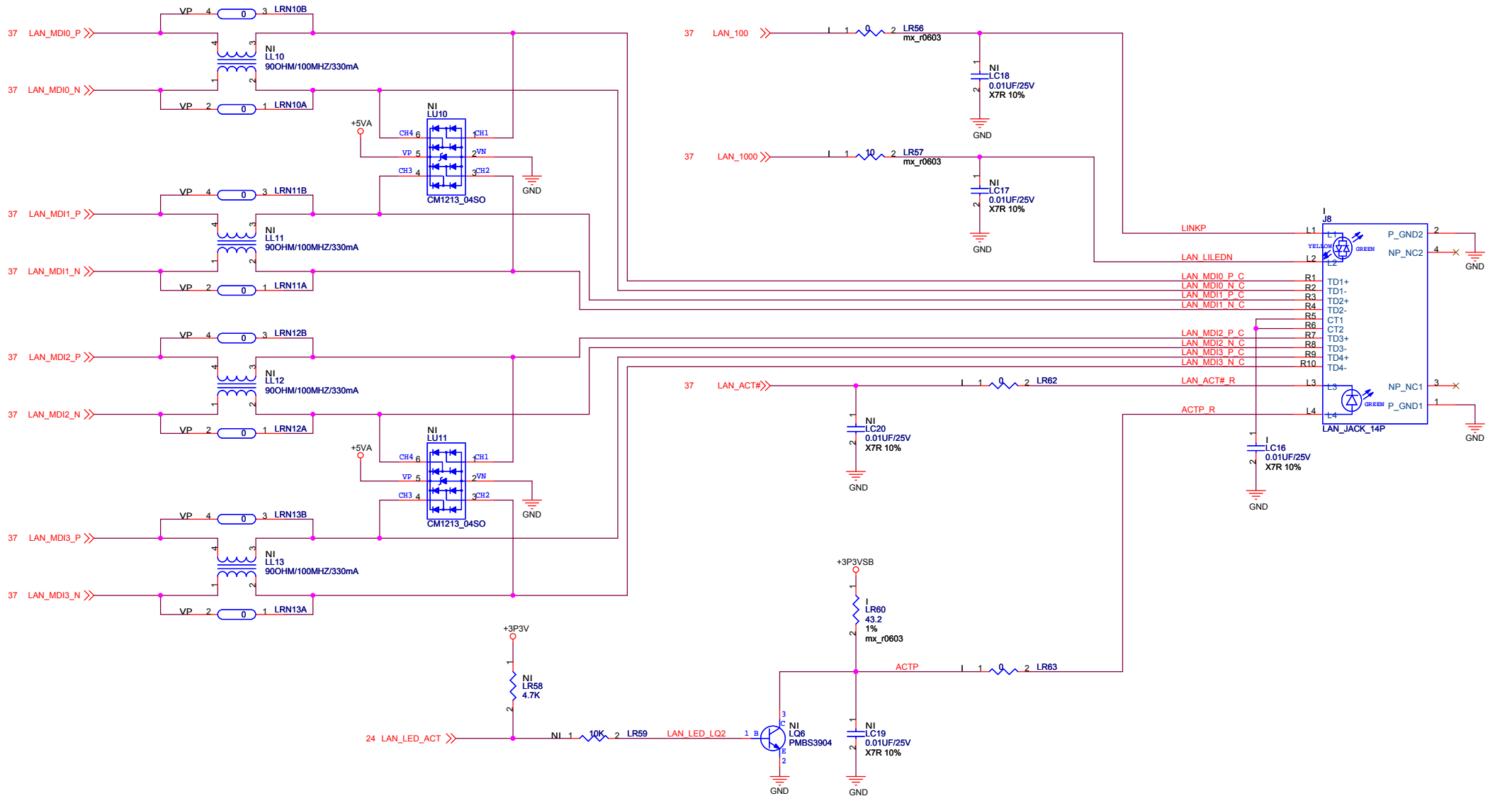
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : MINI-PCIE SLOT-2

Pegatron Corp. Engineer: Haoche_Tsai

Size A3 Project Name IPISB-AG Rev 1.04

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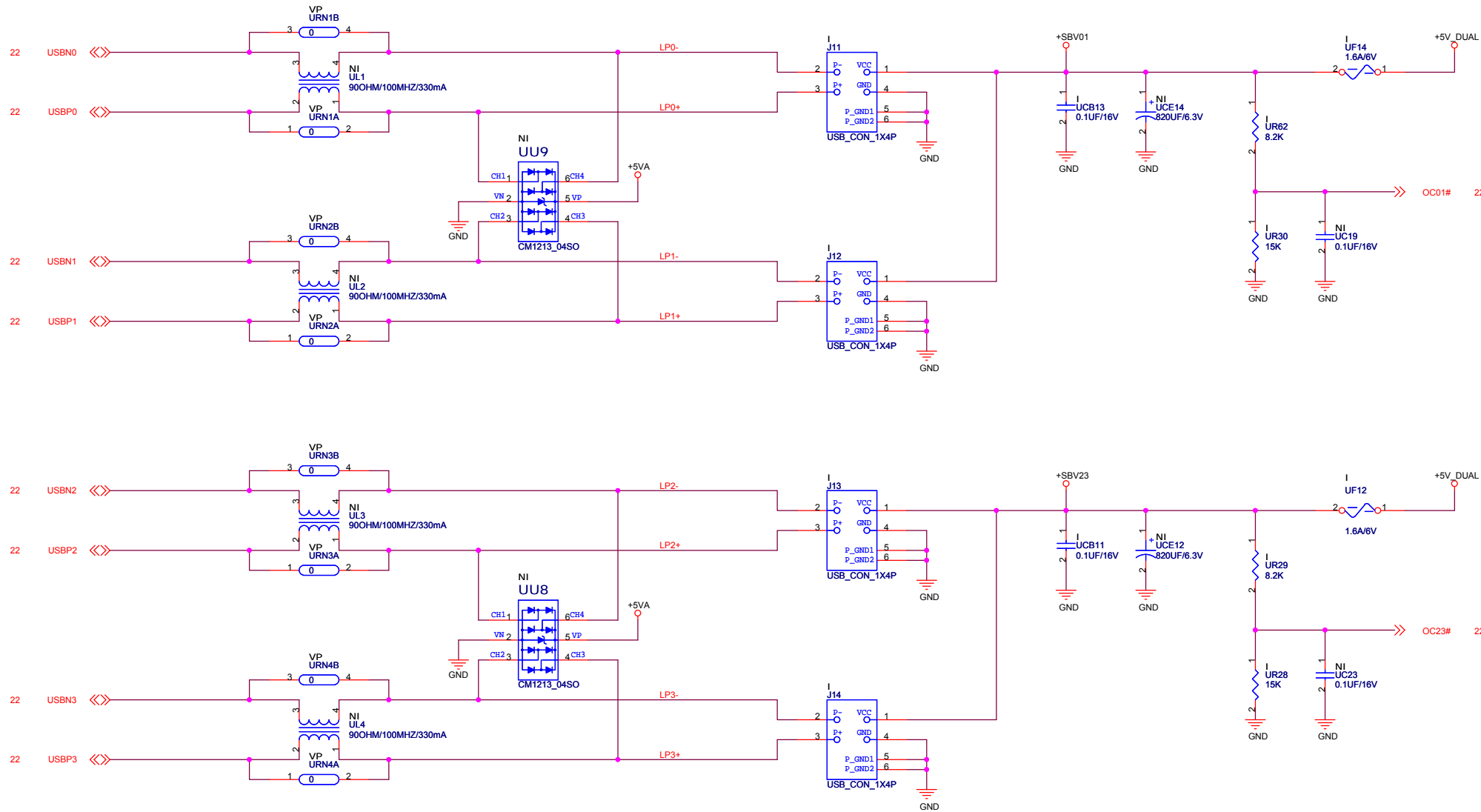
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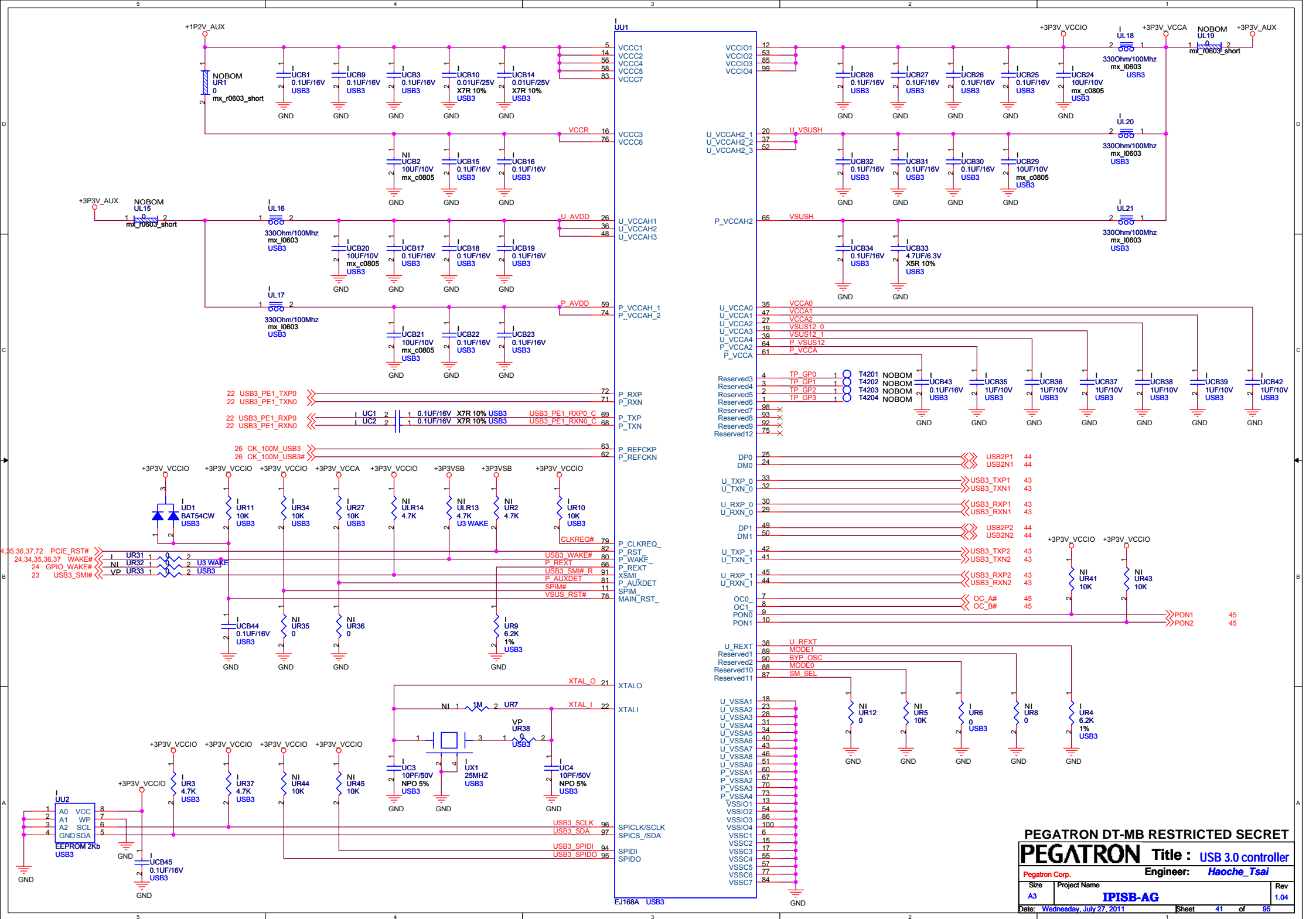
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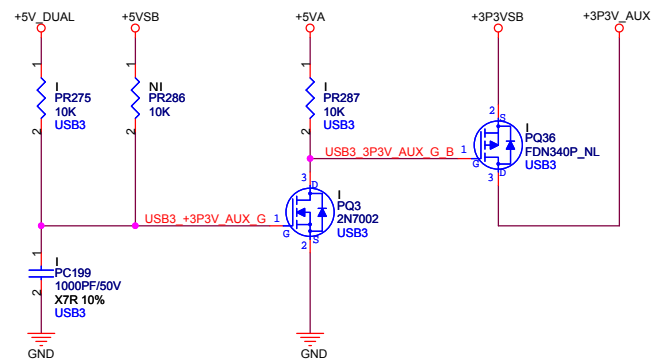
Pegatron Corp. Engineer: **Haoche_Tsai**

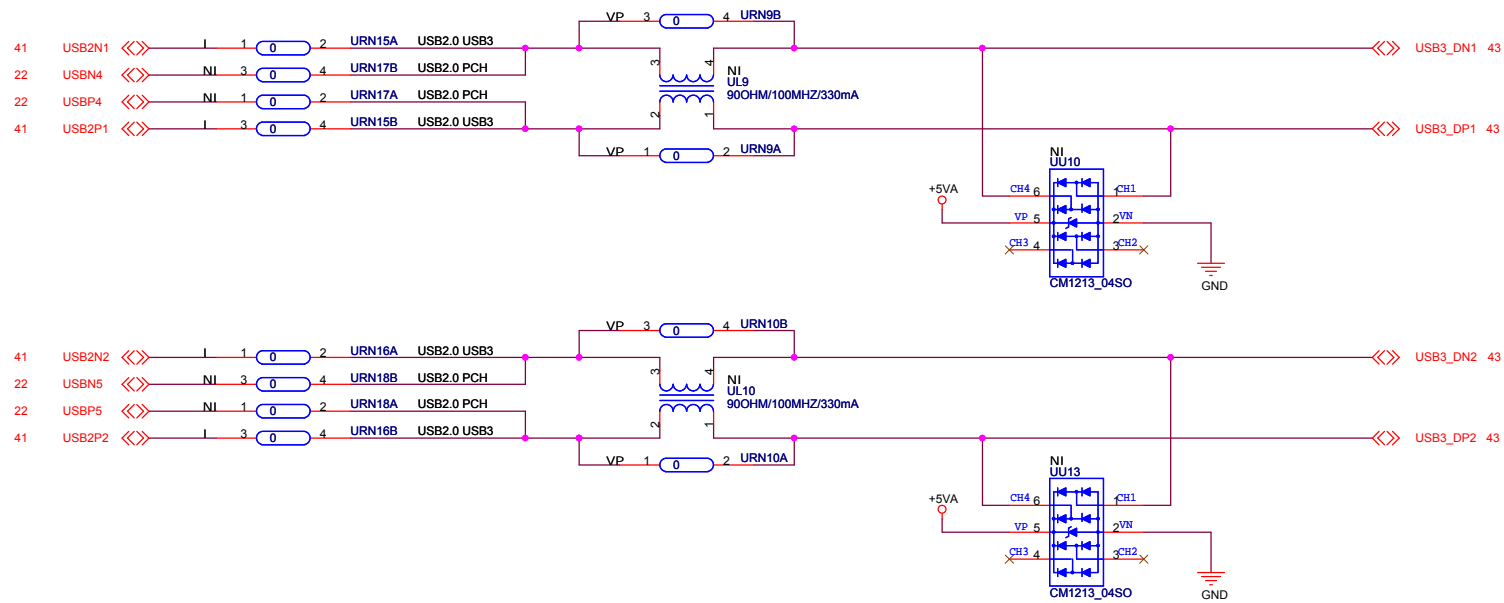
Size A3	Project Name IPISB-AG	Rev 1.04
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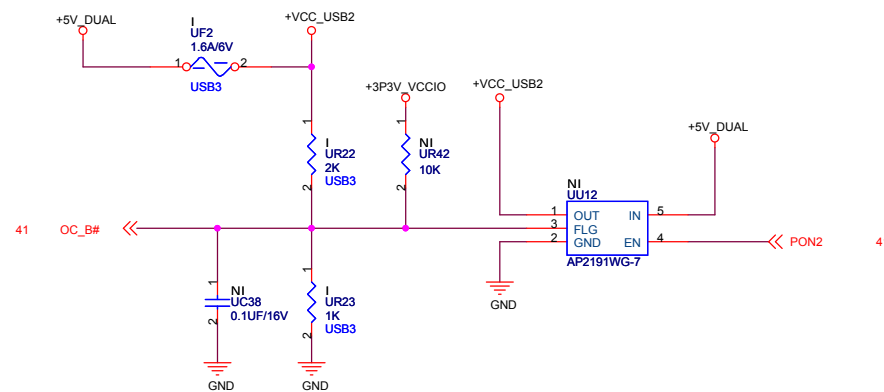
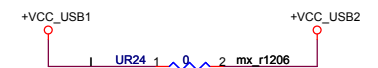
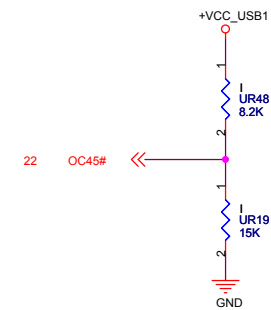
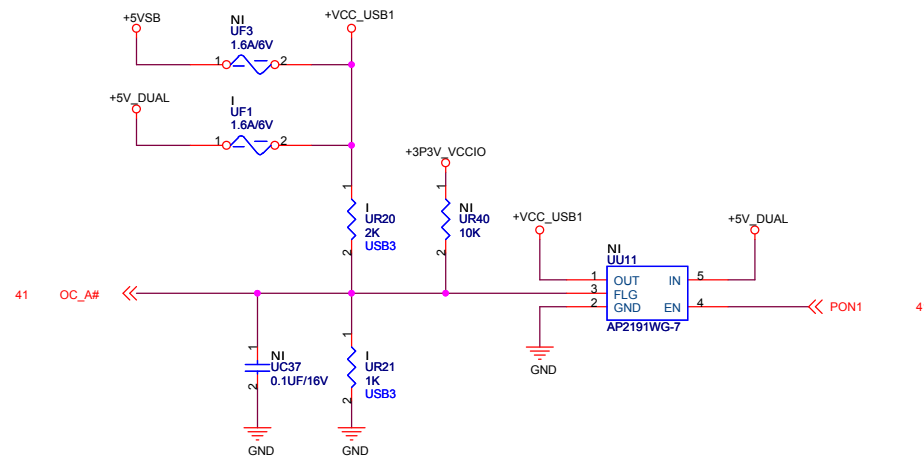
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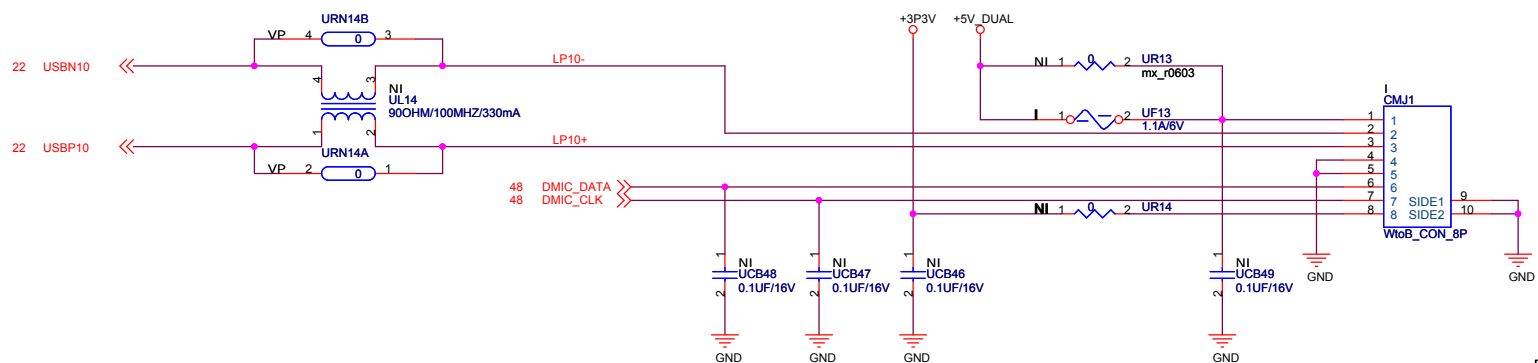
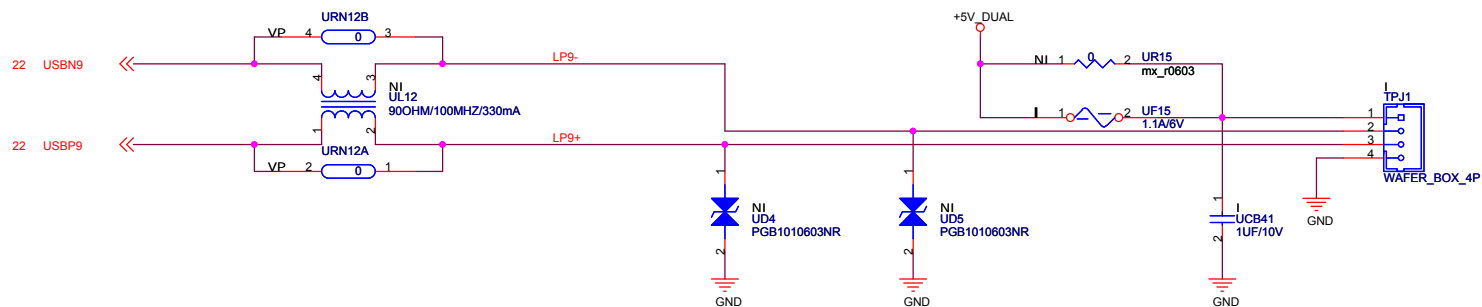
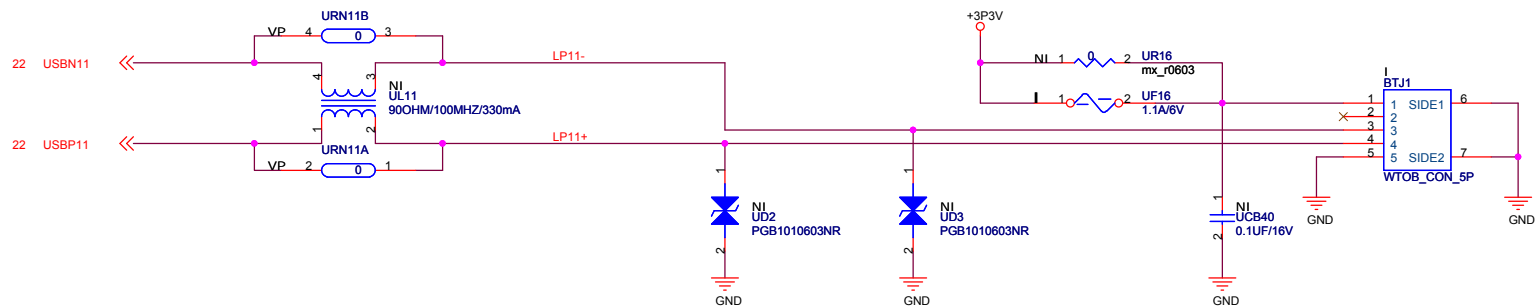
PEGATRON Title : SIDE USB2.0 PORT

Pegatron Corp. Engineer: Haoche Tsai

Size A3	Project Name IPISB-AG	Rev 1.04
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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : USB HEADER

Pegatron Corp. Engineer: Haoche_Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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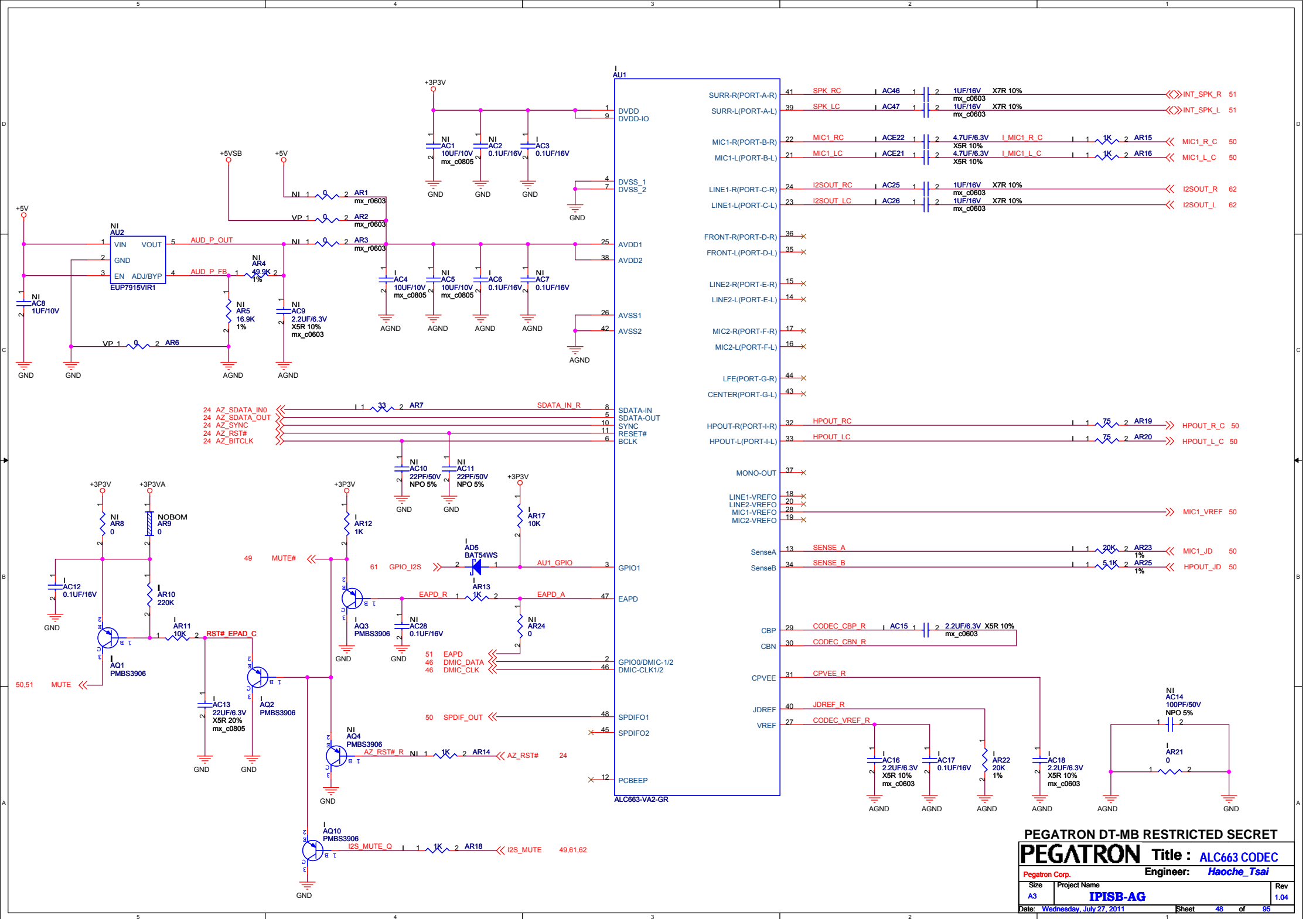
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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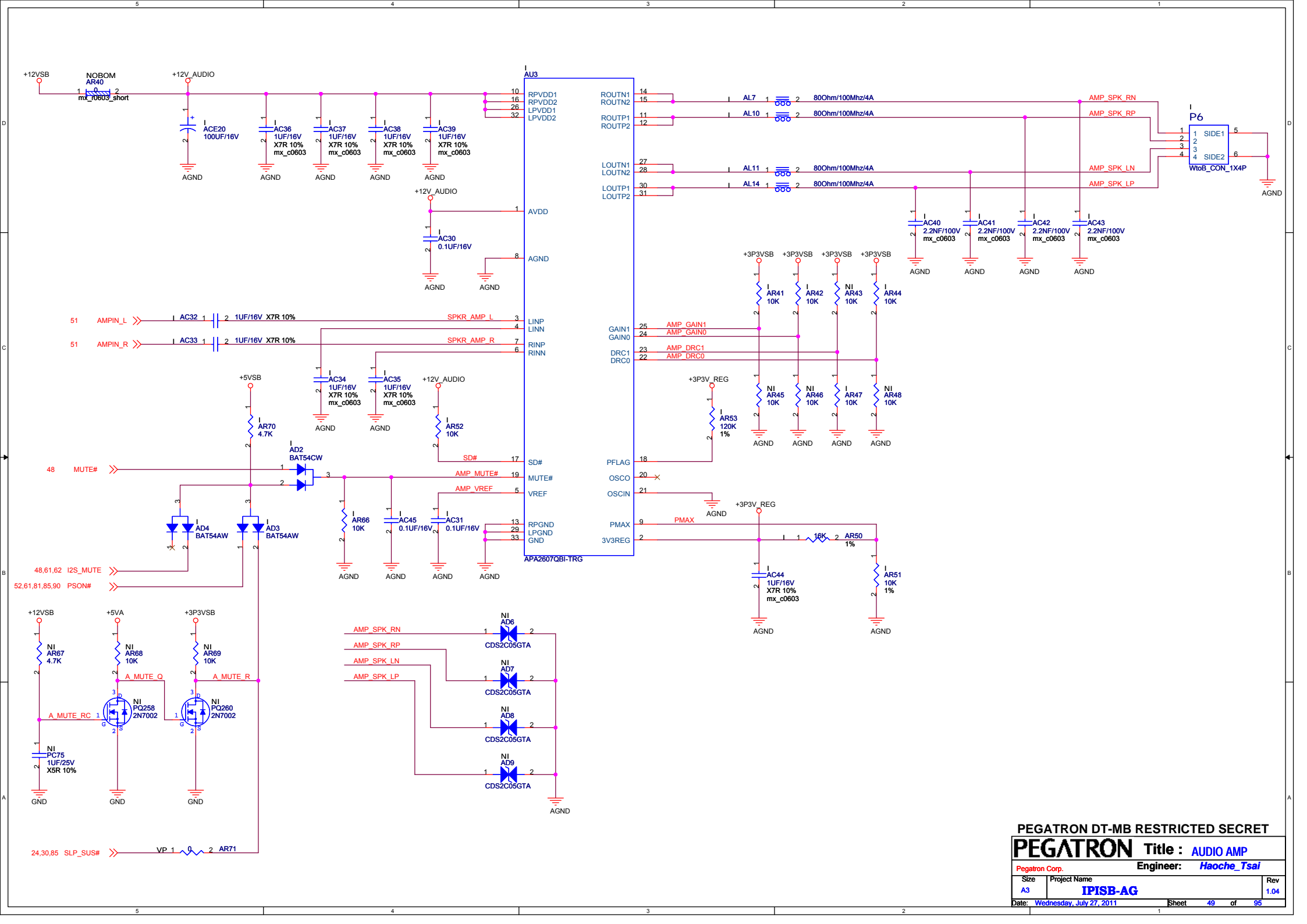
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **ALC663 CODEC**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size	Project Name	Rev
A3	IPISB-AG	1.04

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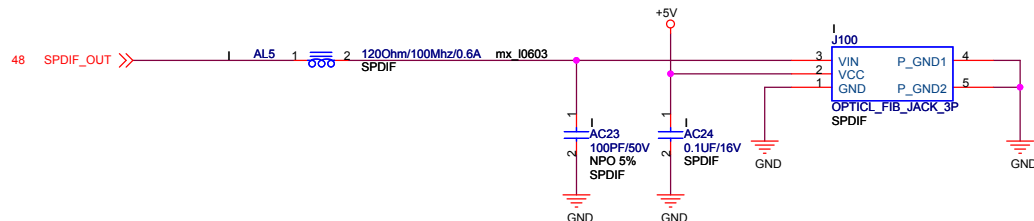
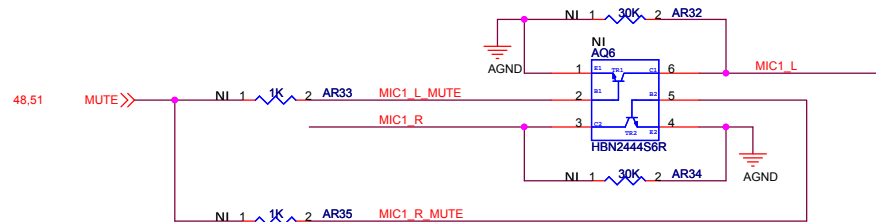
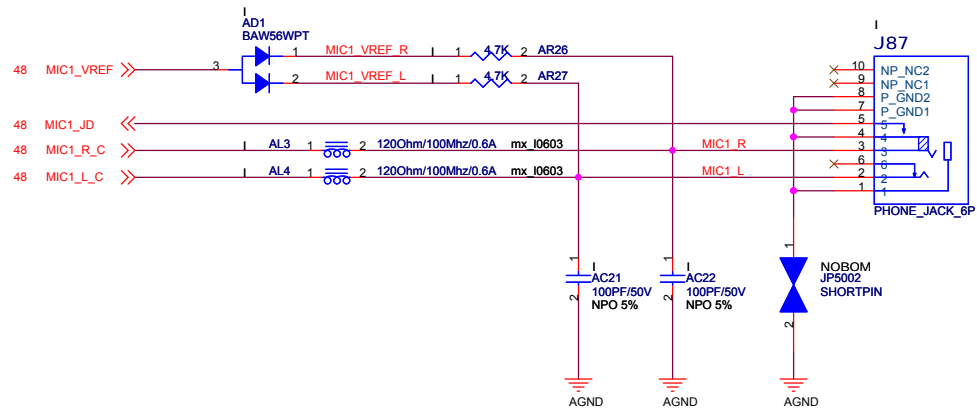
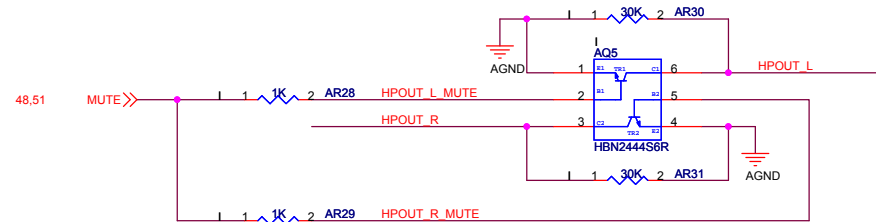
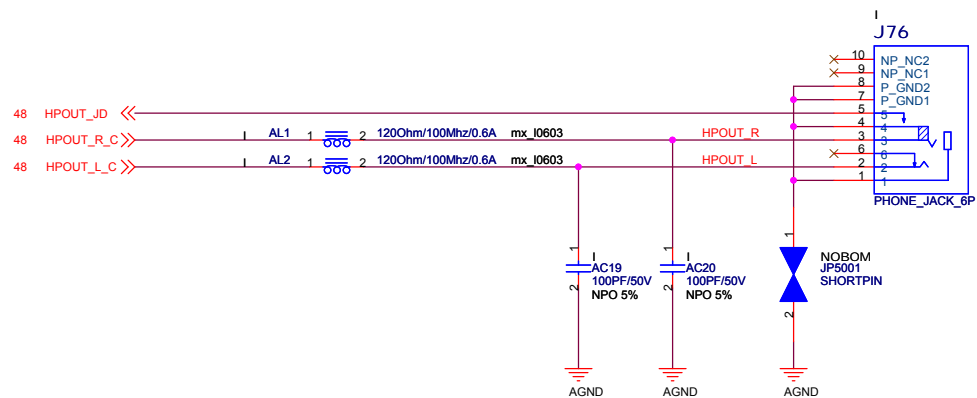
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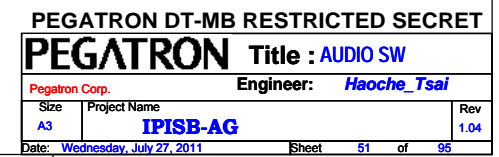
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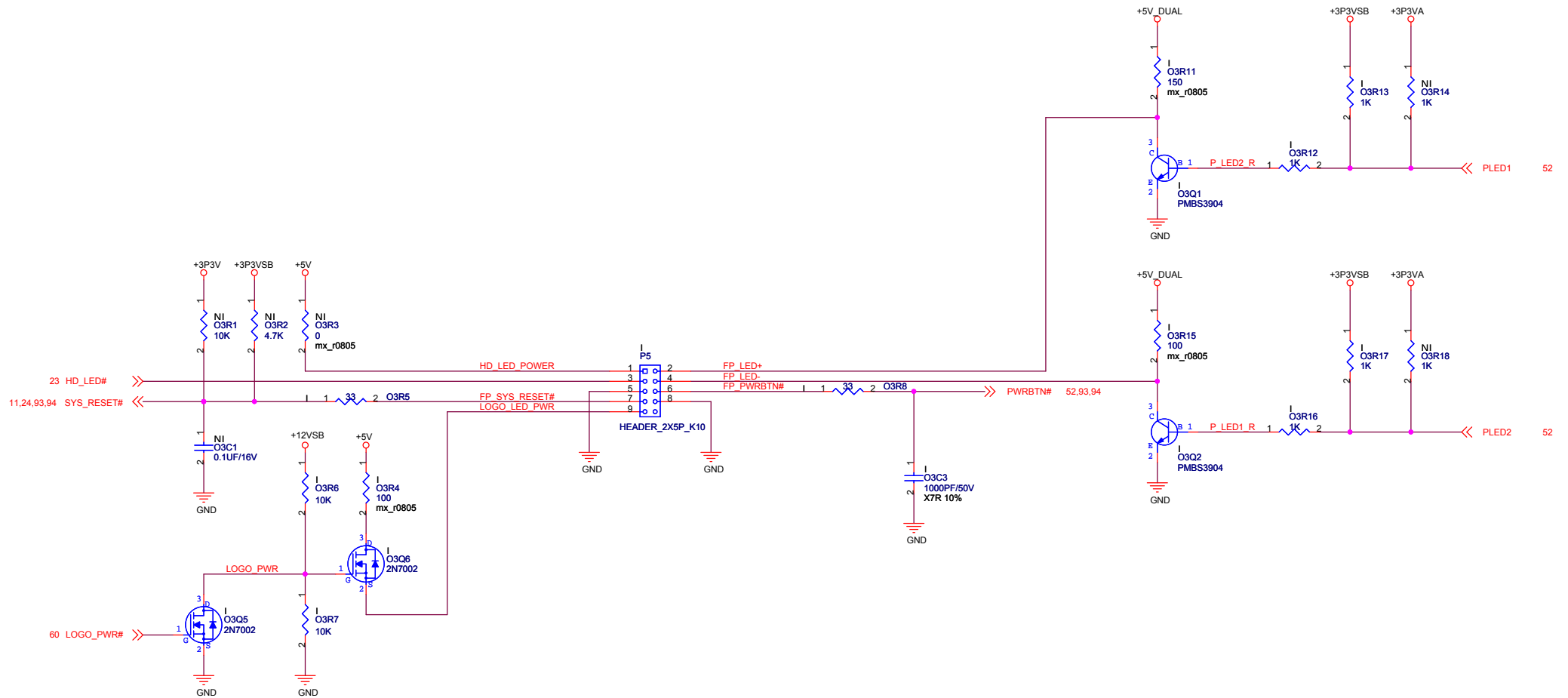
Pegatron Corp. Engineer: **Haoche_Tsai**

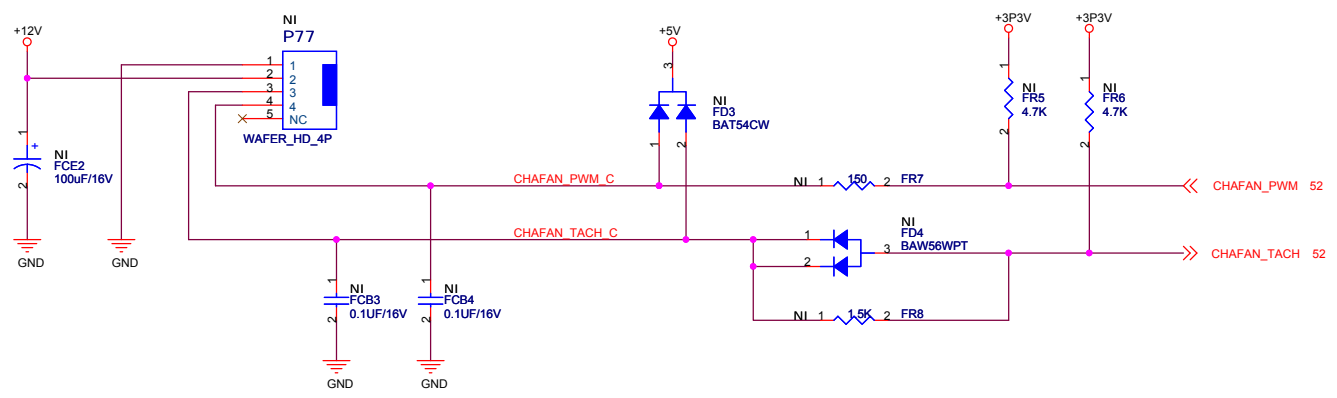
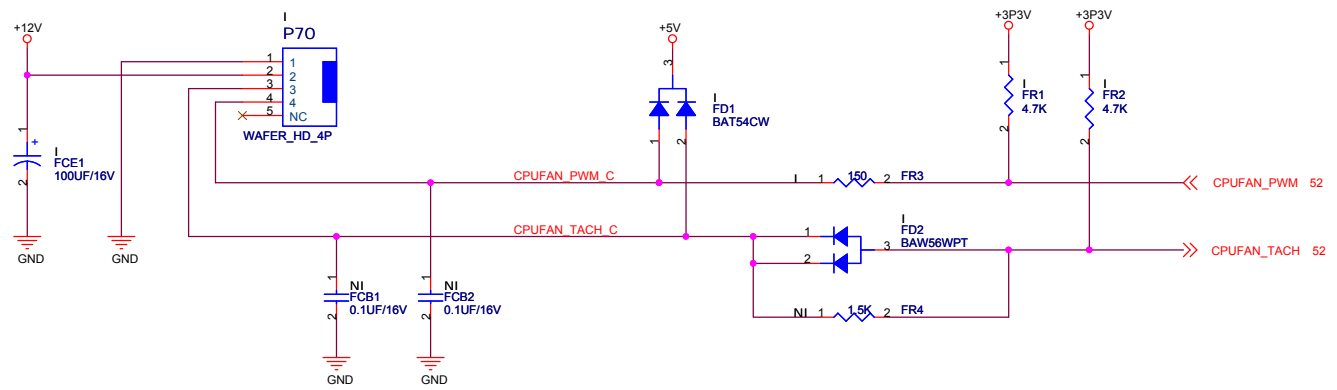
Size	Project Name	Rev
A3	IPISB-AG	1.04

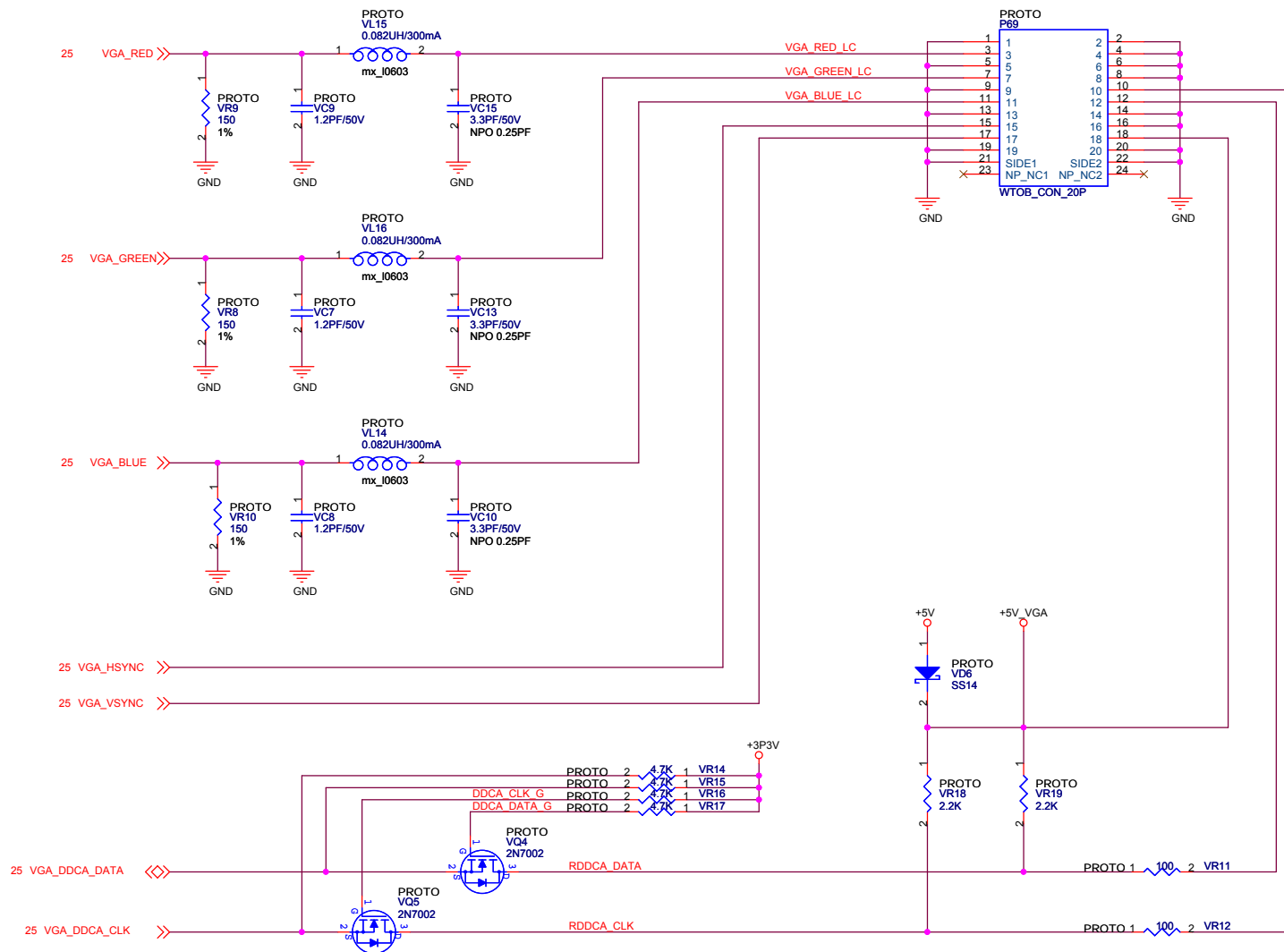
Date: Wednesday, July 27, 2011 Sheet 49 of 95

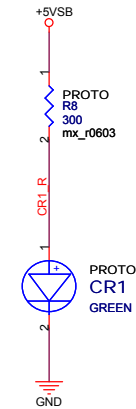
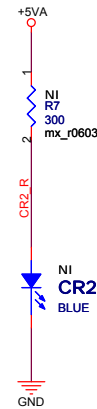
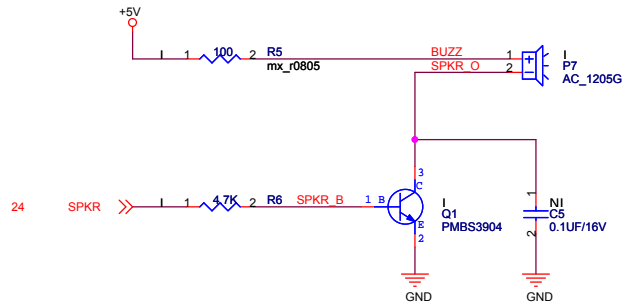
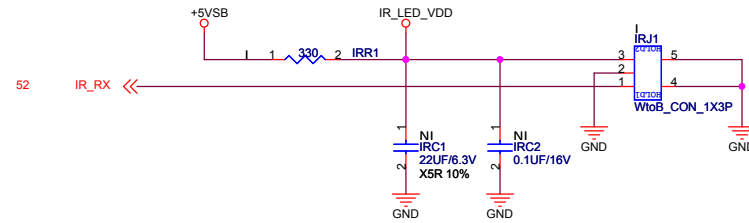












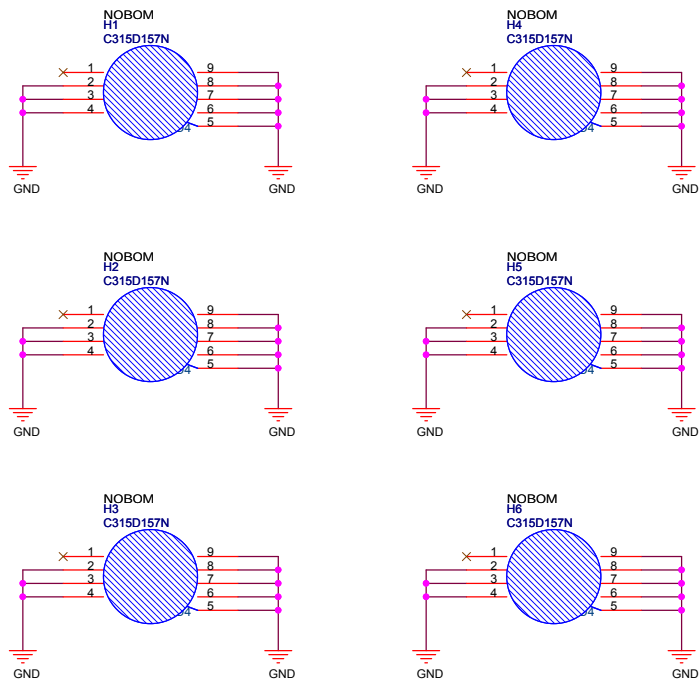
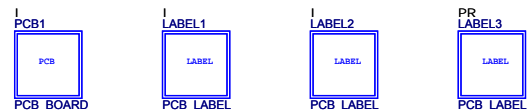
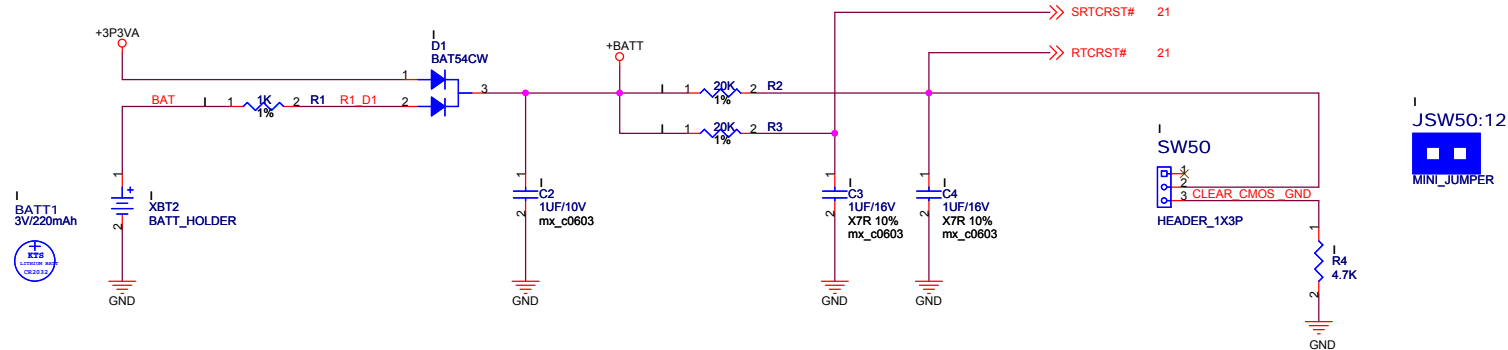
PEGATRON DT-MB RESTRICTED SECRET

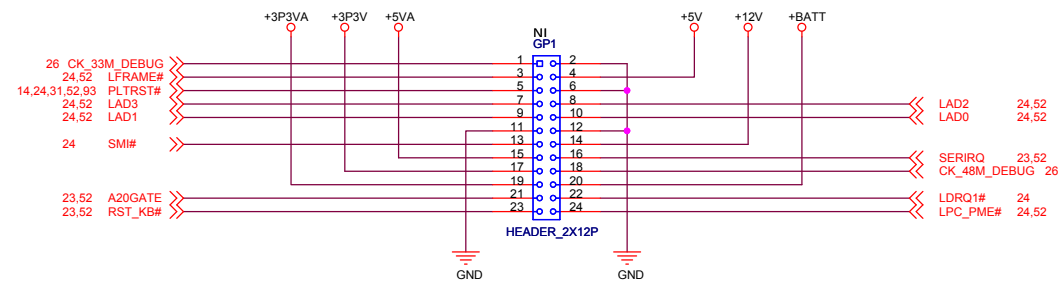
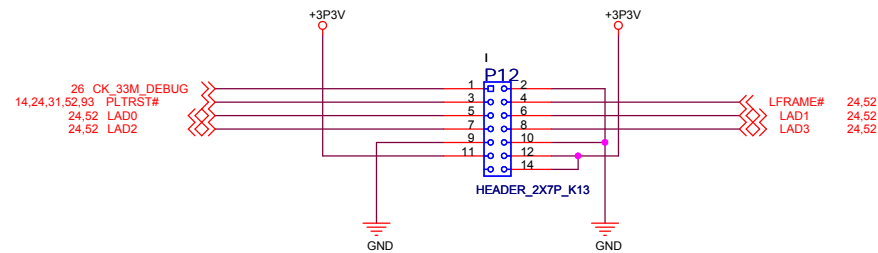
PEGATRON Title : **IR / SPKR**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **DEBUG HEADER**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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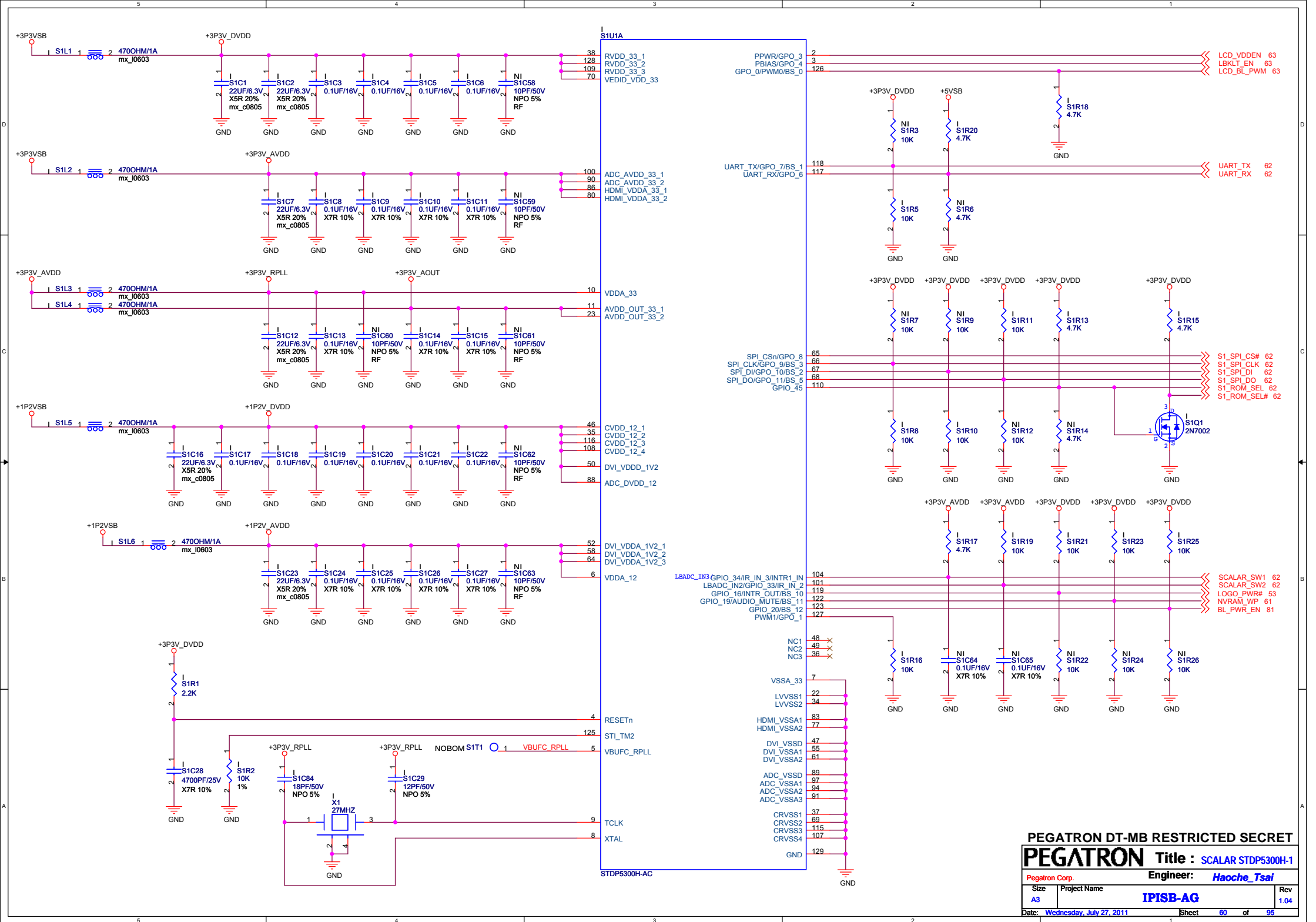
PEGATRON DT-MB RESTRICTED SECRET

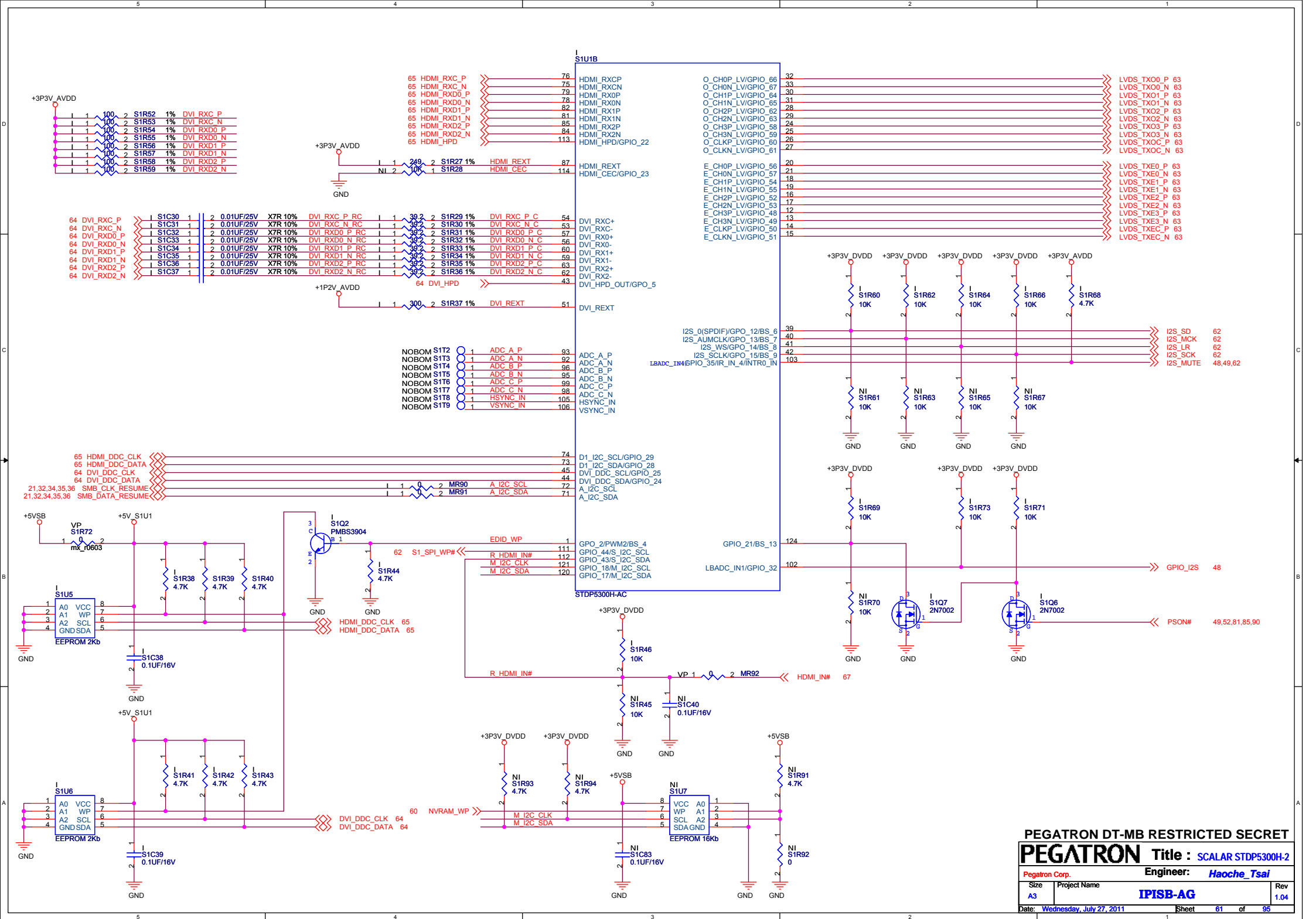
PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Haoche_Tsai**

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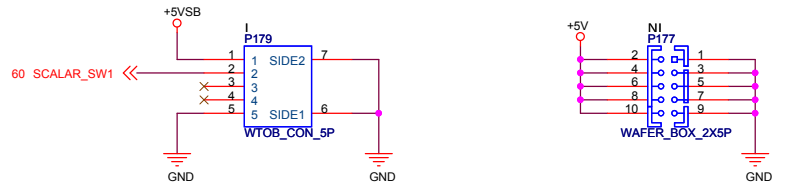
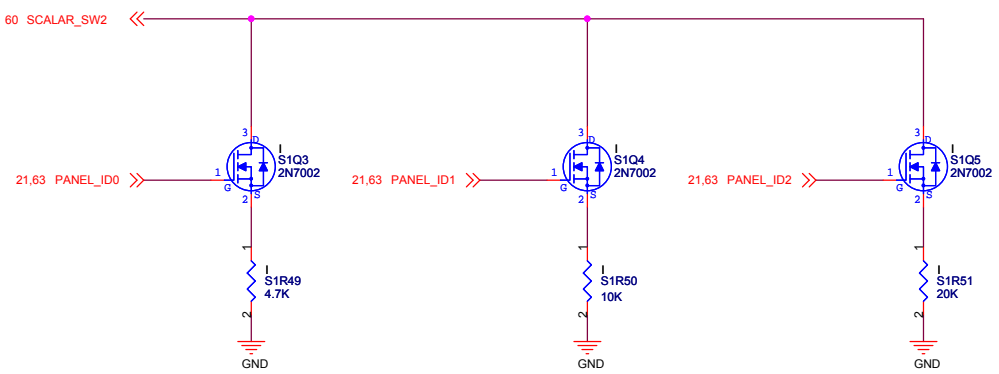
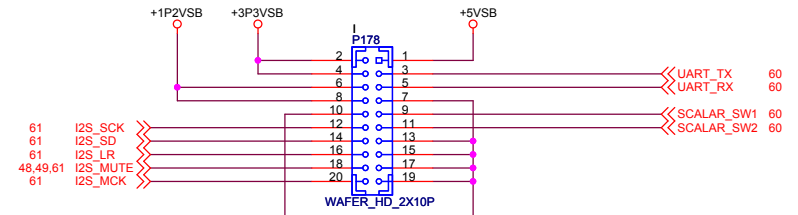
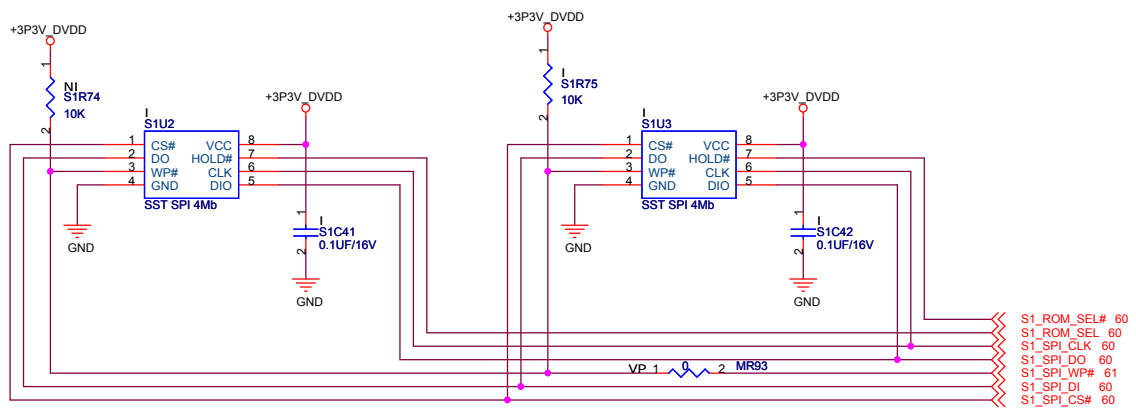
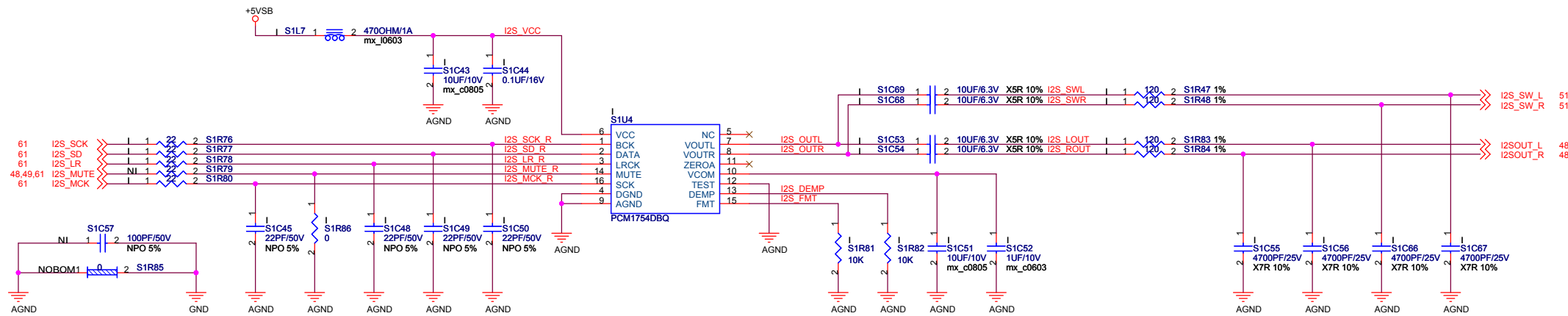
PEGATRON DT-MB RESTRICTED SECRET

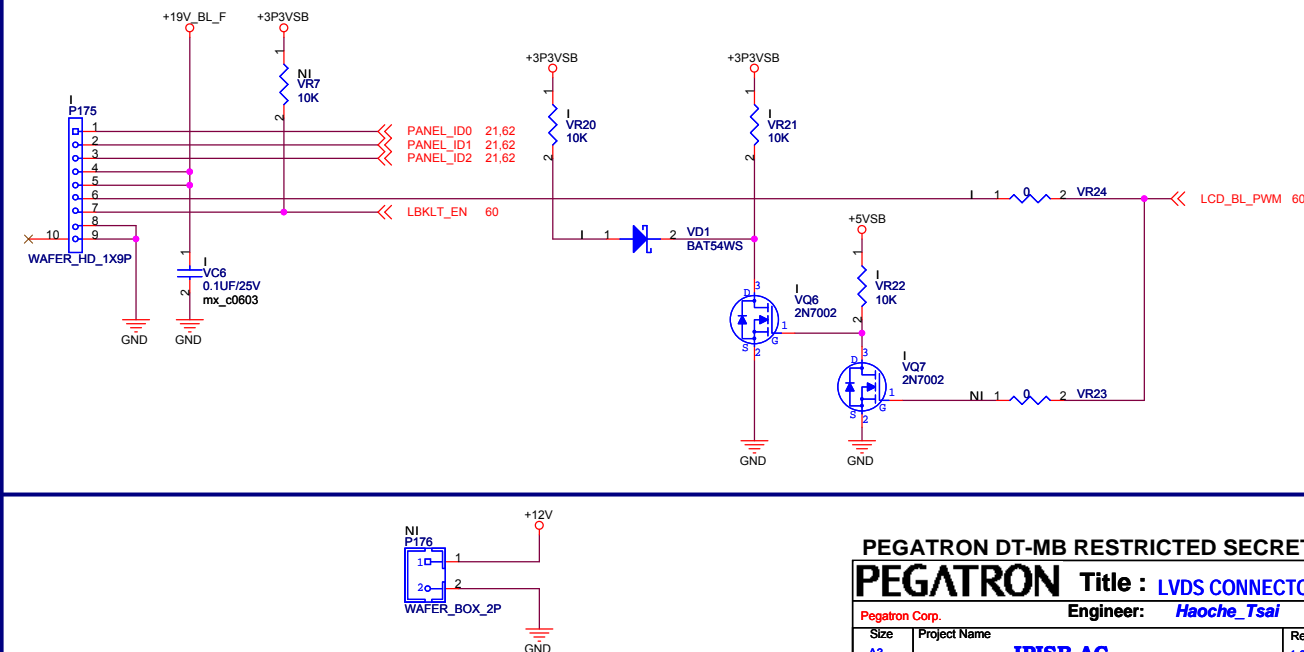
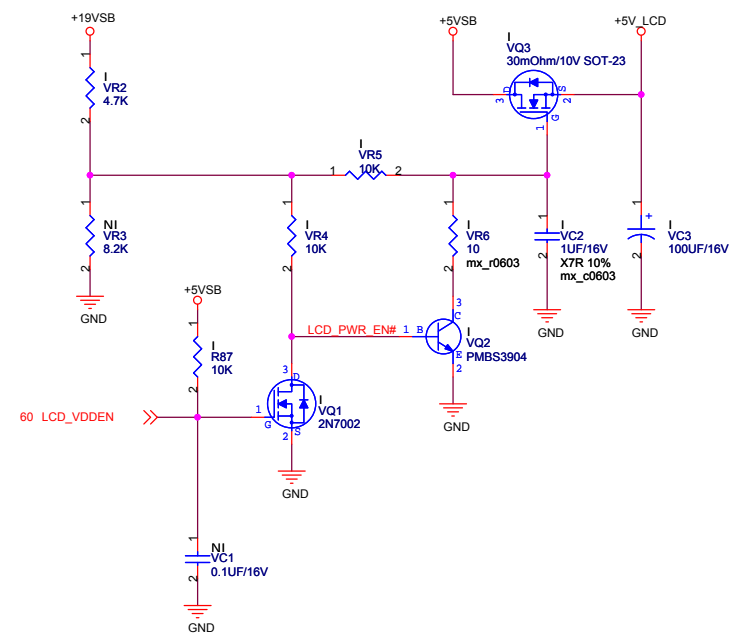
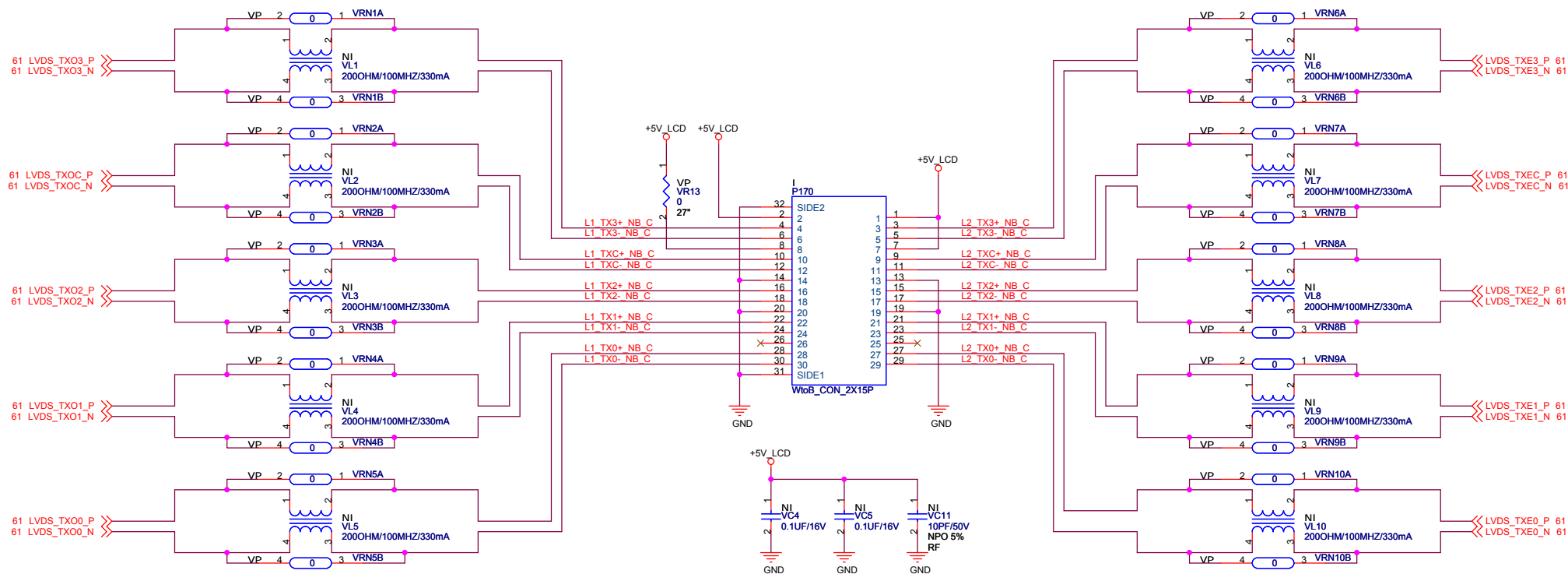
PEGATRON Title : SCALAR STDP5300H-2

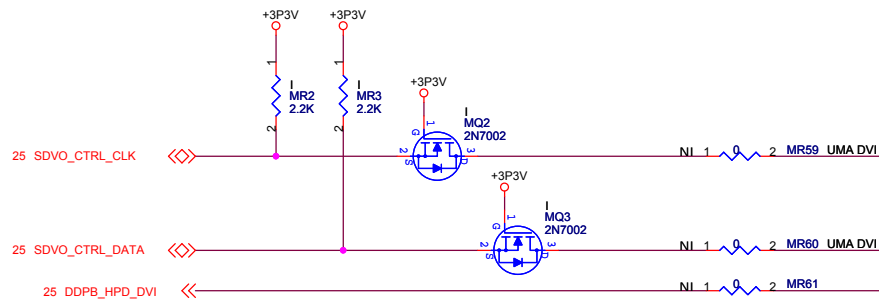
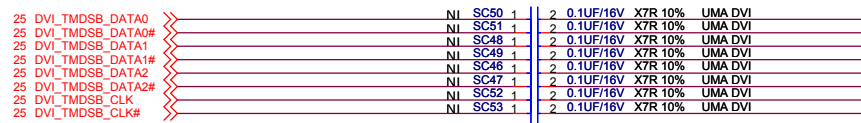
Pegatron Corp. Engineer: Haoche_Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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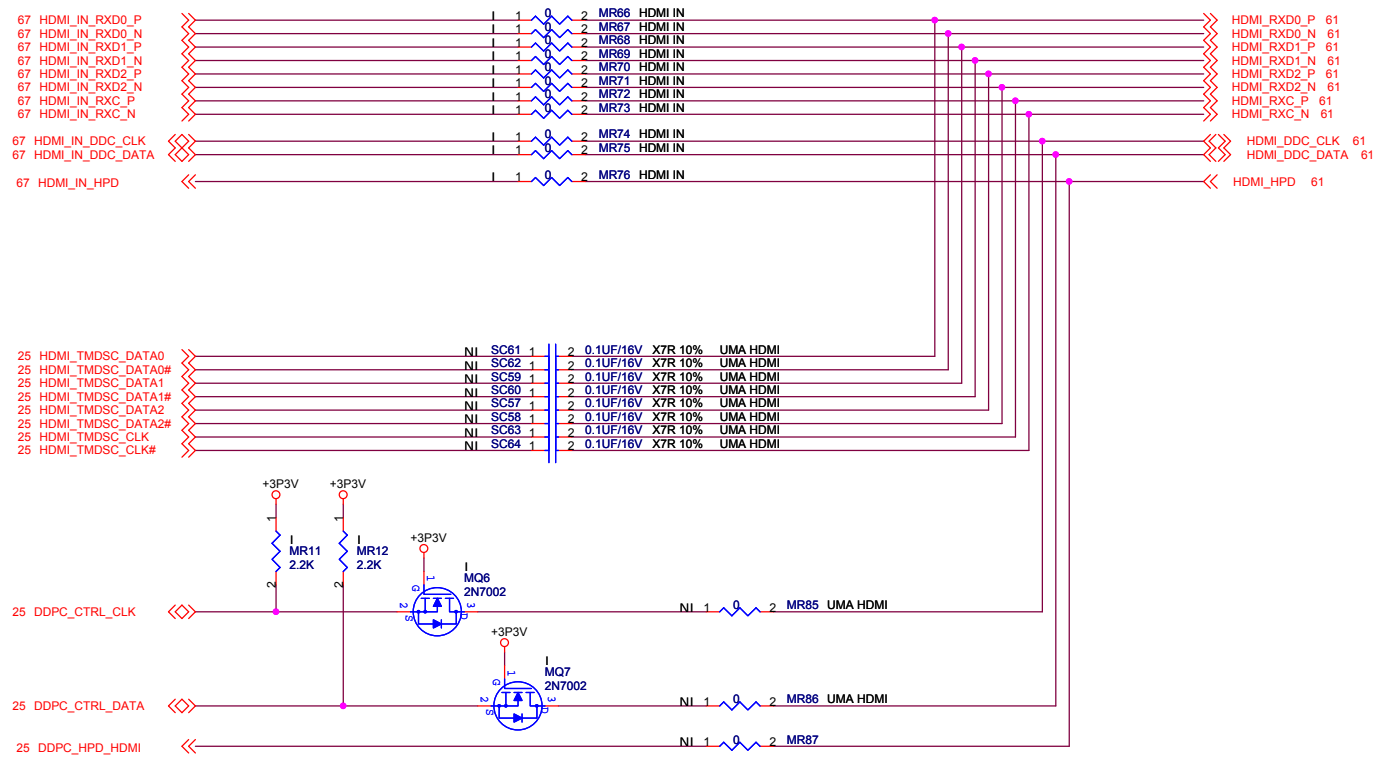


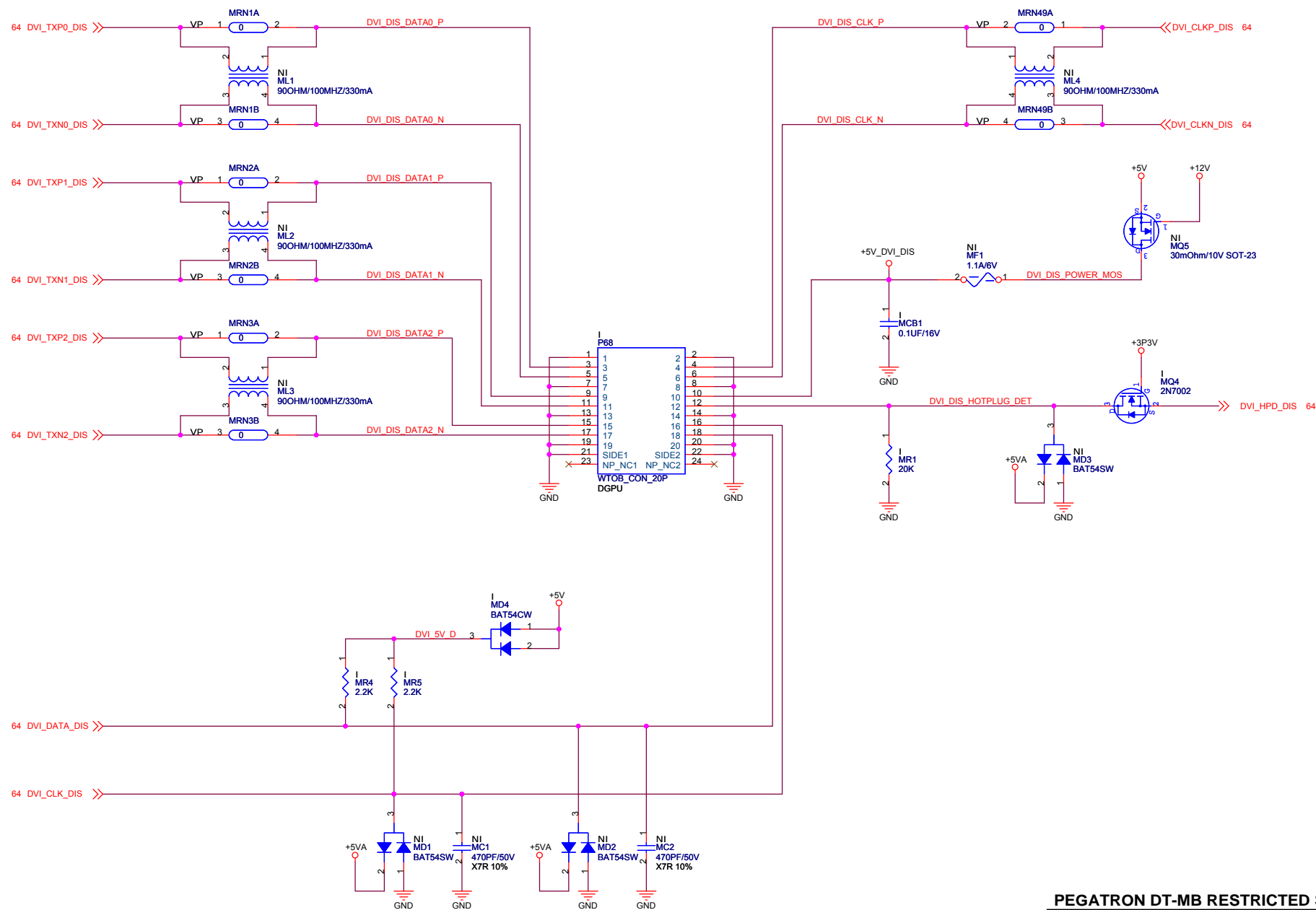


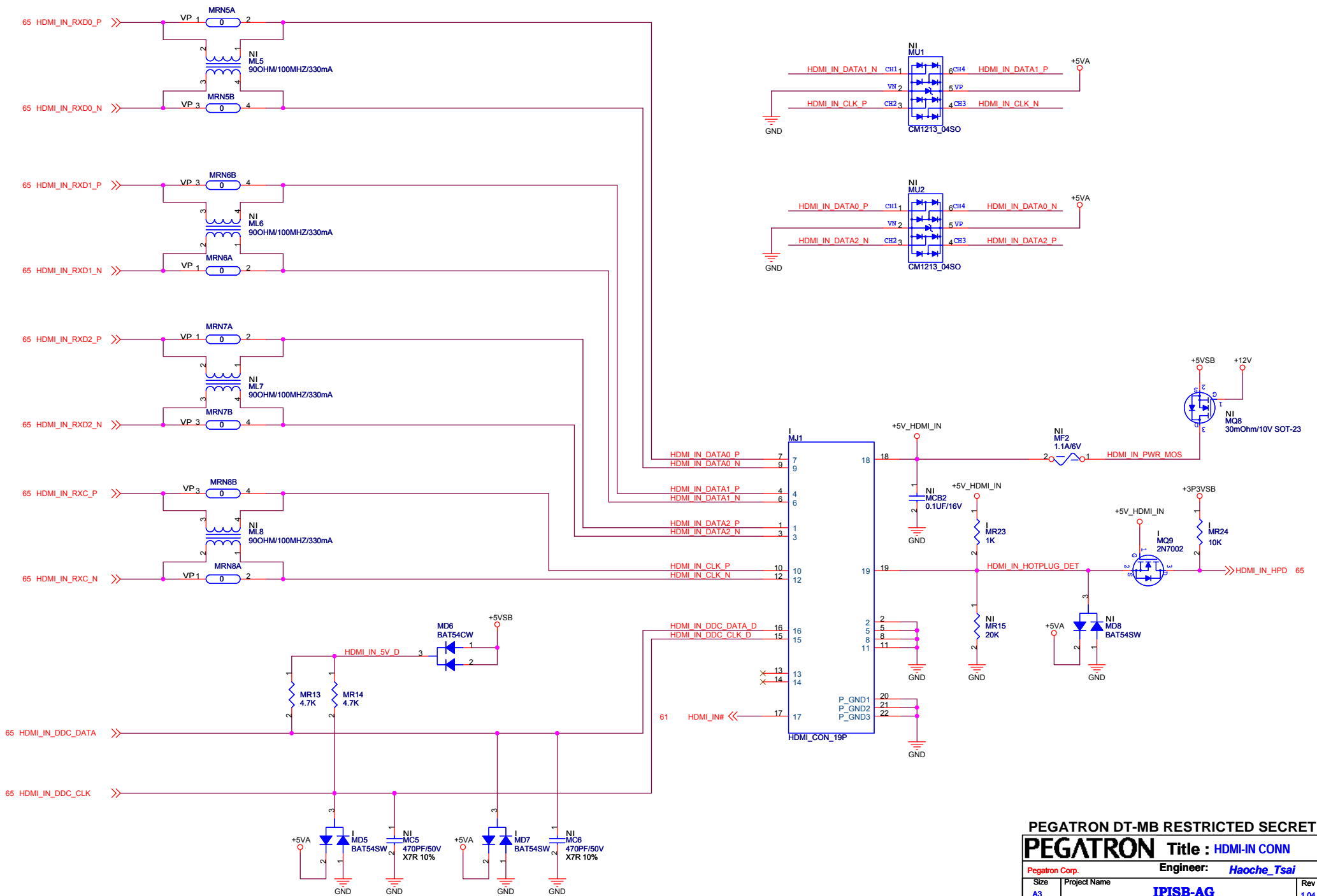
PEGATRON DT-MB RESTRICTED SECRET

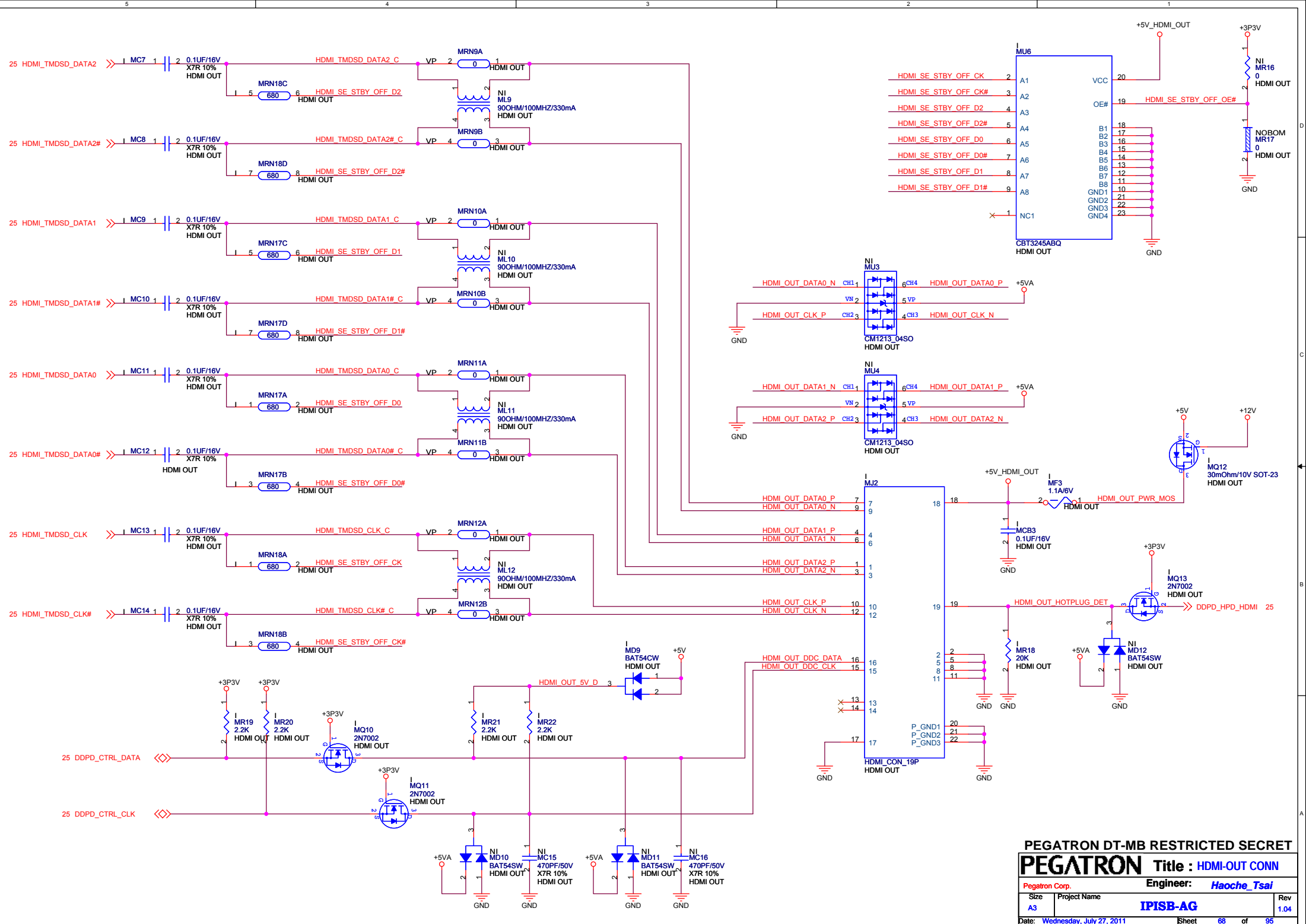
PEGATRON Title : **DVI MUX**
Engineer: **Haoche Tsai**

Size	Project Name	Rev
A3	IPISB-AG	1.04
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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : HDMI-OUT CONN

Pegatron Corp. Engineer: Haoche_Tsai

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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : HDMI CONNECTOR

Pegatron Corp. Engineer: Haoche_Tsai

Size A3	Project Name IPISB-AG	Rev 1.04
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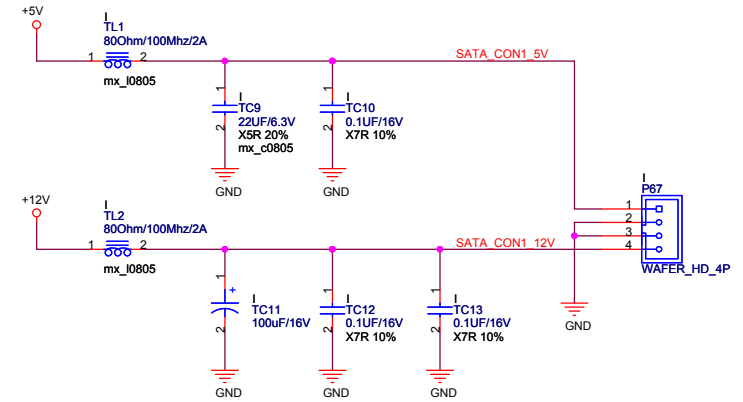
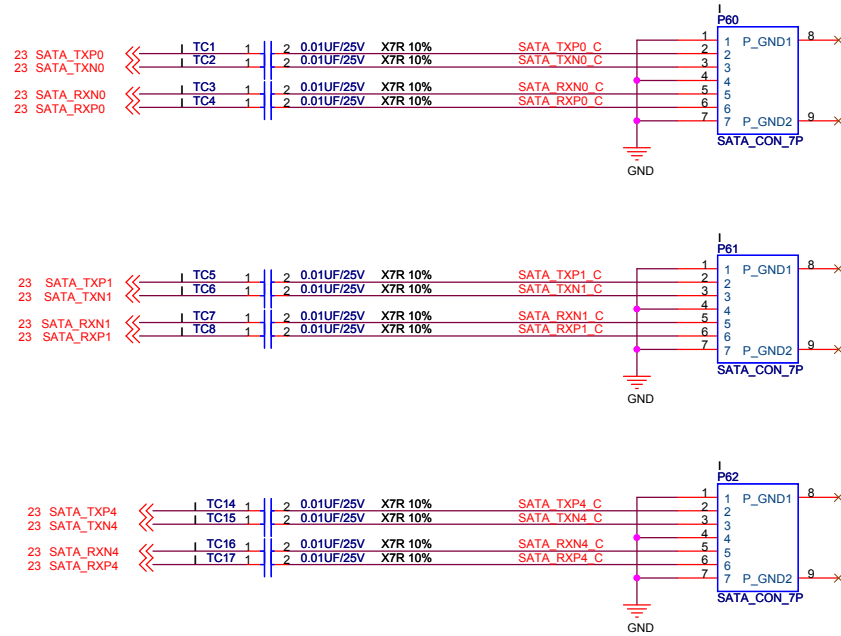


PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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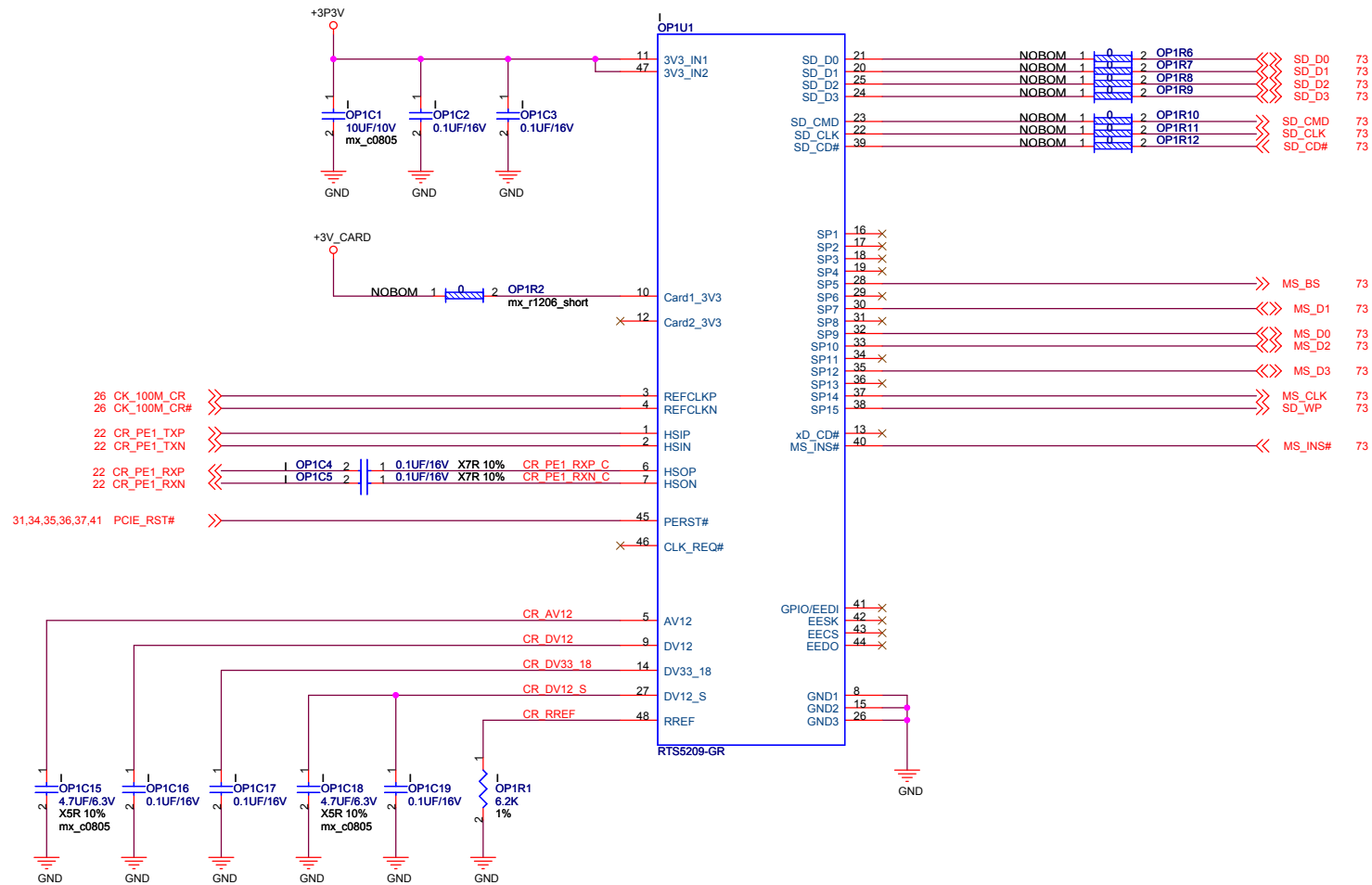
PEGATRON DT-MB RESTRICTED SECRET

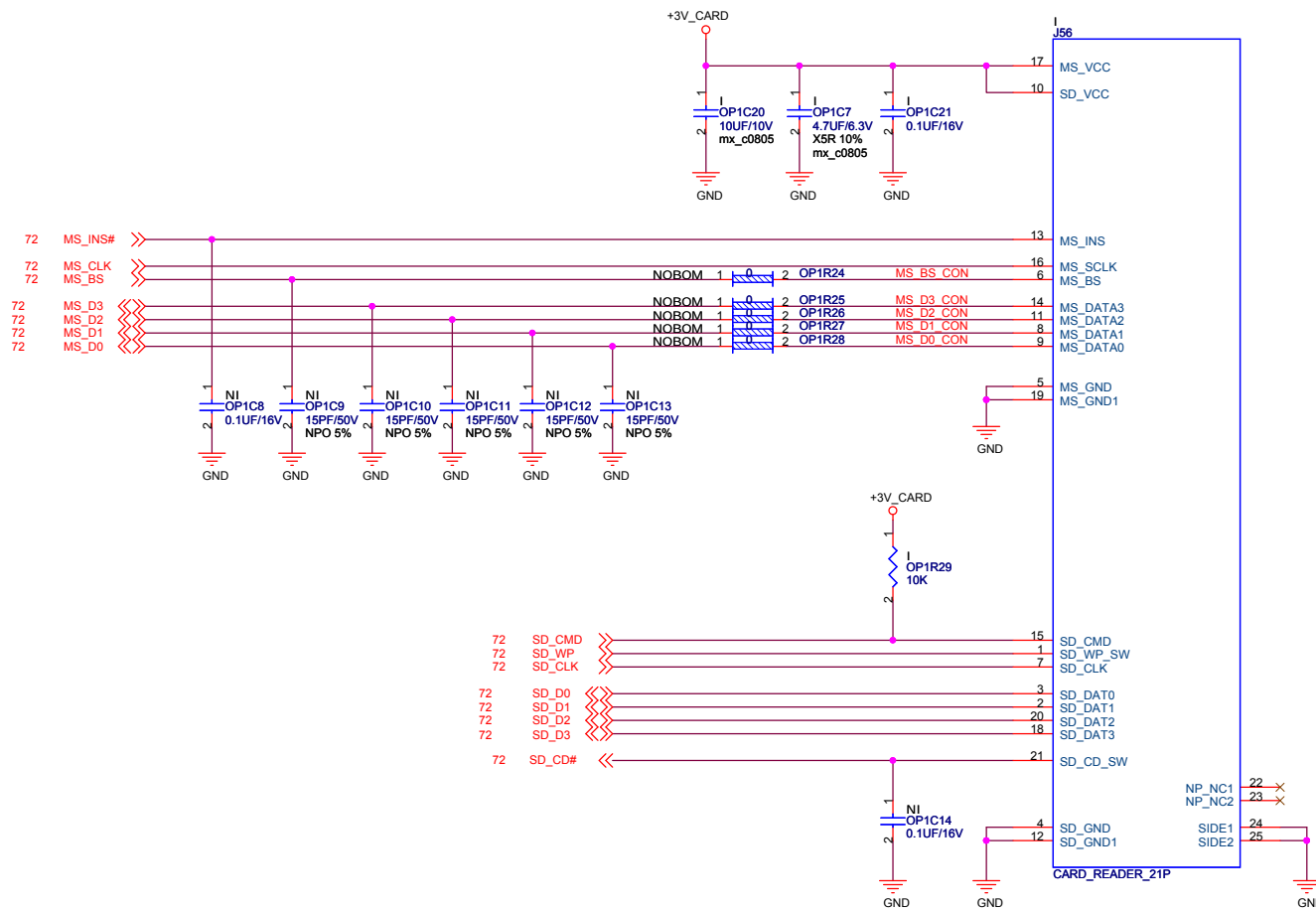
PEGATRON Title : **SATA CONNECTOR**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size	Project Name	Rev
A3	IPISB-AG	1.04

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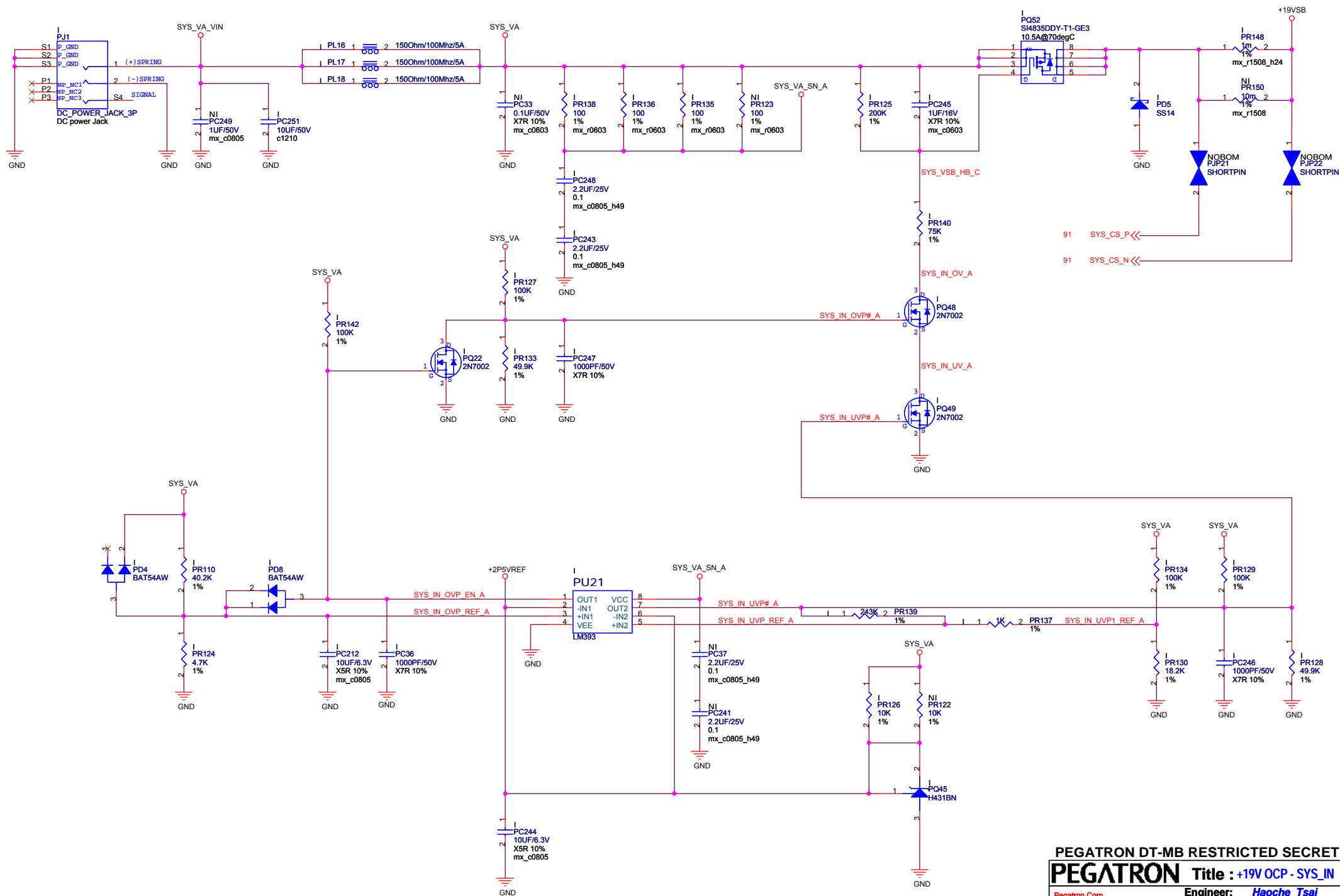


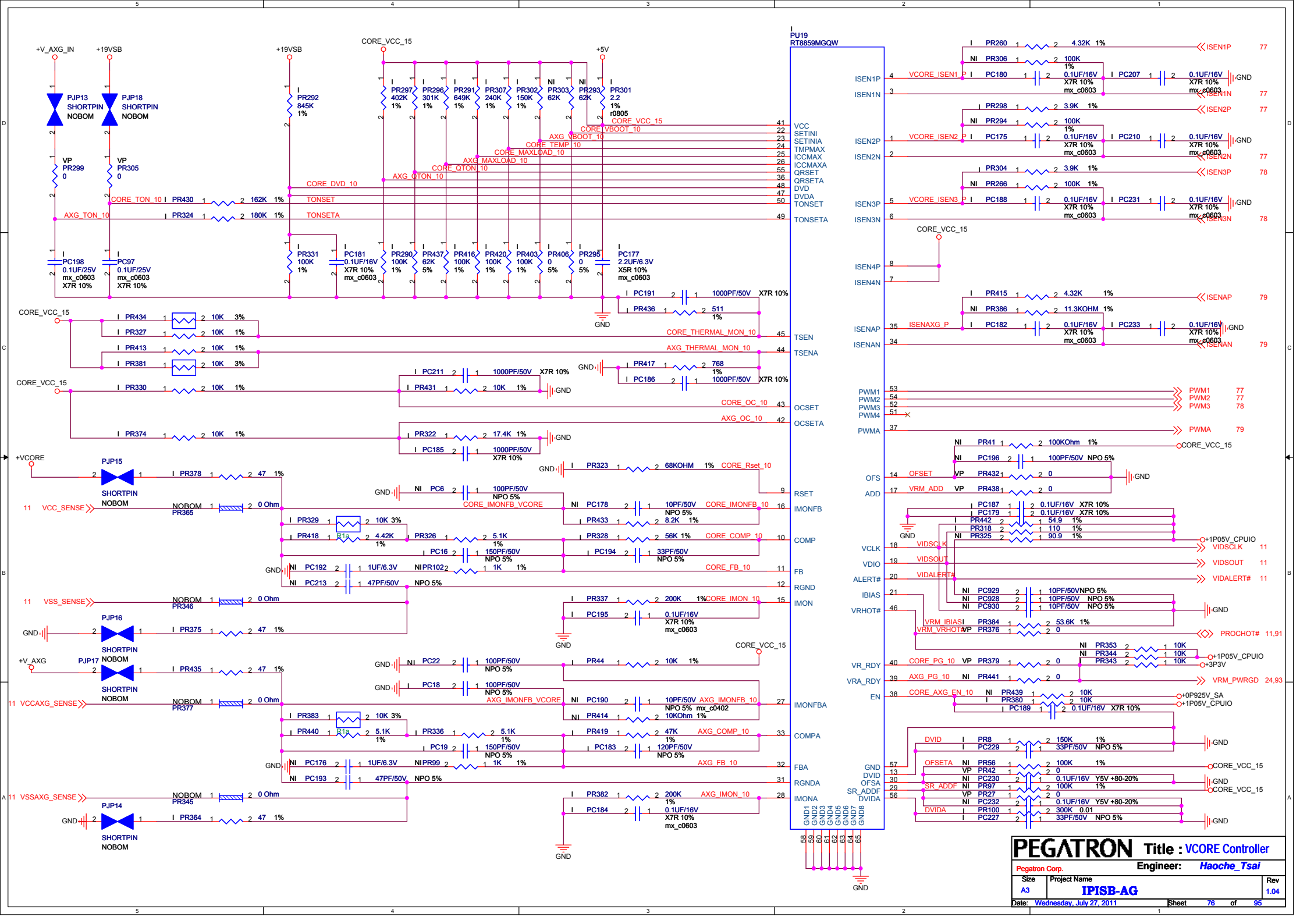
PEGATRON DT-MB RESTRICTED SECRET

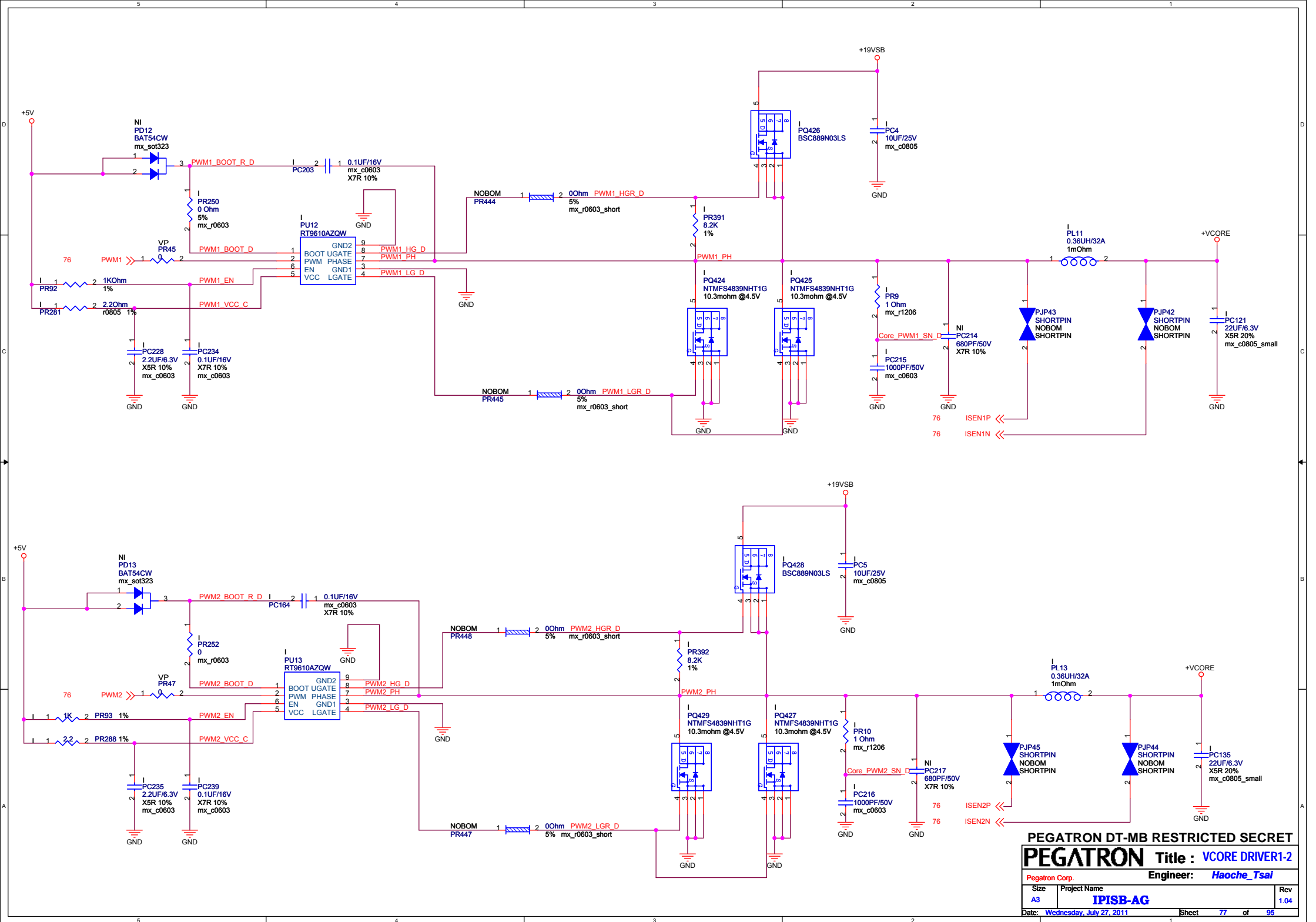
PEGATRON Title : **XXXX**

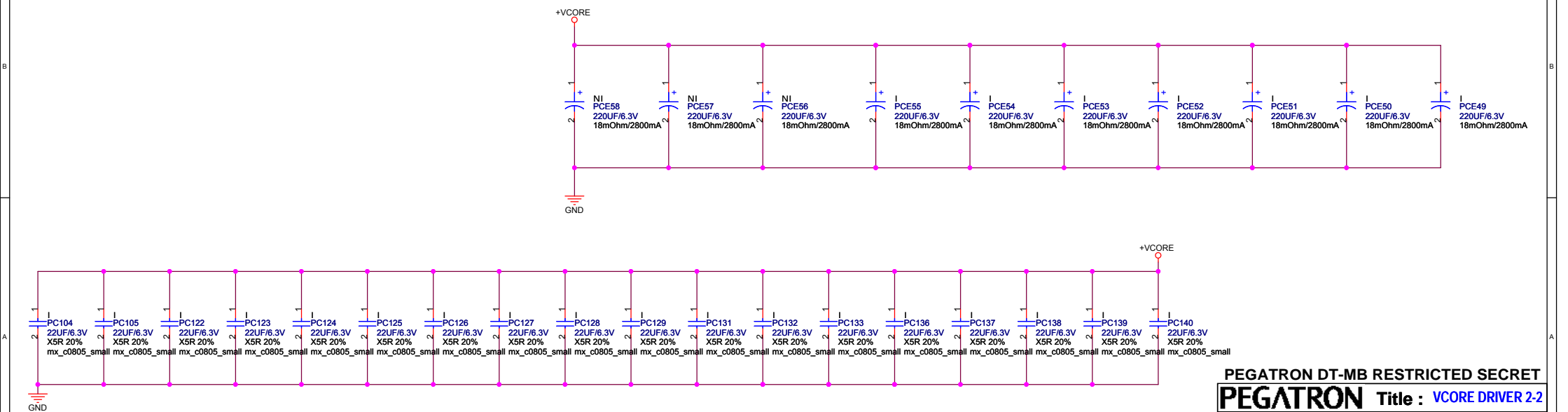
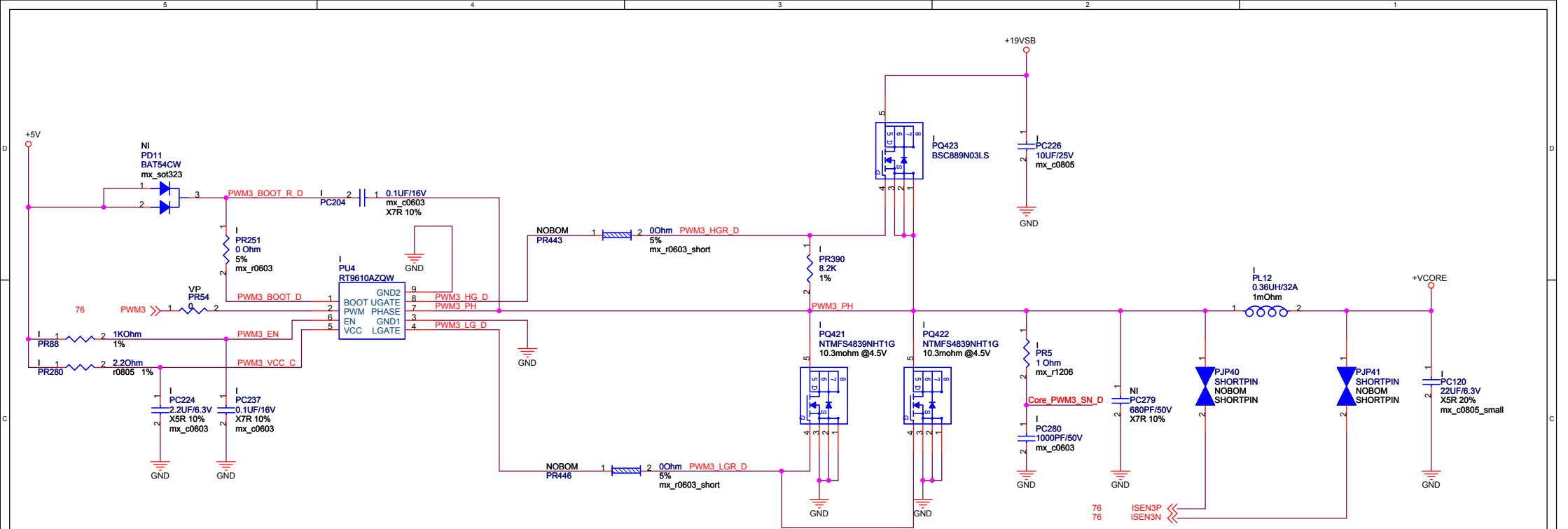
Pegatron Corp. Engineer: **Haoche_Tsai**

Size	Project Name	Rev
A3	IPISB-AG	1.04









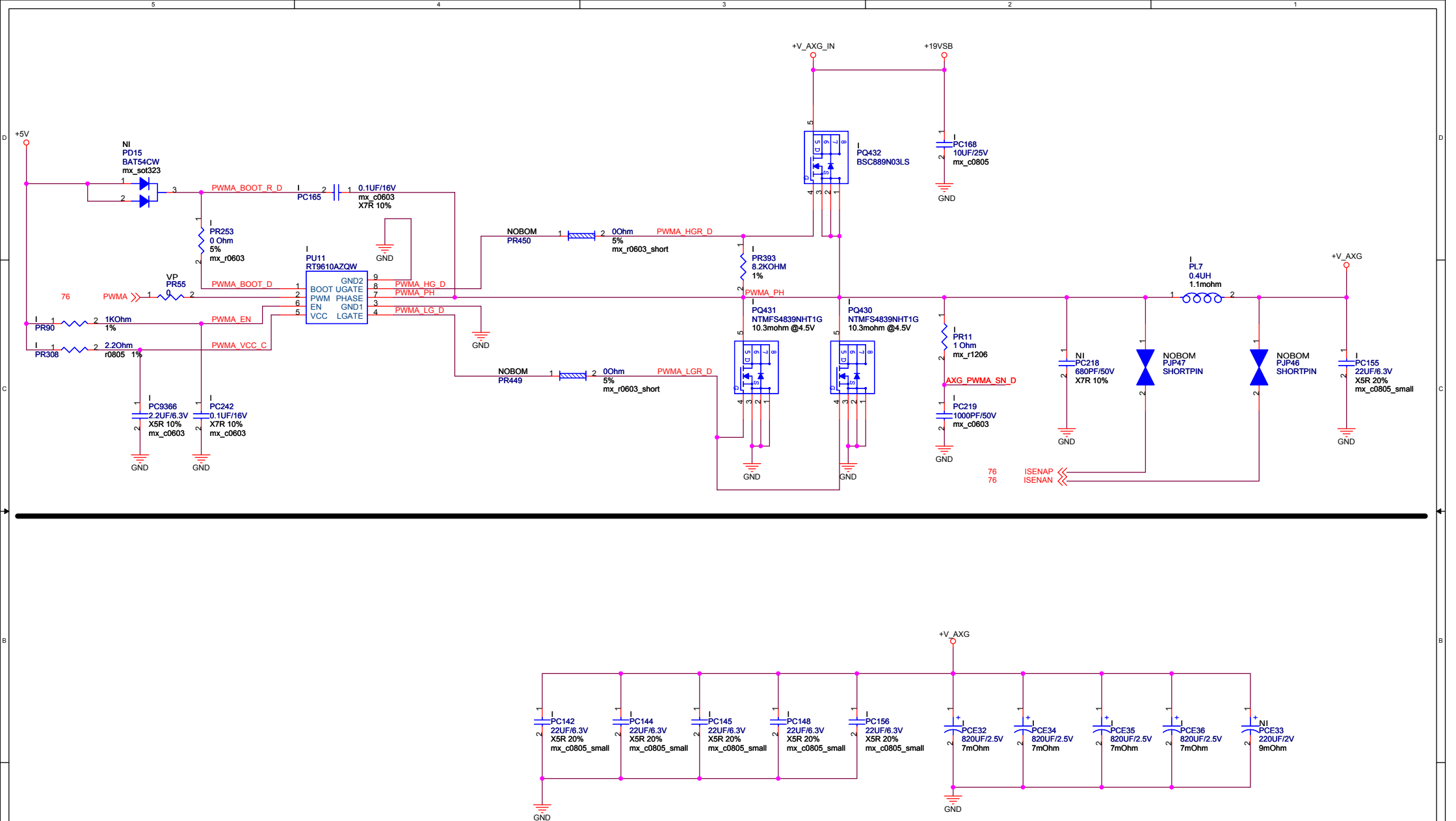
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : VCORE DRIVER 2-2

Pegatron Corp. Engineer: Haoche Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : +V_AXG DRIVER

Pegatron Corp. Engineer: Haoche Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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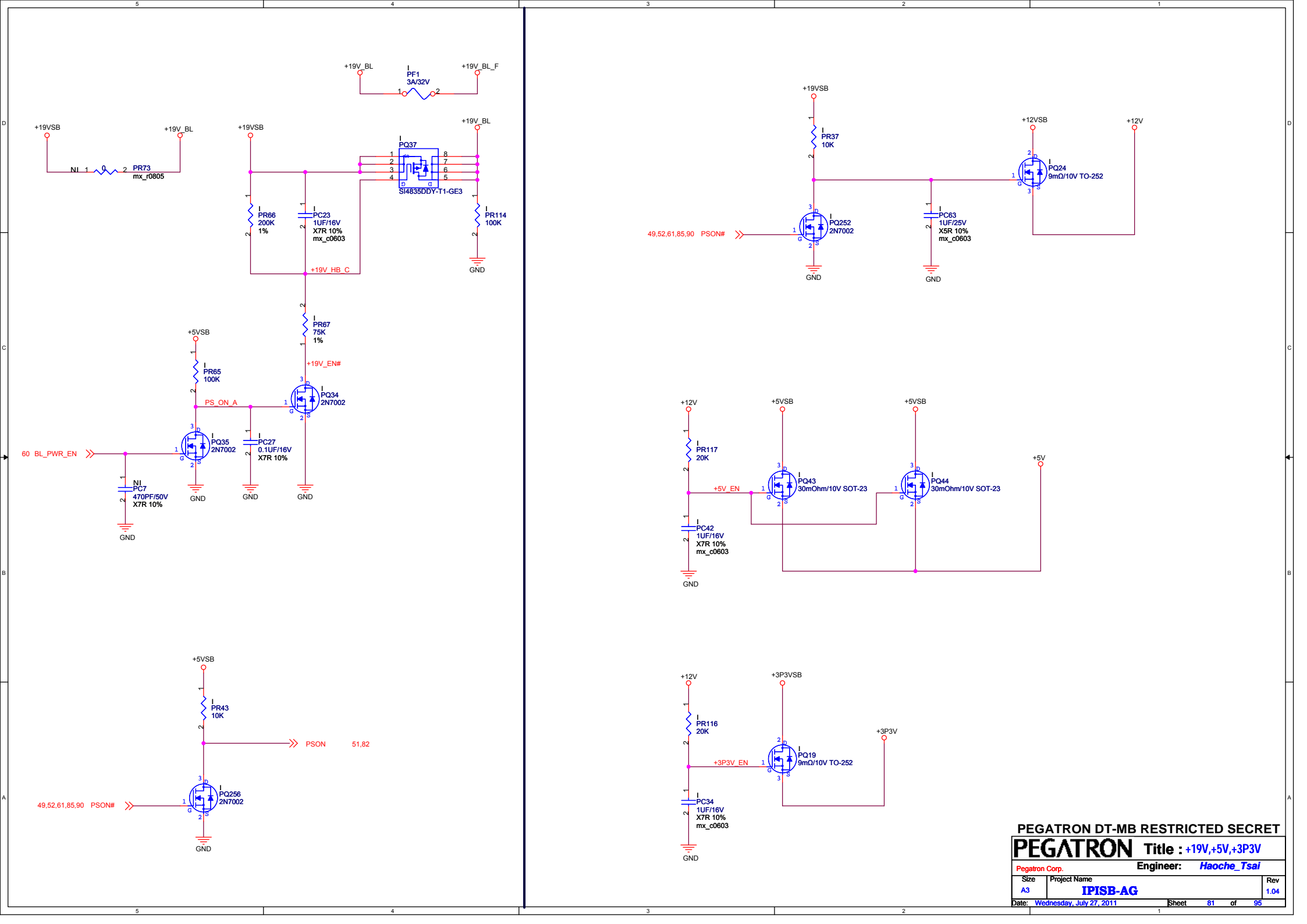
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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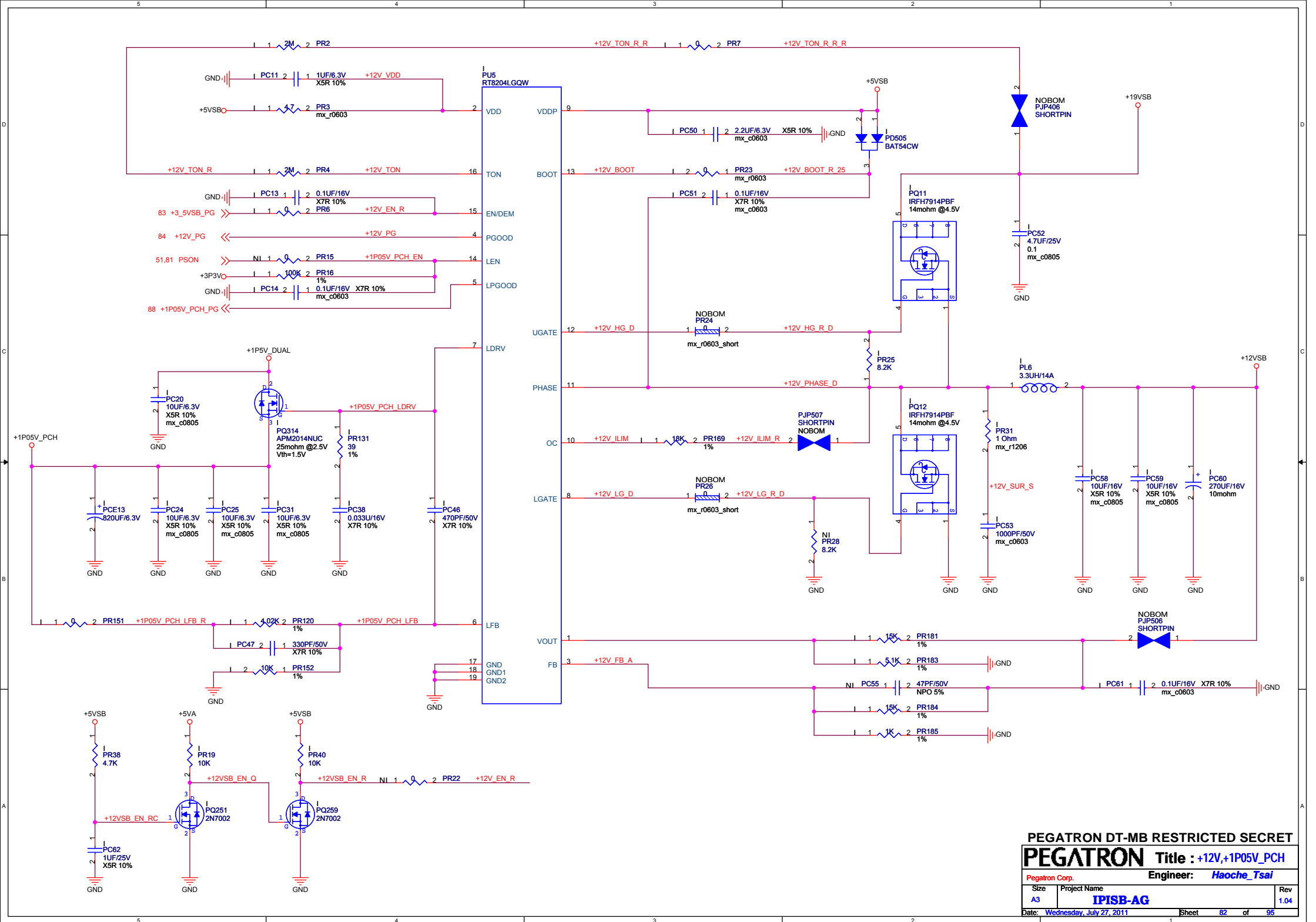
PEGATRON DT-MB RESTRICTED SECRET

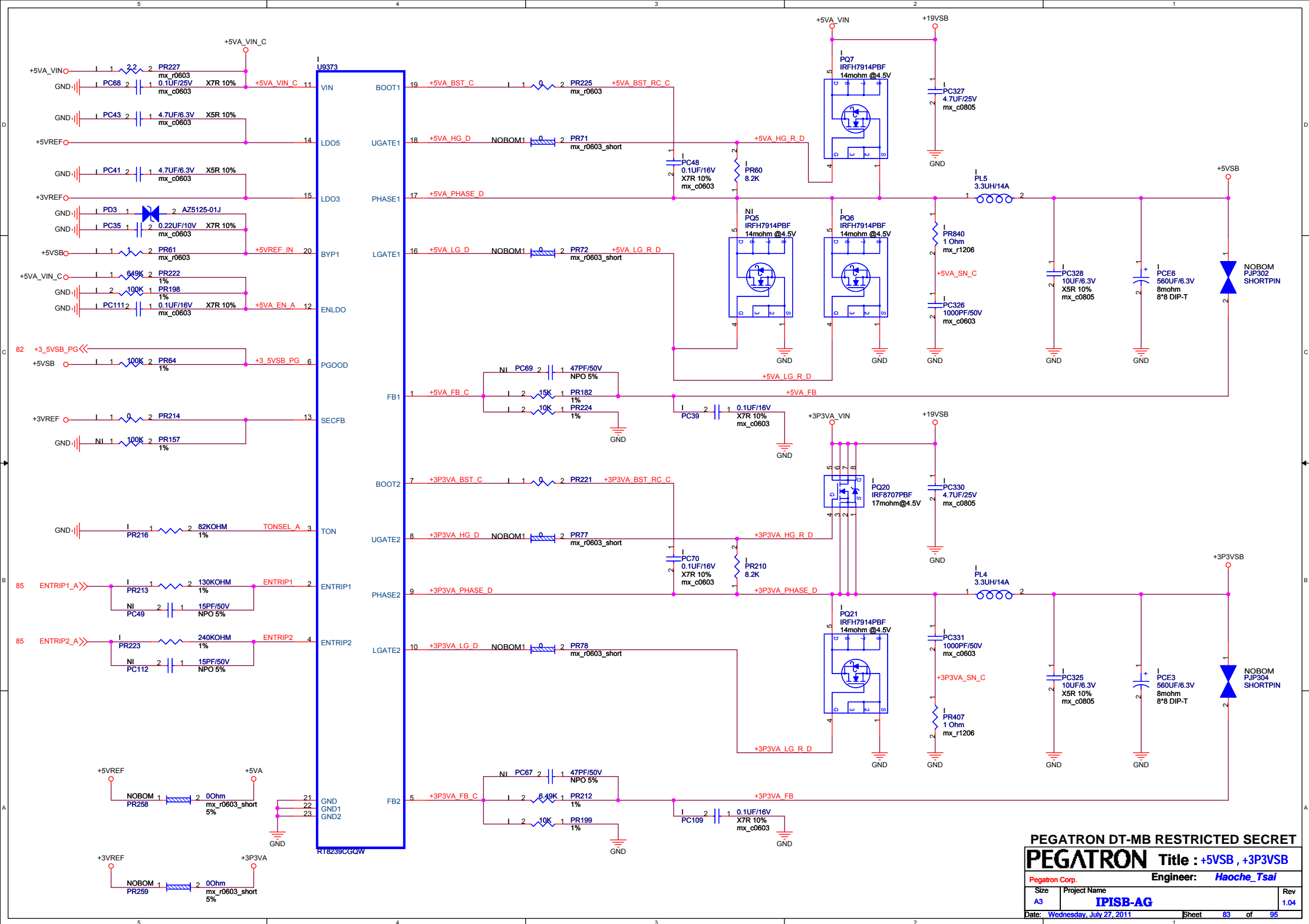
PEGATRON Title : +19V,+5V,+3P3V

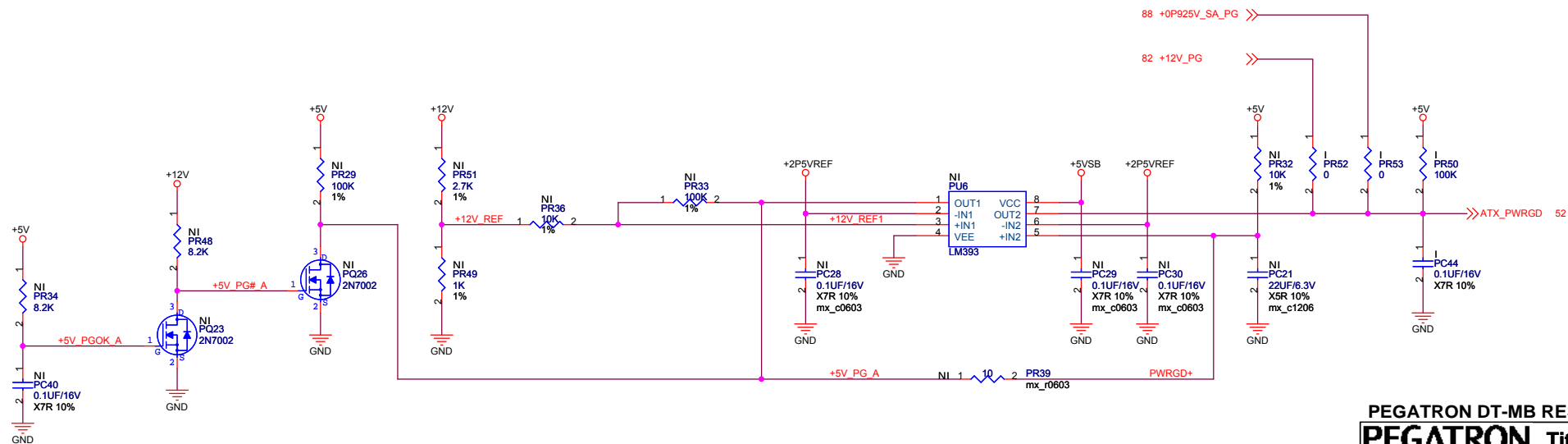
Pegatron Corp. Engineer: Haoche_Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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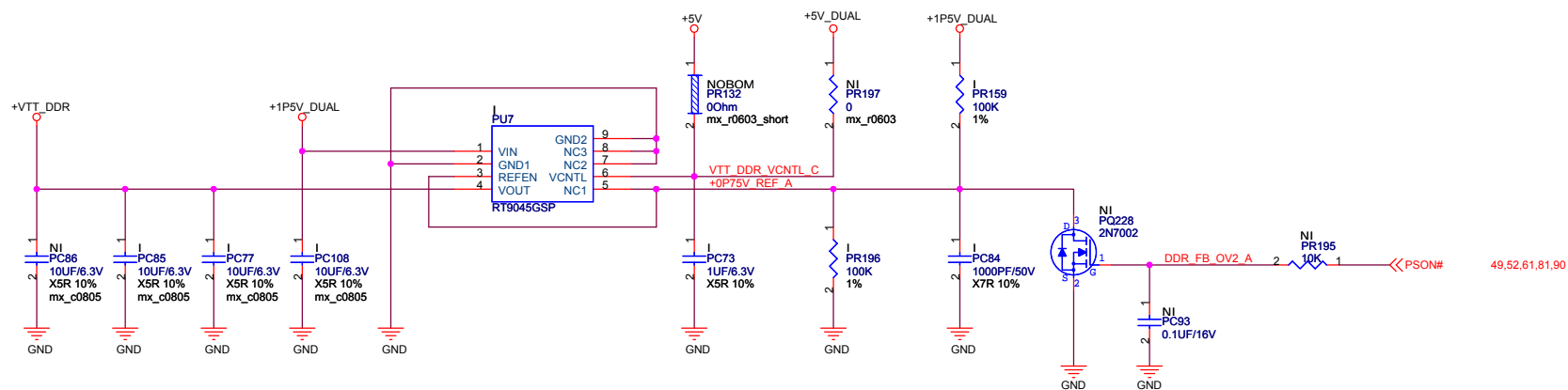
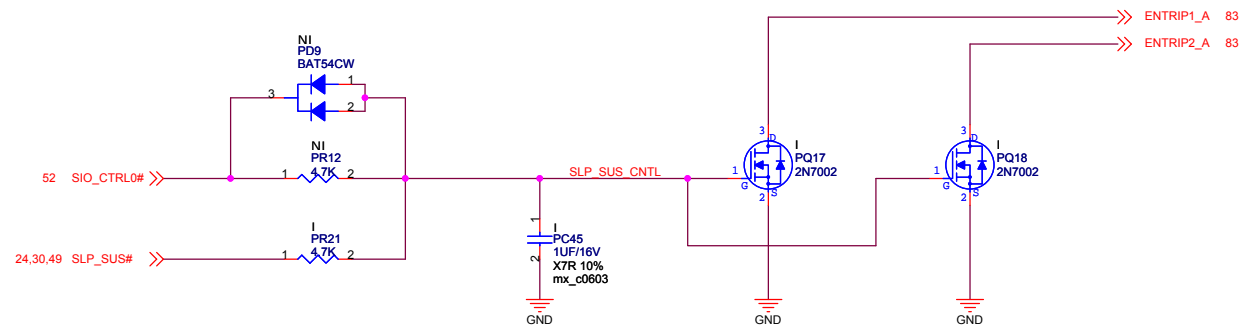
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : +5V_DUAL

Pegatron Corp. Engineer: Haoche_Tsai

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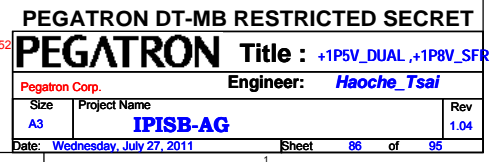
PEGATRON DT-MB RESTRICTED SECRET

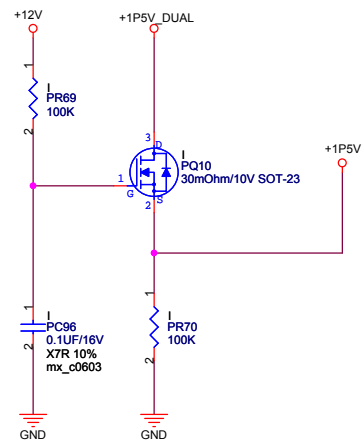
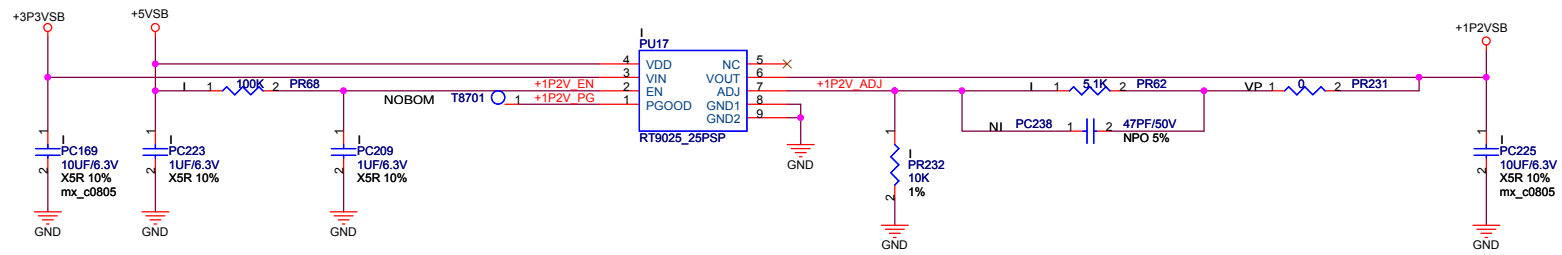
PEGATRON Title : +VTT_DDR

Pegatron Corp. Engineer: Haoche Tsai

Size A3	Project Name IPISB-AG	Rev 1.04
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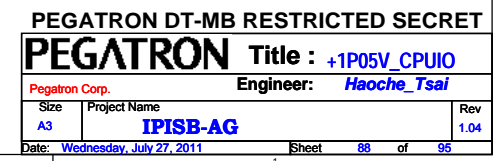
PEGATRON DT-MB RESTRICTED SECRET

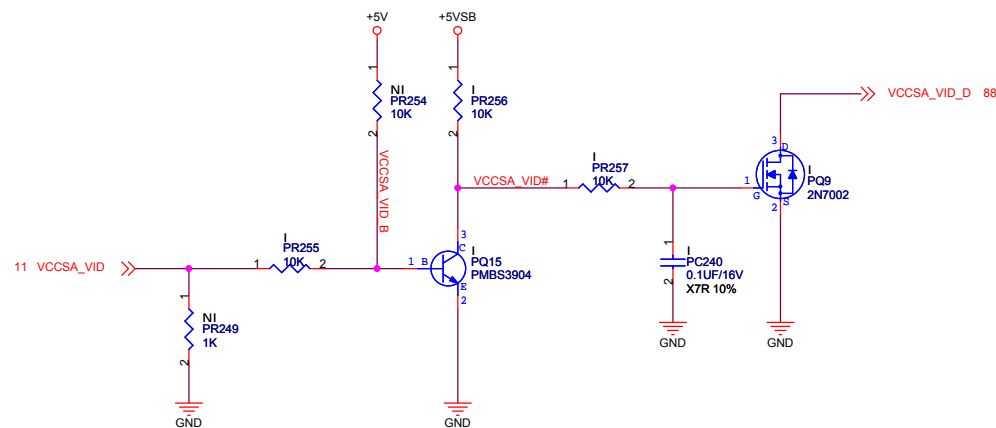
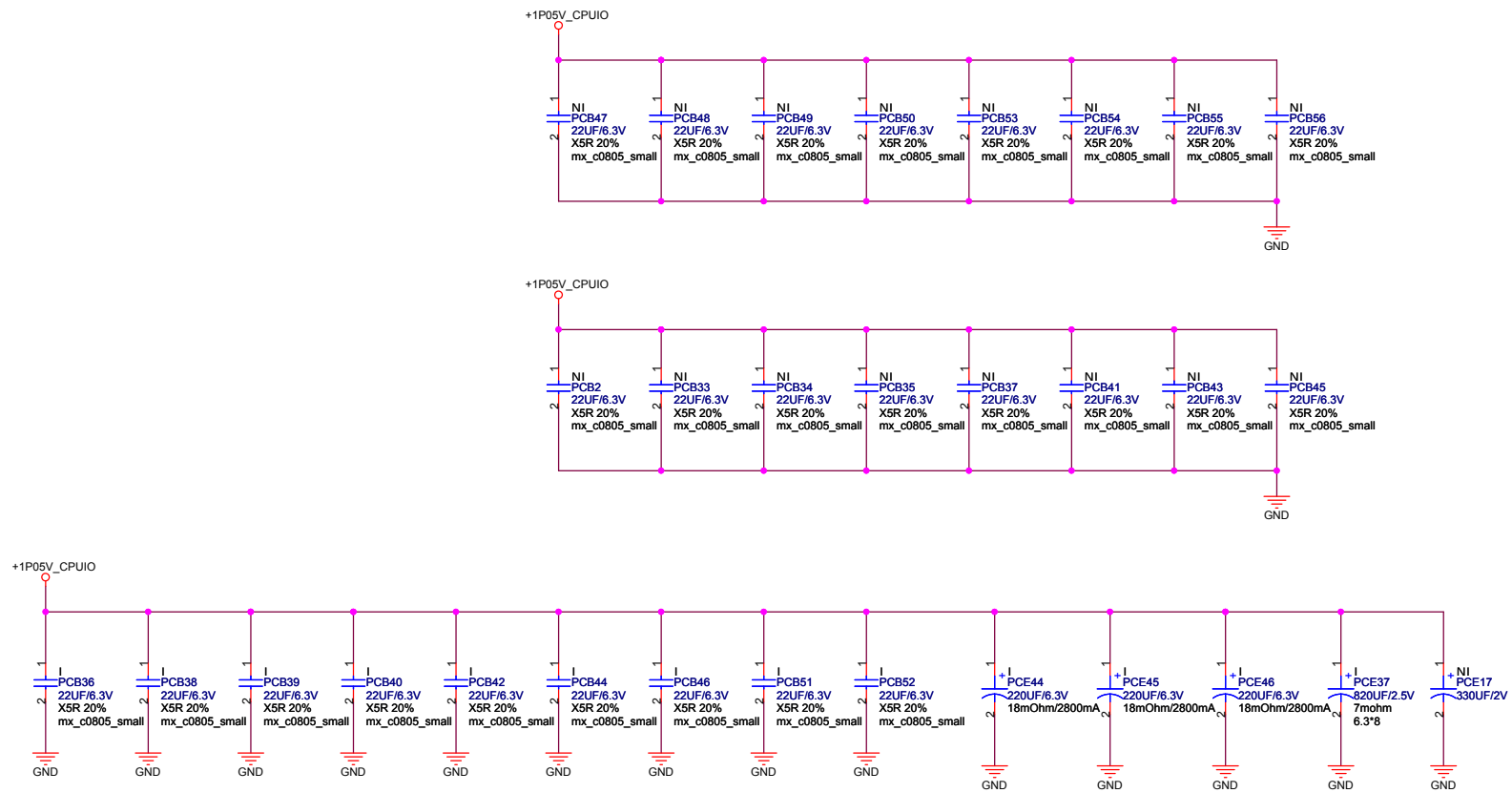
PEGATRON Title : +1P2V , +1P5V

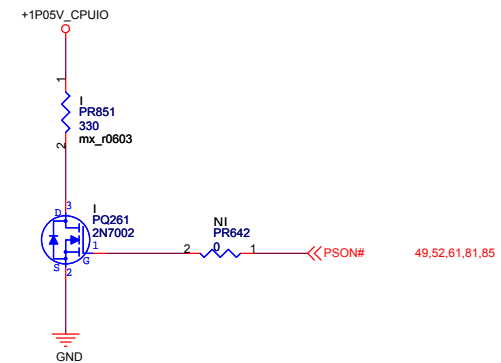
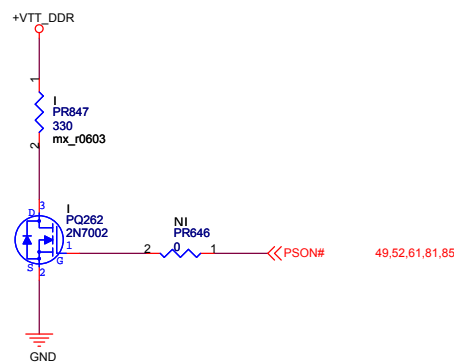
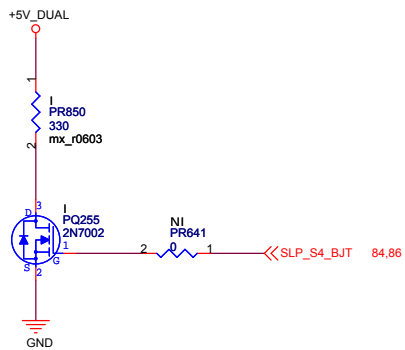
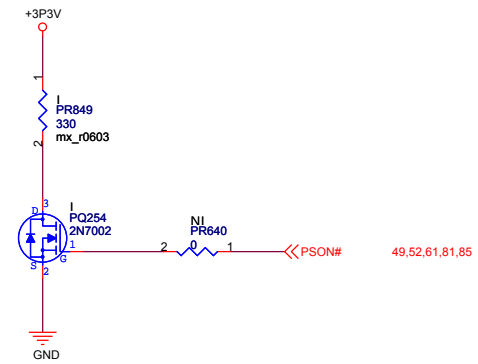
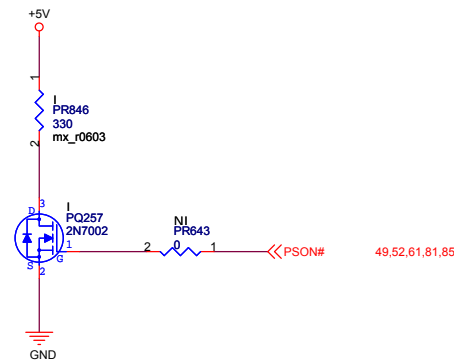
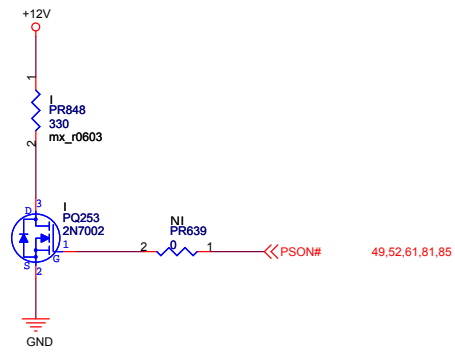
Pegatron Corp. Engineer: Haoche_Tsai

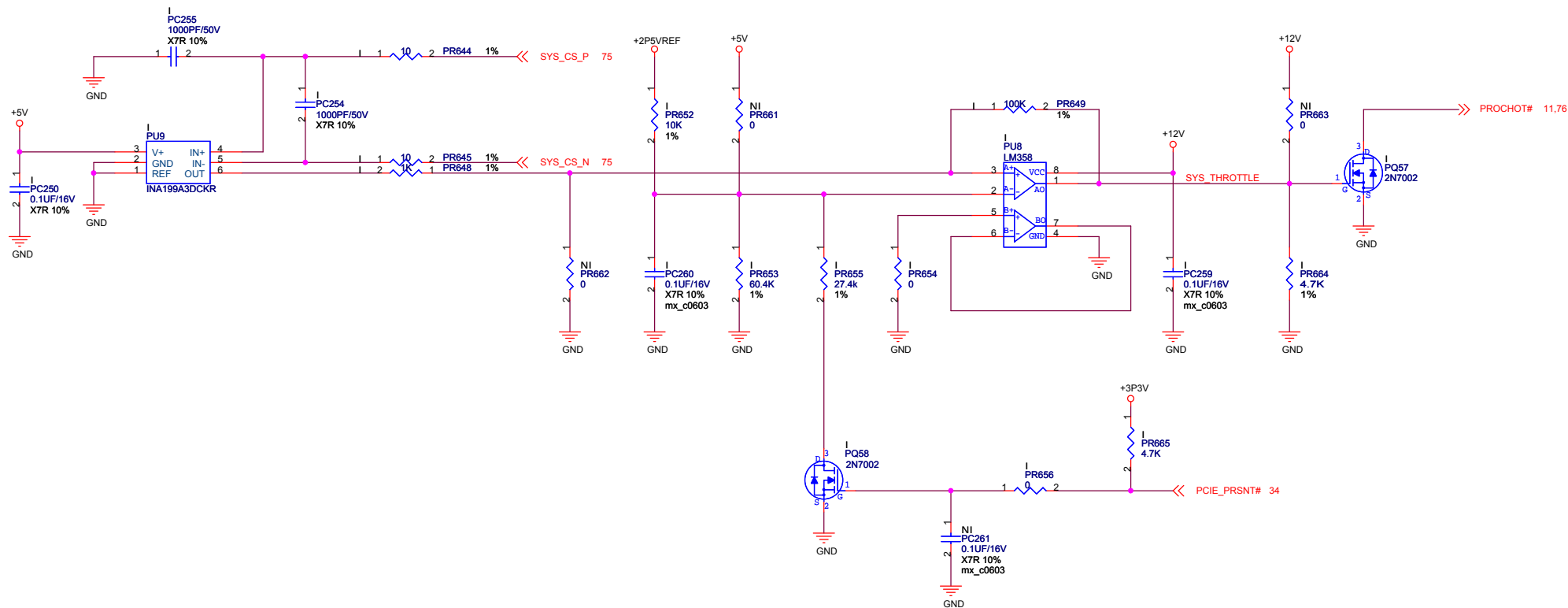
Size A3	Project Name IPISB-AG	Rev 1.04
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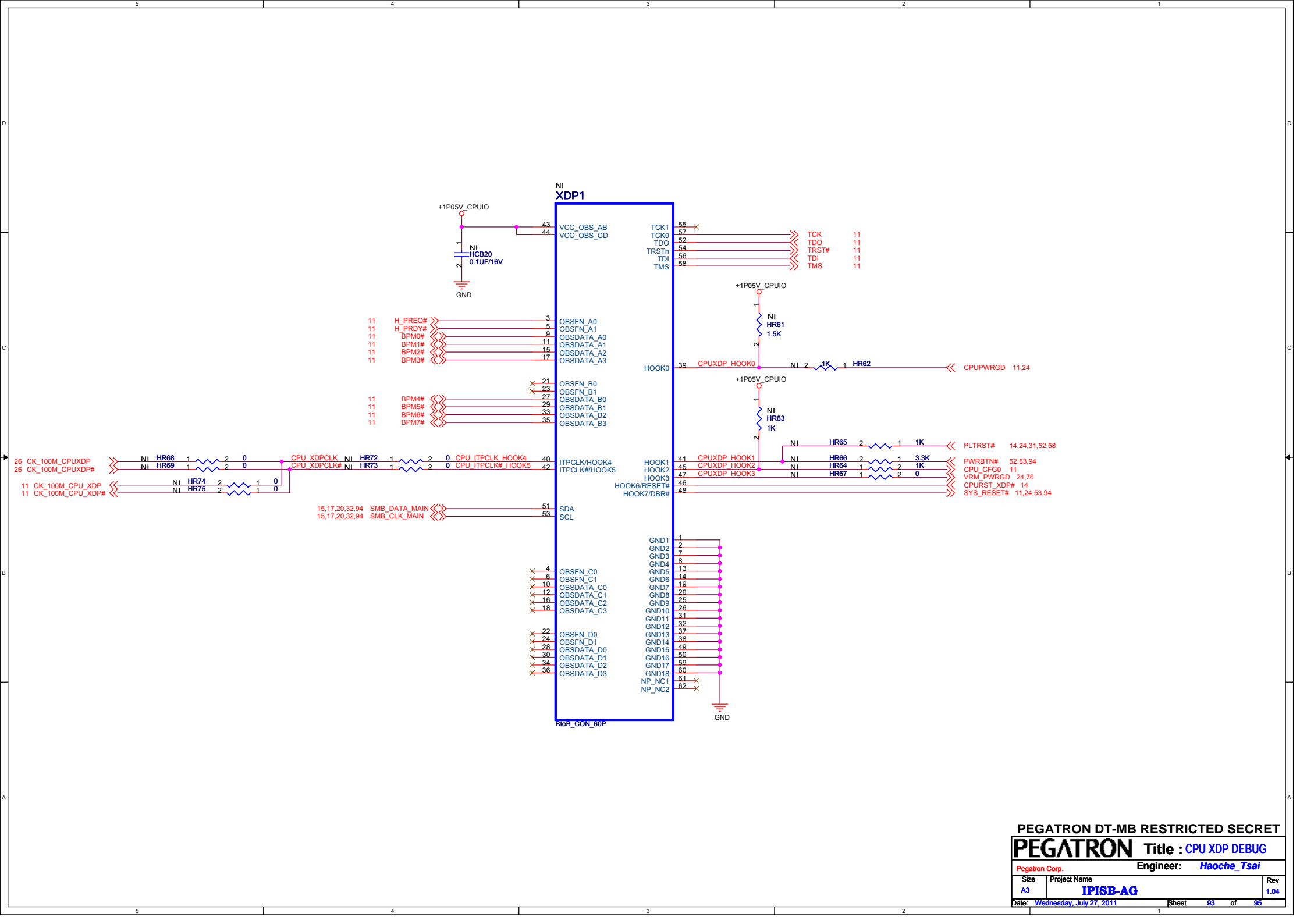
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Haoche_Tsai**

Size A3	Project Name IPISB-AG	Rev 1.04
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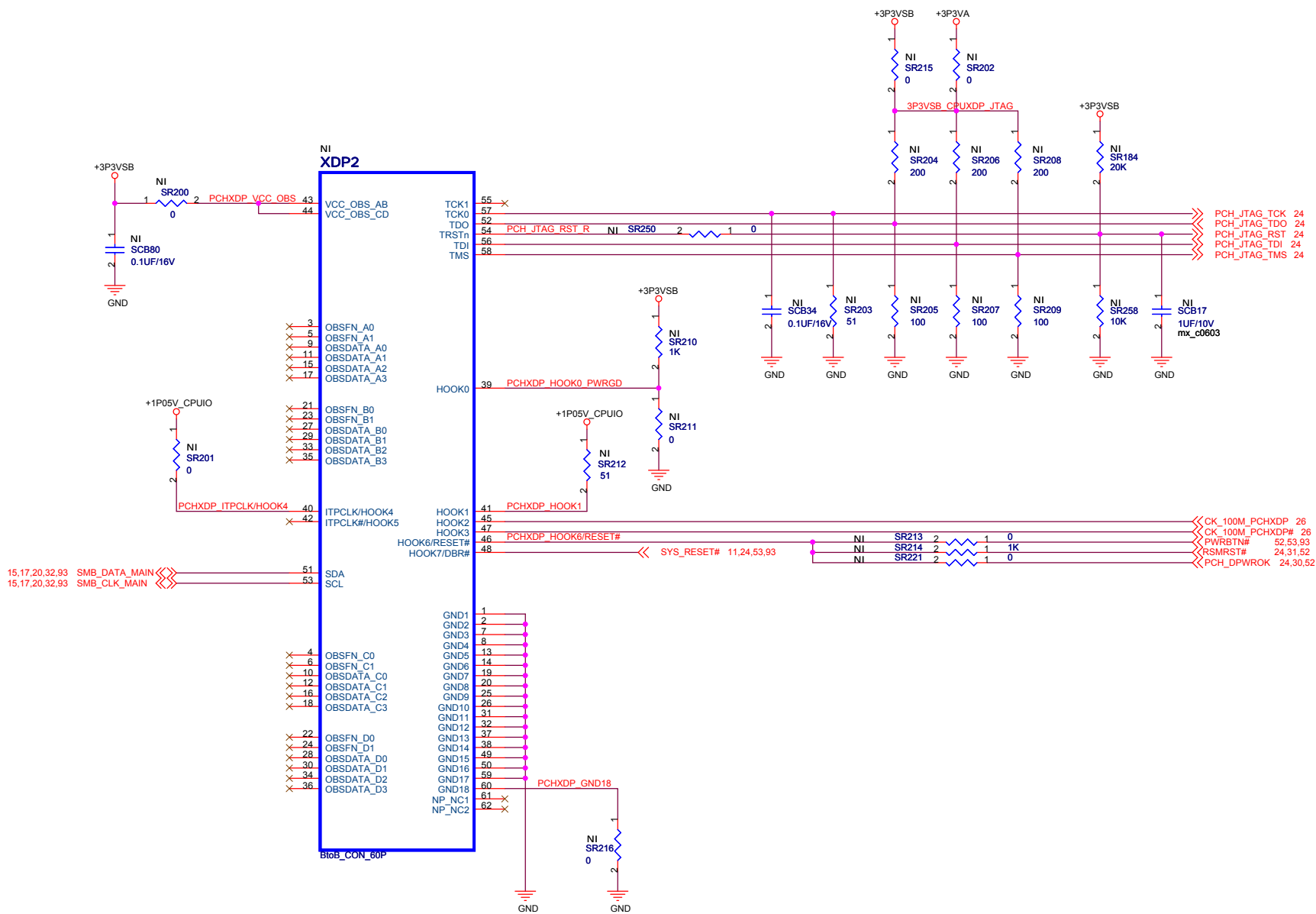
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : CPU XDP DEBUG

Pegatron Corp. Engineer: Haoche Tsai

Size	Project Name	Rev
A3	IPISB-AG	1.04

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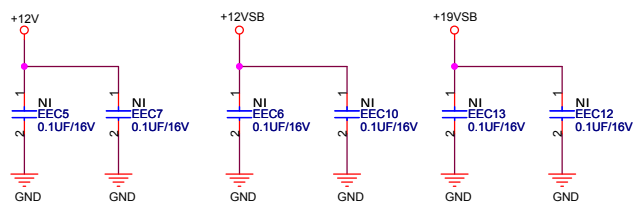
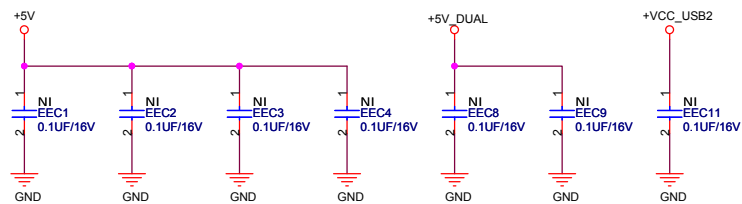
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : PCH XDP DEBUG

Pegatron Corp. Engineer: Haoche Tsai

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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **EMI CAP**

Pegatron Corp. Engineer: **Haoche Tsai**

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