Model Name: GA-H55-USB3 REV: 1.01

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1156-A
05	CPU_LGA1156-B
06	CPU_LGA1156-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	DDR III POWER CAP
10	PCH_FDI,DMI,USB,PCIE,NVRAM
11	PCH_DP,CLK BUFFER
12	PCH_HOST,SATA,PCI
13	PCH_GPIO,CTRL,AUDIO
14	PCH_PWR,GND
15	PCI EXPRESS*16 SLOT
16	PCIEx1 ; PCIE SWITCH
17	PCI SLOT 1, 2 , 3
18	CLOCK GEN RTM885N-914-GRT
19	ALC889/892R
20	REAR AUDIO JACK
21	ITE 8720 LPC IO
22	HWM,KB/MS , FAN CTRL
23	Dual BIOS,PHOT,D-OC
24	DISCRETE POWER
25	DDR_15V,PWR SEQ
26	CPU_VAXG PWM_ISL6314CRZ
27	CPU_VTT PWM_ISL6322G

SHEET	TITLE
28	VCORE PWM ISL6334ACRZ
29	VCORE PWM_ISL6611ACR
30	F_PANEL , F_USB , FDD
31	ATX POWER,TPM
32	REALTEK RTL8111D
33	HDMI,DVI,DP
34	TI TSB43AB23 1394
35	JM363
36	NEC UP720200
37	TABLE LIST
38	
39	

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Title: Cover Sheet		
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
Date: Friday, February 12, 2010	Sheet 1 of 37	

GA-H55-USB3

Component value change history

2009/08/27

Data	Change Item	Reason
	rev 1.0C	
2010/01/26	first release	
	rev 1.0D	
2010/01/27	fix Line-out AP issue	
	rev 1.01E	
2010/02/03	fix USB3.0 Gen2 issue	

Circuit or PCB layout change for next version

DATE	Change Item	Reason

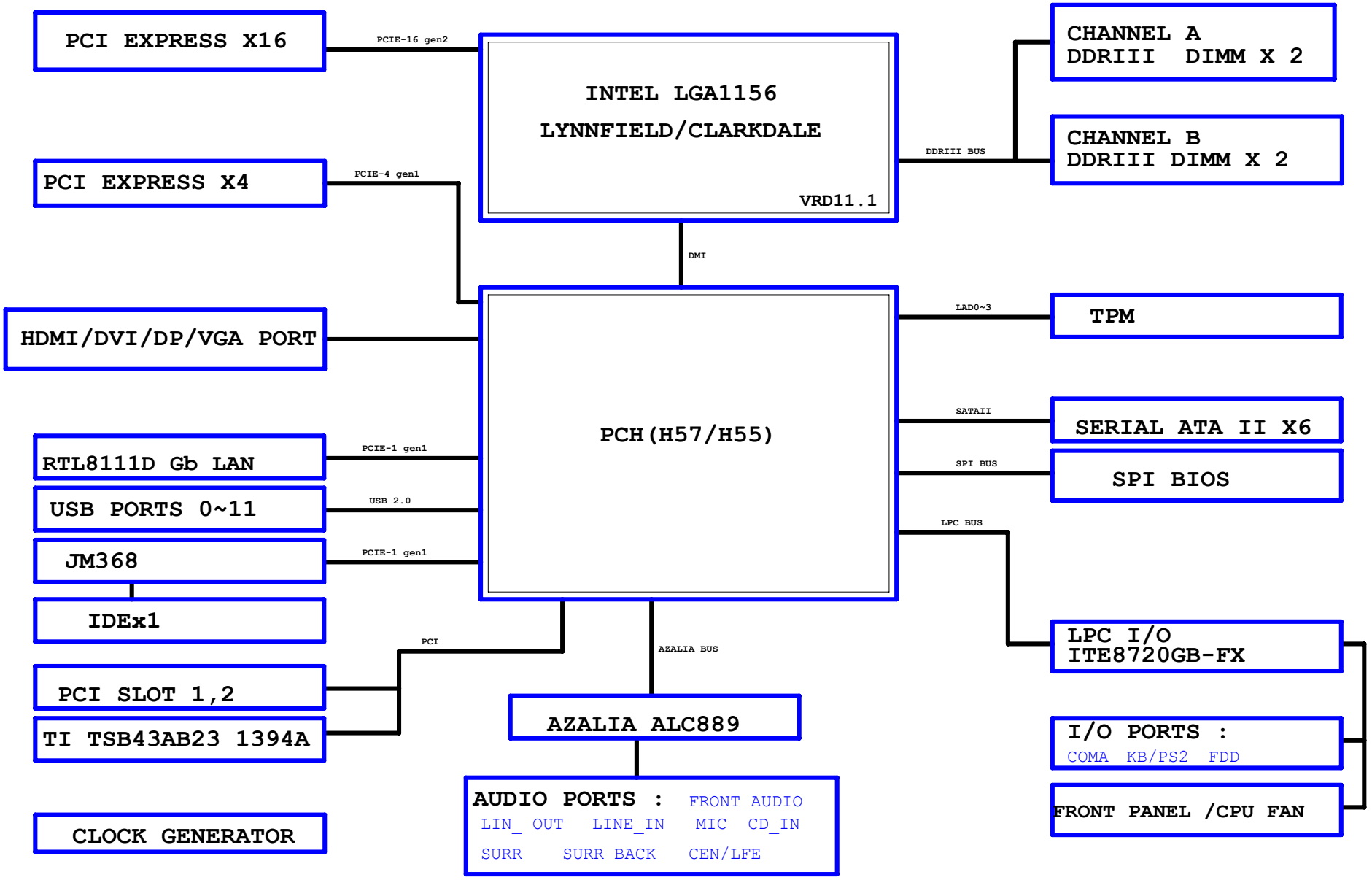
Gigabyte Technology

Title: BOM & PCB MODIFY HISTORY

Size: Custom | Document Number: GA-H55-USB3 | Rev: 1.01

Date: Friday, February 12, 2010 | Sheet 2 of 37

BLOCK DIAGRAM



Gigabyte Technology

Title	BLOCK DIAGRAM	
Size C	Document Number GA-H55-USB3	Rev 1.01
Date: Friday, February 12, 2010	Sheet 3 of 37	

FDI : 16/5/5/5/5/16(breakout min 8/4/5/4/8)

Impedance=80 +- 17.5%

PCIEX16:16/5/5/5/5/16(breakout min 8/4/5/4/8)

Impedance=80 +- 17.5%

DMI:12/5/5/5/12

Impedance=80 +- 17.5%

OC PCIE CLK

For PCIE2.0 issue
Must remove after
fixed issue

CFG 0-5 all internal PULL-UP

CFG	H	L	NOTE
0			H:1X16 , L:2X8
1	RSVD		
2	RSVD		
3	NORM	RSVD	LANE REVERSAL
4	DISABLE	ENABLE	DP PRESENCE
5	RSVD		
6	RSVD		
7	RSVD		ENGINEERING EXPERIMENT
15	RSVD		ENGINEERING EXPERIMENT

	FUNCTION	DEFAULT
VID0	MSI0	0
VID1	MSI1	1
VID2	MSI2	1
VID3	IMON CFG0	1
VID4	IMON CFG1	1
VID5	IMON CFG2	1
VID6	RSVD	0
VID7	VRD SEL	0
PSI#	RSVD	

POWER ON CONFIG TABLE (Default=1.2250V)

Check Trace length/ width

Gigabyte Technology

Title: CPU LGA1156-A

Size: Custom | Document Number: GA-H55-USB3 | Rev: 1.01

Date: Friday, February 12, 2010 | Sheet 4 of 37

LGA1156A

DDR_A

1 OF 10

LGA1156[10SC1-F01156-04R]

LGA1156B

DDR_B

2 OF 10

LGA1156[10SC1-F01156-04R]

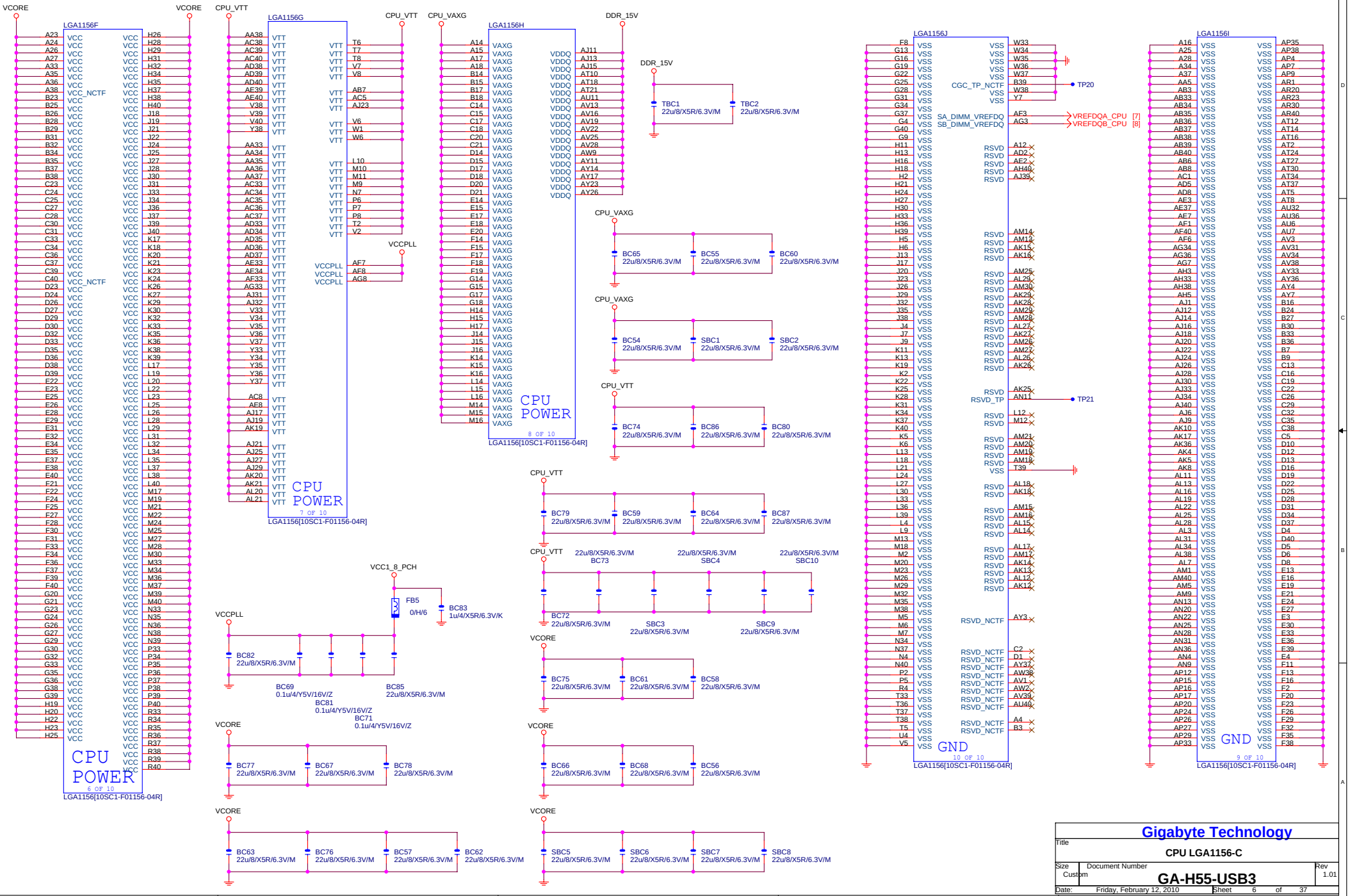
CR
CPU RETAINTION/X

Need check the new CPU ME

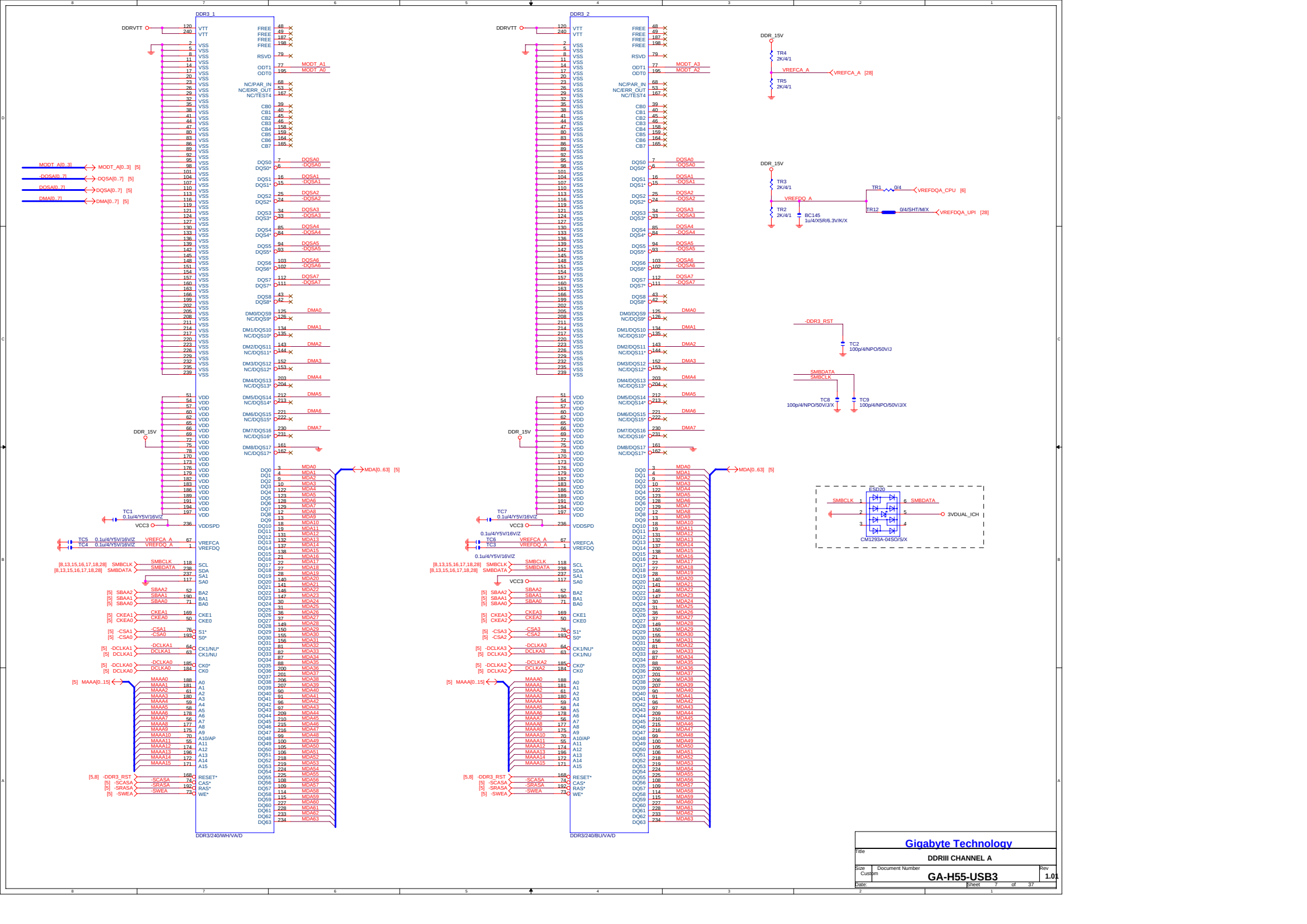
LGA1156_P

PLATE+ILM/[12KRC-0F0001-04R]

Gigabyte Technology		
Title: CPU LGA1156-B		
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
Date: Friday, February 12, 2010	Sheet 5 of 37	

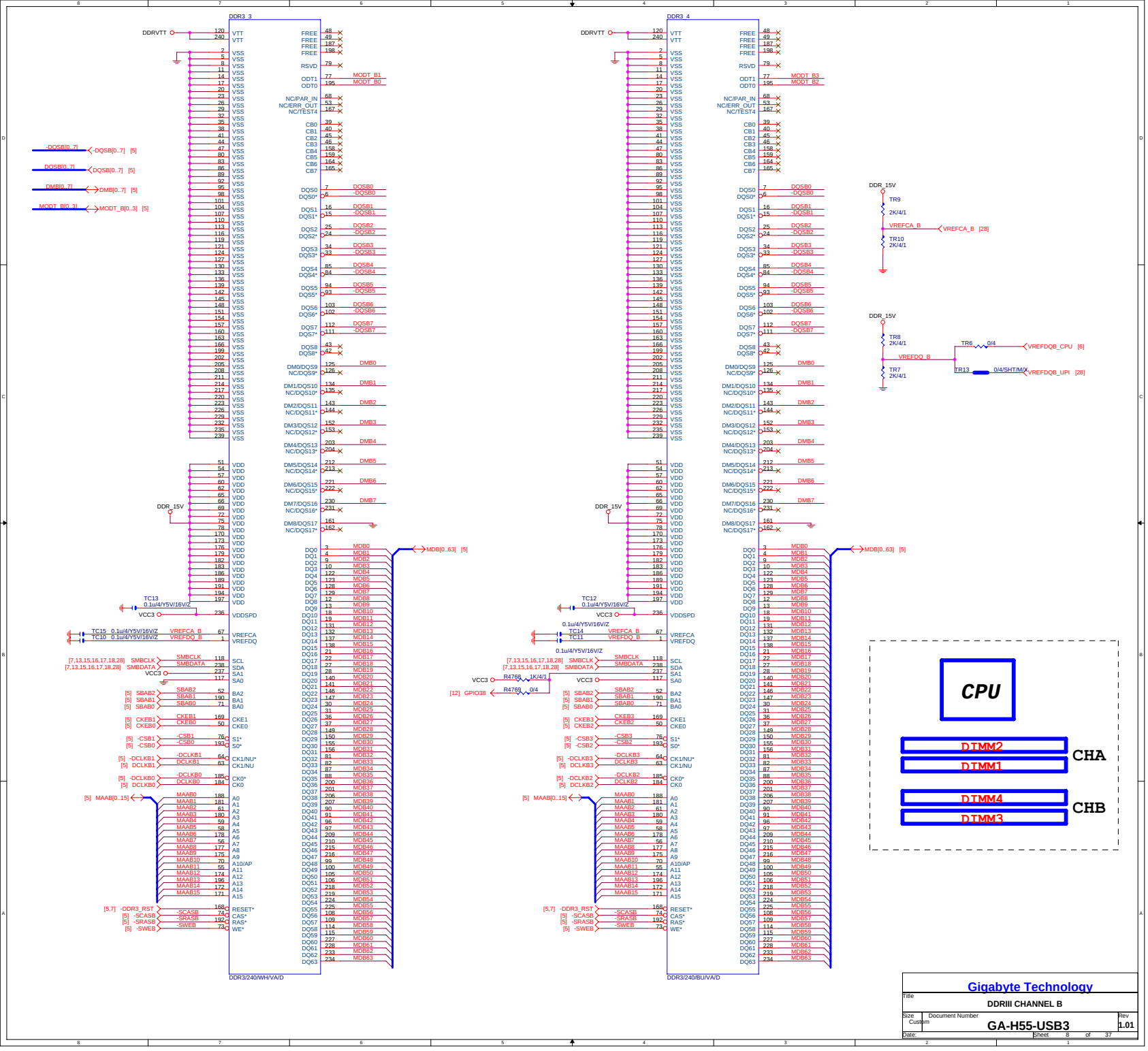


Gigabyte Technology		
Title: CPU LGA1156-C		
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
Date: Friday, February 12, 2010	Sheet 6 of 37	



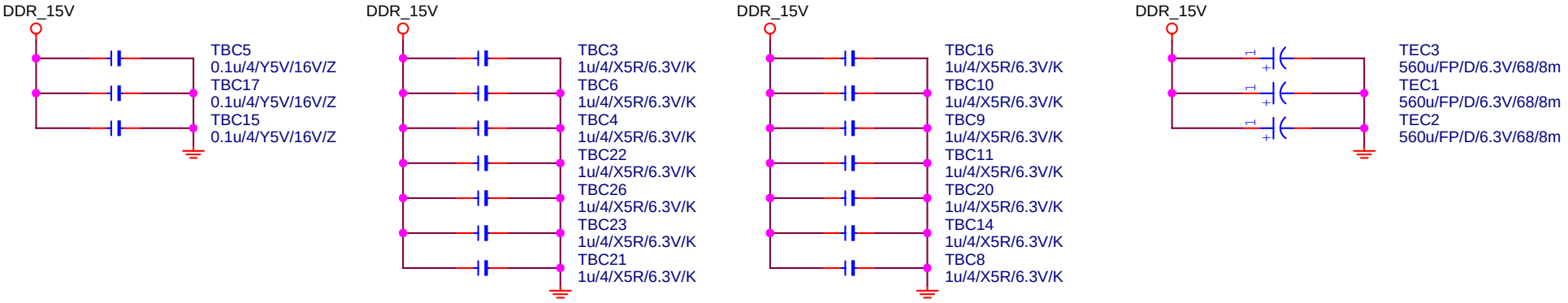
Gigabyte Technology

Title	DDRIII CHANNEL A	
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
Date:	Sheet 7 of 37	

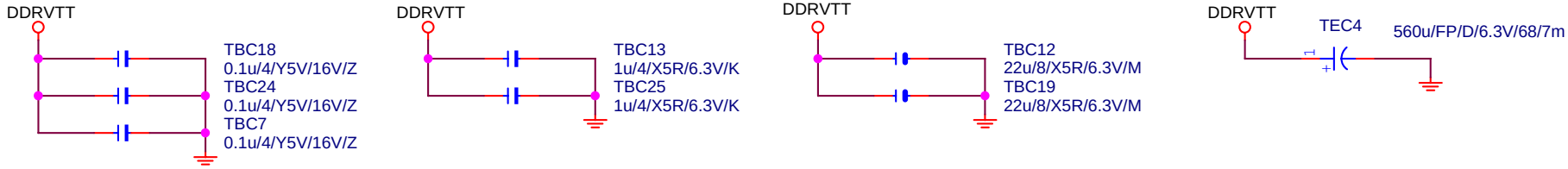

DDR3_3
DDR3_4
DDRVTT
DDR_15V
VTT
VSS
VDD
FREE
RSVD
ODT1
ODT0
NC/PAR_IN
NC/ERR_OUT
NC/TEST4
CB0
CB1
CB2
CB3
CB4
CB5
CB6
CB7
DQS0
DQS0*
DQS1
DQS1*
DQS2
DQS2*
DQS3
DQS3*
DQS4
DQS4*
DQS5
DQS5*
DQS6
DQS6*
DQS7
DQS7*
DQS8
DQS8*
DM0/DQS9
NC/DQS9*
DM1/DQS10
NC/DQS10*
DM2/DQS11
NC/DQS11*
DM3/DQS12
NC/DQS12*
DM4/DQS13
NC/DQS13*
DM5/DQS14
NC/DQS14*
DM6/DQS15
NC/DQS15*
DM7/DQS16
NC/DQS16*
DM8/DQS17
NC/DQS17*
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
DQ8
DQ9
DQ10
DQ11
DQ12
DQ13
DQ14
DQ15
DQ16
DQ17
DQ18
DQ19
DQ20
DQ21
DQ22
DQ23
DQ24
DQ25
DQ26
DQ27
DQ28
DQ29
DQ30
DQ31
DQ32
DQ33
DQ34
DQ35
DQ36
DQ37
DQ38
DQ39
DQ40
DQ41
DQ42
DQ43
DQ44
DQ45
DQ46
DQ47
DQ48
DQ49
DQ50
DQ51
DQ52
DQ53
DQ54
DQ55
DQ56
DQ57
DQ58
DQ59
DQ60
DQ61
DQ62
DQ63
-DQSB[0..7]
DQSB[0..7]
DMB[0..7]
MODT_B[0..3]
MDB[0..63]
MODT_B1
MODT_B0
MODT_B3
MODT_B2
TC13
TC15
TC10
TC12
TC14
TC11
0.1u/4/Y5V/16V/Z
VCC3
VDDSPD
VREFCA
VREFDQ
VREFCA_B
VREFDQ_B
SMBCLK
SMBDATA
SCL
SDA
SA1
SA0
SBAB2
SBAB1
SBAB0
BA2
BA1
BA0
CKE1
CKE0
CSB1
CSB0
S1*
S0*
DCLKB1
DCLKB0
CK1/NU*
CK1/NU
CK0*
CK0
MAAB0
MAAB1
MAAB2
MAAB3
MAAB4
MAAB5
MAAB6
MAAB7
MAAB8
MAAB9
MAAB10
MAAB11
MAAB12
MAAB13
MAAB14
MAAB15
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10/AP
A11
A12
A13
A14
A15
DDR3_RST
SCASB
SRASB
SWEB
RESET*
CAS*
RAS*
WE*
R4769
1K/4/1
0/4
GPIO38
TR9
TR10
TR8
TR7
2K/4/1
TR6
TR13
0/4/SHT/M/X
VREFCA_B [28]
VREFDQB_CPU [6]
VREFDQB_UPI [28]
DDR3/240/WH/VA/D
DDR3/240/BU/VA/D
CPU
DIMM2
DIMM1
DIMM4
DIMM3
CHA
CHB
Gigabyte Technology
DDRIII CHANNEL B
GA-H55-USB3
1.01
Custom
Sheet 8 of 37

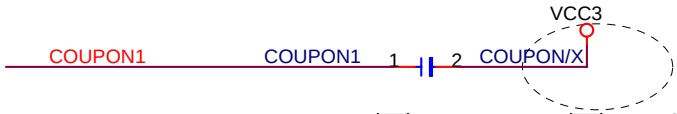
DDR TERMINATION CHANNEL A/B

DDR15V Decouple



DDRVTT Decouple





REF VCC層GND,GND層GND要塞孔

REF GND層GND,VCC層GND要塞孔

Gigabyte Technology		
Title: DDRIII POWER CAP		
Size: A	Document Number: GA-H55-USB3	Rev: 1.01
Date: Friday, February 12, 2010	Sheet 9 of 37	

DMI:12/5/5/5/12
Impedance=80 +- 17.5%

Impedance=80 +- 17.5%

PCIE X1 :15/5/5/5/15

USB:15/4.5/7.5/4.5/15
Impedance=90+- 15%

H55-->PORT6,7:N/A

OC[3:0]# for Device 29 (ports 0-7)

OC[7:4]# for Device 26 (ports 8-13)

H55 PCIE(7,8)-->N/A

Impedance=50+- 15%
ONFI: NV_DQ 4/5
NV_DQS 4/10
NV_CTRL 4/10
NV_CK 4/15

USB OC#	Configure
OC0#	USB0,1(F_USB1)
OC1#	USB2,3(F_USB2)
OC2#	USB4,5(F_USB3)
OC3#	USB6,7(F-USB4)H55-->N/A
OC4#	USB8,9(USB_LAN)
OC5#	USB10~11(USB_1394_ESATA)
OC6#	USB12~13(KB_USB)
OC7#	GPIO14

NV_ALE	
Hi	Enable Danbury
Lo	Disable Danbury

Intel anti theft techonlogy

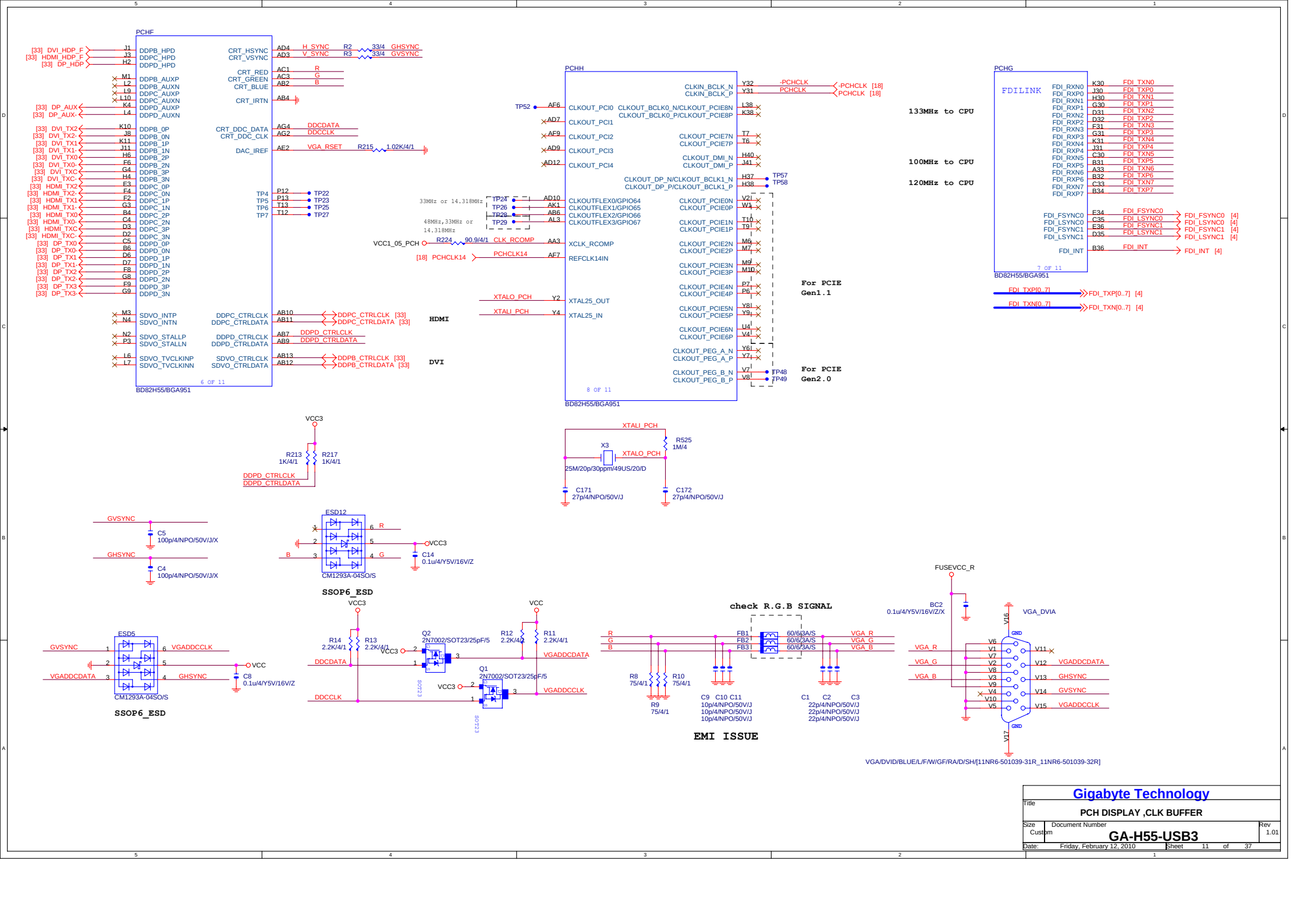
DMI Terminator voltage
HI : AC COUP : TX/RX TO VCC
LO : DC COUP : HALF SWING

PCH_HS

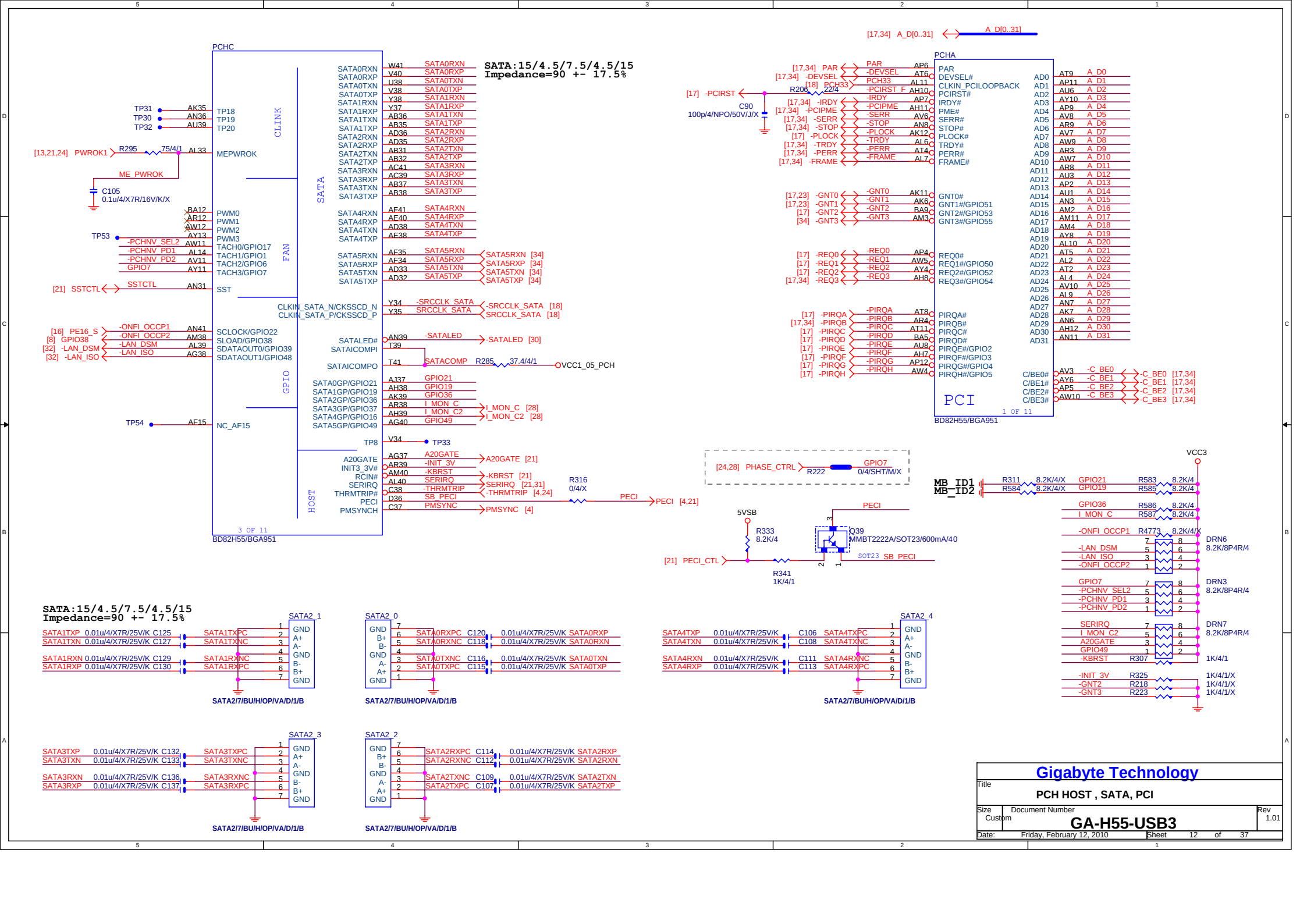
PCH_HS[12SP2-030030-C1R]

Gigabyte Technology

Title		
PCH FDI,DMI,USB ,PCIE,NVRAM		
Size B	Document Number GA-H55-USB3	Rev 1.01
Date: Friday, February 12, 2010	Sheet 10 of 37	



Gigabyte Technology			
Title: PCH DISPLAY ,CLK BUFFER			
Size: Custom	Document Number: GA-H55-USB3		Rev: 1.01
Date: Friday, February 12, 2010	Sheet 11 of 37		


PCHC
SATA:15/4.5/7.5/4.5/15
Impedance=90 +- 17.5%
CLINK
SATA
FAN
GPIO
HOST
MEPWROK
ME_PWROK
SST
NC_AF15
SATALED#
SATAICOMPI
SATAICOMPO
PECI
PMSYNCH
3 OF 11
BD82H55/BGA951
PCHA
PCI
1 OF 11
BD82H55/BGA951
A_D[0..31]
PHASE_CTRL
GPIO7
0/4/SHT/M/X
MB_ID1
MB_ID2
VCC3
5VSB
Q39
MMBT2222A/SOT23/600mA/40
SOT23
SB_PECI
PECI_CTL
SATA:15/4.5/7.5/4.5/15
Impedance=90 +- 17.5%
SATA2_1
SATA2_0
SATA2_4
SATA2_3
SATA2_2
SATA2/7/BU/H/OP/VA/D/1/B
DRN6
DRN3
DRN7
8.2K/8P4R/4
Gigabyte Technology
PCH HOST , SATA, PCI
GA-H55-USB3
Rev 1.01
Friday, February 12, 2010
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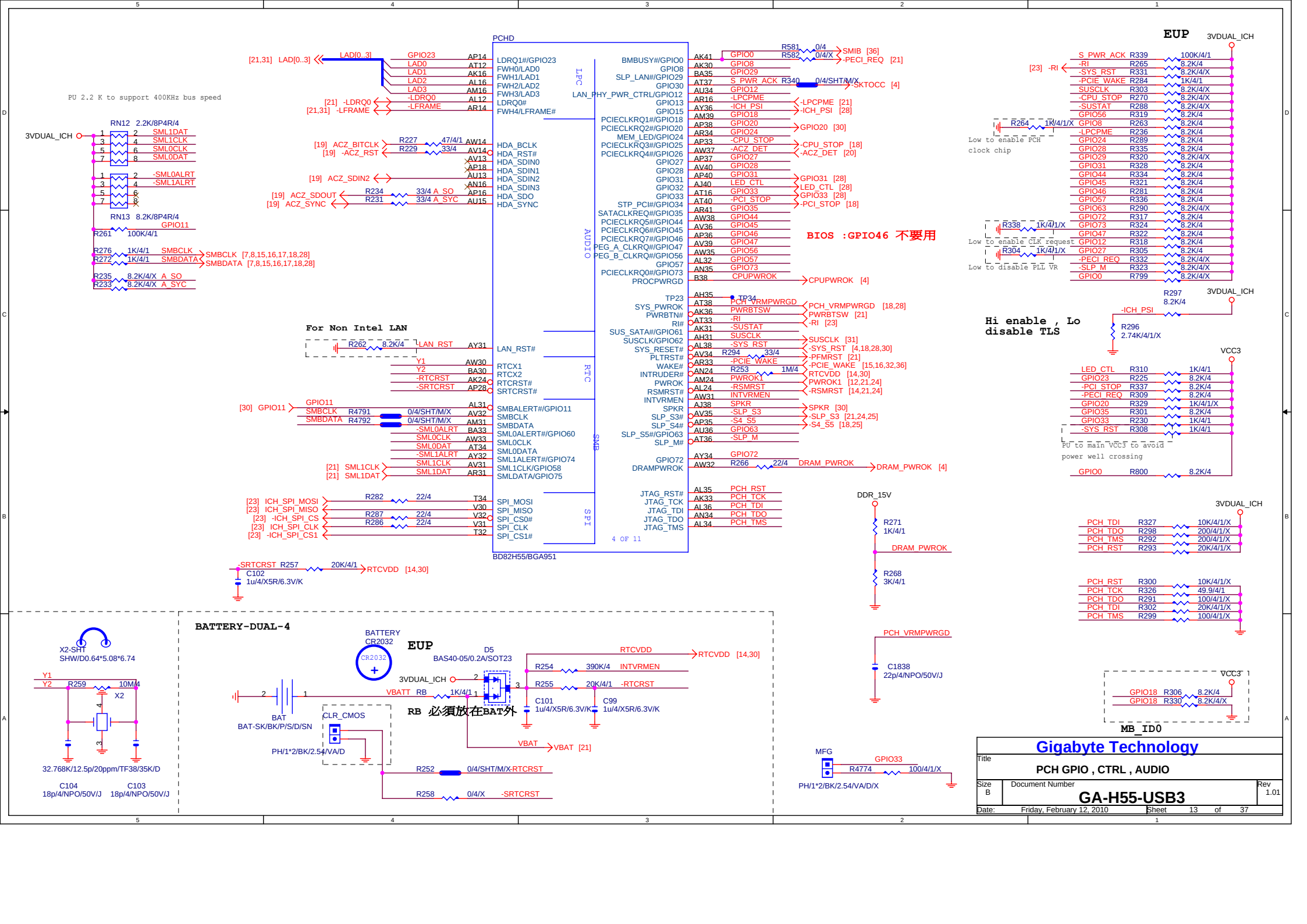
PCHD
LPC
AUDIO
RTC
SMB
SPI
LDRQ1#/GPIO23
FWH0/LAD0
FWH1/LAD1
FWH2/LAD2
FWH3/LAD3
LDRQ0#
FWH4/LFRAME#
LAN_PHY_PWR_CTRL/GPIO12
HDA_BCLK
HDA_RST#
HDA_SDIN0
HDA_SDIN1
HDA_SDIN2
HDA_SDIN3
HDA_SDO
HDA_SYNC
LAN_RST#
RTCX1
RTCX2
RTCRST#
SRTCRST#
SMBALERT#/GPIO11
SMBCLK
SMBDATA
SML0ALERT#/GPIO60
SML0CLK
SML0DATA
SML1ALERT#/GPIO74
SML1CLK/GPIO58
SMLDATA/GPIO75
SPI_MOSI
SPI_MISO
SPI_CS0#
SPI_CLK
SPI_CS1#
4 OF 11
BD82H55/BGA951
BMBUSY#/GPIO0
GPIO8
SLP_LAN#/GPIO29
GPIO30
GPIO13
GPIO15
PCIECLKRQ1#/GPIO18
PCIECLKRQ2#/GPIO20
MEM_LED/GPIO24
PCIECLKRQ3#/GPIO25
PCIECLKRQ4#/GPIO26
GPIO27
GPIO28
GPIO31
GPIO32
GPIO33
STP_PCI#/GPIO34
SATACLKREQ#/GPIO35
PCIECLKRQ5#/GPIO44
PCIECLKRQ6#/GPIO45
PCIECLKRQ7#/GPIO46
PEG_A_CLKRQ#/GPIO47
PEG_B_CLKRQ#/GPIO56
GPIO57
PCIECLKRQ0#/GPIO73
PROCPWRGD
TP23
SYS_PWROK
PWRBTN#
RI#
SUS_SATA#/GPIO61
SUSCLK/GPIO62
SYS_RESET#
PLTRST#
WAKE#
INTRUDER#
PWROK
RSMRST#
INTVRMEN
SPKR
SLP_S3#
SLP_S4#
SLP_S5#/GPIO63
SLP_M#
GPIO72
DRAMPWROK
JTAG_RST#
JTAG_TCK
JTAG_TDI
JTAG_TDO
JTAG_TMS
PU 2.2 K to support 400KHz bus speed
RN12 2.2K/8P4R/4
RN13 8.2K/8P4R/4
3VDUAL_ICH
SML1DAT
SML1CLK
SML0CLK
SML0DAT
-SML0ALRT
-SML1ALRT
GPIO11
R261 100K/4/1
R276 1K/4/1 SMBCLK
R272 1K/4/1 SMBDATA
SMBCLK [7,8,15,16,17,18,28]
SMBDATA [7,8,15,16,17,18,28]
R235 8.2K/4/X A_SO
R233 8.2K/4/X A_SYC
[21,31] LAD[0..3]
LAD[0..3]
GPIO23
LAD0
LAD1
LAD2
LAD3
-LDRQ0
-LFRAME
[21] -LDRQ0
[21,31] -LFRAME
[19] ACZ_BITCLK
[19] -ACZ_RST
R227 47/4/1
R229 33/4
[19] ACZ_SDIN2
[19] ACZ_SDOUT
[19] ACZ_SYNC
R234 33/4 A_SO
R231 33/4 A_SYC
For Non Intel LAN
R262 8.2K/4
LAN_RST
Y1
Y2
-RTCRST
-SRTCRST
[30] GPIO11
SMBCLK R4791 0/4/SHT/M/X
SMBDATA R4792 0/4/SHT/M/X
-SML0ALRT
SML0CLK
SML0DAT
-SML1ALRT
SML1CLK
SML1DAT
[21] SML1CLK
[21] SML1DAT
[23] ICH_SPI_MOSI
[23] ICH_SPI_MISO
[23] -ICH_SPI_CS
[23] ICH_SPI_CLK
[23] -ICH_SPI_CS1
R282 22/4
R287 22/4
R286 22/4
SRTCRST R257 20K/4/1
RTCVDD [14,30]
C102
1u/4/X5R/6.3V/K
GPIO0
GPIO8
GPIO29
S_PWR_ACK R340 0/4/SHT/M/X
GPIO12
-LPCPME
-ICH_PSI
GPIO20
-CPU_STOP
-ACZ_DET
GPIO27
GPIO28
GPIO31
LED_CTL
GPIO33
-PCI_STOP
GPIO35
GPIO44
GPIO45
GPIO46
GPIO47
GPIO56
GPIO57
GPIO73
CPUPWROK
R581 0/4
R582 0/4/X
SMIB [36]
-PECI_REQ [21]
SKTOCC [4]
-LPCPME [21]
-ICH_PSI [28]
GPIO20 [30]
-CPU_STOP [18]
-ACZ_DET [20]
GPIO31 [28]
LED_CTL [28]
GPIO33 [28]
-PCI_STOP [18]
BIOS :GPIO46 不要用
CPUPWROK [4]
TP34
PCH_VRMPWRGD
PWRBTSW
-RI
-SUSTAT
SUSCLK
-SYS_RST
R294 33/4
-PCIE_WAKE
R253 1M/4
PWROK1
-RSMRST
INTVRMEN
SPKR
-SLP_S3
-S4_S5
GPIO63
-SLP_M
GPIO72
R266 22/4
DRAM_PWROK
DRAM_PWROK [4]
PCH_VRMPWRGD [18,28]
PWRBTSW [21]
-RI [23]
SUSCLK [31]
-SYS_RST [4,18,28,30]
-PFMRST [21]
-PCIE_WAKE [15,16,32,36]
RTCVDD [14,30]
PWROK1 [12,21,24]
-RSMRST [14,21,24]
SPKR [30]
-SLP_S3 [21,24,25]
-S4_S5 [18,25]
PCH_RST
PCH_TCK
PCH_TDI
PCH_TDO
PCH_TMS
DDR_15V
R271 1K/4/1
DRAM_PWROK
R268 3K/4/1
PCH_VRMPWRGD
C1838 22p/4/NPO/50V/J
MFG
GPIO33
R4774 100/4/1/X
PH/1*2/BK/2.54/VA/D/X
EUP
3VDUAL_ICH
S_PWR_ACK R339 100K/4/1
[23] -RI
-RI R265 8.2K/4
-SYS_RST R331 8.2K/4/X
-PCIE_WAKE R284 1K/4/1
SUSCLK R303 8.2K/4/X
-CPU_STOP R270 8.2K/4/X
-SUSTAT R288 8.2K/4/X
GPIO56 R319 8.2K/4
R264 1K/4/1/X GPIO8 R263 8.2K/4
Low to enable PCH clock chip
-LPCPME R236 8.2K/4
GPIO24 R289 8.2K/4
GPIO28 R335 8.2K/4
GPIO29 R320 8.2K/4/X
GPIO31 R328 8.2K/4
GPIO44 R334 8.2K/4
GPIO45 R321 8.2K/4
GPIO46 R281 8.2K/4
GPIO57 R336 8.2K/4
GPIO63 R290 8.2K/4/X
GPIO72 R317 8.2K/4
R338 1K/4/1/X GPIO73 R324 8.2K/4
Low to enable CLK request
GPIO47 R322 8.2K/4
R304 1K/4/1/X GPIO12 R318 8.2K/4
Low to disable PLL VR
GPIO27 R305 8.2K/4
-PECI_REQ R332 8.2K/4/X
-SLP_M R323 8.2K/4/X
GPIO0 R799 8.2K/4/X
R297 8.2K/4
-ICH_PSI
R296 2.74K/4/1/X
Hi enable , Lo disable TLS
VCC3
LED_CTL R310 1K/4/1
GPIO23 R225 8.2K/4
-PCI_STOP R337 8.2K/4
-PECI_REQ R309 8.2K/4
GPIO20 R329 1K/4/1/X
GPIO35 R301 8.2K/4
GPIO33 R230 1K/4/1
-SYS_RST R308 1K/4/1
PU to main VCC3 to avoid power well crossing
GPIO0 R800 8.2K/4
PCH_TDI R327 10K/4/1/X
PCH_TDO R298 200/4/1/X
PCH_TMS R292 200/4/1/X
PCH_RST R293 20K/4/1/X
PCH_RST R300 10K/4/1/X
PCH_TCK R326 49.9/4/1
PCH_TDO R291 100/4/1/X
PCH_TDI R302 20K/4/1/X
PCH_TMS R299 100/4/1/X
GPIO18 R306 8.2K/4
GPIO18 R330 8.2K/4/X
MB_ID0
BATTERY-DUAL-4
X2-SHT
SHW/D0.64*5.08*6.74
Y1
Y2
R259 10M/4
X2
32.768K/12.5p/20ppm/TF38/35K/D
C104 18p/4/NPO/50V/J
C103 18p/4/NPO/50V/J
BATTERY
CR2032
BAT
BAT-SK/BK/P/S/D/SN
VBATT
RB 1K/4/1
CLR_CMOS
PH/1*2/BK/2.54/VA/D
D5
BAS40-05/0.2A/SOT23
R254 390K/4 INTVRMEN
R255 20K/4/1 -RTCRST
C101 1u/4/X5R/6.3V/K
C99 1u/4/X5R/6.3V/K
RB 必須放在BAT外
VBAT
VBAT [21]
R252 0/4/SHT/M/X-RTCRST
R258 0/4/X -SRTCRST
Gigabyte Technology
Title
PCH GPIO , CTRL , AUDIO
Size B
Document Number
GA-H55-USB3
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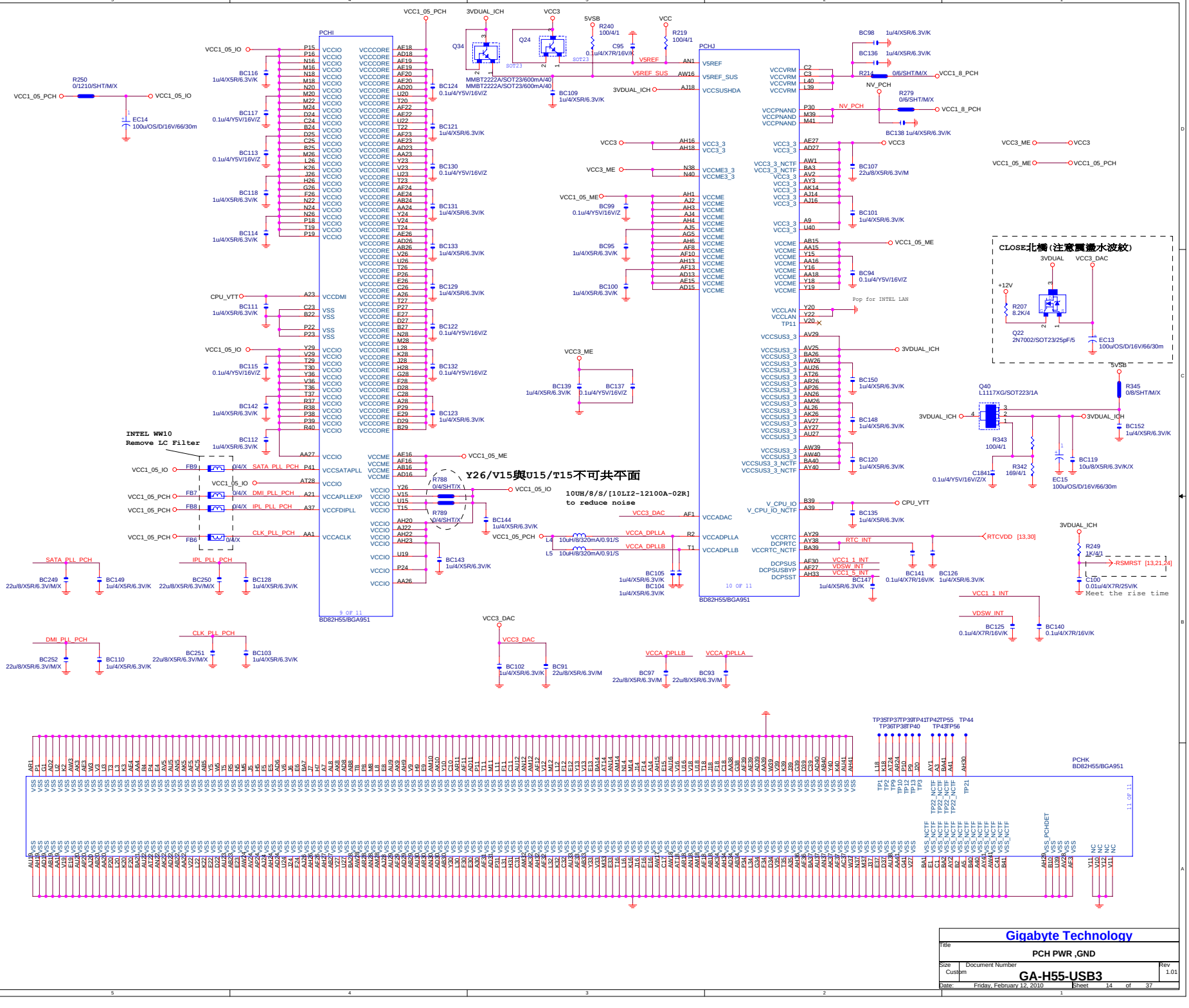
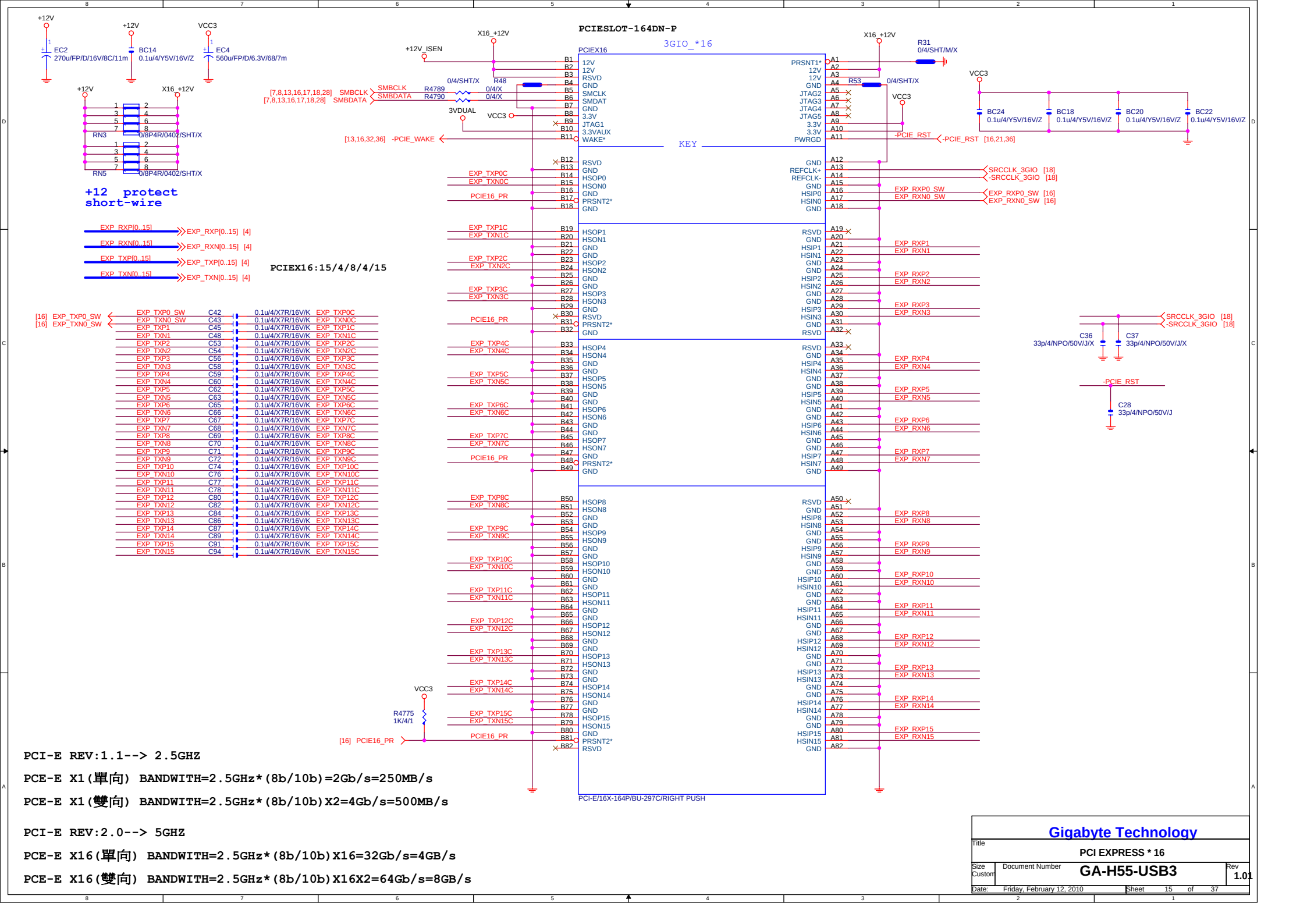

PCHI
PCHJ
PCHK
BD82H55/BGA951
INTEL WW10
Remove LC Filter
Y26/V15與U15/T15不可共平面
10UH/8/S/[10LI2-12100A-02R]
to reduce noise
CLOSE北橋(注意震盪水波紋)
Pop for INTEL LAN
Meet the rise time
Gigabyte Technology
PCH PWR ,GND
GA-H55-USB3
Friday, February 12, 2010


PCIESLOT-164DN-P
3GIO_*16
PCIEX16
KEY
PCIEX16:15/4/8/4/15
+12 protect short-wire
PCI-E/16X-164P/BU-297C/RIGHT PUSH
PCI-E REV:1.1--> 2.5GHZ
PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s
PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s
PCI-E REV:2.0--> 5GHZ
PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s
PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s
Gigabyte Technology
Title
PCI EXPRESS * 16
Size Custom
Document Number
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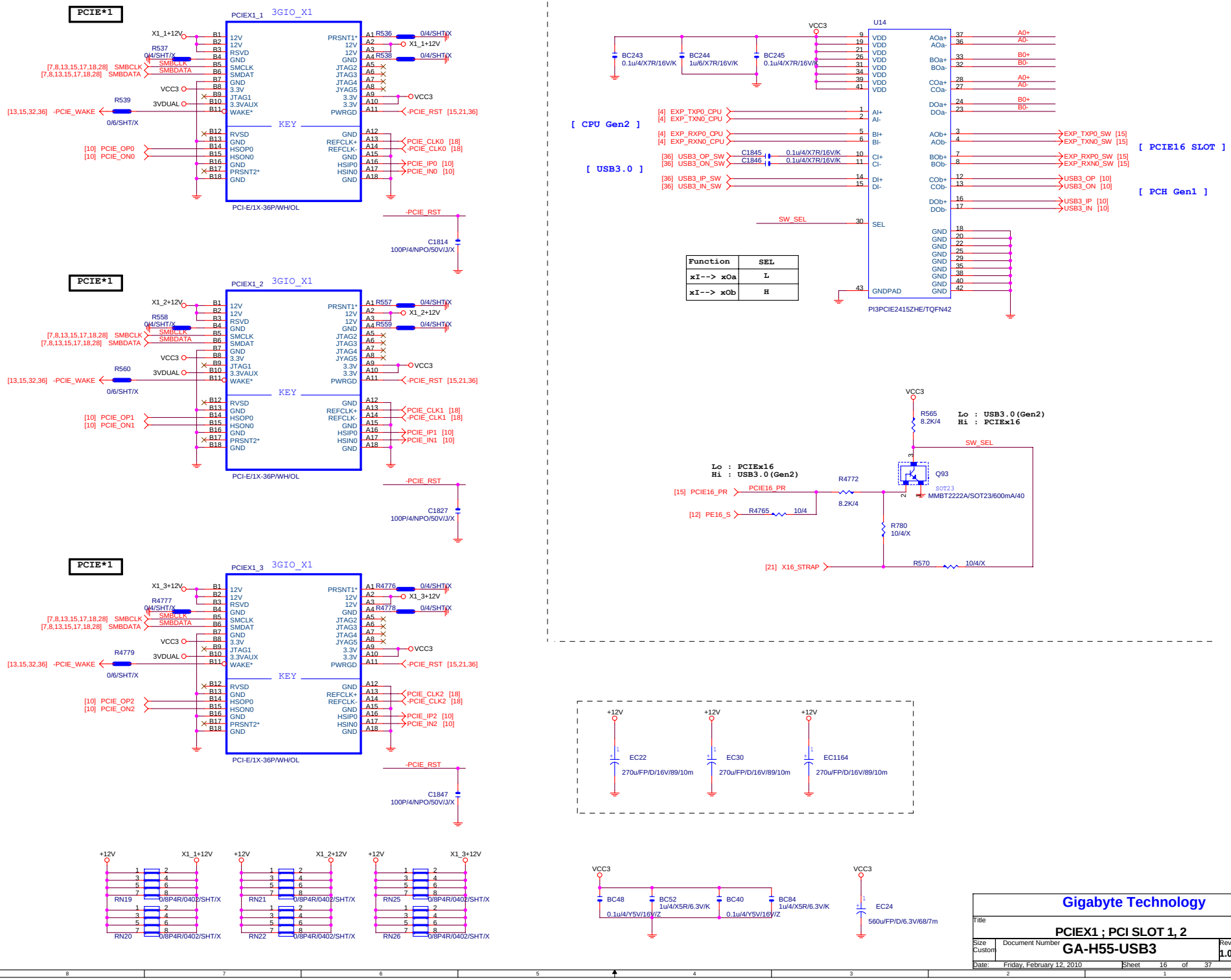
PCIE*1
PCIEX1_1
3GIO_X1
X1_1+12V
R537
0/4/SHT/X
[7,8,13,15,17,18,28] SMBCLK
[7,8,13,15,17,18,28] SMBDATA
SMBCLK
SMBDATA
VCC3
R539
3VDUAL
[13,15,32,36] -PCIE_WAKE
0/6/SHT/X
KEY
[10] PCIE_OP0
[10] PCIE_ON0
PRSNT1*
JTAG2
JTAG3
JTAG4
JYAG5
PWRGD
R536
R538
X1_1+12V
-PCIE_RST [15,21,36]
PCIE_CLK0 [18]
-PCIE_CLK0 [18]
PCIE_IP0 [10]
PCIE_IN0 [10]
PCI-E/1X-36P/WH/OL
-PCIE_RST
C1814
100P/4/NPO/50V/J/X
PCIEX1_2
X1_2+12V
R558
R560
[10] PCIE_OP1
[10] PCIE_ON1
R557
R559
X1_2+12V
PCIE_CLK1 [18]
-PCIE_CLK1 [18]
PCIE_IP1 [10]
PCIE_IN1 [10]
C1827
PCIEX1_3
X1_3+12V
R4777
R4779
[10] PCIE_OP2
[10] PCIE_ON2
R4776
R4778
X1_3+12V
PCIE_CLK2 [18]
-PCIE_CLK2 [18]
PCIE_IP2 [10]
PCIE_IN2 [10]
C1847
+12V
X1_1+12V
X1_2+12V
X1_3+12V
RN19
RN20
RN21
RN22
RN25
RN26
0/8P4R/0402/SHT/X
U14
VCC3
BC243
0.1u/4/X7R/16V/K
BC244
1u/6/X7R/16V/K
BC245
[CPU Gen2]
[USB3.0]
[4] EXP_TXP0_CPU
[4] EXP_TXN0_CPU
[4] EXP_RXP0_CPU
[4] EXP_RXN0_CPU
C1845
C1846
0.1u/4/X7R/16V/K
[36] USB3_OP_SW
[36] USB3_ON_SW
[36] USB3_IP_SW
[36] USB3_IN_SW
SW_SEL
VDD
GNDPAD
AOa+
AOa-
BOa+
BOa-
COa+
COa-
DOa+
DOa-
AOb+
AOb-
BOb+
BOb-
COb+
COb-
DOb+
DOb-
AI+
AI-
BI+
BI-
CI+
CI-
DI+
DI-
SEL
GND
A0+
A0-
B0+
B0-
EXP_TXP0_SW [15]
EXP_TXN0_SW [15]
EXP_RXP0_SW [15]
EXP_RXN0_SW [15]
USB3_OP [10]
USB3_ON [10]
USB3_IP [10]
USB3_IN [10]
[PCIE16 SLOT]
[PCH Gen1]
PI3PCIE2415ZHE/TQFN42
Function
SEL
xI--> xOa
L
xI--> xOb
H
R565
8.2K/4
Lo : USB3.0(Gen2)
Hi : PCIEx16
Lo : PCIEx16
Hi : USB3.0(Gen2)
R4772
8.2K/4
Q93
SOT23
MMBT2222A/SOT23/600mA/40
[15] PCIE16_PR
PCIE16_PR
[12] PE16_S
R4765
10/4
R780
10/4/X
[21] X16_STRAP
R570
10/4/X
EC22
EC30
EC1164
270u/FP/D/16V/89/10m
BC48
BC52
1u/4/X5R/6.3V/K
BC40
BC84
0.1u/4/Y5V/16V/Z
EC24
560u/FP/D/6.3V/68/7m
Gigabyte Technology
Title
PCIEX1 ; PCI SLOT 1, 2
Size
Custom
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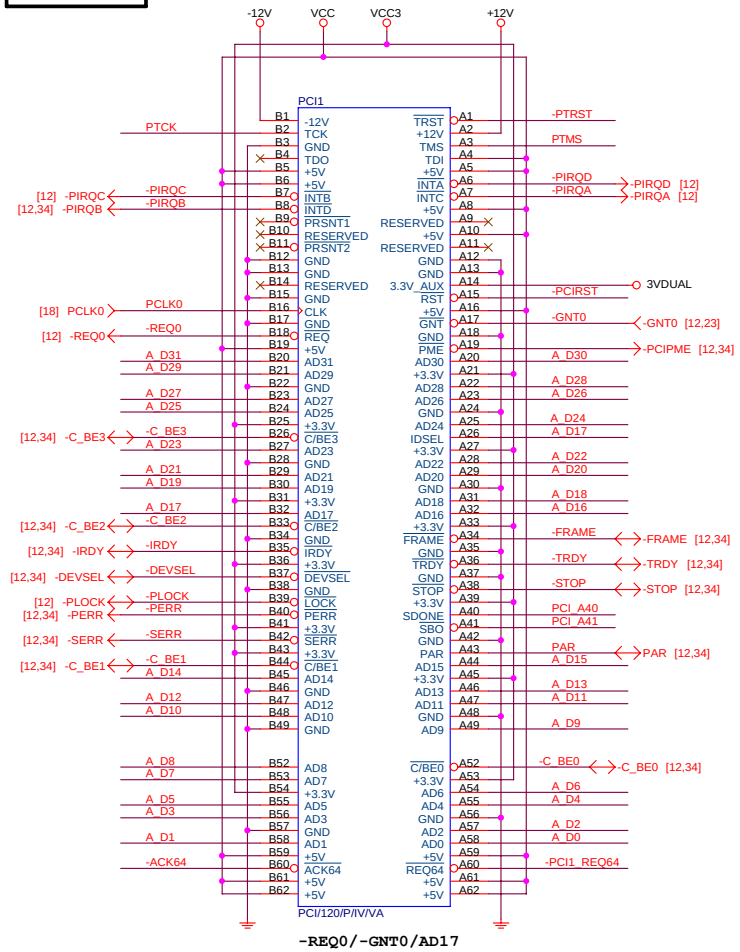
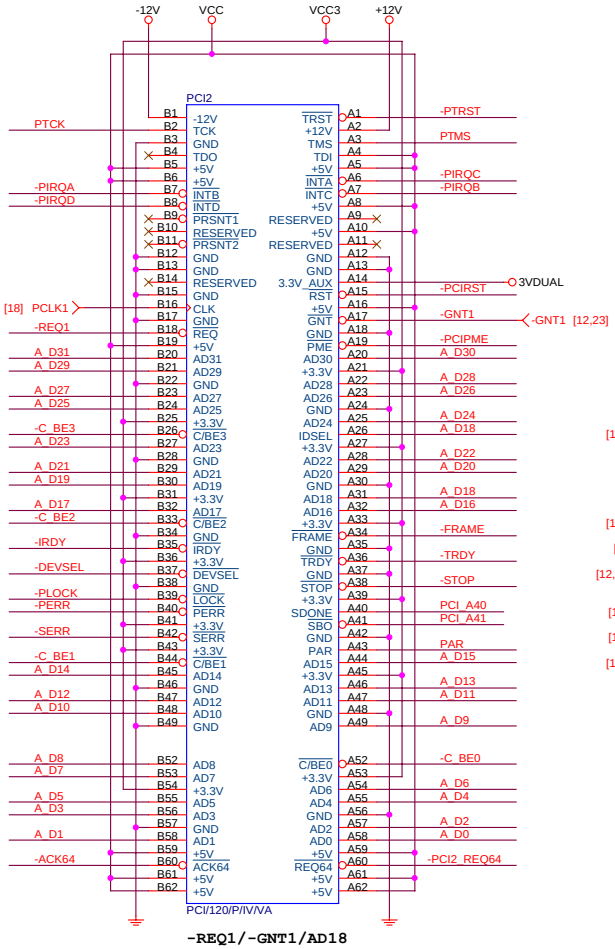
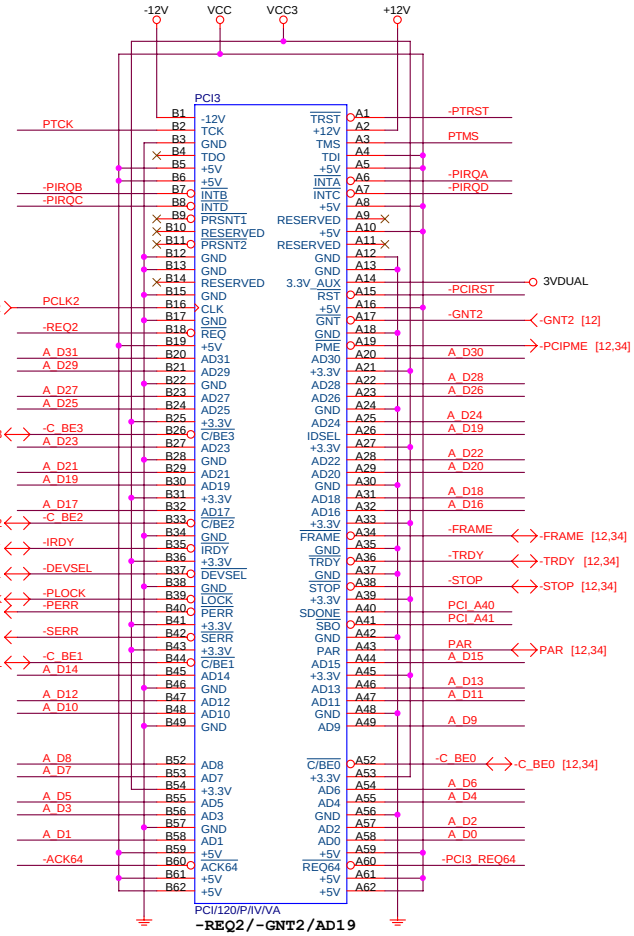
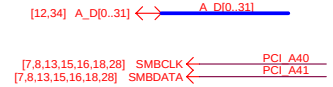
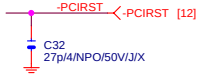
PCI1,2 SLOT



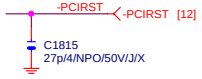




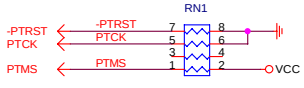


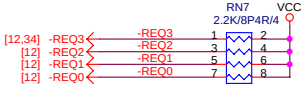


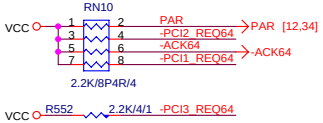
Place close to PCI1

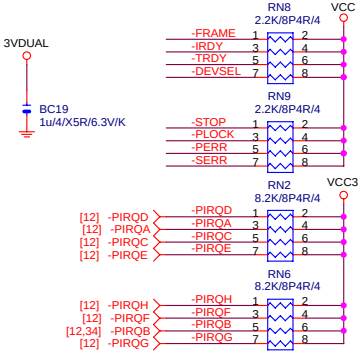


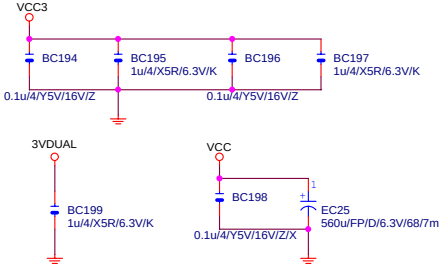
Place close to PCI3











Gigabyte Technology		
Title: PCIEX1 ; PCI SLOT 1, 2		
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
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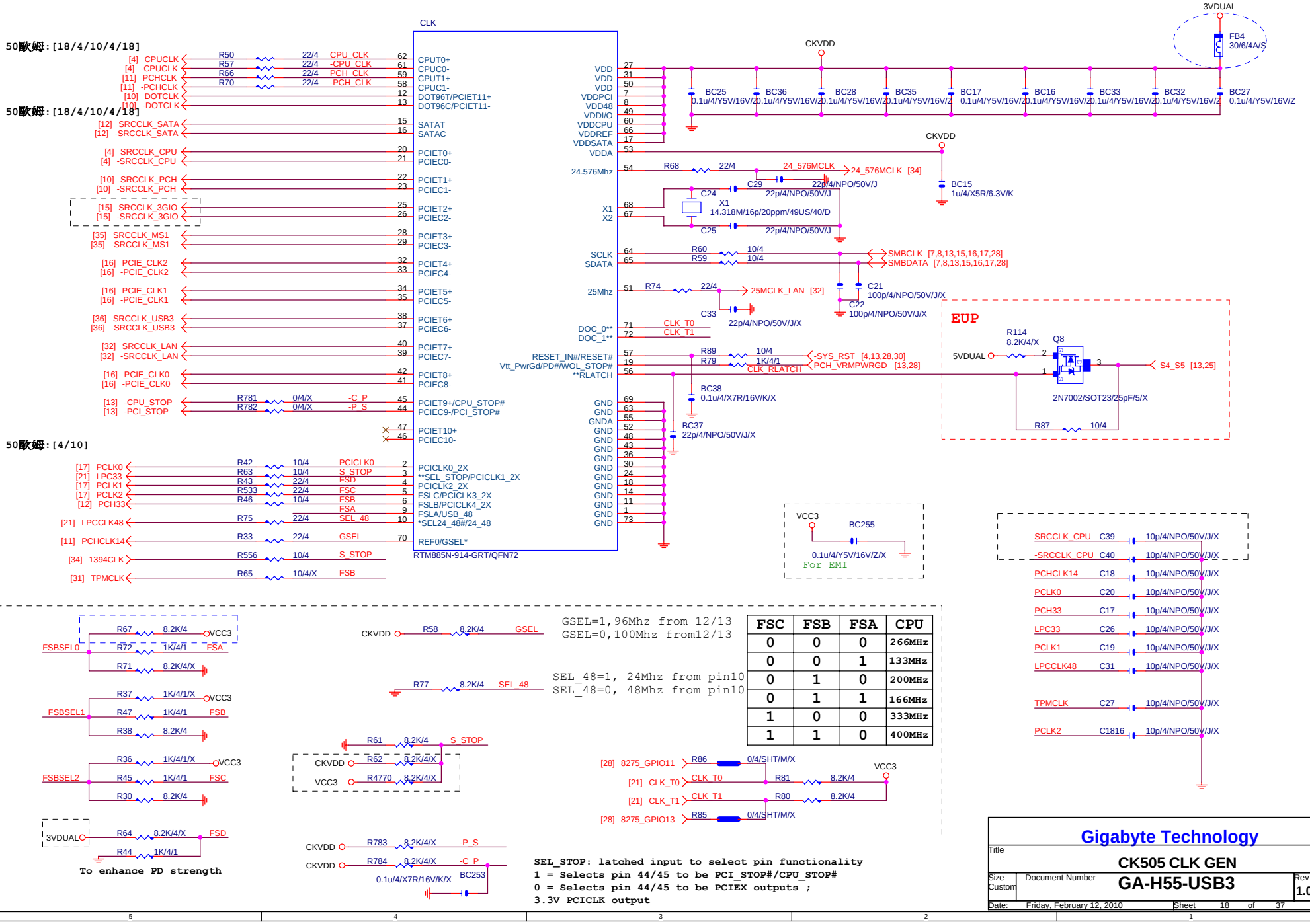
CLK GEN CK505



FSC	FSB	FSA	CPU
0	0	0	266MHz
0	0	1	133MHz
0	1	0	200MHz
0	1	1	166MHz
1	0	0	333MHz
1	1	0	400MHz

Gigabyte Technology	
Title	CK505 CLK GEN
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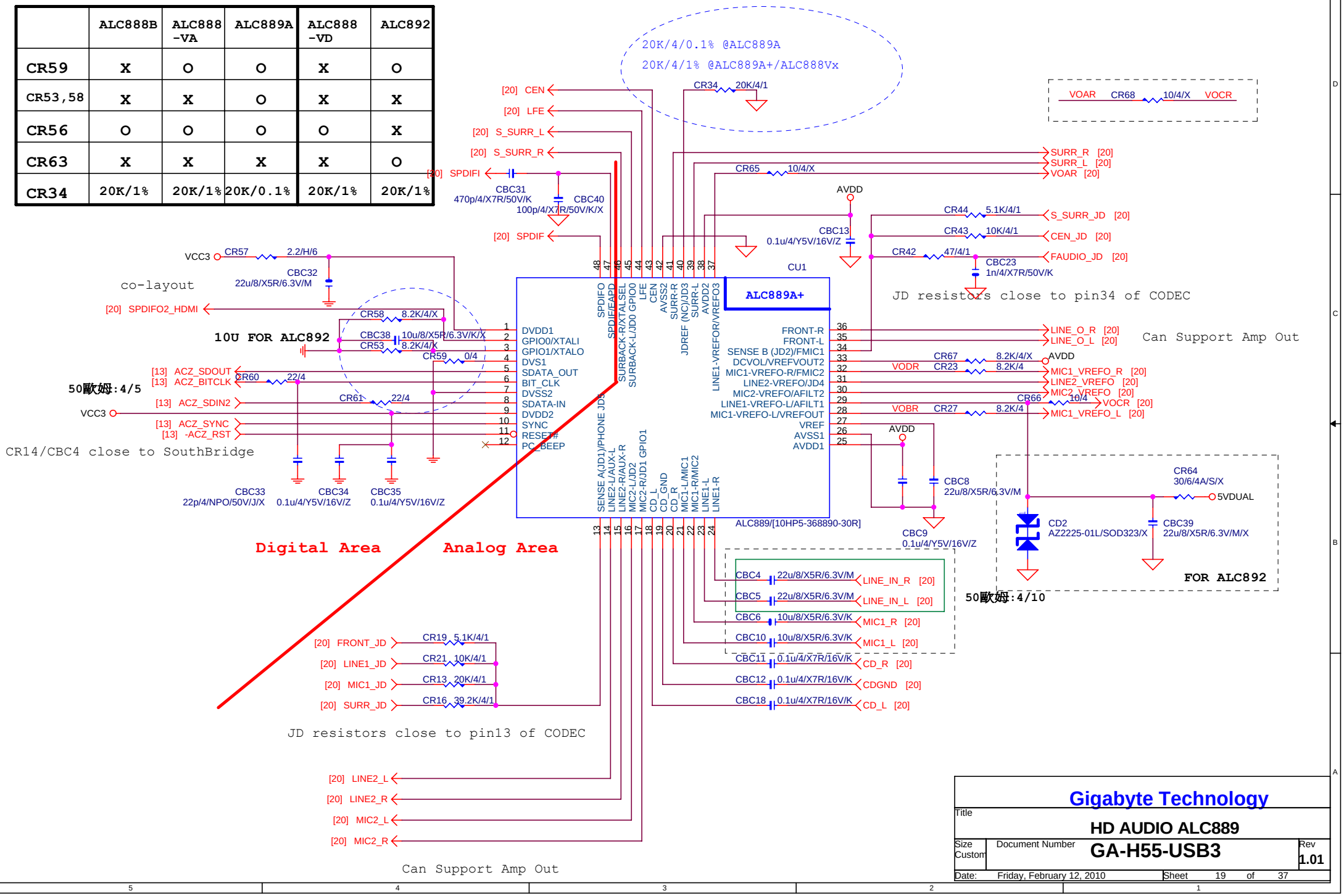
AZALIA CODEC

ALC892/ALC889A/ALC888Vx/ALC888B Colay

	ALC888B	ALC888 -VA	ALC889A	ALC888 -VD	ALC892
CR59	X	O	O	X	O
CR53,58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%



Gigabyte Technology

Title: HD AUDIO ALC889

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CODEC POWER/EMI PAD

5VSB 5VDUAL EUP +12V

CR71 0/4/X CR72 0/4 CQ4 AVDD CD1

CD4148WP/1206/300mA CD4148WP/1206/300mA

CQ5 78L05/SOT89/0.1A

CBC29 22u/8/X5R/6.3V/M CBC36 0.1u/4/Y5V/16V/Z/X

CD3 AZ2225-01L/SOD323

CR31 0/4/X

CR48 0/6/SHT/M/X CR47 0/6/SHT/M/X CR20 0/6/SHT/M/X

CD IN

CD_IN

[19] CD_L [19] CDGND [19] CD_R

CR30 8.2K/4 CR49 8.2K/4 CR29 8.2K/4 SHR/1*4/BK/P/2.54/VA/D

DP_HDMI_SPDIFC

FUSEVCC_R [19] SPDIF SPDIF

S1 S2 S3 G1 G2 G3 G4

DP+HDMI+SPDIF/20P+19P+3P/BK/RA

SPDIF_IN

CR62 0/4/SHT/X SPDIF_I

VCC [19] SPDIFI

SHR/1*3/WH/P/2.54/VA/D

[19] SPDIF CR56 10/4/X SPDIFO

[19] SPDIFO2_HDMI CR63 10/4

For HDMI SPDIF

CBC37 100p/4/NPO/50V/J

PIN SPDIF_O PH/1*2/BK/2.54/VA/D

For HDMI SPDIF

AZALIA JACK

BTX AZALIA CONNECTOR

11NR6-403007-21R

ESD3

LINE2_R LINE2_L AVDD MIC2_R MIC2_L

CM1293A-04SO/S/X

AUDIOB

[19] LINE1_JD LINE1_JD AJ_A5 AJ_A2

[19] FRONT_JD FRONT_JD AJ_B5 AJ_B2

[19] MIC1_JD MIC1_JD AJ_C5 AJ_C2

LINE-IN LINE-OUT MIC-IN

BLUE LINE-IN

GREEN LINE-OUT

PINK MIC-IN

2X3RP/26P/OR,BK,GY,BU,GE,PK/RA

AUDIOA

[19] CEN_JD CEN_JD BJ_B5 BJ_B2

[19] SURR_JD SURR_JD BJ_C5 BJ_C2

[19] S_SURR_JD S_SURR_JD BJ_A5 BJ_A2

REAR CEN SIDE

Orange CEN/LFE

Black SURROUND

Gray SURROUND SIDE

2X3RP/26P/OR,BK,GY,BU,GE,PK/RA

LINE-OUT

[19] LINE_O_R CEC6 100u/OS/D/16V/66/30m CR32 62/4 CR14 10K/4/1 CR33 10K/4/1 AJ_B5

[19] LINE_O_L CEC3 100u/OS/D/16V/66/30m CR15 62/4 AJ_B2

[19] VOAR CQ6 CR69 8.2K/4/X CR70 8.2K/4/X

BAT54A/SOT23/200mA/X

CBC7 CBC20 180p/4/NPO/50V/J

Only reserved for ALC888

LINE-IN

[19] LINE_IN_R CR12 62/4 AJ_A5

[19] LINE_IN_L CR17 62/4 AJ_A2

[19] VOCR CQ2 CR11 8.2K/4 CR18 8.2K/4

For 889A/888

BAT54A/SOT23/200mA

CBC16 CBC21 180p/4/NPO/50V/J

Verify MIC function in LINE-in

MIC-IN

[19] MIC1_R CR22 62/4 AJ_C5

[19] MIC1_L CR26 62/4 AJ_C2

[19] MIC1_VREFO_L [19] MIC1_VREFO_R

CBC2 CBC3 180p/4/NPO/50V/J

SURROUND

[19] SURR_R CEC9 100u/OS/D/16V/66/30m CR51 62/4 CR28 10K/4/1 CR45 10K/4/1 BJ_C5

[19] SURR_L CEC5 100u/OS/D/16V/66/30m CR25 62/4 BJ_C2

EMI

CBC14 CBC27 180p/4/NPO/50V/J

CEN/LFE

[19] LFE CEC10 100u/OS/D/16V/66/30m CR52 62/4 CR41 10K/4/1 CR46 10K/4/1 BJ_B5

[19] CEN CEC4 100u/OS/D/16V/66/30m CR40 62/4 BJ_B2

EMI

CBC22 CBC28 180p/4/NPO/50V/J

SURR BACK

[19] S_SURR_R CEC2 100u/OS/D/16V/66/30m CR3 62/4 CR2 10K/4/1 CR4 10K/4/1 BJ_A5

[19] S_SURR_L CEC1 100u/OS/D/16V/66/30m CR1 62/4 BJ_A2

EMI

CBC1 CBC26 180p/4/NPO/50V/J

AZALIA FRONT PANEL

CQ3 BAT54A/SOT23/200mA SOT23

[19] LINE2_VREFO CR37 8.2K/4 CR50 8.2K/4

CQ1 BAT54A/SOT23/200mA

[19] MIC2_VREFO CR5 8.2K/4 CR8 8.2K/4

[19] MIC2_L [19] MIC2_R CBC17 CBC15 10u/8/X5R/6.3V/K CR9 CR6 62/4 M2_L M2_R

[19] FAUDIO_JD L2_R L2_L CR36 CR38 62/4

F_AUDIO

CR35 22K/4 CR39 22K/4

Digital Area

3VDUAL CR10 8.2K/4

ACZ_DET [13]

CR55 20K/4/1 CR54 39.2K/4/1

PH/2*5K8/GED/2.54/VA/D

[19] LINE2_R [19] LINE2_L CEC7 CEC8 100u/OS/D/16V/66/30m L2_R L2_L

CBC24 CBC25 CBC30 CBC19 180p/4/NPO/50V/J

Gigabyte Technology		
Title: AUDIO JACK		
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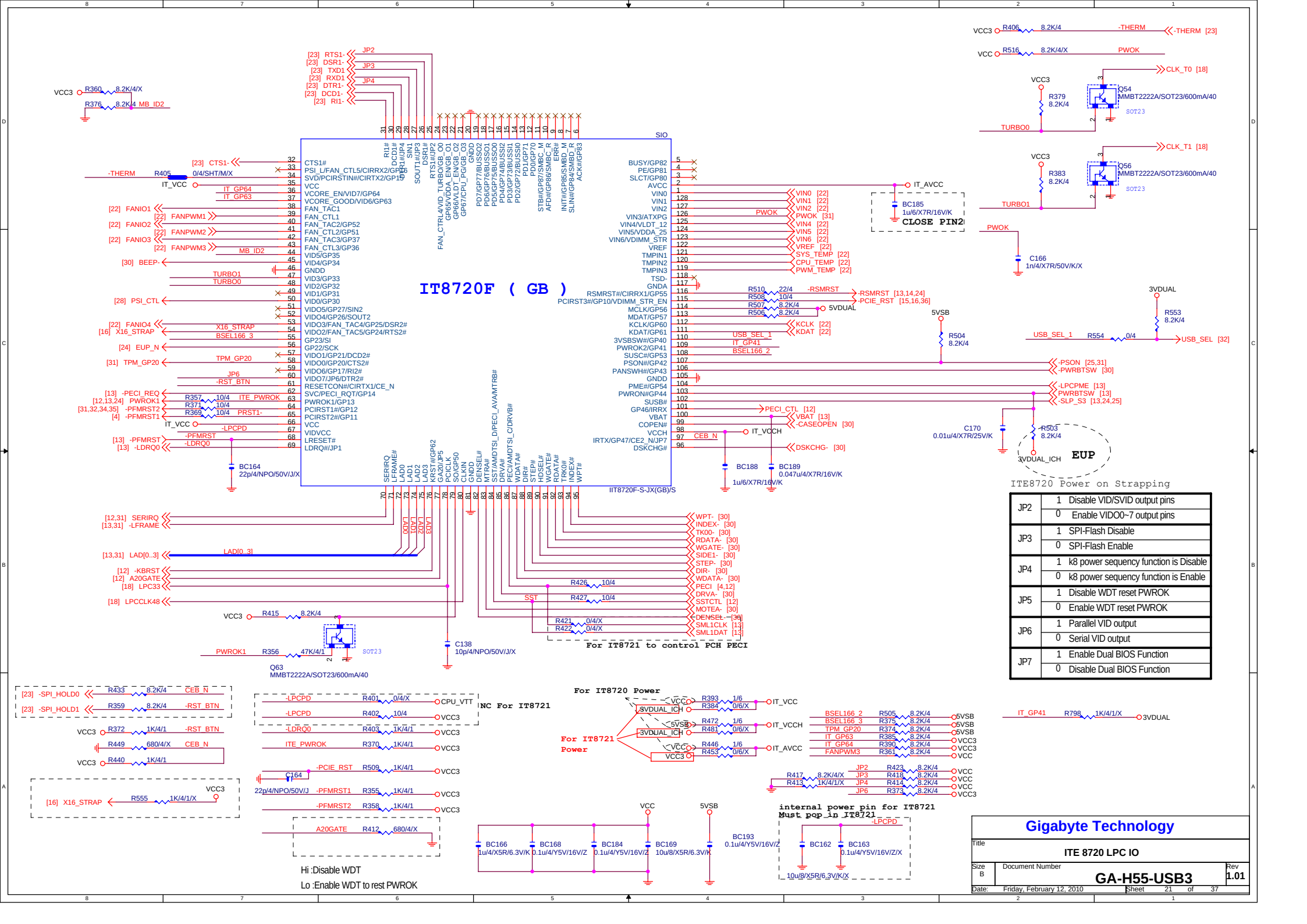
IT8720F (GB)
IIT8720F-S-JX(GB)/S
SIO
JP2
JP3
JP4
JP6
Q54 MMBT2222A/SOT23/600mA/40
Q56 MMBT2222A/SOT23/600mA/40
Q63 MMBT2222A/SOT23/600mA/40
EUP
ITE8720 Power on Strapping
JP2 1 Disable VID/SVID output pins
JP2 0 Enable VIDO0~7 output pins
JP3 1 SPI-Flash Disable
JP3 0 SPI-Flash Enable
JP4 1 k8 power sequency function is Disable
JP4 0 k8 power sequency function is Enable
JP5 1 Disable WDT reset PWROK
JP5 0 Enable WDT reset PWROK
JP6 1 Parallel VID output
JP6 0 Serial VID output
JP7 1 Enable Dual BIOS Function
JP7 0 Disable Dual BIOS Function
For IT8721 to control PCH PECI
For IT8720 Power
For IT8721 Power
NC For IT8721
internal power pin for IT8721
Must pop_in IT8721
Hi :Disable WDT
Lo :Enable WDT to rest PWROK
Gigabyte Technology
ITE 8720 LPC IO
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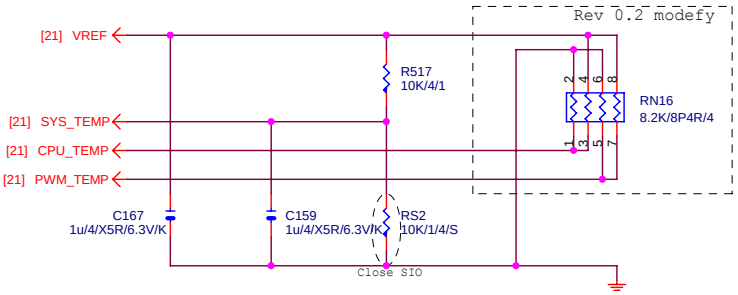
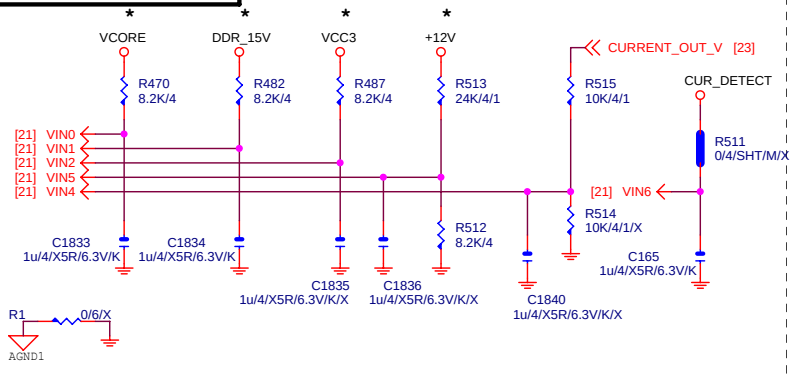
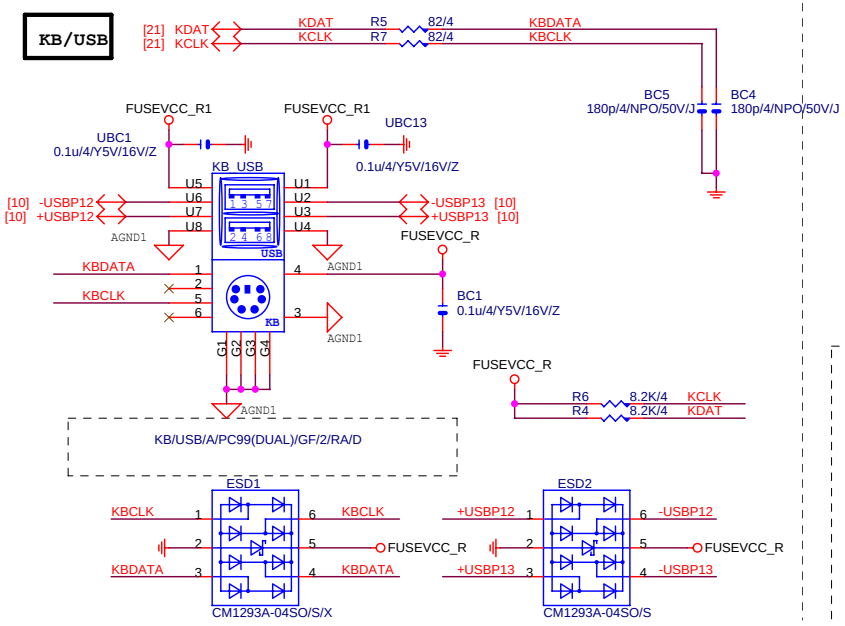
TEMP H/W MONITOR



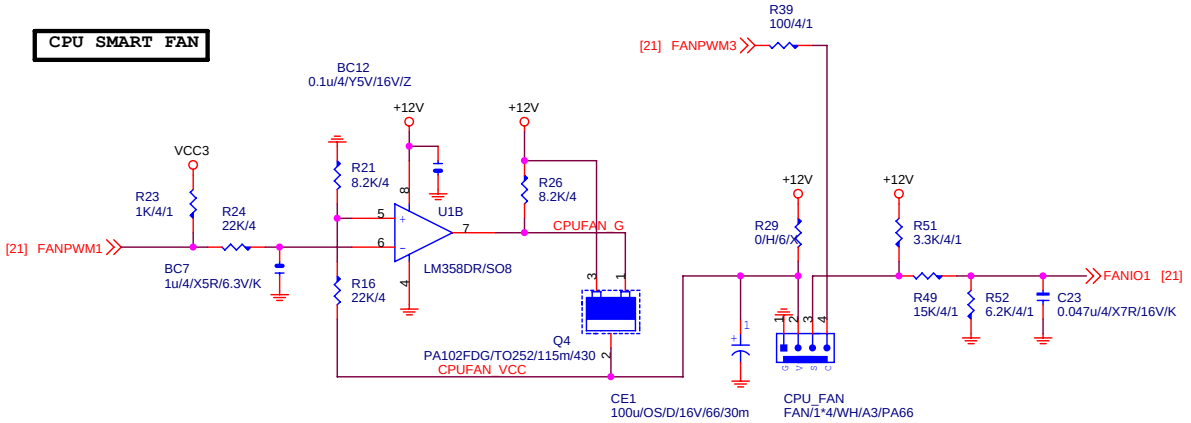
VOLTAGE-- H/W MONITOR



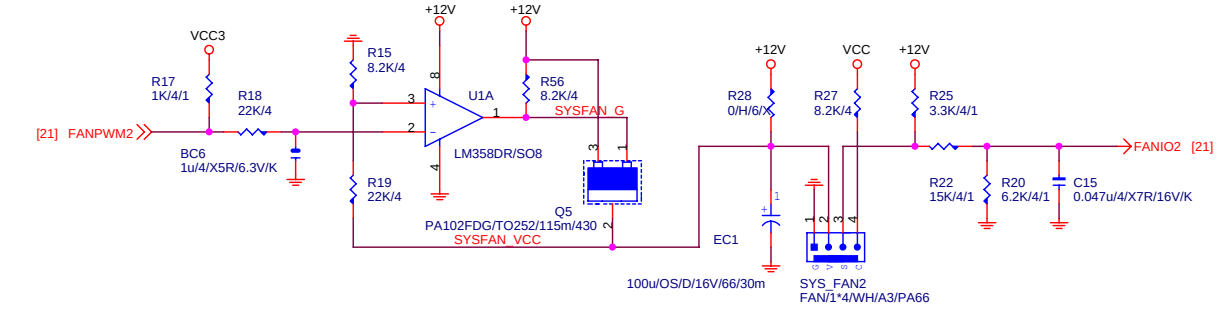
KB/USB



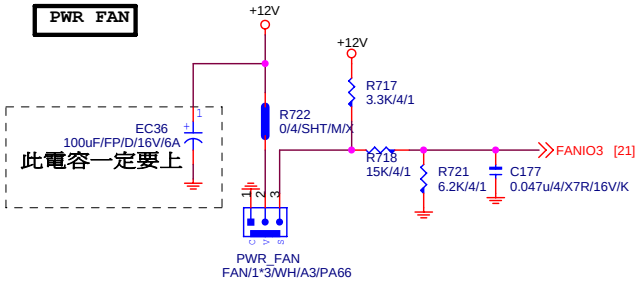
CPU SMART FAN

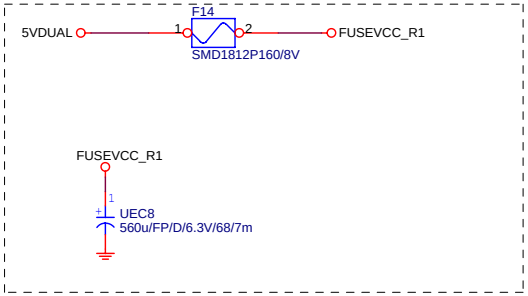


SYS SMART FAN Linear SYS_FAN

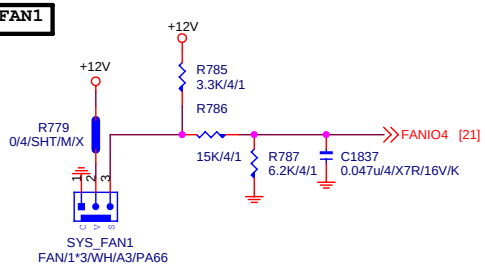


PWR FAN





SYS FAN1



Gigabyte Technology		
Title: HWM,KB/MS, FAN CTRL		
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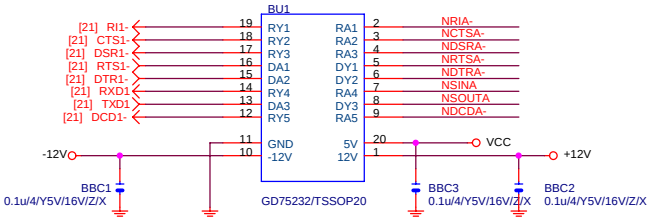
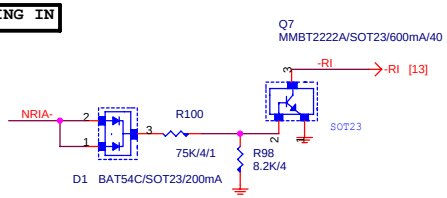
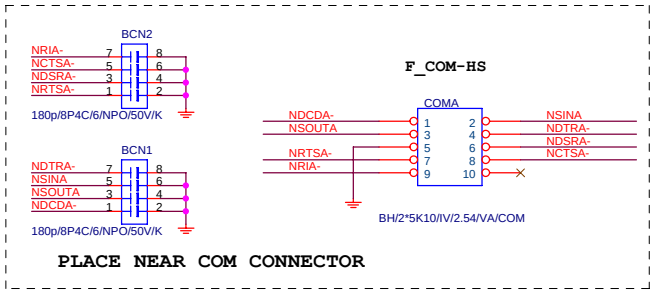
COMB

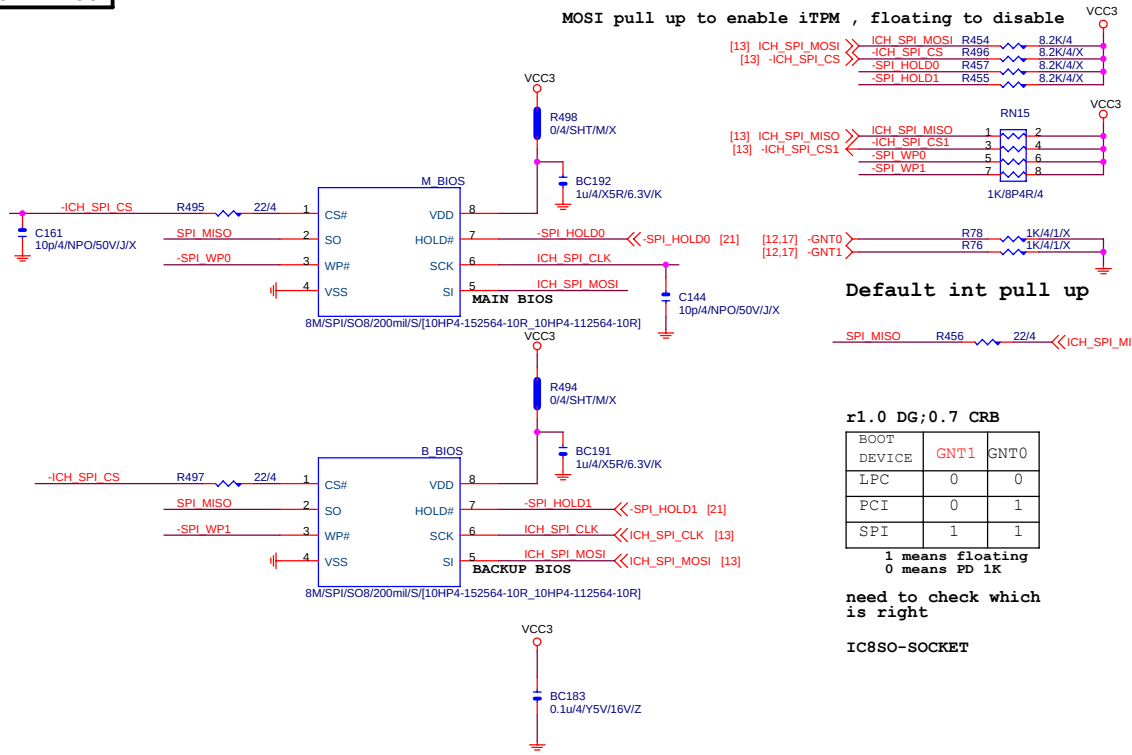


RING IN





DUAL BIOS



r1.0 DG;0.7 CRB

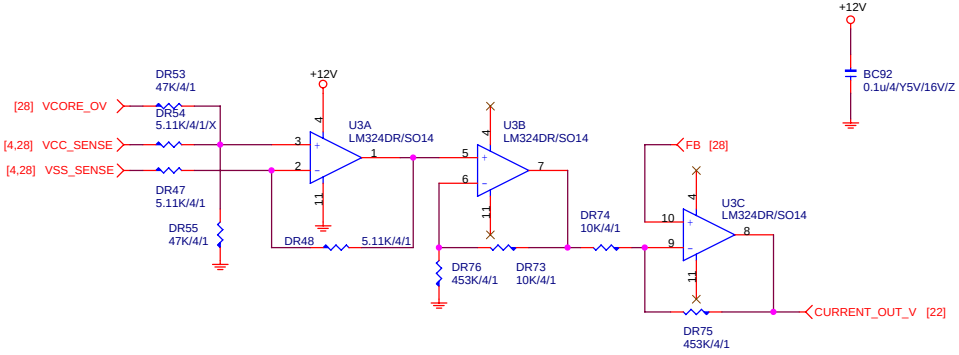
BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating
0 means PD 1K

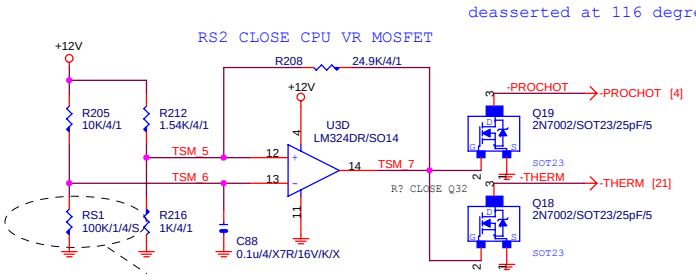
need to check which is right

IC8SO-SOCKET

DYNAMIC CURRENT OC



-PROHOT



Gigabyte Technology

Title: COM & PROHOT/Dynamic O.C.

Size: Custom | Document Number: GA-H55-USB3 | Rev: 1.01

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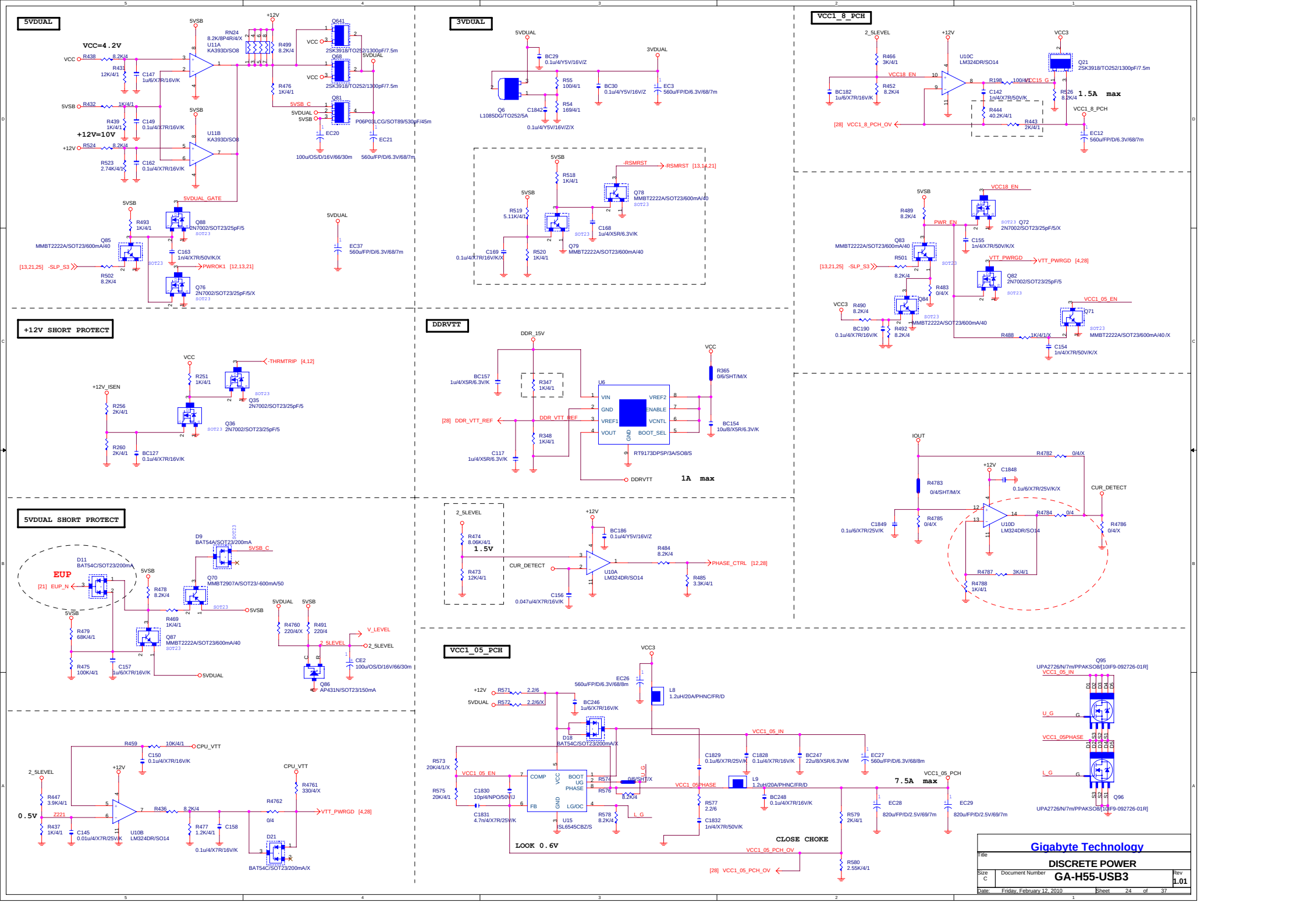
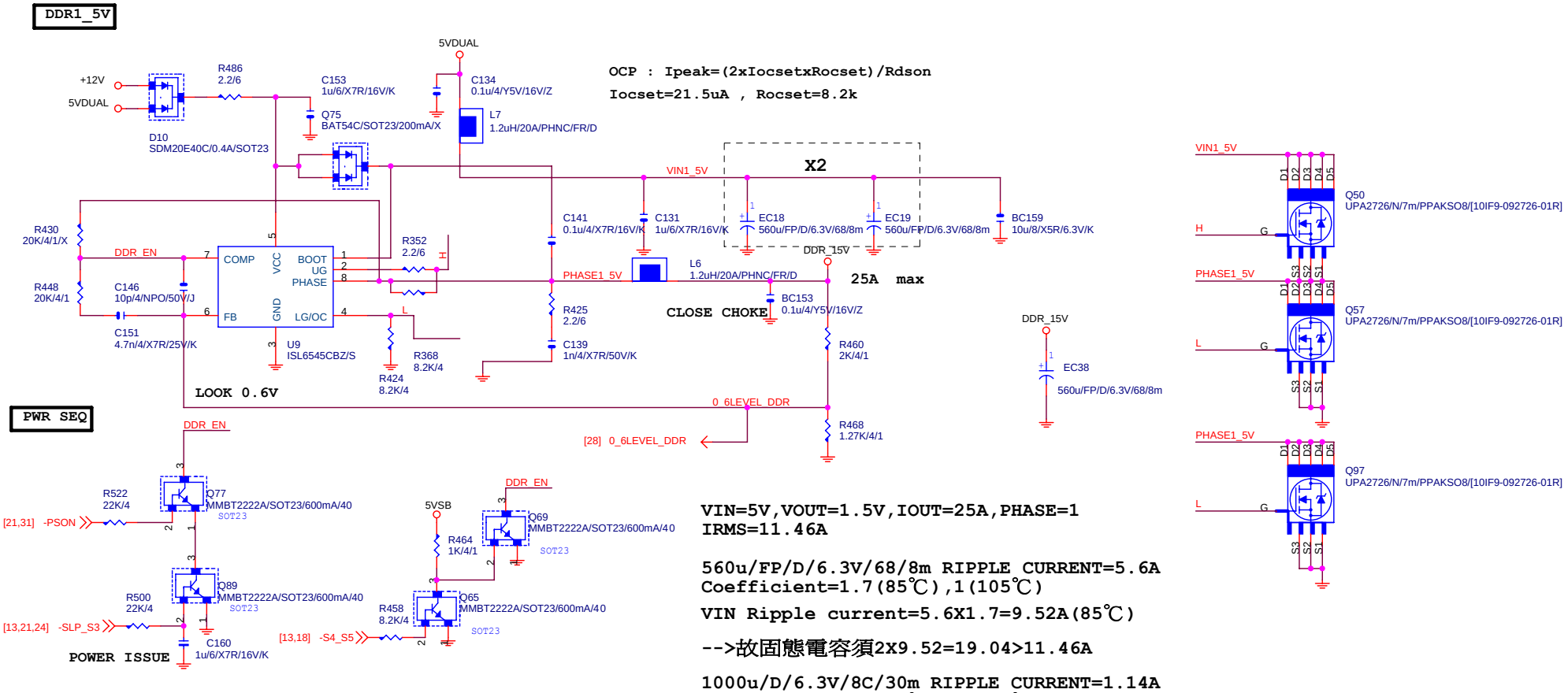

5VDUAL
VCC=4.2V
+12V=10V
5VDUAL_GATE
3VDUAL
VCC1_8_PCH
1.5A max
+12V SHORT PROTECT
DDRVTT
1A max
5VDUAL SHORT PROTECT
EUP
7.5A max
VCC1_05_PCH
LOOK 0.6V
CLOSE CHOKE
0.5V
Gigabyte Technology
DISCRETE POWER
GA-H55-USB3
Rev 1.01
Friday, February 12, 2010



VIN=5V,VOUT=1.5V,IOUT=25A,PHASE=1
IRMS=11.46A

560u/FP/D/6.3V/68/8m RIPPLE CURRENT=5.6A
Coefficient=1.7(85℃),1(105℃)

VIN Ripple current=5.6X1.7=9.52A(85℃)

-->故固態電容須2X9.52=19.04>11.46A

1000u/D/6.3V/8C/30m RIPPLE CURRENT=1.14A
Coefficient=1.7(85℃),1(105℃)

VIN Ripple current=1.14X1.7=1.938A(85℃)

-->故電解電容須6X1.938=11.628>11.46A

VIN=3V,VOUT=1.05V,IOUT=7.5A,PHASE=1
IRMS=3.5A

-->故固態電容須1X9.52=9.52>3.5A

-->故電解電容須2X1.938=3.876>3.5A

Gigabyte Technology		
Title: DDR_15V		
Size: B	Document Number: GA-H55-USB3	Rev: 1.01
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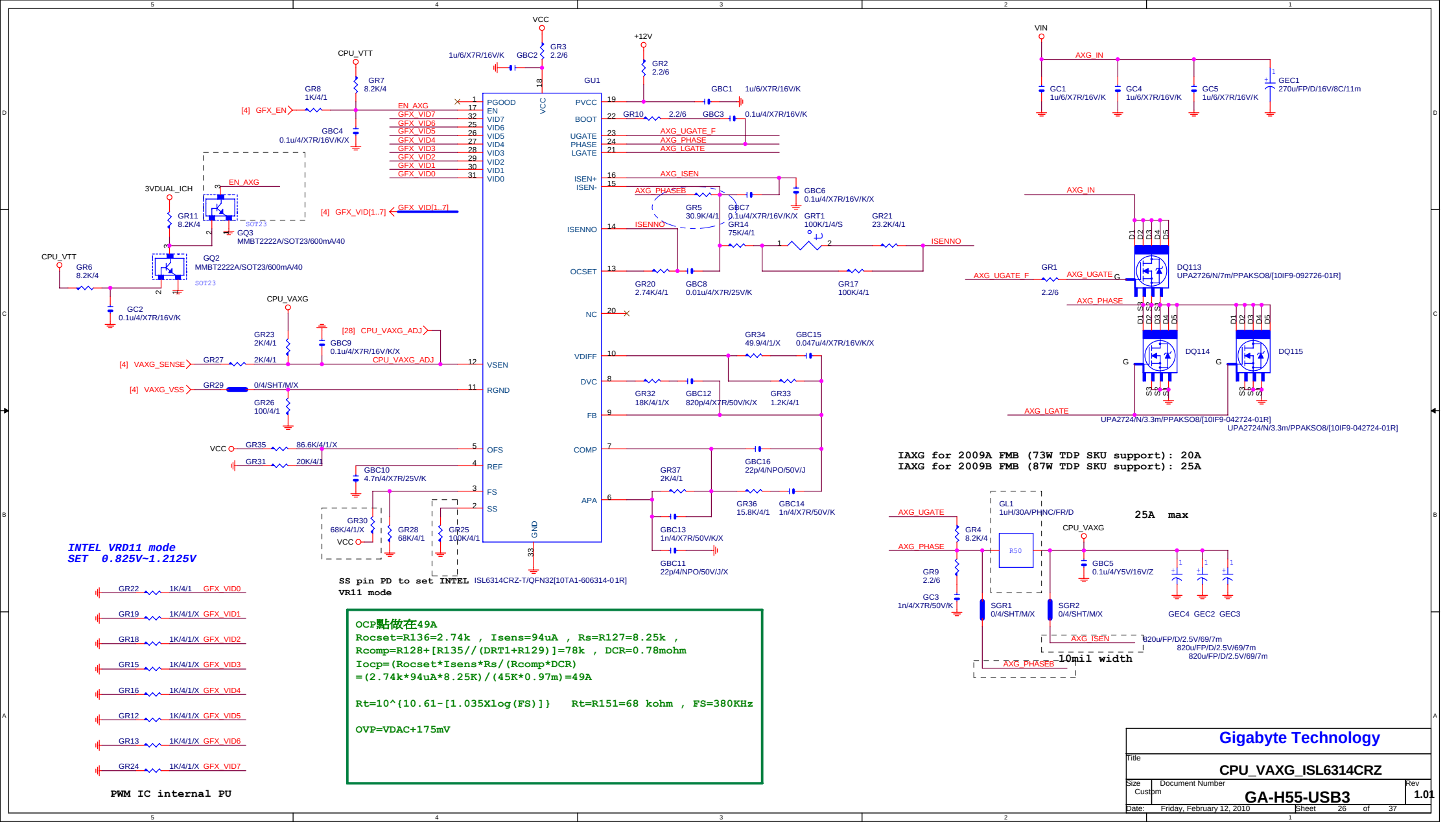
VCC
CPU_VTT
GR7 8.2K/4
GR8 1K/4/1
[4] GFX_EN
GBC4 0.1u/4/X7R/16V/K/X
1u/6/X7R/16V/K
GBC2
GR3 2.2/6
+12V
GR2 2.2/6
GU1
GBC1 1u/6/X7R/16V/K
EN_AXG
GFX_VID7
GFX_VID6
GFX_VID5
GFX_VID4
GFX_VID3
GFX_VID2
GFX_VID1
GFX_VID0
PGOOD
EN
VID7
VID6
VID5
VID4
VID3
VID2
VID1
VID0
PVCC
BOOT
UGATE
PHASE
LGATE
GR10 2.2/6
GBC3 0.1u/4/X7R/16V/K
AXG_UGATE_F
AXG_PHASE
AXG_LGATE
ISEN+
ISEN-
AXG_ISEN
AXG_PHASEB
GBC6 0.1u/4/X7R/16V/K/X
GR5 30.9K/4/1
GBC7 0.1u/4/X7R/16V/K/X
GR14 75K/4/1
GRT1 100K/1/4/S
GR21 23.2K/4/1
ISENNO
OCSET
GR20 2.74K/4/1
GBC8 0.01u/4/X7R/25V/K
GR17 100K/4/1
NC
VDIFF
DVC
FB
COMP
APA
GR34 49.9/4/1/X
GBC15 0.047u/4/X7R/16V/K/X
GR32 18K/4/1/X
GBC12 820p/4/X7R/50V/K/X
GR33 1.2K/4/1
GBC16 22p/4/NPO/50V/J
GR37 2K/4/1
GR36 15.8K/4/1
GBC14 1n/4/X7R/50V/K
GBC13 1n/4/X7R/50V/K/X
GBC11 22p/4/NPO/50V/J/X
3VDUAL_ICH
EN_AXG
GR11 8.2K/4
GQ3 MMBT2222A/SOT23/600mA/40
[4] GFX_VID[1..7]
GQ2 MMBT2222A/SOT23/600mA/40
GR6 8.2K/4
GC2 0.1u/4/X7R/16V/K
CPU_VAXG
GR23 2K/4/1
GBC9 0.1u/4/X7R/16V/K/X
[28] CPU_VAXG_ADJ
[4] VAXG_SENSE
GR27 2K/4/1
[4] VAXG_VSS
GR29 0/4/SHT/M/X
GR26 100/4/1
VSEN
RGND
GR35 86.6K/4/1/X
GR31 20K/4/1
OFS
REF
FS
SS
GBC10 4.7n/4/X7R/25V/K
GR30 68K/4/1/X
GR28 68K/4/1
GR25 100K/4/1
GND
SOT23
VIN
AXG_IN
GC1 1u/6/X7R/16V/K
GC4 1u/6/X7R/16V/K
GC5 1u/6/X7R/16V/K
GEC1 270u/FP/D/16V/8C/11m
GR1 2.2/6
DQ113 UPA2726/N/7m/PPAKSO8/[10IF9-092726-01R]
DQ114
DQ115
UPA2724/N/3.3m/PPAKSO8/[10IF9-042724-01R]
IAXG for 2009A FMB (73W TDP SKU support): 20A
IAXG for 2009B FMB (87W TDP SKU support): 25A
25A max
GL1 1uH/30A/PHNC/FR/D
GR4 8.2K/4
GR9 2.2/6
GC3 1n/4/X7R/50V/K
GBC5 0.1u/4/Y5V/16V/Z
SGR1 0/4/SHT/M/X
SGR2 0/4/SHT/M/X
GEC4 GEC2 GEC3
820u/FP/D/2.5V/69/7m
10mil width
INTEL VRD11 mode
SET 0.825V~1.2125V
GR22 1K/4/1 GFX_VID0
GR19 1K/4/1/X GFX_VID1
GR18 1K/4/1/X GFX_VID2
GR15 1K/4/1/X GFX_VID3
GR16 1K/4/1/X GFX_VID4
GR12 1K/4/1/X GFX_VID5
GR13 1K/4/1/X GFX_VID6
GR24 1K/4/1/X GFX_VID7
PWM IC internal PU
SS pin PD to set INTEL VR11 mode
ISL6314CRZ-T/QFN32[10TA1-606314-01R]
OCP點做在49A
Rocset=R136=2.74k , Isens=94uA , Rs=R127=8.25k ,
Rcomp=R128+[R135//(DRT1+R129)]=78k , DCR=0.78mohm
Iocp=(Rocset*Isens*Rs/(Rcomp*DCR)
=(2.74k*94uA*8.25K)/(45K*0.97m)=49A
Rt=10^{10.61-[1.035Xlog(FS)]} Rt=R151=68 kohm , FS=380KHz
OVP=VDAC+175mV
Gigabyte Technology
Title
CPU_VAXG_ISL6314CRZ
Size Custom
Document Number
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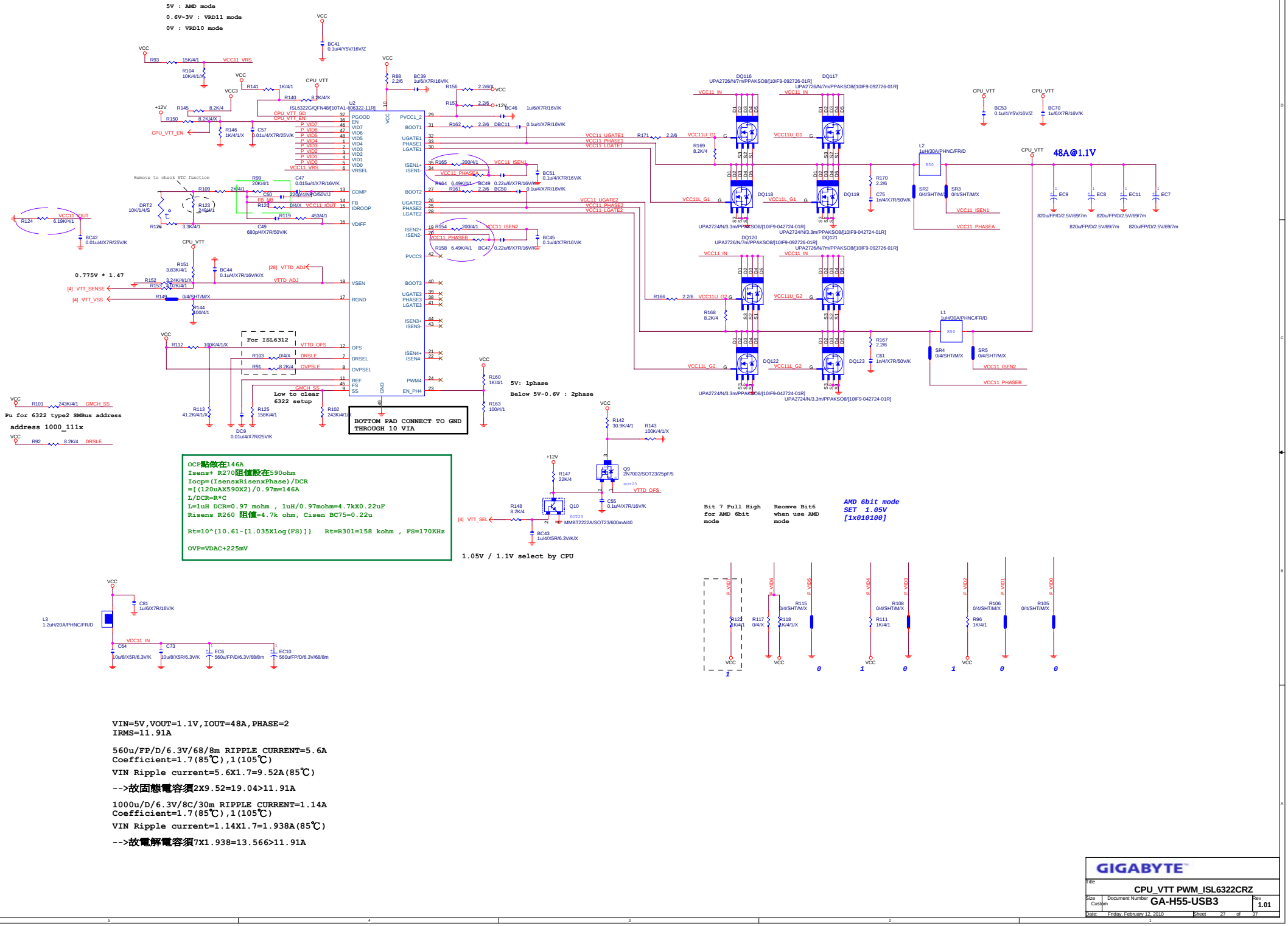
5V : AMD mode
0.6V~3V : VRD11 mode
0V : VRD10 mode
U2
ISL6322G/QFN48{10TA1-606322-11R}
Remove to check NTC function
For ISL6312
Low to clear 6322 setup
BOTTOM PAD CONNECT TO GND THROUGH 10 VIA
0.775V * 1.47
Pu for 6322 type2 SMBus address
address 1000_111x
5V: 1phase
Below 5V-0.6V : 2phase
1.05V / 1.1V select by CPU
OCP點做在146A
Isens+ R270阻值設在590ohm
Iocp=(IsensxRisenxPhase)/DCR
=[(120uAX590X2)/0.97m=146A
L/DCR=R*C
L=1uH DCR=0.97 mohm , 1uH/0.97mohm=4.7kX0.22uF
Risens R260 阻值=4.7k ohm, Cisen BC75=0.22u
Rt=10^{10.61-[1.035Xlog(FS)]} Rt=R301=158 kohm , FS=170KHz
OVP=VDAC+225mV
Bit 7 Pull High for AMD 6bit mode
Reomve Bit6 when use AMD mode
AMD 6bit mode SET 1.05V [1x010100]
48A@1.1V
VIN=5V,VOUT=1.1V,IOUT=48A,PHASE=2
IRMS=11.91A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=5.6A
Coefficient=1.7(85℃),1(105℃)
VIN Ripple current=5.6X1.7=9.52A(85℃)
-->故固態電容須2X9.52=19.04>11.91A
1000u/D/6.3V/8C/30m RIPPLE CURRENT=1.14A
Coefficient=1.7(85℃),1(105℃)
VIN Ripple current=1.14X1.7=1.938A(85℃)
-->故電解電容須7X1.938=13.566>11.91A
GIGABYTE
CPU_VTT PWM_ISL6322CRZ
GA-H55-USB3
1.01
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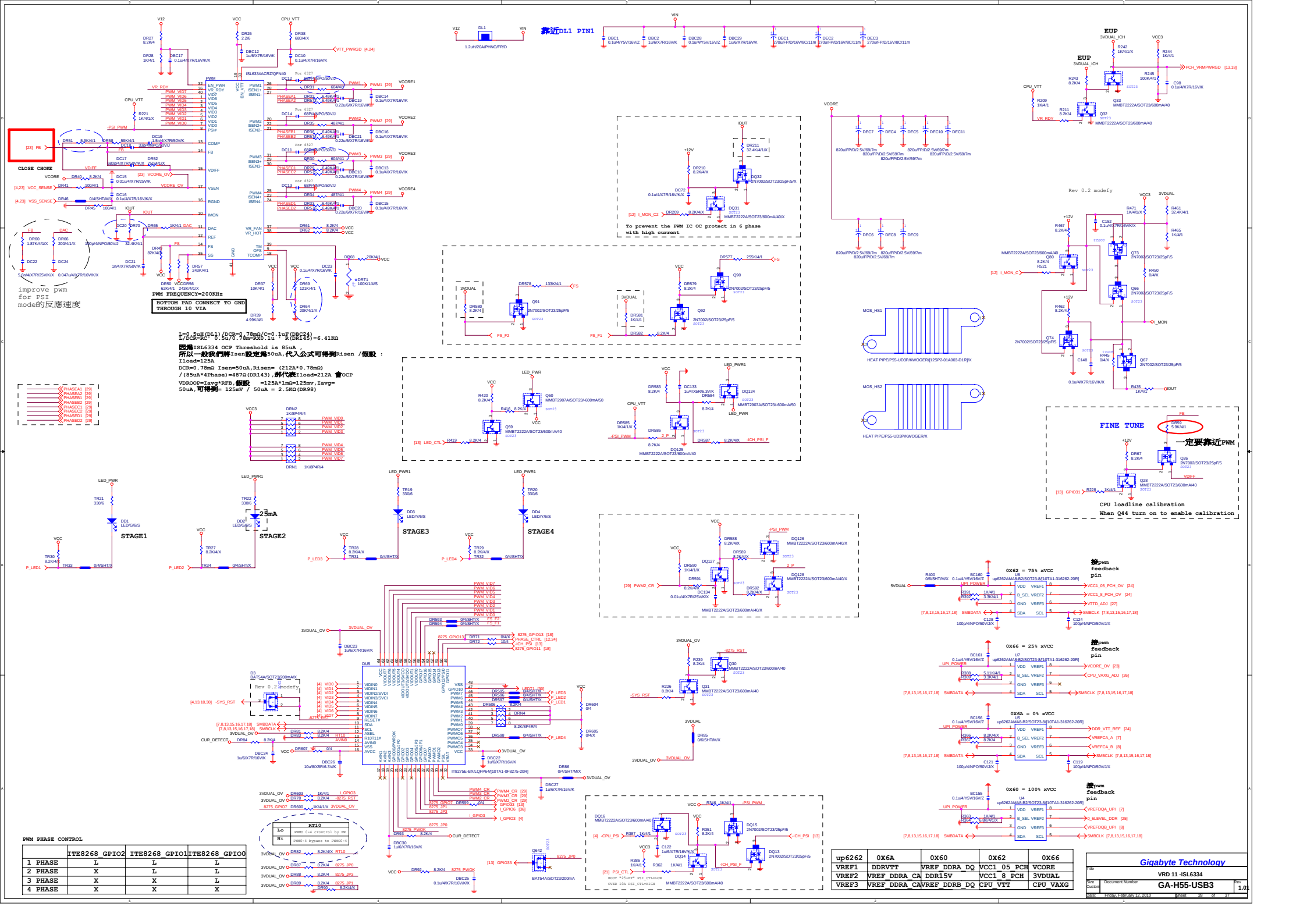

	ITE8268_GPIO2	ITE8268_GPIO1	ITE8268_GPIO0
1 PHASE	L	L	L
2 PHASE	X	L	L
3 PHASE	X	X	L
4 PHASE	X	X	X

up6262	0X6A	0X60	0X62	0X66
VREF1	DDRVTT	VREF_DDRA_DQ	VCC1_05_PCH	VCORE
VREF2	VREF_DDRA_CA	DDR15V	VCC1_8_PCH	3VDUAL
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	CPU_VTT	CPU_VAXG

Gigabyte Technology

Title	VRD 11 -ISL6334		
Size	Document Number	GA-H55-USB3	Rev 1.01
Custom			
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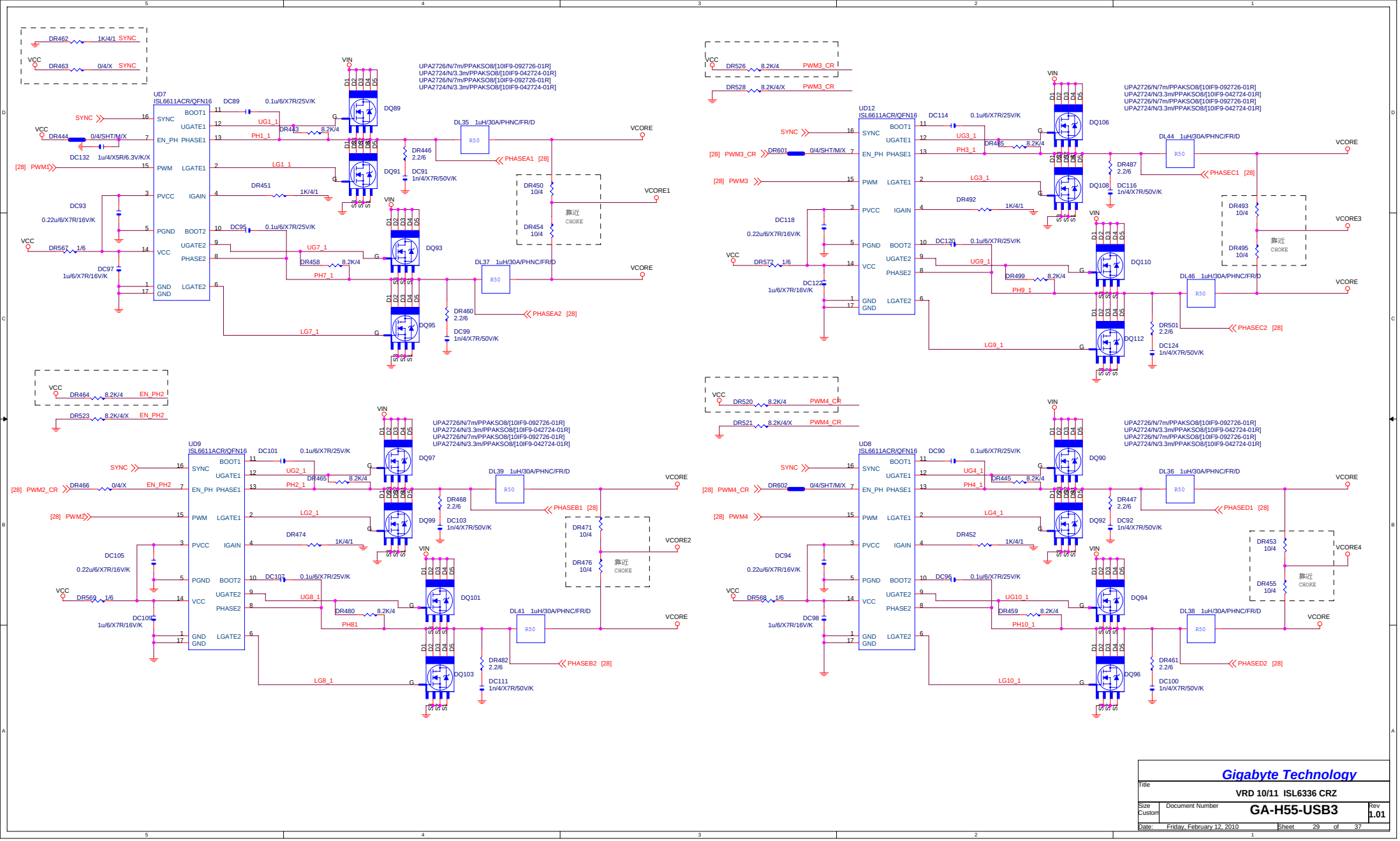
UD7
ISL6611ACR/QFN16
UD12
ISL6611ACR/QFN16
UD9
ISL6611ACR/QFN16
UD8
ISL6611ACR/QFN16
SYNC
BOOT1
UGATE1
EN_PH
PHASE1
PWM
LGATE1
PVCC
IGAIN
PGND
BOOT2
VCC
UGATE2
PHASE2
GND
LGATE2
VIN
VCORE
VCORE1
VCORE2
VCORE3
VCORE4
UPA2726/N/7m/PPAKSO8/[10IF9-092726-01R]
UPA2724/N/3.3m/PPAKSO8/[10IF9-042724-01R]
靠近
CHOKE
R50
Gigabyte Technology
VRD 10/11 ISL6336 CRZ
GA-H55-USB3
Custom
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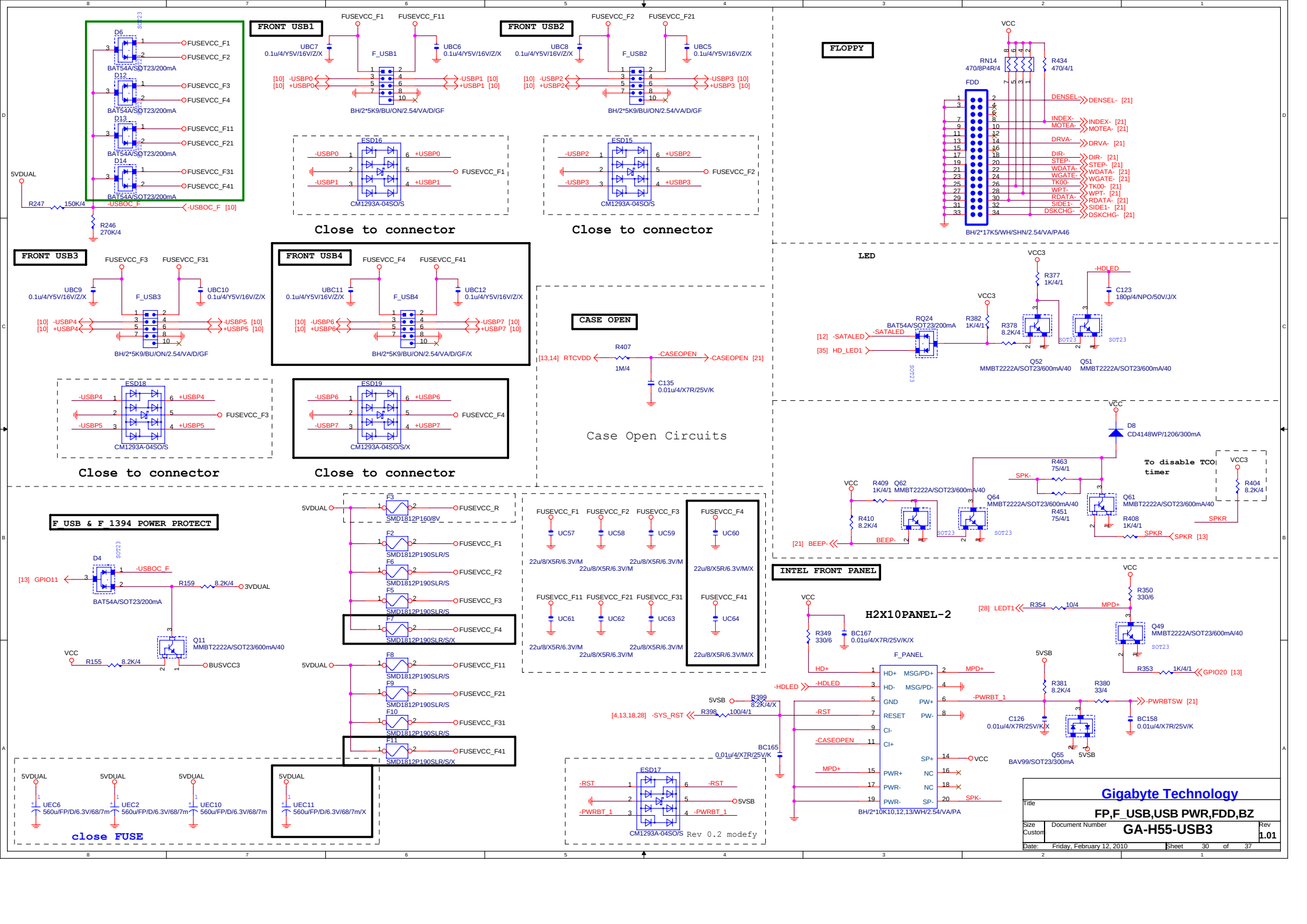
FRONT USB1
FRONT USB2
FRONT USB3
FRONT USB4
FLOPPY
LED
CASE OPEN
F_USB & F_1394 POWER PROTECT
INTEL FRONT PANEL
H2X10PANEL-2
Close to connector
Close to connector
Close to connector
Close to connector
Case Open Circuits
close FUSE
To disable TCO timer
Gigabyte Technology
FP,F_USB,USB PWR,FDD,BZ
GA-H55-USB3
1.01
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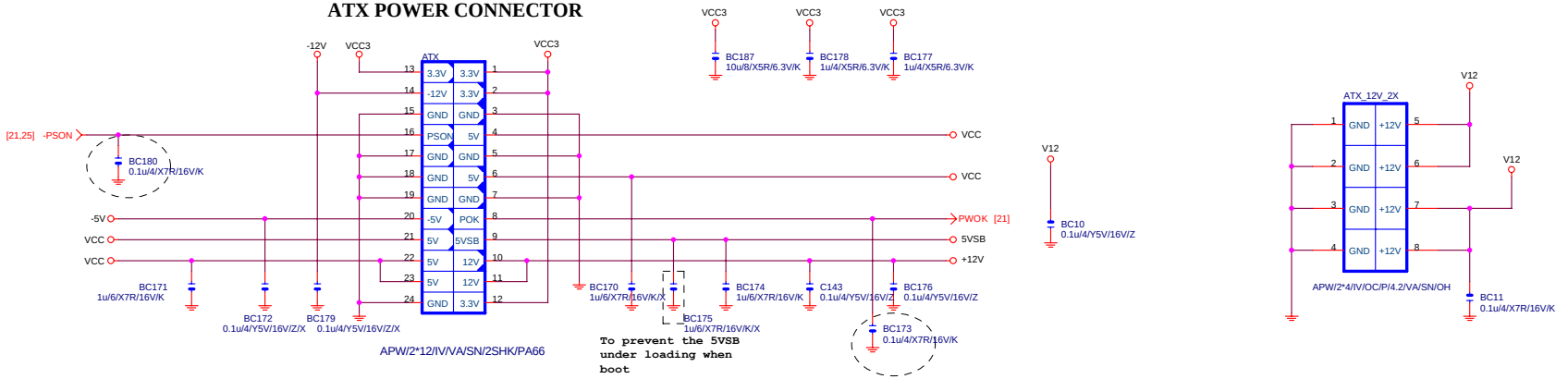
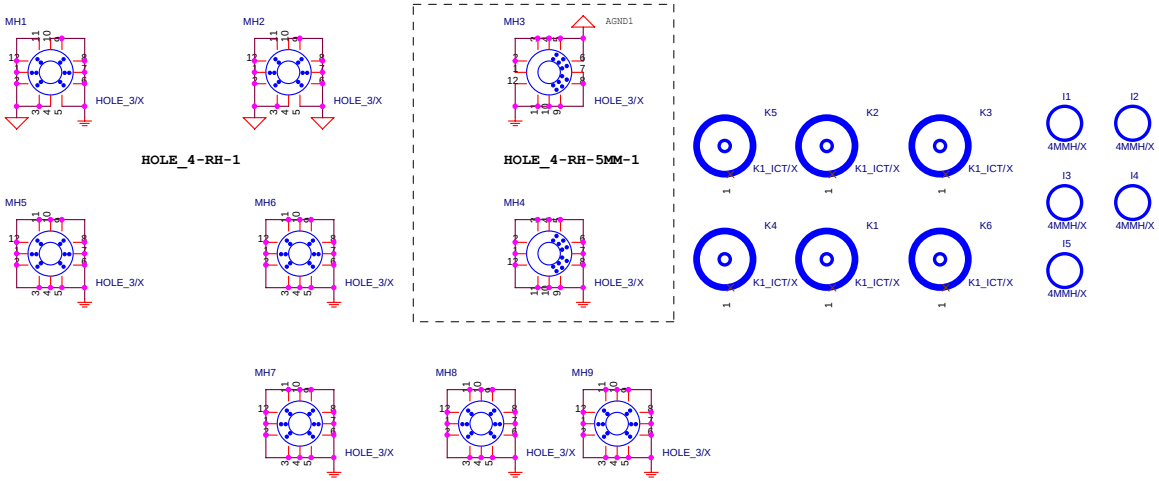
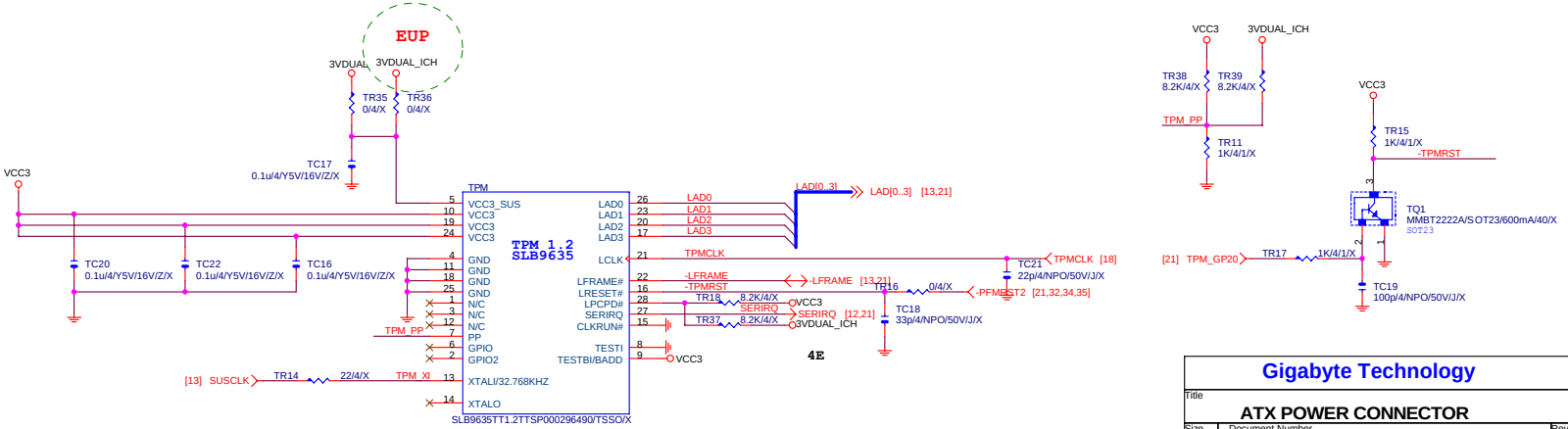
Rev 0.2 modefy

ATX POWER CONNECTOR





TPM



Gigabyte Technology		
Title: ATX POWER CONNECTOR		
Size: Custom	Document Number: GA-H55-USB3	Rev: 1.01
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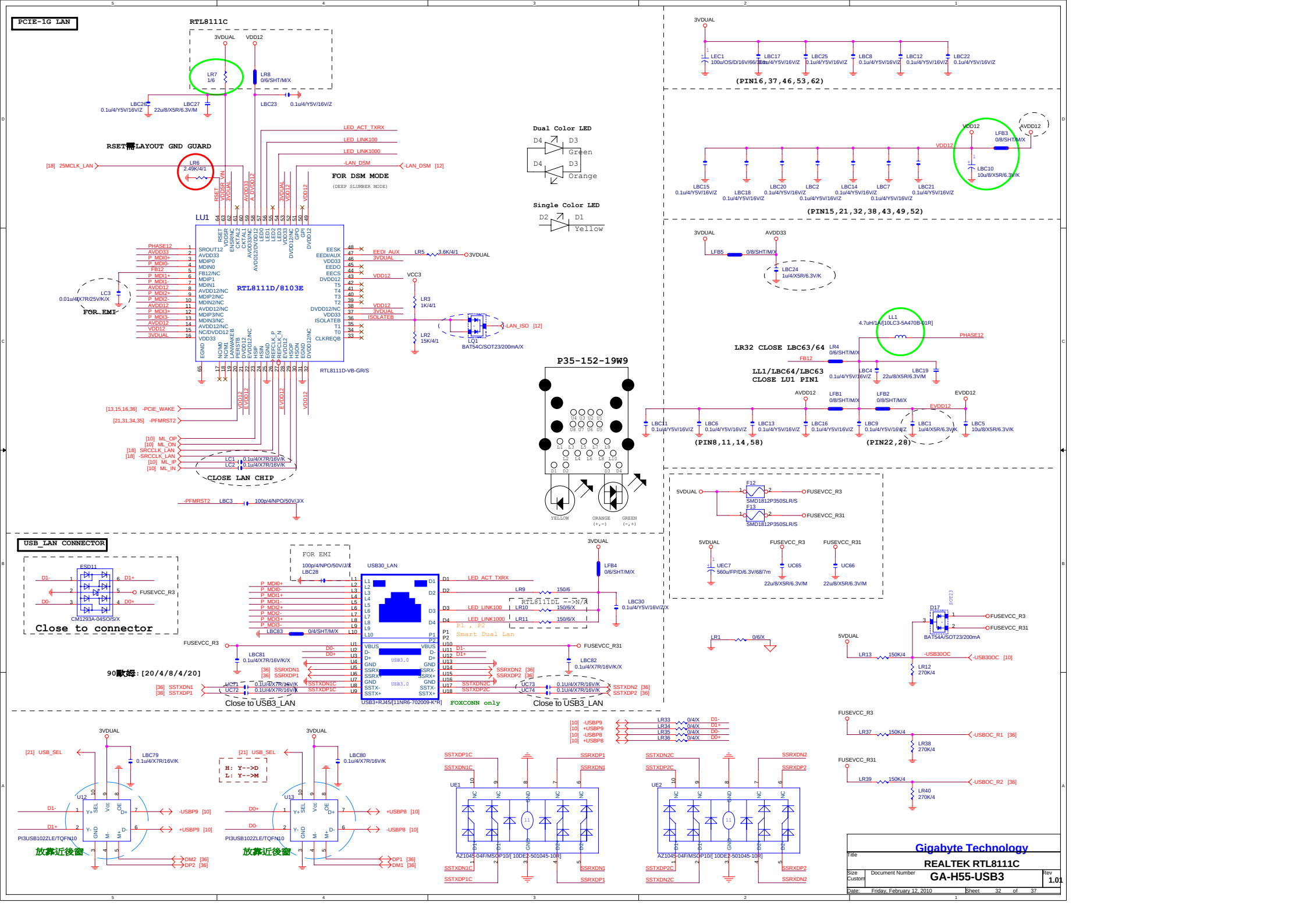
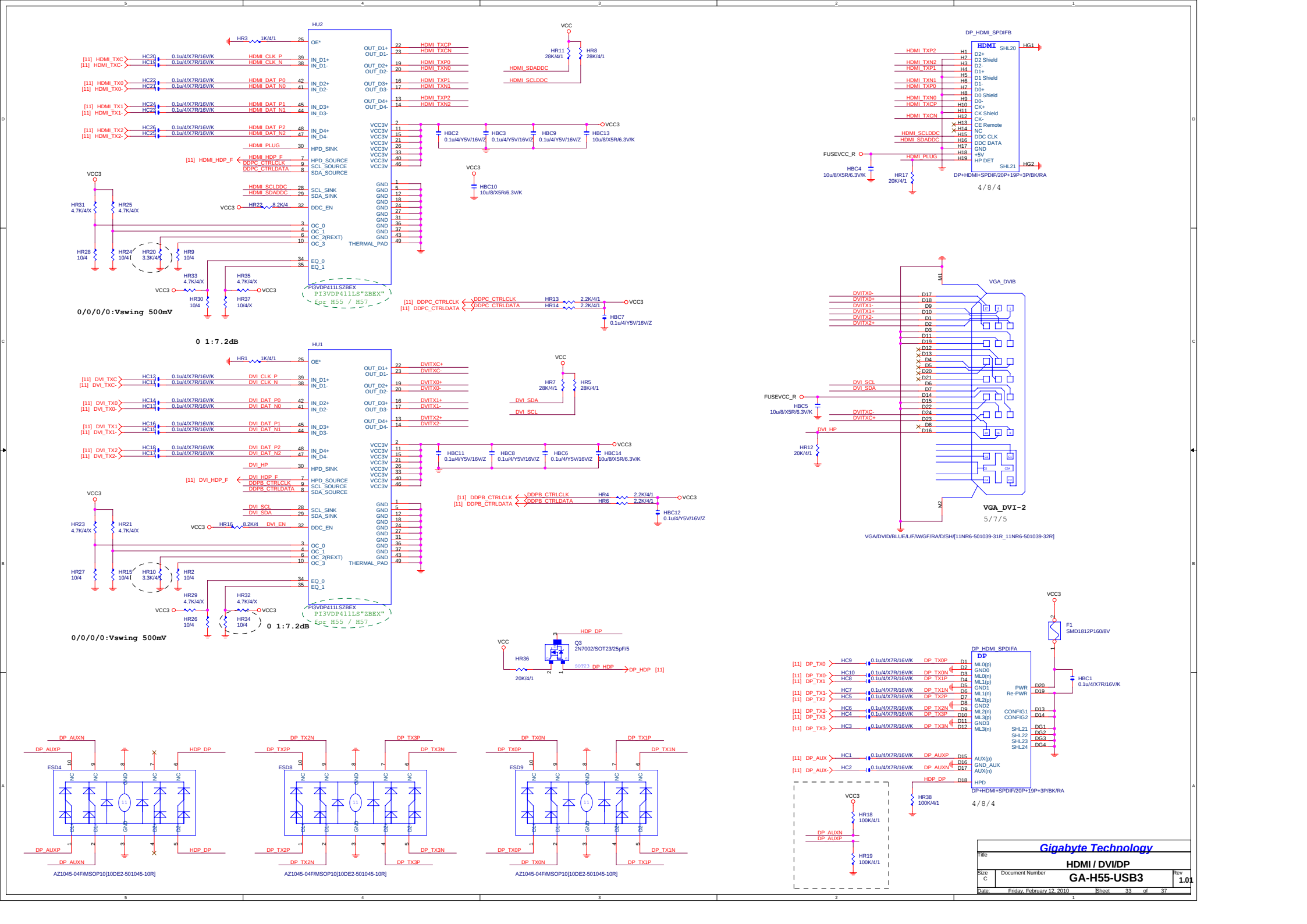

PCIE-1G LAN
RTL8111C
3VDUAL
VDD12
LR7 1/6
LR8 0/6/SHT/M/X
LBC26 0.1u/4/Y5V/16V/Z
LBC27 22u/8/X5R/6.3V/M
LBC23 0.1u/4/Y5V/16V/Z
RSET需LAYOUT GND GUARD
LR6 2.49K/4/1
[18] 25MCLK_LAN
LED_ACT_TXRX
LED_LINK100
LED_LINK1000
-LAN_DSM
-LAN_DSM [12]
FOR DSM MODE
(DEEP SLUMBER MODE)
Dual Color LED
D4 D3 Green
D4 D3 Orange
Single Color LED
D2 D1 Yellow
LU1
RTL8111D/8103E
RTL8111D-VB-GR/S
PHASE12
FB12
LC3 0.01u/4X7R/25V/K/X
FOR EMI
LR5 3.6K/4/1
LR3 1K/4/1
LR2 15K/4/1
VCC3
EEDI_AUX
LO1 BAT54C/SOT23/200mA/X
-LAN_ISO [12]
[13,15,16,36] -PCIE_WAKE
[21,31,34,35] -PFMRST2
[10] ML_OP
[10] ML_ON
[18] SRCCLK_LAN
[18] -SRCCLK_LAN
[10] ML_IP
[10] ML_IN
LC1 0.1u/4/X7R/16V/K
LC2 0.1u/4/X7R/16V/K
CLOSE LAN CHIP
-PFMRST2
LBC3 100p/4/NPO/50V/J/X
P35-152-19W9
YELLOW
ORANGE (+,-)
GREEN (-,+)
3VDUAL
LEC1 100u/OS/D/16V/66/10m
LBC17 0.1u/4/Y5V/16V/Z
LBC25 0.1u/4/Y5V/16V/Z
LBC8 0.1u/4/Y5V/16V/Z
LBC12 0.1u/4/Y5V/16V/Z
LBC22 0.1u/4/Y5V/16V/Z
(PIN16,37,46,53,62)
VDD12
AVDD12
LFB3 0/8/SHT/M/X
LBC10 10u/8/X5R/6.3V/K
LBC15 0.1u/4/Y5V/16V/Z
LBC18 0.1u/4/Y5V/16V/Z
LBC20 0.1u/4/Y5V/16V/Z
LBC2 0.1u/4/Y5V/16V/Z
LBC14 0.1u/4/Y5V/16V/Z
LBC7 0.1u/4/Y5V/16V/Z
LBC21 0.1u/4/Y5V/16V/Z
(PIN15,21,32,38,43,49,52)
AVDD33
LFB5 0/8/SHT/M/X
LBC24 1u/4/X5R/6.3V/K
LR32 CLOSE LBC63/64
LL1/LBC64/LBC63
CLOSE LU1 PIN1
LL1 4.7uH/1A/[10LC3-5A470B-01R]
LR4 0/6/SHT/M/X
LBC4 0.1u/4/Y5V/16V/Z
LBC19 22u/8/X5R/6.3V/M
LFB1 0/8/SHT/M/X
LFB2 0/8/SHT/M/X
LBC11 0.1u/4/Y5V/16V/Z
LBC6 0.1u/4/Y5V/16V/Z
LBC13 0.1u/4/Y5V/16V/Z
LBC16 0.1u/4/Y5V/16V/Z
LBC9 0.1u/4/Y5V/16V/Z
LBC1 1u/4/X5R/6.3V/K
LBC5 10u/8/X5R/6.3V/M
(PIN8,11,14,58)
(PIN22,28)
5VDUAL
F12 SMD1812P350SLR/S
F13 SMD1812P350SLR/S
FUSEVCC_R3
FUSEVCC_R31
UEC7 560u/FP/D/6.3V/68/7m
UC65 22u/8/X5R/6.3V/M
UC66 22u/8/X5R/6.3V/M
USB_LAN CONNECTOR
ESD11 CM1293A-04SO/S/X
Close to connector
FOR EMI
100p/4/NPO/50V/J/X
LBC28
USB30_LAN
LFB4 0/6/SHT/M/X
LED_ACT_TXRX
LED_LINK100
LED_LINK1000
LR9 150/6
LR10 150/6/X
LR11 150/6/X
LBC30 0.1u/4/Y5V/16V/Z/X
RTL8111DL --SN74
Smart Dual Lan
LBC83 0/4/SHT/M/X
LBC81 0.1u/4/X7R/16V/K/X
LBC82 0.1u/4/X7R/16V/K/X
90歐姆:[20/4/8/4/20]
[36] SSTXDN1
[36] SSTXDP1
[36] SSRXDN1
[36] SSRXDP1
SSTXDN1C
SSTXDP1C
SSRXDN2 [36]
SSRXDP2 [36]
UC71 0.1u/4/X7R/16V/K
UC72 0.1u/4/X7R/16V/K
UC73 0.1u/4/X7R/16V/K
UC74 0.1u/4/X7R/16V/K
SSTXDN2 [36]
SSTXDP2 [36]
Close to USB3_LAN
Close to USB3_LAN
USB3+RJ45[11NR6-702009-K*R]
FOXCONN only
LR1 0/6/X
D17 BAT54A/SOT23/200mA
LR13 150K/4
LR12 270K/4
-USB30OC
-USB30OC [10]
[21] USB_SEL
LBC79 0.1u/4/X7R/16V/K
U12
PI3USB102ZLE/TQFN10
-USBP9 [10]
+USBP9 [10]
放靠近後窗
H: Y-->D
L: Y-->M
LBC80 0.1u/4/X7R/16V/K
U13
+USBP8 [10]
-USBP8 [10]
DM2 [36]
DP2 [36]
DP1 [36]
DM1 [36]
[10] -USBP9
[10] +USBP9
[10] -USBP8
[10] +USBP8
LR33 0/4/X
LR34 0/4/X
LR35 0/4/X
LR36 0/4/X
D1-
D1+
D0-
D0+
UE1
UE2
AZ1045-04F/MSOP10/[10DEZ-501045-10R]
SSTXDP1C
SSTXDN1C
SSRXDP1
SSRXDN1
SSTXDP2C
SSTXDN2C
SSRXDP2
SSRXDN2
-USBOC_R1 [36]
-USBOC_R2 [36]
LR37 150K/4
LR38 270K/4
LR39 150K/4
LR40 270K/4
Gigabyte Technology
Title
REALTEK RTL8111C
Size Custom
Document Number
GA-H55-USB3
Rev 1.01
Date: Friday, February 12, 2010
Sheet 32 of 37


HU2
HR3 1K/4/1
OE*
[11] HDMI_TXC
[11] HDMI_TXC-
HC20 0.1u/4/X7R/16V/K
HC19 0.1u/4/X7R/16V/K
HDMI_CLK_P
HDMI_CLK_N
IN_D1+
IN_D1-
[11] HDMI_TX0
[11] HDMI_TX0-
HC22 0.1u/4/X7R/16V/K
HC21 0.1u/4/X7R/16V/K
HDMI_DAT_P0
HDMI_DAT_N0
IN_D2+
IN_D2-
[11] HDMI_TX1
[11] HDMI_TX1-
HC24 0.1u/4/X7R/16V/K
HC23 0.1u/4/X7R/16V/K
HDMI_DAT_P1
HDMI_DAT_N1
IN_D3+
IN_D3-
[11] HDMI_TX2
[11] HDMI_TX2-
HC26 0.1u/4/X7R/16V/K
HC25 0.1u/4/X7R/16V/K
HDMI_DAT_P2
HDMI_DAT_N2
IN_D4+
IN_D4-
HDMI_PLUG
HPD_SINK
[11] HDMI_HDP_F
HDMI_HDP_F
DDPC_CTRLCLK
DDPC_CTRLDATA
HPD_SOURCE
SCL_SOURCE
SDA_SOURCE
HDMI_SCLDDC
HDMI_SDADDC
SCL_SINK
SDA_SINK
VCC3
HR22 8.2K/4
DDC_EN
OC_0
OC_1
OC_2(REXT)
OC_3
EQ_0
EQ_1
THERMAL_PAD
OUT_D1+
OUT_D1-
OUT_D2+
OUT_D2-
OUT_D3+
OUT_D3-
OUT_D4+
OUT_D4-
HDMI_TXCP
HDMI_TXCN
HDMI_TX0P
HDMI_TXN0
HDMI_TX1P
HDMI_TXN1
HDMI_TX2P
HDMI_TXN2
VCC3V
GND
HR31 4.7K/4/X
HR25 4.7K/4/X
HR28 10/4
HR24 10/4
HR20 3.3K/4/X
HR9 10/4
HR33 4.7K/4/X
HR35 4.7K/4/X
HR30 10/4
HR37 10/4/X
PI3VDP411LSZBEX
PI3VDP411LS"ZBEX"
for H55 / H57
0/0/0/0:Vswing 500mV
0 1:7.2dB
HBC2 0.1u/4/Y5V/16V/Z
HBC3 0.1u/4/Y5V/16V/Z
HBC9 0.1u/4/Y5V/16V/Z
HBC13 10u/8/X5R/6.3V/K
HBC10 10u/8/X5R/6.3V/K
HR11 28K/4/1
HR8 28K/4/1
HDMI_SDADDC
HDMI_SCLDDC
[11] DDPC_CTRLCLK
[11] DDPC_CTRLDATA
HR13 2.2K/4/1
HR14 2.2K/4/1
HBC7 0.1u/4/Y5V/16V/Z
HU1
HR1 1K/4/1
[11] DVI_TXC
[11] DVI_TXC-
HC12 0.1u/4/X7R/16V/K
HC11 0.1u/4/X7R/16V/K
DVI_CLK_P
DVI_CLK_N
[11] DVI_TX0
[11] DVI_TX0-
HC14 0.1u/4/X7R/16V/K
HC13 0.1u/4/X7R/16V/K
DVI_DAT_P0
DVI_DAT_N0
[11] DVI_TX1
[11] DVI_TX1-
HC16 0.1u/4/X7R/16V/K
HC15 0.1u/4/X7R/16V/K
DVI_DAT_P1
DVI_DAT_N1
[11] DVI_TX2
[11] DVI_TX2-
HC18 0.1u/4/X7R/16V/K
HC17 0.1u/4/X7R/16V/K
DVI_DAT_P2
DVI_DAT_N2
DVI_HP
[11] DVI_HDP_F
DVI_HDP_F
DDPB_CTRLCLK
DDPB_CTRLDATA
DVI_SCL
DVI_SDA
HR16 8.2K/4
DVI_EN
HR23 4.7K/4/X
HR21 4.7K/4/X
HR27 10/4
HR15 10/4
HR10 3.3K/4/X
HR2 10/4
HR29 4.7K/4/X
HR32 4.7K/4/X
HR26 10/4
HR34 10/4
DVITXC+
DVITXC-
DVITX0+
DVITX0-
DVITX1+
DVITX1-
DVITX2+
DVITX2-
HR7 28K/4/1
HR5 28K/4/1
HBC11 0.1u/4/Y5V/16V/Z
HBC8 0.1u/4/Y5V/16V/Z
HBC6 0.1u/4/Y5V/16V/Z
HBC14 10u/8/X5R/6.3V/K
[11] DDPB_CTRLCLK
[11] DDPB_CTRLDATA
HR4 2.2K/4/1
HR6 2.2K/4/1
HBC12 0.1u/4/Y5V/16V/Z
HDP_DP
Q3
2N7002/SOT23/25pF/5
SOT23
HR36 20K/4/1
DP_HDP [11]
DP_HDMI_SPDIFB
HDMI
SHL20
HG1
HDMI_TXP2
HDMI_TXN2
HDMI_TXP1
HDMI_TXN1
HDMI_TXP0
HDMI_TXN0
HDMI_TXCP
HDMI_TXCN
HDMI_SCLDDC
HDMI_SDADDC
HDMI_PLUG
SHL21
HG2
FUSEVCC_R
HBC4 10u/8/X5R/6.3V/K
HR17 20K/4/1
DP+HDMI+SPDIF/20P+19P+3P/BK/RA
4/8/4
VGA_DVIB
M1
M2
DVITX0-
DVITX0+
DVITX1-
DVITX1+
DVITX2-
DVITX2+
DVITXC-
DVITXC+
DVI_SCL
DVI_SDA
DVI_HP
FUSEVCC_R
HBC5 10u/8/X5R/6.3V/K
HR12 20K/4/1
VGA_DVI-2
5/7/5
VGA/DVID/BLUE/L/F/W/GF/RA/D/SH/[11NR6-501039-31R_11NR6-501039-32R]
DP_HDMI_SPDIFA
DP
ML0(p)
GND0
ML0(n)
ML1(p)
GND1
ML1(n)
ML2(p)
GND2
ML2(n)
ML3(p)
GND3
ML3(n)
AUX(p)
GND_AUX
AUX(n)
HPD
PWR
Re-PWR
CONFIG1
CONFIG2
SHL21
SHL22
SHL23
SHL24
F1 SMD1812P160/8V
HBC1 0.1u/4/X7R/16V/K
DP_TX0P
DP_TX0N
DP_TX1P
DP_TX1N
DP_TX2P
DP_TX2N
DP_TX3P
DP_TX3N
DP_AUX
DP_AUXN
HR38 100K/4/1
HR18 100K/4/1
HR19 100K/4/1
DP+HDMI+SPDIF/20P+19P+3P/BK/RA
4/8/4
ESD4
ESD8
ESD9
AZ1045-04F/MSOP10[10DE2-501045-10R]
AZ1045-04F/MSOP10[10DE2-501045-10R]
AZ1045-04F/MSOP10[10DE2-501045-10R]
Gigabyte Technology
Title
HDMI / DVI/DP
Size C
Document Number
GA-H55-USB3
Rev 1.01
Date: Friday, February 12, 2010
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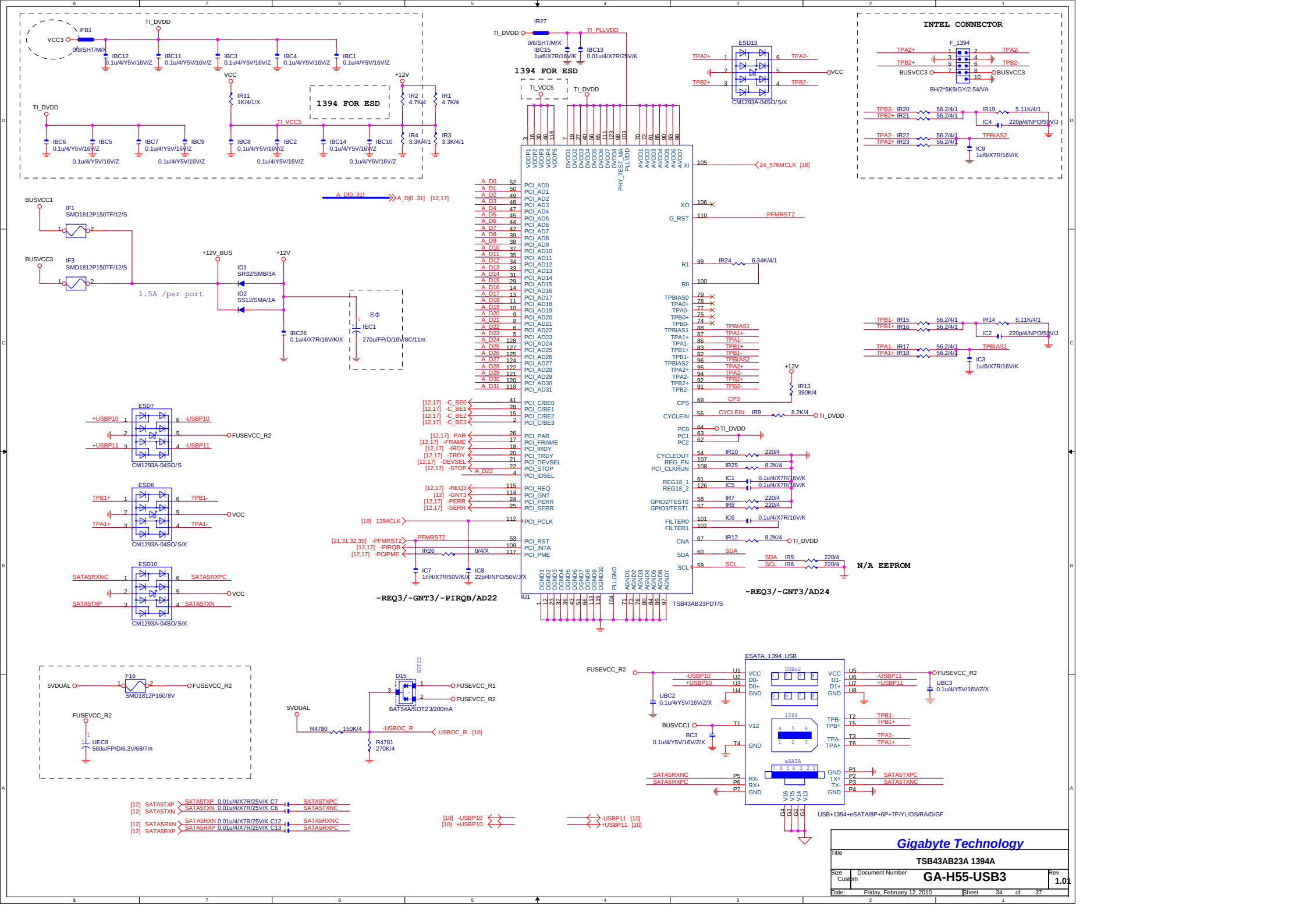
INTEL CONNECTOR
F_1394
BH/2*5K9/GY/2.54/VA
1394 FOR ESD
1394 FOR ESD
ESD13
CM1293A-04SO/S/X
TSB43AB23PDT/S
-REQ3/-GNT3/-PIRQB/AD22
-REQ3/-GNT3/AD24
N/A EEPROM
ESD7
CM1293A-04SO/S
ESD6
CM1293A-04SO/S/X
ESD10
CM1293A-04SO/S/X
ESATA_1394_USB
USB+1394+eSATA/8P+6P+7P/YL/OS/RA/D/GF
BAT54A/SOT23/200mA
SMD1812P160/8V
SMD1812P150TF/12/S
SMD1812P150TF/12/S
SR32/SMB/3A
SS12/SMA/1A
1.5A /per port
270u/FP/D/16V/8C/11m
560u/FP/D/6.3V/68/7m
Gigabyte Technology
TSB43AB23A 1394A
GA-H55-USB3
1.01
Friday, February 12, 2010
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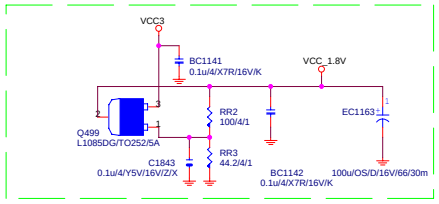
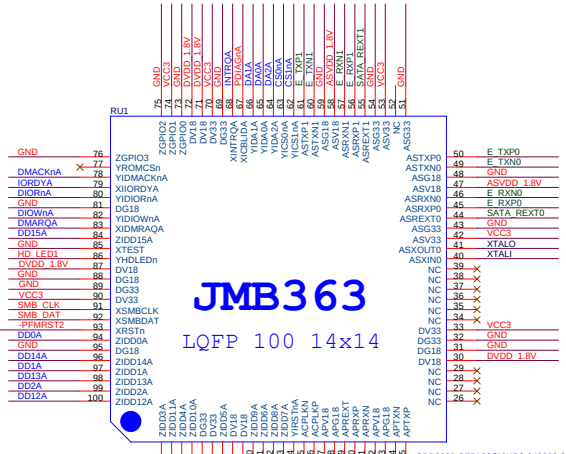
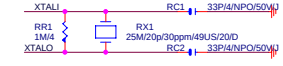
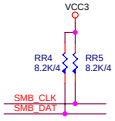
JMicron

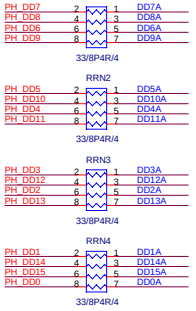
3.3V to 1.8V Voltage Regulator

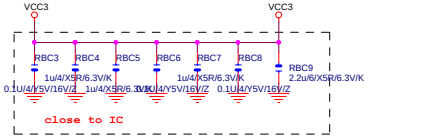


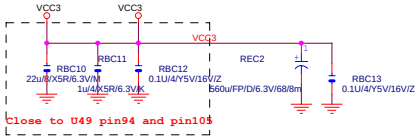


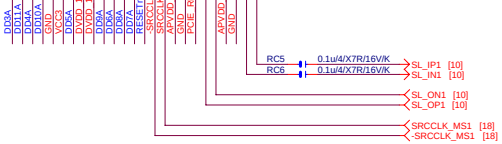


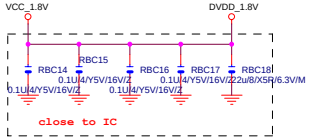


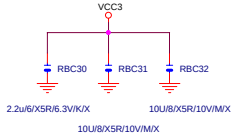


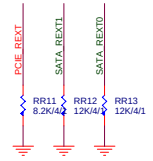




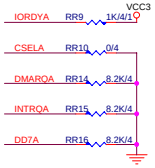


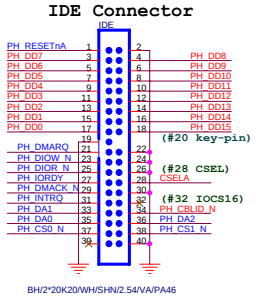






check SATA eye
PCIE_REXT --> 8.06K/4/1





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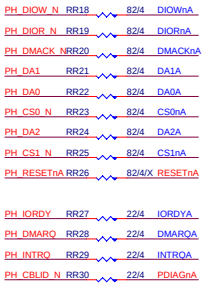
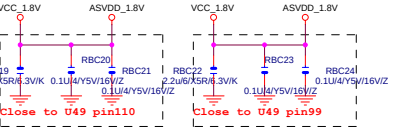
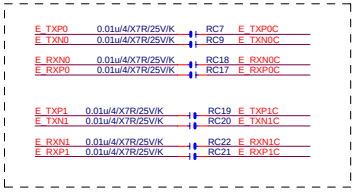
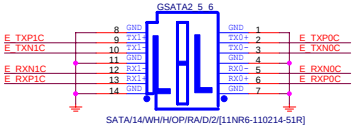
connector

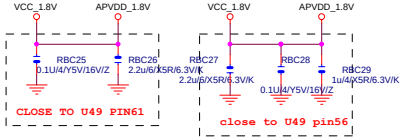


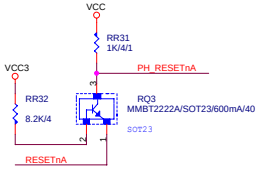


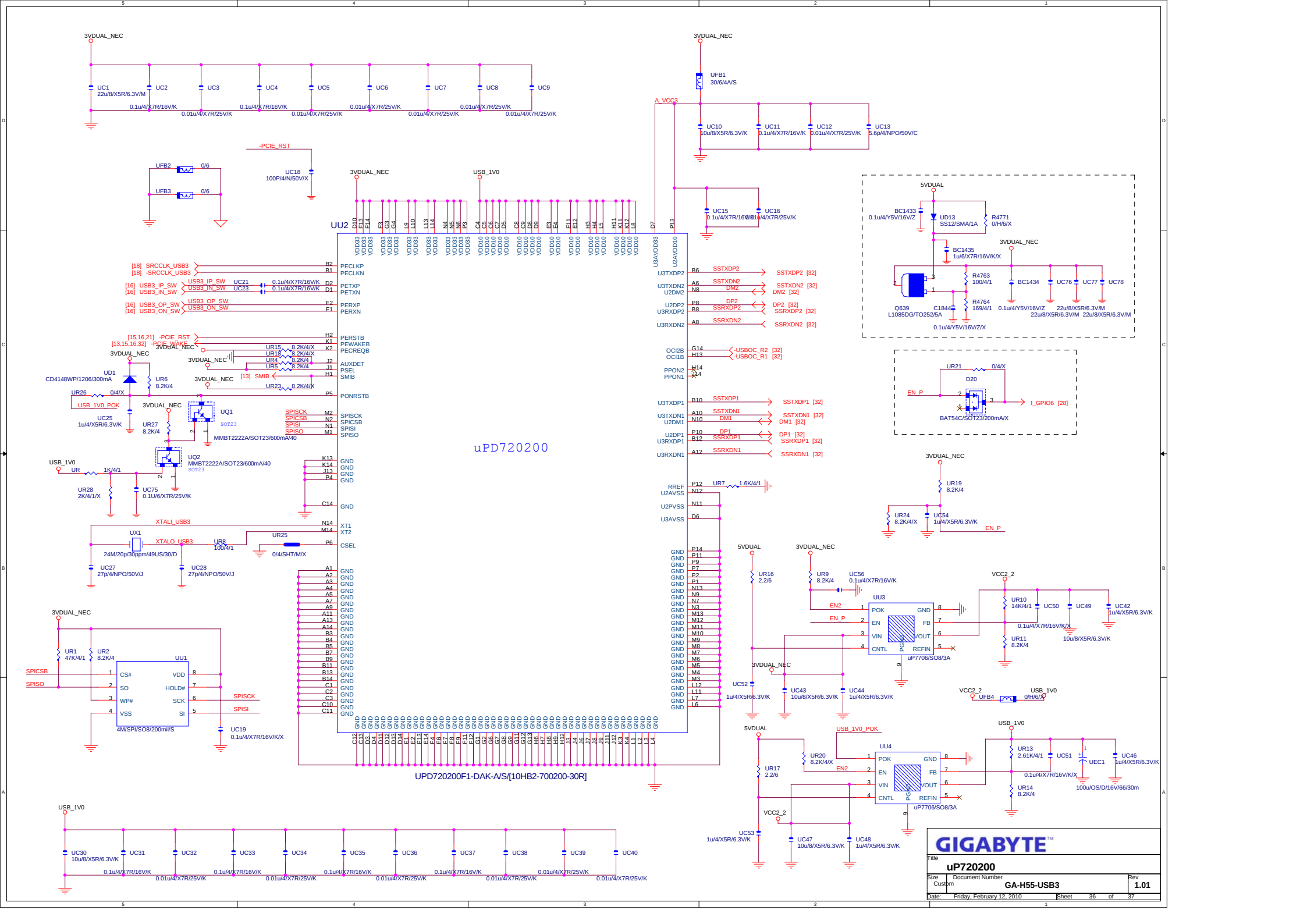




白色
connector






uPD720200
UU2
UPD720200F1-DAK-A/S/[10HB2-700200-30R]
3VDUAL_NEC
USB_1V0
A_VCC3
5VDUAL
-PCIE_RST
UFB1
UFB2
UFB3
UFB4
UC1
UC2
UC3
UC4
UC5
UC6
UC7
UC8
UC9
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UC11
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UC51
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UC54
UC56
UC75
UC76
UC77
UC78
UEC1
UD1
UD13
UD20
UQ1
UQ2
UR1
UR2
UR3
UR4
UR5
UR6
UR7
UR8
UR9
UR10
UR11
UR12
UR13
UR14
UR15
UR16
UR17
UR18
UR19
UR20
UR21
UR23
UR24
UR25
UR26
UR27
UR28
UU1
UU3
UU4
UX1
BC1433
BC1434
BC1435
Q639
C1844
R4763
R4764
R4771
BAT54C/SOT23/200mA/X
MMBT2222A/SOT23/600mA/40
uP7706/SO8/3A
4M/SPI/SO8/200mil/S
24M/20p/30ppm/49US/30/D
PECLKP
PECLKN
PETXP
PETXN
PERXP
PERXN
PERSTB
PEWAKEB
PECREQB
AUXDET
PSEL
SMIB
PONRSTB
SPISCK
SPICSB
SPISI
SPISO
XT1
XT2
CSEL
XTALI_USB3
XTALO_USB3
U3TXDP2
U3TXDN2
U2DM2
U2DP2
U3RXDP2
U3RXDN2
OCI2B
OCI1B
PPON2
PPON1
U3TXDP1
U3TXDN1
U2DM1
U2DP1
U3RXDP1
U3RXDN1
RREF
U2AVSS
U2PVSS
U3AVSS
U3AVDD33
U2AVDD10
SSTXDP2
SSTXDN2
DM2
DP2
SSRXDP2
SSRXDN2
SSTXDP1
SSTXDN1
DM1
DP1
SSRXDP1
SSRXDN1
-USBOC_R2
-USBOC_R1
SMIB
I_GPIO6
EN_P
EN2
USB_1V0_POK
GND
VDD33
VDD10
GIGABYTE
Title
uP720200
Size
Custom
Document Number
GA-H55-USB3
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1.01
Date:
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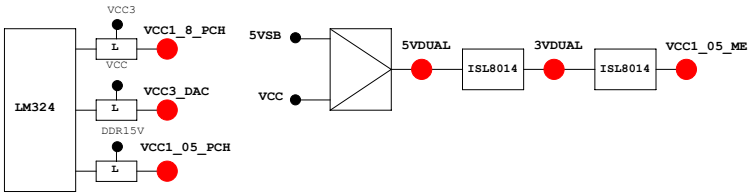
PCH GPIO LIST TABLE

PIN NAME	PWR	AFTER G3	Default	USAGE	NOTE
GP0	MAIN	H-Z	GPI	-PECI_REQ	N/A
GP1/TACH1	MAIN		GPI	ICH_FAN_TACH1	N/A
GP2/PIRQE#	MAIN		GPI	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN		GPI	ICH_FAN_TACH2	N/A
GP7/TACH3	MAIN		GPI	ICH_FAN_TACH3	N/A
GP8	STBY	H	GPO	GPIO8	P/U 8.2K 3VDUAL
GP9/OC5#	STBY		NATIVE	OC5#	N/A
GP10/OC6#	STBY		NATIVE	OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE	-SMBALERT	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI	LAN_PHY_PWR_CTRL	P/U 8.2K 3VDUAL
GP13	STBY	L	GPI	GPIO13	P/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE	OC7#	N/A
GP15	STBY	L	GPO	GPIO15	N/A
GP16	MAIN		GPI	-SKTOCC	P/U 8.2K VCC3
GP17/TACH0	MAIN		GPI	ICH_FAN_TACH0	N/A
GP18	MAIN		NATIVE	MB_ID0	P/D 8.2K GND
GP19	MAIN		GPI	-LAN1_ISO	P/U 8.2K VCC3
GP20	MAIN		NATIVE	LED_CTL	P/U 1K VCC3
GP21	MAIN		GPI	VCC18_PCH_OV2	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN		NATIVE	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	L	GPO	TLS	P/U 8.2K 3VDUAL
GP25	STBY		NATIVE	-CPU_STOP	P/U 8.2K 3VDUAL
GP26	STBY		NATIVE	-ACZ_DET	P/U 8.2K 3VDUAL
GP27	STBY	H	GPO	GPIO27	P/U 8.2K 3VDUAL
GP28	STBY	H	GPO	GPIO28	P/U 8.2K 3VDUAL
GP29	STBY	L	GPI	GPIO29	N/A
GP30	STBY	H-Z	GPI	S_PWR_ACK	P/U 100K 3VDUAL
GP31	STBY	H-Z	GPI	N/A(Reverse)	P/U 8.2K VCC3
GP32	MAIN	H	GPO	MB_ID1	P/D 8.2K GND
GP33	MAIN	H	GPO	LOAD-LINE	P/U 1K VCC3
GP34	MAIN	H-Z	GPI	-PCI_STOP	P/U 8.2K VCC3
GP35	MAIN	L	GPO	GPIO35	P/U 8.2K VCC3
GP36	MAIN		GPI	-LAN1_DSM	P/U 8.2K VCC3
GP37	MAIN		GPI	N/A	P/U 8.2K VCC3
GP38	MAIN	H-Z	GPI	VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	H-Z	GPI	-LAN_DSM	P/U 8.2K VCC3
GP40	STBY		NATIVE	OC1#	N/A
GP41	STBY		NATIVE	OC2#	N/A
GP42	STBY		NATIVE	OC3#	N/A
GP43	STBY		NATIVE	OC4#	N/A
GP44	STBY	L	NATIVE	N/A	P/U 8.2K 3VDUAL
GP45	STBY		NATIVE	-LPCPME	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	PWR_LED	P/U 8.2K 3VDUAL
GP47	STBY		NATIVE	PSI LED	P/U 8.2K 3VDUAL
GP48	MAIN	H-Z	IN	EN_PWM	P/U 8.2K VCC3
GP49	MAIN	H-Z	IN	VCC18_OV1	P/U 8.2K VCC3
GP50	MAIN		NATIVE	-REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1	N/A
GP52	MAIN		NATIVE	-REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2	N/A
GP54	MAIN		NATIVE	-REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3	N/A
GP56	STBY		NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP57	STBY	H-Z	IN	VCORE_OV1	P/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE	F_USB_OC	P/U 8.2K 3VDUAL
GP59	STBY		NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE	-SUSTAT	N/A
GP62	STBY	L	NATIVE	SUSCLK	N/A
GP63	STBY	L	NATIVE	GPIO63	N/A
GP64	MAIN	L	NATIVE	CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE	CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE	CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE	CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE	VCORE_OV4	P/U 8.2K 3VDUAL
GP73	STBY		NATIVE	1_05V_OV1	P/U 8.2K 3VDUAL
GP74	STBY	H-Z	NATIVE	1_05V_OV2	P/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL

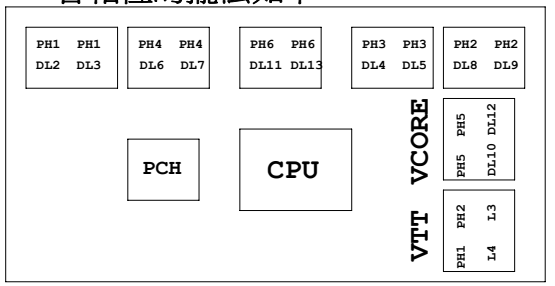
Super I/O ITE8720 GPIO Table

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSSO0	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSI0	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VSBSW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSSO1	MB_ID3	
PD7/GP77/BUSSO2	MB_ID4	
AFD#/GP86/SMBC_R	空 PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSSO0	SB_LED3_C	



PWM各相位的擺法如下：



BIOS超電壓對應表：

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Terminatio
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

散熱模組料號：

8IBP:
1.12SP2-01A001-Y1R/Y2R
2.12SP2-01A001-Z1R/Z2R
(HIBRID模組)包材階

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH

Gigabyte Technology

Title: TABLE LIST

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