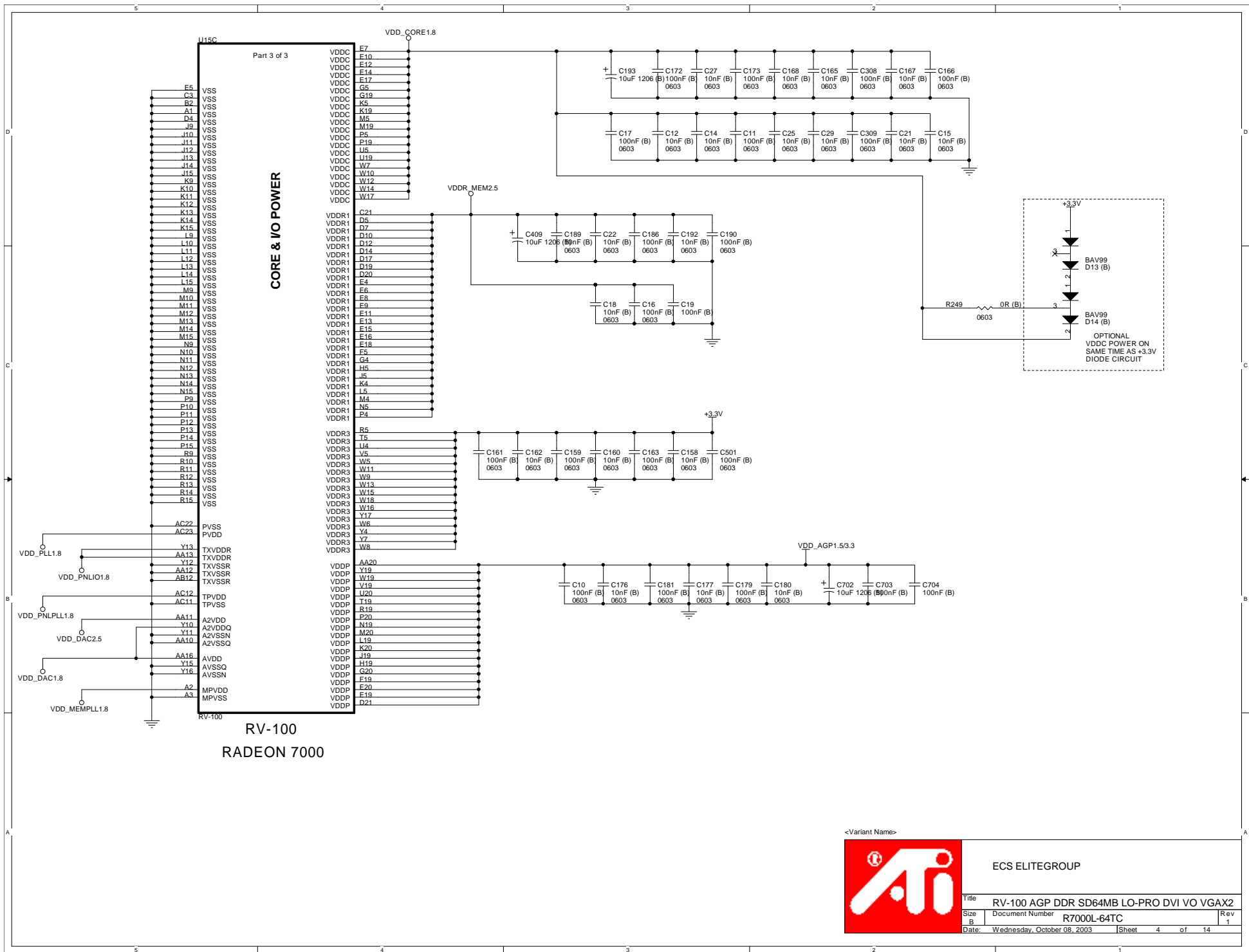


<Variant Name>



ECS ELITEGROUP

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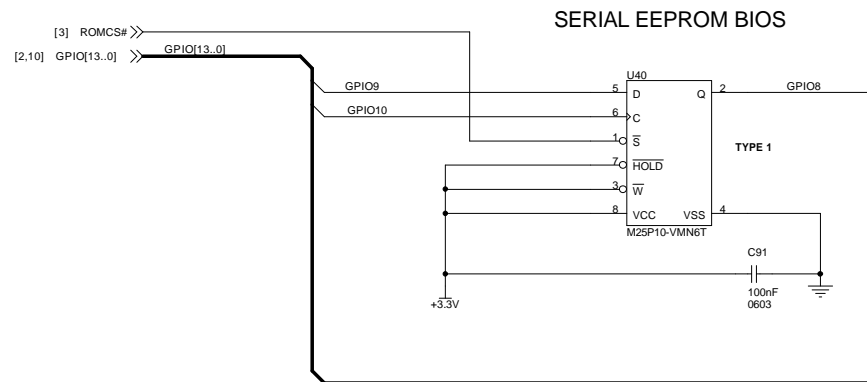


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ECS ELITEGROUP

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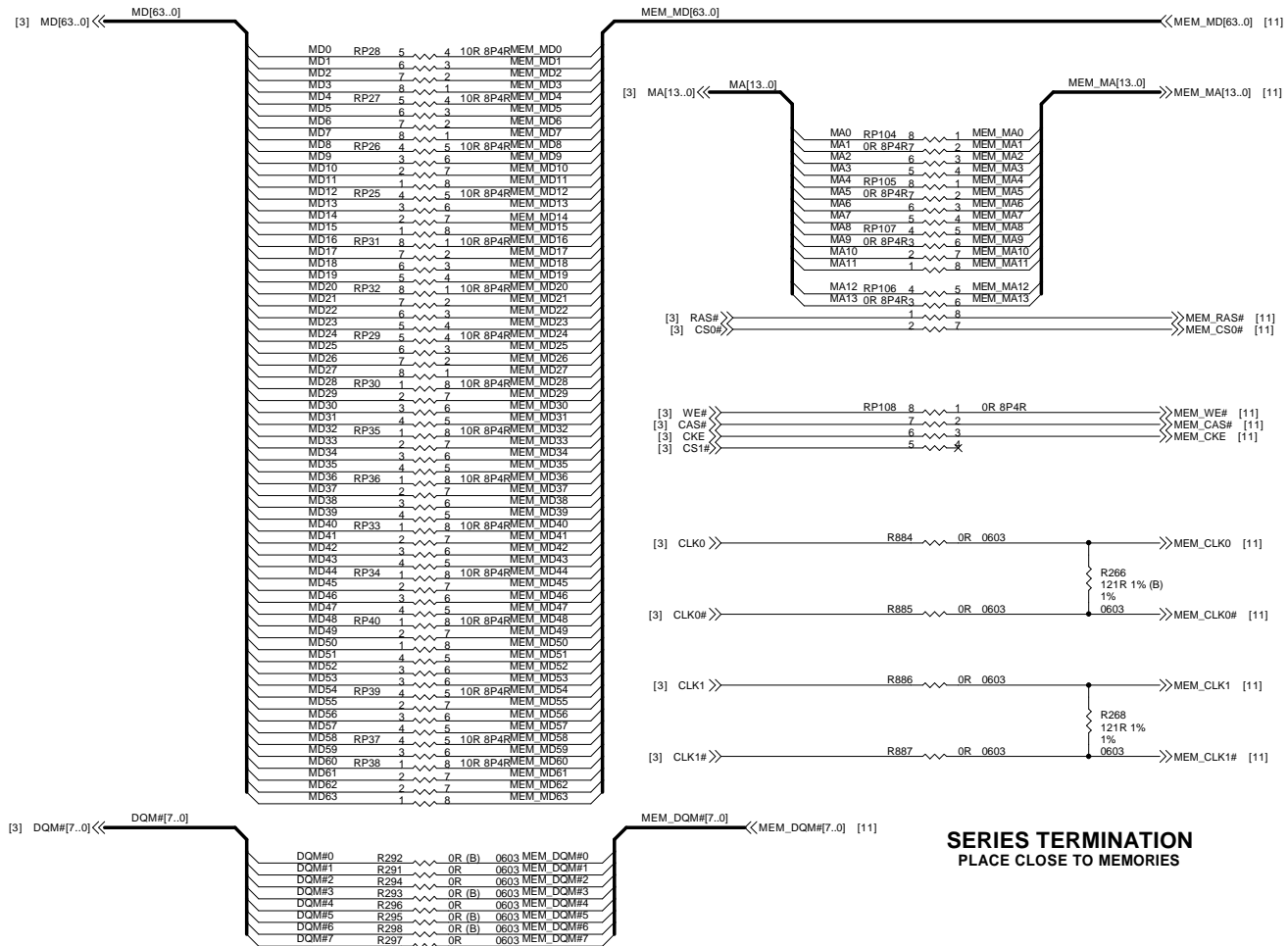


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ECS ELITEGROUP

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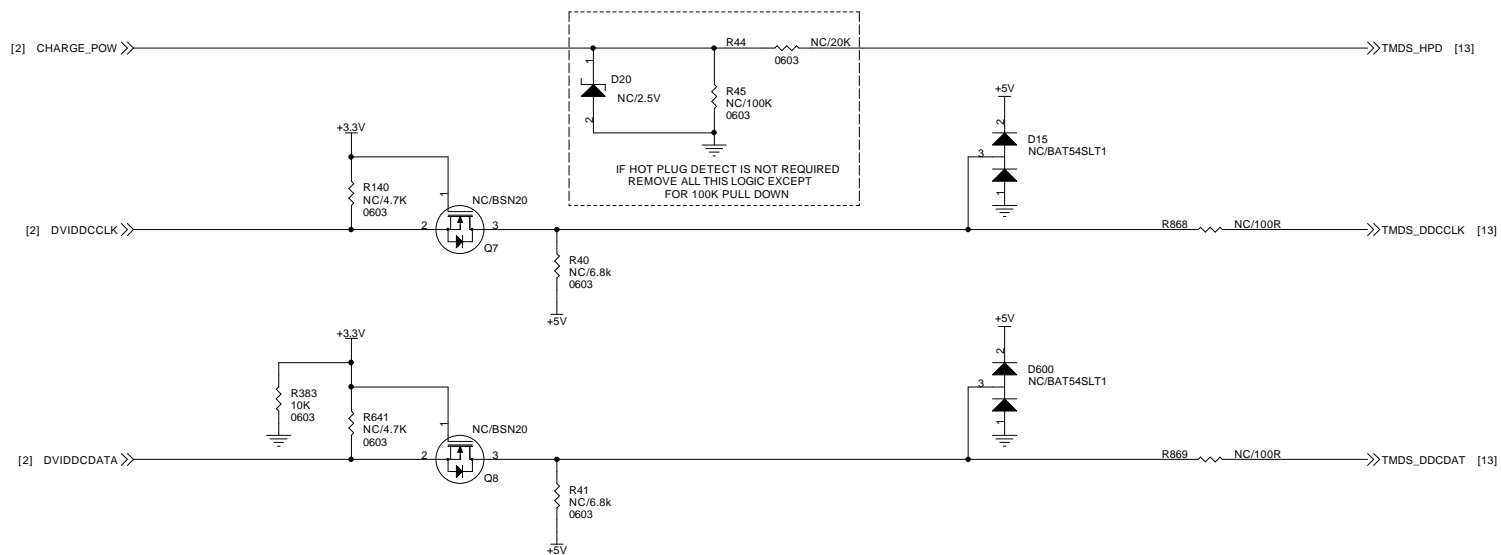
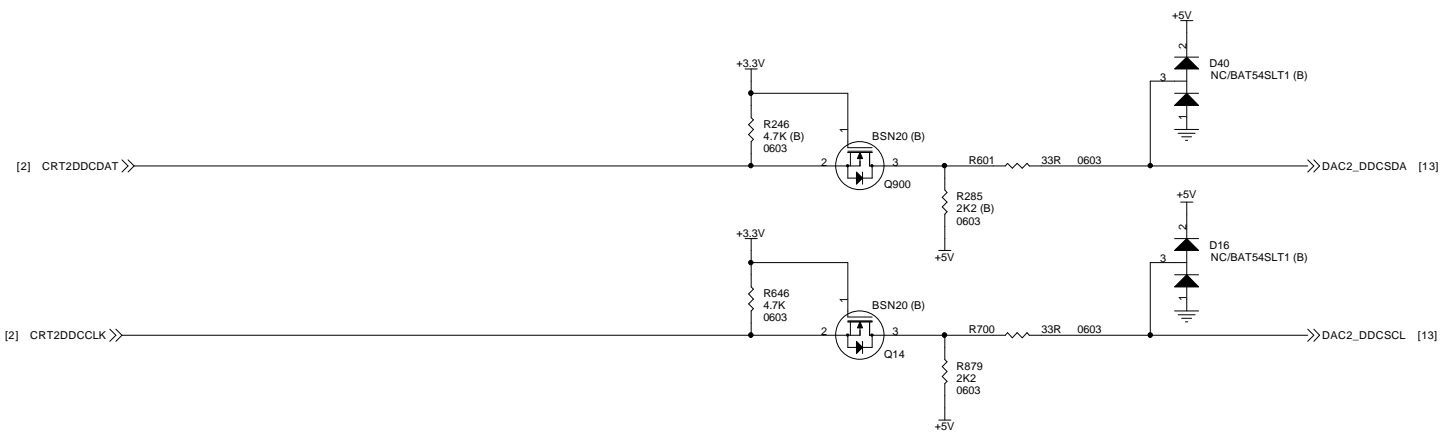


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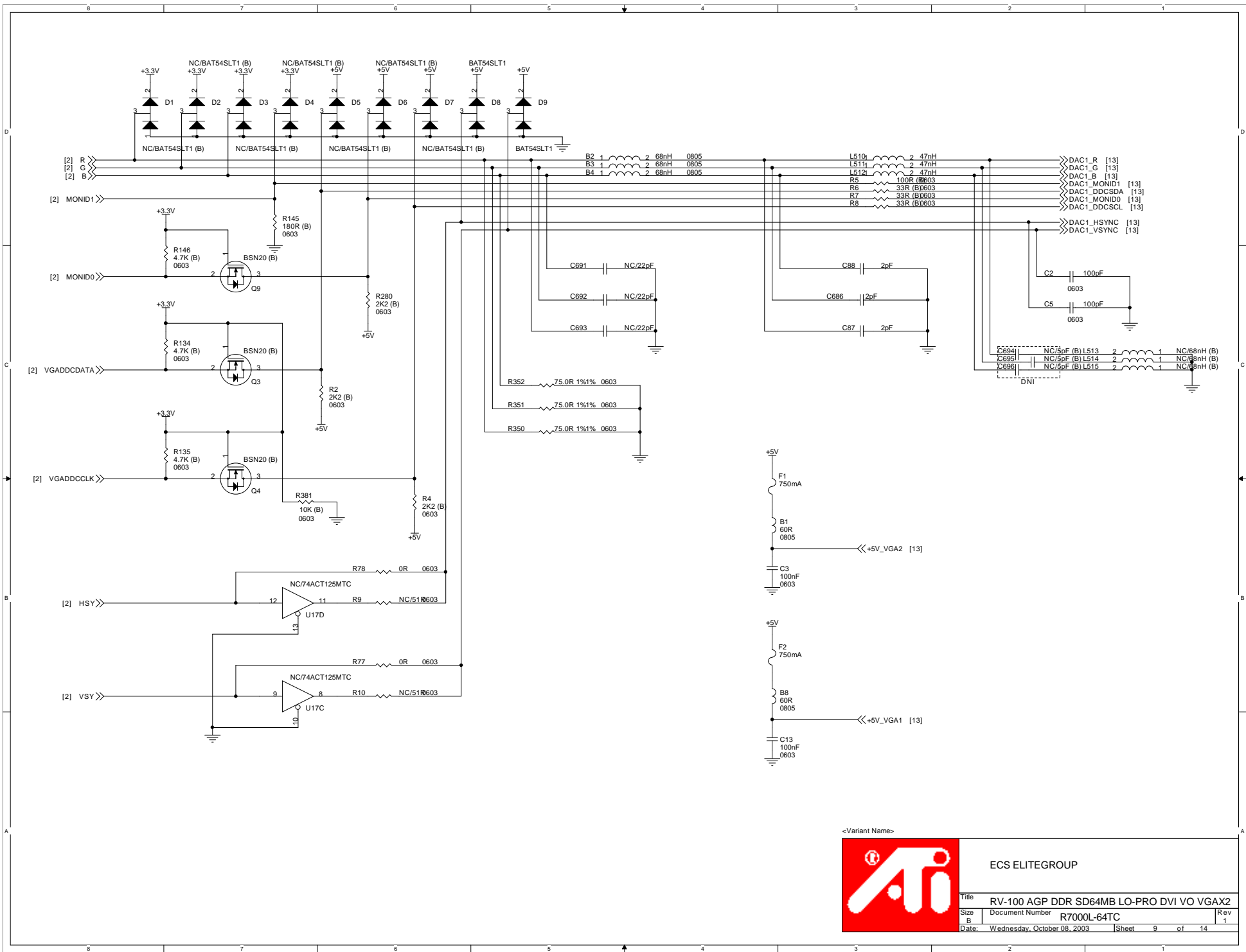


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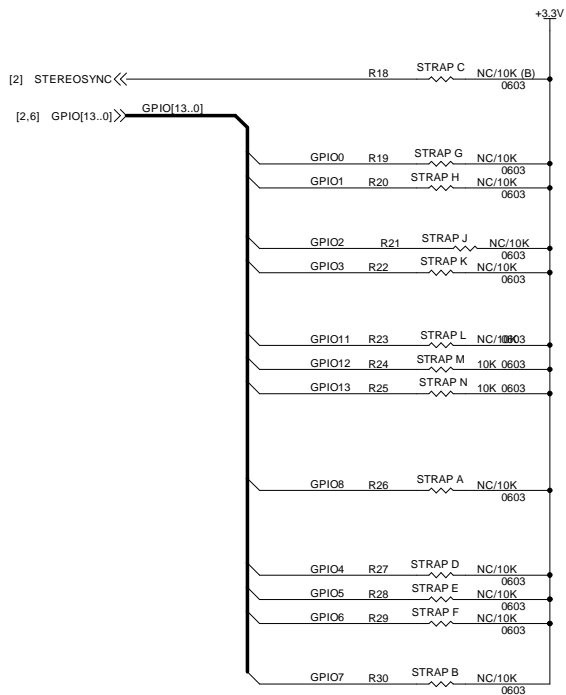


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STRAP C	INTERRUPT
DNI	ENABLE (DEFAULT)
10K	DISABLE

STRAP H	STRAP G	AGP1X CLOCK FEEDBACK PHASE ADJUSTMENT WRT REFCLK (CPUCLK)
DNI	DNI	(DEFAULT) SEE PINOUT SPEC FOR OPTION SETTINGS

STRAP K	STRAP J	AGP CLOCK PHASE ADJUSTMENT BETWEEN X1 AND X2 CLOCK
DNI	DNI	(DEFAULT) SEE PINOUT SPEC FOR OPTION SETTINGS

STRAP N	STRAP M	STRAP L	ROM IDENTIFIER
			SEE PINOUT SPEC FOR OPTION SETTINGS

STRAP A	ID DISABLE
DNI	NORMAL OPERATION
10K	SHUT DOWN

STRAP D	STRAP E	STRAP F	BUS_TYPE
			SEE PIN BASED STRAPS IN PINOUT SPEC

STRAP B	VGA
DNI	ENABLE (DEFAULT)
10K	DISABLE

NOTE: THE RV-100 SUPPORTS THE USE OF STRAP RESISTORS (AS AN ALTERNATIVE TO CUSTOMIZED BIOS) TO CONFIGURE CERTAIN ASPECTS OF THE GRAPHICS SUBSYSTEM. THE USE OF EXTERNAL STRAPS PROVIDES ADDED FLEXIBILITY AND EASE OF FUTURE UPGRADE. STRAPPED VALUES ARE LOADED INTO INTERNAL REGISTERS ON THE FIRST PCI COMMAND AFTER RESET# IS INACTIVE.

NOTE:
THE I/O BUFFERS USE INTERNAL PULLDOWN RESISTOR. THIS DICTATES THE FOLLOWING STRAP CONFIGURATION:
STRAP TO VCC VIA 10K RESISTOR.
THIS PROVIDES THE LOGIC LEVELS SHOWN:
'0' WHEN 10K RESISTOR NOT INSTALLED
'1' WHEN 10K RESISTOR INSTALLED.

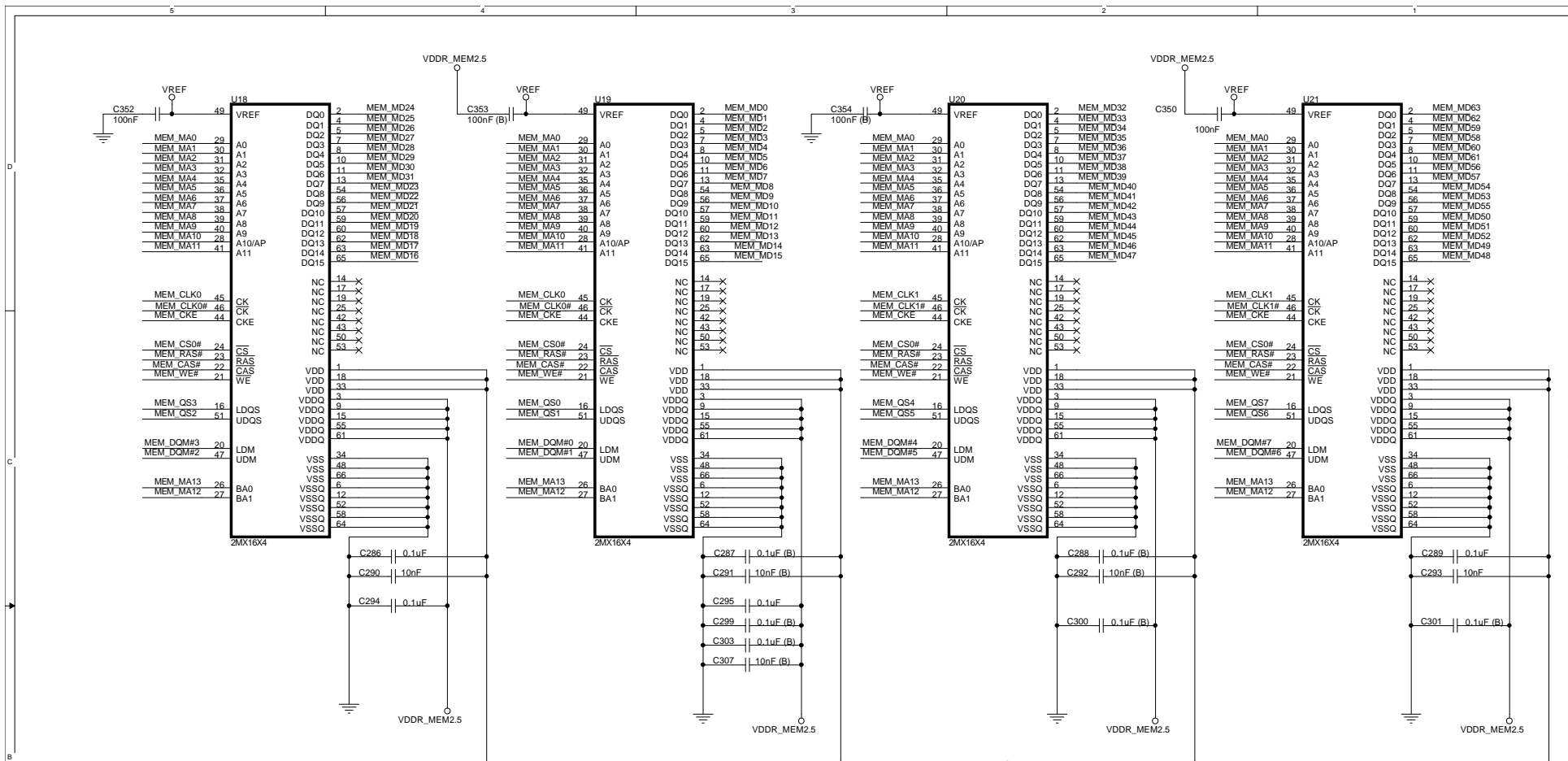
OPTION STRAPS

<Variant Name>



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Rank 0 of 64MB DDR 8X16 SDRAM

[7] MEM_QS0 << MEM_QS0
[7] MEM_QS1 << MEM_QS1
[7] MEM_QS2 << MEM_QS2
[7] MEM_QS3 << MEM_QS3
[7] MEM_QS4 << MEM_QS4
[7] MEM_QS5 << MEM_QS5
[7] MEM_QS6 << MEM_QS6
[7] MEM_QS7 << MEM_QS7

[7] MEM_CLK1 << MEM_CLK1#
[7] MEM_CLK1# << MEM_CLK0
[7] MEM_CLK0 << MEM_CLK0#

[7] MEM_CS0# << MEM_CS0#
[7] MEM_RAS# << MEM_RAS#
[7] MEM_CAS# << MEM_CAS#
[7] MEM_WE# << MEM_WE#
[7] MEM_CKE << MEM_CKE

[7] MEM_DQM#[7..0] << MEM_DQM#[7..0]
[7] MEM_MD[63..0] << MEM_MD[63..0]
[7] MEM_MA[13..0] << MEM_MA[13..0]

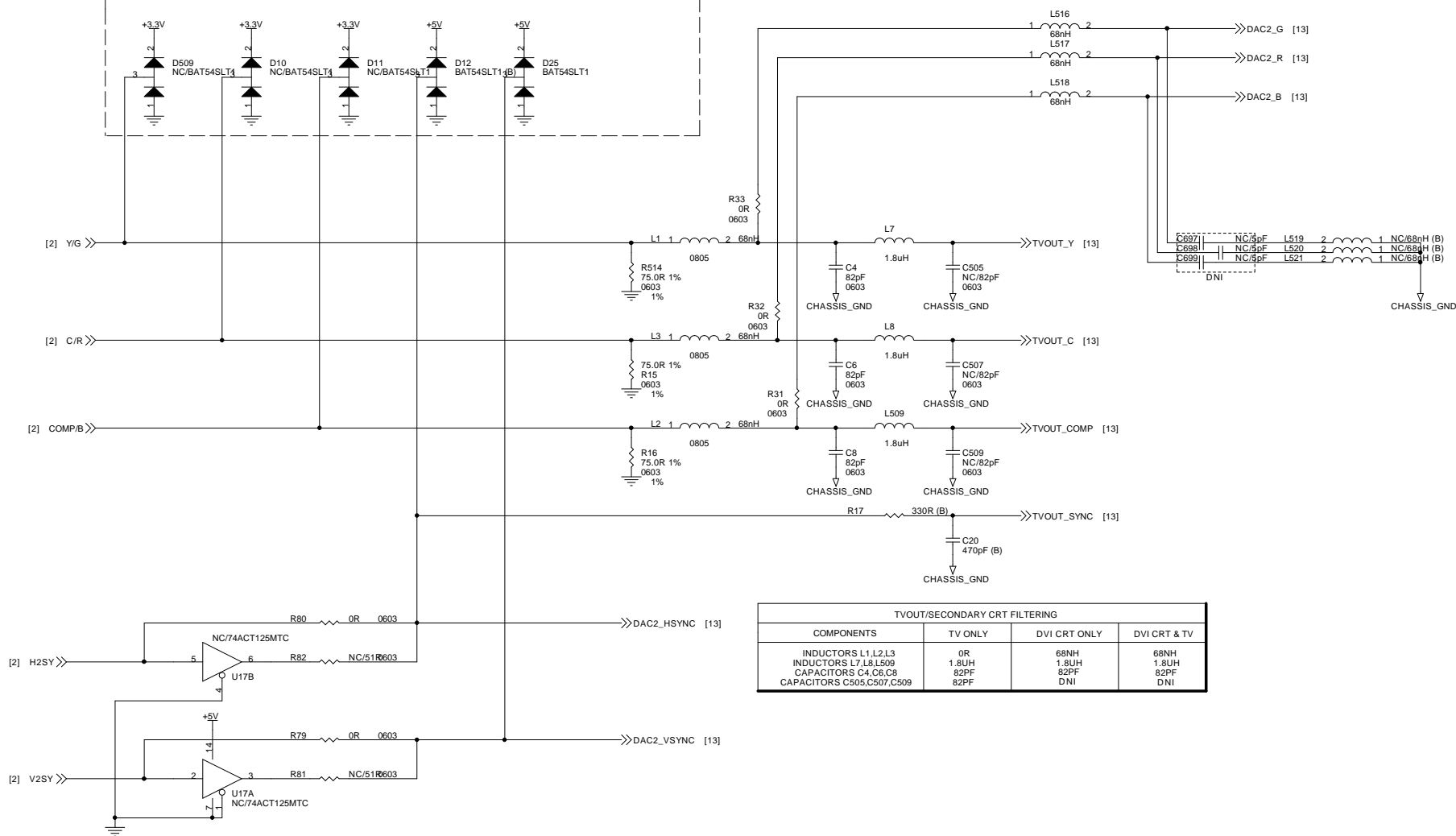
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OPTIONAL ESD PROTECTION DIODES



TVOUT/SECONDARY CRT FILTERING			
COMPONENTS	TV ONLY	DVI CRT ONLY	DVI CRT & TV
INDUCTORS L1,L2,L3	0R	68NH	68NH
INDUCTORS L7,L8,L509	1.8UH	1.8UH	1.8UH
CAPACITORS C4,C6,C8	82PF	82PF	82PF
CAPACITORS C505,C507,C509	82PF	DNI	DNI

<Variant Name>



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CRT SCREWS

ASSY2

SCREW
JACKSCREW

ASSY4

SCREW
JACKSCREW

BRACKET SCREWS

ASSY8

SCREW
PAN HEAD

ASSY9

SCREW
PAN HEAD

CRT ONLY
BRACKET

MISC. BOARD PARTS

ASSY1

BLANK
LABEL
1.50W X 0.50H

ASSY6

BLANK
LABEL
0.375 X 0.375

ASSY7

ANTISTATIC
BAG
6 X 11

REF1

PCB
109-GN924-00

REF2

SCHEMATIC
105-GN924-00

CRT AND VIDEO-IN AND
VIDEO-OUT

ASSY10

BRACKET

CRT, DVI, S-VID OUT, 215

ASSY11

BRACKET

CRT, CRT, S-VID OUT, 216

ASSY12

BRACKET

DVI, S-VID OUT, 217

ASSY13

BRACKET

CRT, S-VID OUT, 218



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R7000-64TD

CRT+DVI+TVOUT

Wednesday, October 08, 2003

REVISION HISTORY

Rev 1

Sch Rev	PCB Rev	Date	REVISION DESCRIPTION
1	A	06/12/03	PCB V.A