ECS ELITEGROUP

(P4 478P Processor with DDR SDRAM Mainboard)

P13G V1.0

1.FAN2 & FAN3 CHANGE (FOR 83637)
2.CO-LAY ALC655
3.CO-LAY AGP EXPERS
4.ADD CNR
5.CHANGE PWM RDS-ON TO DC-R
6.ADD EMI AND FUSE

Page Title of Schematic :

Job	Signature	Date
Schematics Designer	Kelvin	
Layout Company	ECS	
Approval		

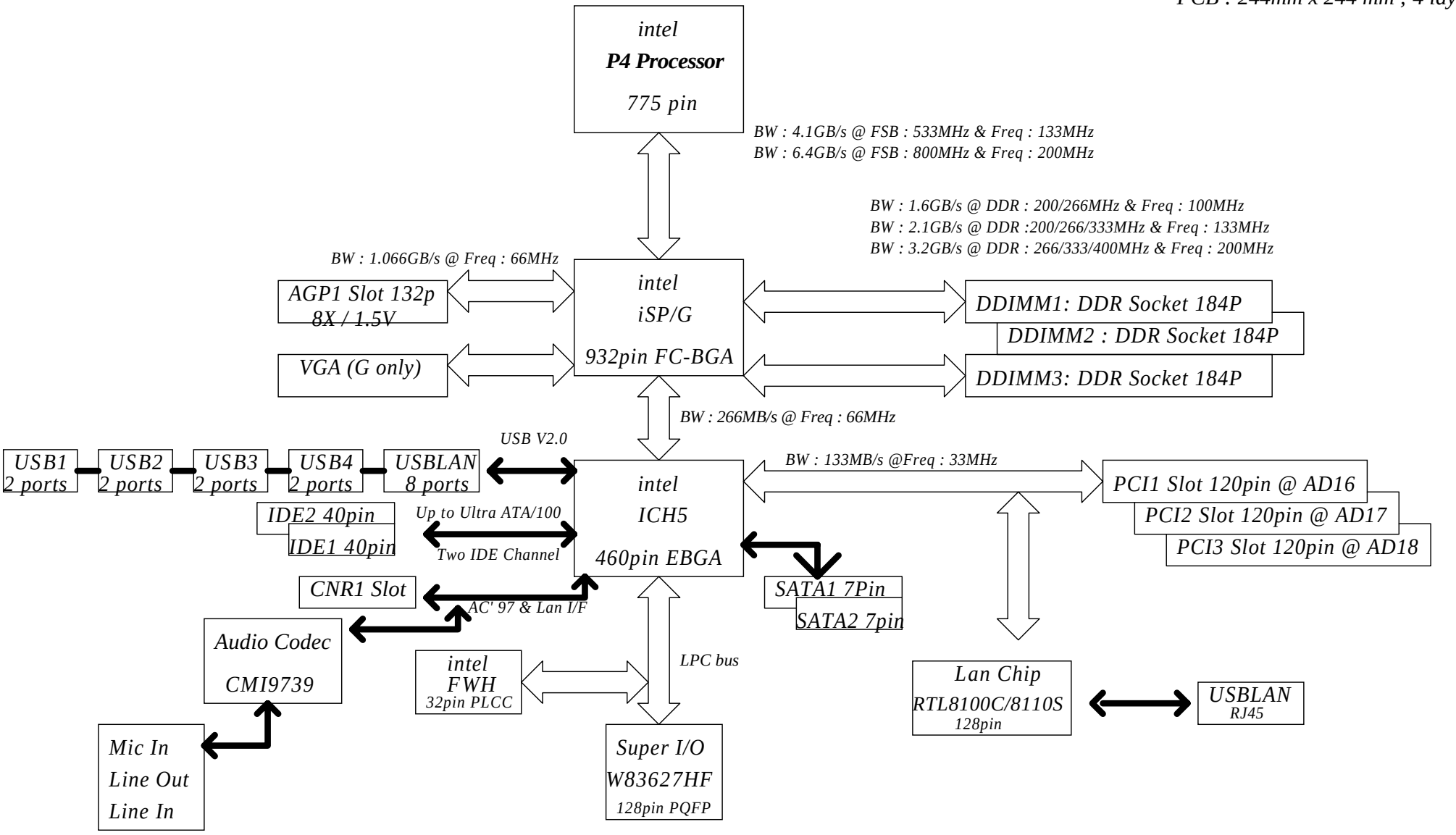
ECS Elitegroup Computer Systems

Title: Cover Sheet

Size: Custom | Document Number: P13G | Rev: 3.1

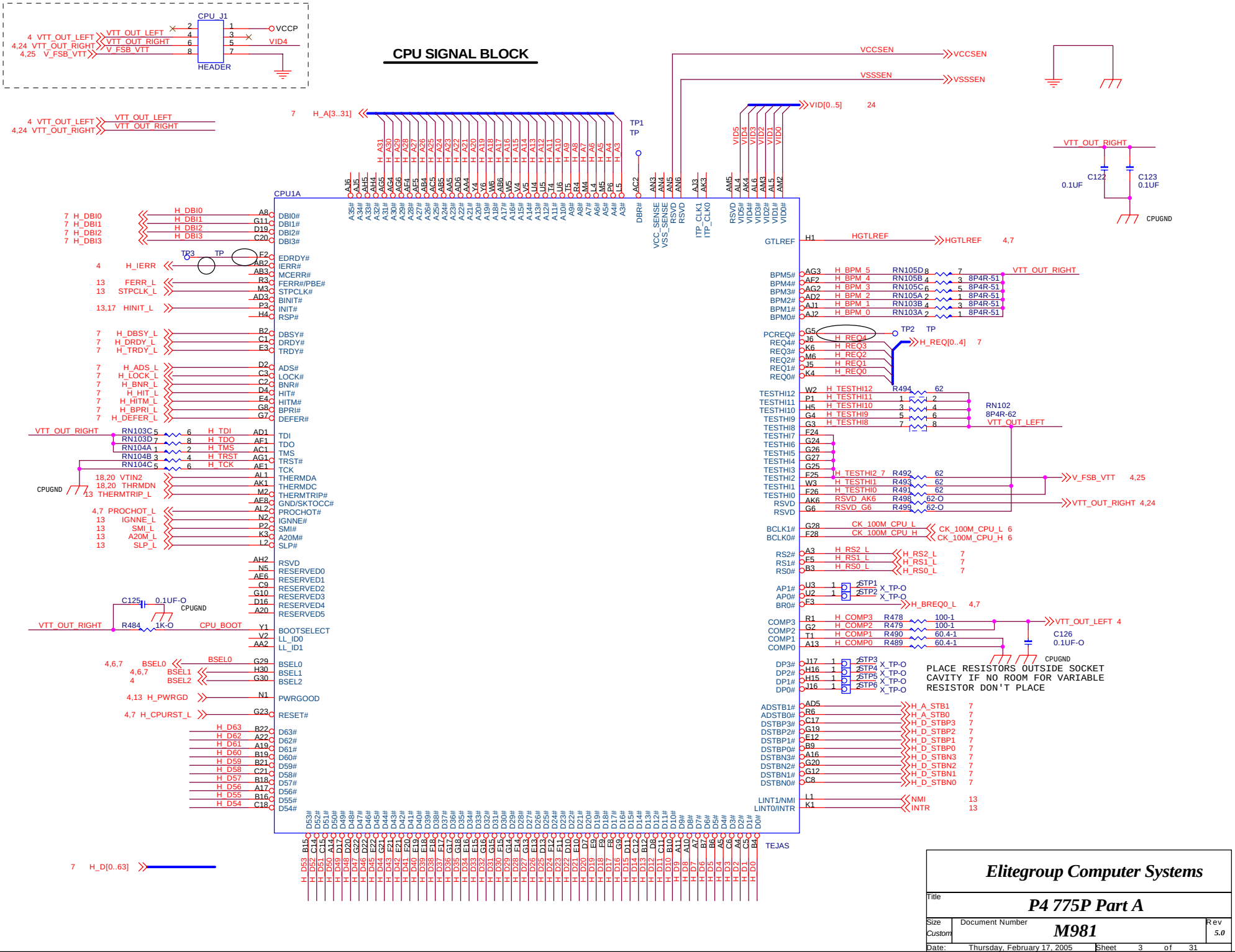
Date: Thursday, February 17, 2005 | Sheet 1 of 31

PCB : 244mm x 244 mm ; 4 layers

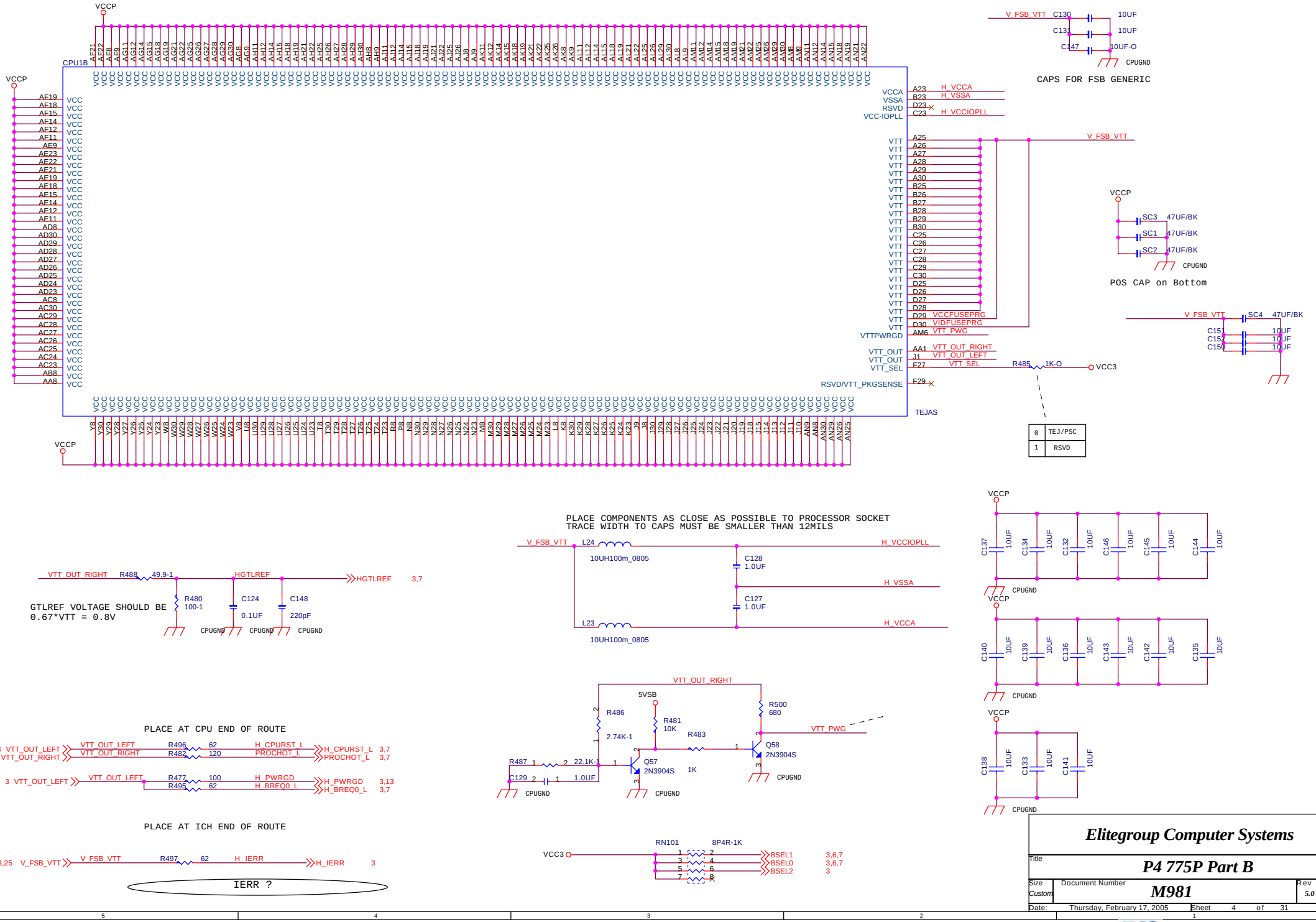


Elitegroup Computer Systems		
Title: System Block Diagram		
Size: B	Document Number: M983	Rev: 5.0
Date: Thursday, February 17, 2005	Sheet 2 of 31	

CPU SIGNAL BLOCK



Company	Elitegroup Computer Systems
Title	P4 775P Part A
Size	Custom
Document Number	M981
Rev	5.0
Date	Thursday, February 17, 2005
Sheet	3 of 31



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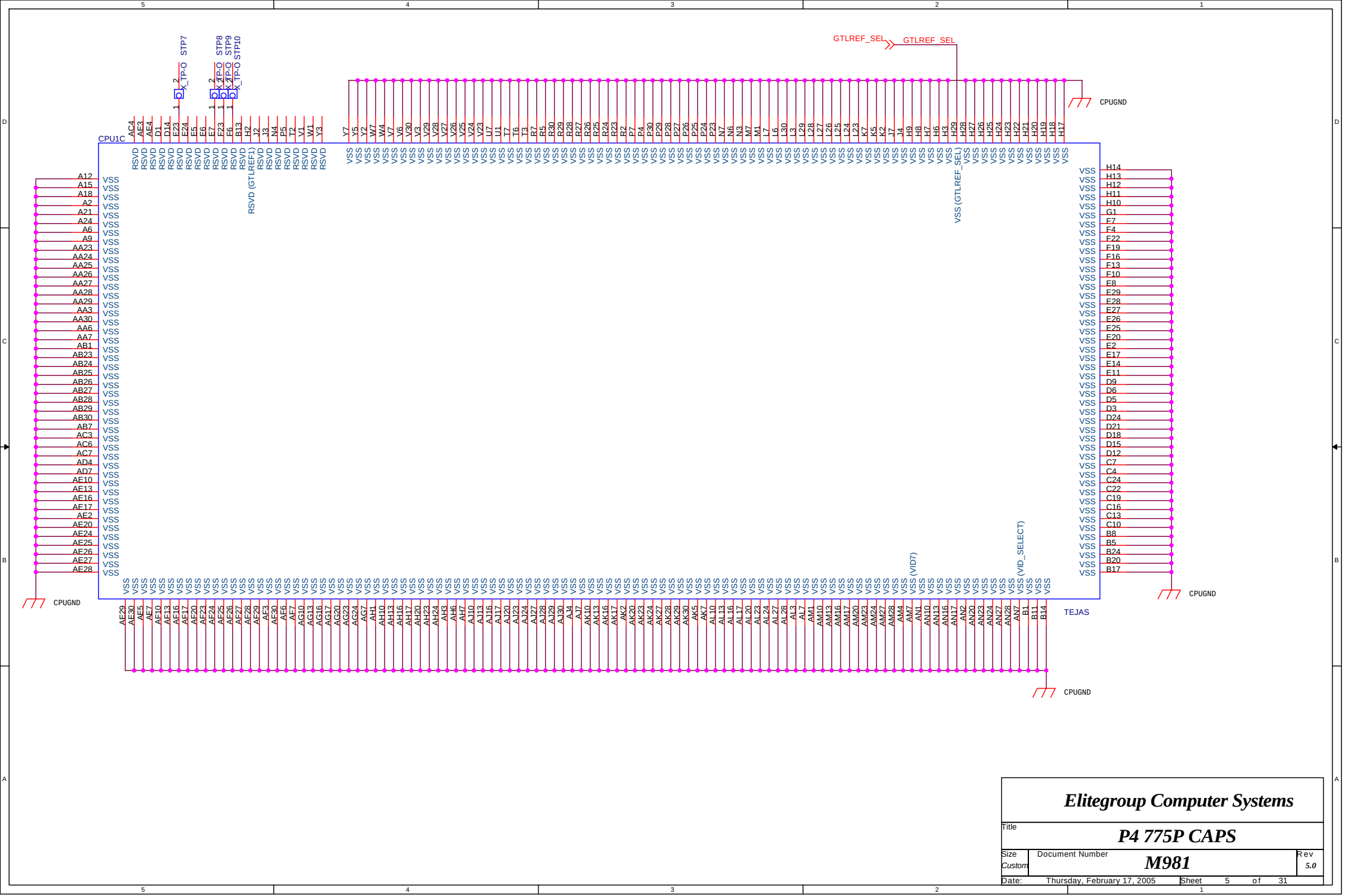
Size: Custom	Document Number: M981
Rev: 5.0	
Date: Thursday, February 17, 2005	Sheet 4 of 31



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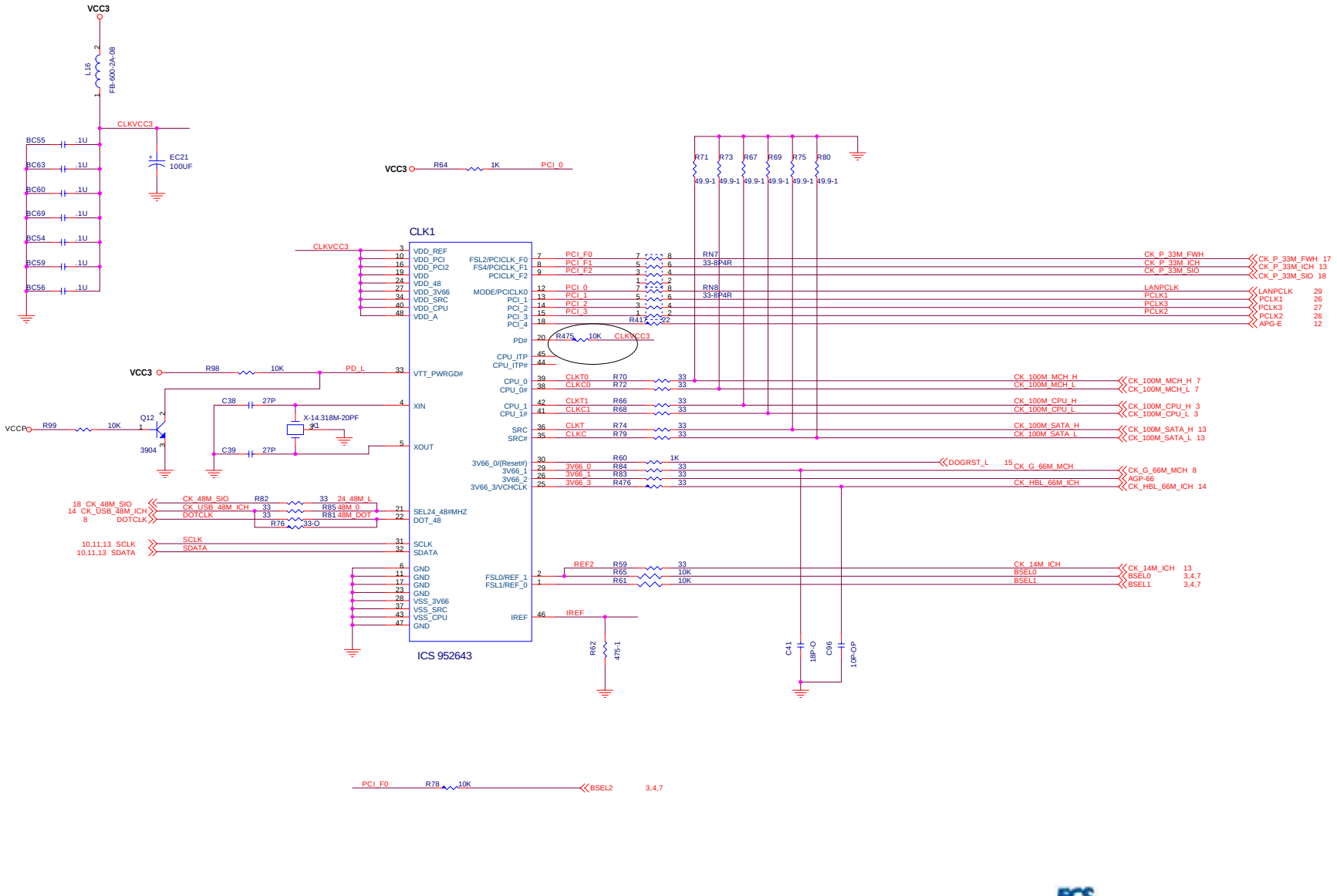
Size: Custom	Document Number: M981		Rev: 5.0
Date: Thursday, February 17, 2005		Sheet 5 of 31	



Elitegroup Computer Systems			
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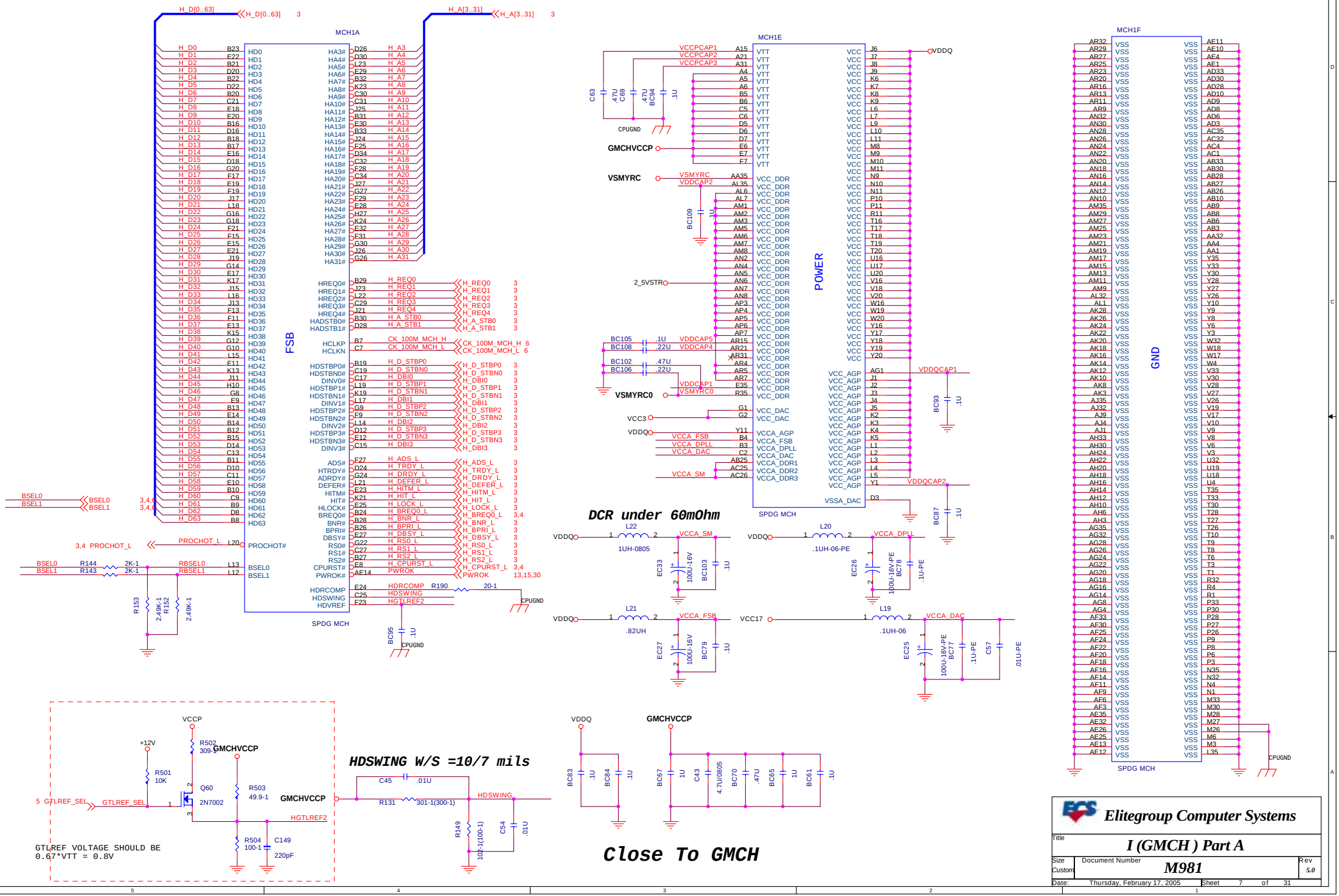
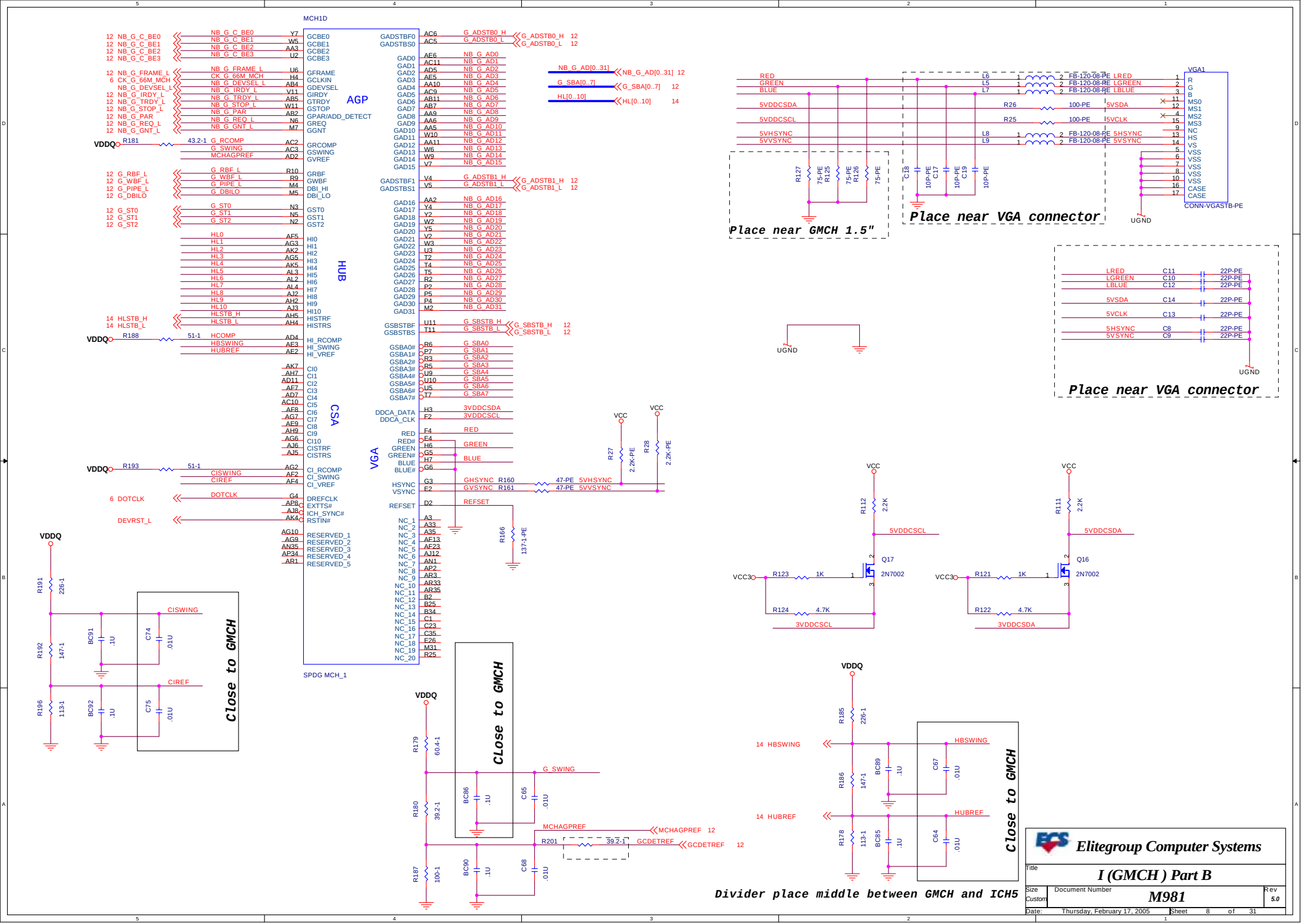
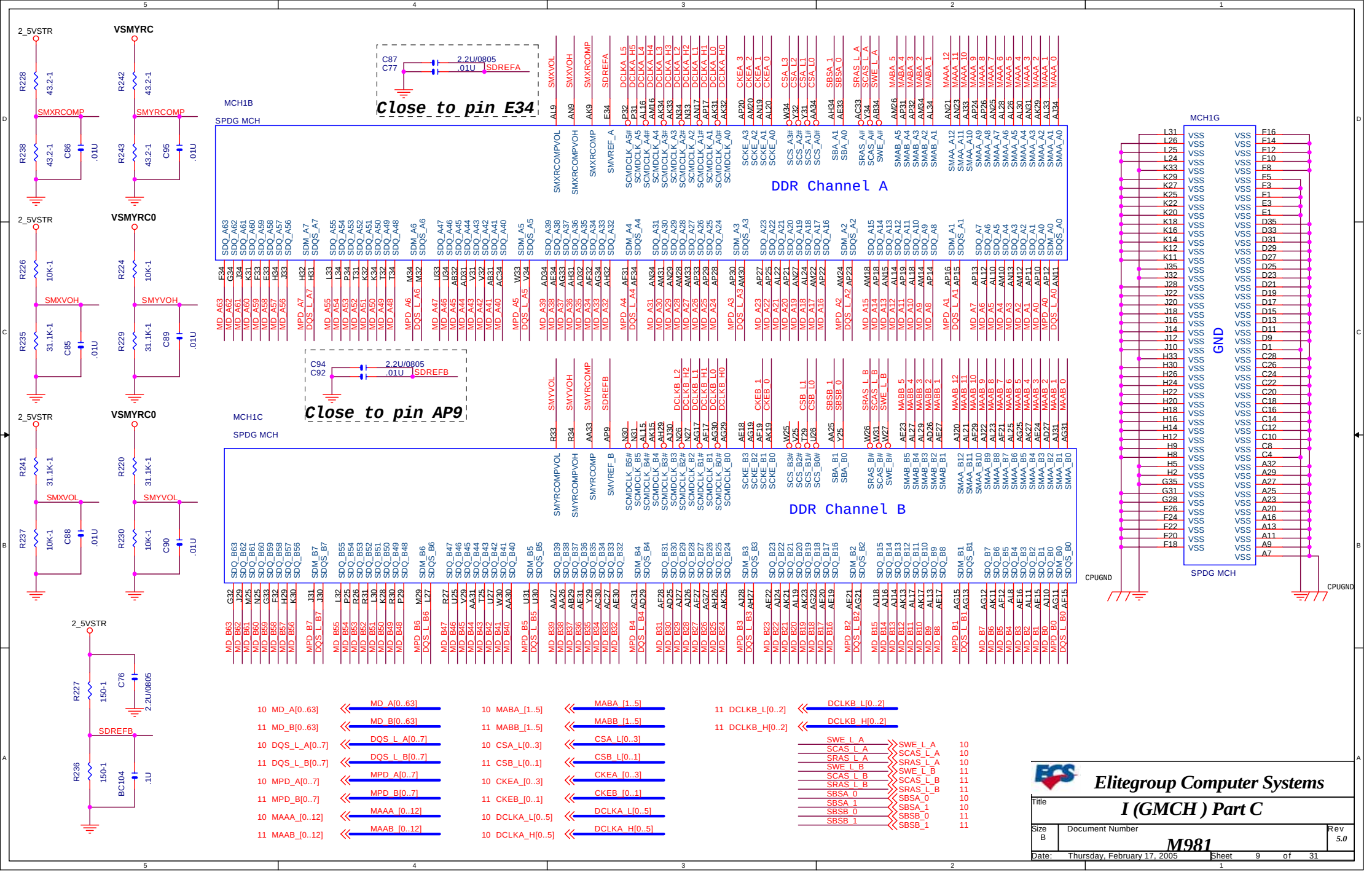
Size: Custom	Document Number: M981		Rev: 5.0
Date: Thursday, February 17, 2005	Sheet 6 of 31		


MCH1A
FSB
SPDG MCH
MCH1E
POWER
SPDG MCH
MCH1F
GND
SPDG MCH
DCR under 60mOhm
HDSWING W/S =10/7 mils
Close To GMCH
GTLREF VOLTAGE SHOULD BE
0.67*VTT = 0.8V
Elitegroup Computer Systems
I (GMCH) Part A
M981
Thursday, February 17, 2005


MCH1D
AGP
HUB
CSA
VGA
SPDG MCH_1
Close to GMCH
Close to GMCH
Close to GMCH
Place near GMCH 1.5"
Place near VGA connector
Place near VGA connector
Divider place middle between GMCH and ICH5
Elitegroup Computer Systems
I (GMCH) Part B
M981
Thursday, February 17, 2005


2_5VSTR
VSMYRC
R228 43.2-1
R242 43.2-1
SMXRCOMP
SMYRCOMP
R238 43.2-1
C86 .01U
R243 43.2-1
C95 .01U
2_5VSTR
VSMYRC0
R226 10K-1
R224 10K-1
SMXVOH
SMYVOH
R235 31.1K-1
C85 .01U
R229 31.1K-1
C89 .01U
2_5VSTR
VSMYRC0
R241 31.1K-1
R220 31.1K-1
SMXVOL
SMYVOL
R237 10K-1
C88 .01U
R230 10K-1
C90 .01U
2_5VSTR
R227 150-1
C76 2.2U/0805
SDREFB
R236 150-1
BC104 .1U
C87
C77
2.2U/0805
.01U
SDREFA
Close to pin E34
MCH1B
SPDG MCH
DDR Channel A
SMXVOL
SMXVOH
SMXRCOMP
SDREFA
SMXRCOMPVOL
SMXRCOMPVOH
SMXRCOMP
SMVREF_A
DCLKA_L5
DCLKA_H5
DCLKA_L4
DCLKA_H4
DCLKA_L3
DCLKA_H3
DCLKA_L2
DCLKA_H2
DCLKA_L1
DCLKA_H1
DCLKA_L0
DCLKA_H0
SCMDCLK_A5#
SCMDCLK_A5
SCMDCLK_A4#
SCMDCLK_A4
SCMDCLK_A3#
SCMDCLK_A3
SCMDCLK_A2#
SCMDCLK_A2
SCMDCLK_A1#
SCMDCLK_A1
SCMDCLK_A0#
SCMDCLK_A0
CKEA_3
CKEA_2
CKEA_1
CKEA_0
SCKE_A3
SCKE_A2
SCKE_A1
SCKE_A0
CSA_L3
CSA_L2
CSA_L1
CSA_L0
SCS_A3#
SCS_A2#
SCS_A1#
SCS_A0#
SBSA_1
SBSA_0
SBA_A1
SBA_A0
SRAS_L_A
SCAS_L_A
SWE_L_A
SRAS_A#
SCAS_A#
SWE_A#
MABA_5
MABA_4
MABA_3
MABA_2
MABA_1
SMAB_A5
SMAB_A4
SMAB_A3
SMAB_A2
SMAB_A1
MAAA_12
MAAA_11
MAAA_10
MAAA_9
MAAA_8
MAAA_7
MAAA_6
MAAA_5
MAAA_4
MAAA_3
MAAA_2
MAAA_1
MAAA_0
SMAA_A12
SMAA_A11
SMAA_A10
SMAA_A9
SMAA_A8
SMAA_A7
SMAA_A6
SMAA_A5
SMAA_A4
SMAA_A3
SMAA_A2
SMAA_A1
SMAA_A0
SDQ_A63
SDQ_A62
SDQ_A61
SDQ_A60
SDQ_A59
SDQ_A58
SDQ_A57
SDQ_A56
SDM_A7
SDQS_A7
SDQ_A55
SDQ_A54
SDQ_A53
SDQ_A52
SDQ_A51
SDQ_A50
SDQ_A49
SDQ_A48
SDM_A6
SDQS_A6
SDQ_A47
SDQ_A46
SDQ_A45
SDQ_A44
SDQ_A43
SDQ_A42
SDQ_A41
SDQ_A40
SDM_A5
SDQS_A5
SDQ_A39
SDQ_A38
SDQ_A37
SDQ_A36
SDQ_A35
SDQ_A34
SDQ_A33
SDQ_A32
SDM_A4
SDQS_A4
SDQ_A31
SDQ_A30
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SDQ_A28
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SDQ_A26
SDQ_A25
SDQ_A24
SDM_A3
SDQS_A3
SDQ_A23
SDQ_A22
SDQ_A21
SDQ_A20
SDQ_A19
SDQ_A18
SDQ_A17
SDQ_A16
SDM_A2
SDQS_A2
SDQ_A15
SDQ_A14
SDQ_A13
SDQ_A12
SDQ_A11
SDQ_A10
SDQ_A9
SDQ_A8
SDM_A1
SDQS_A1
SDQ_A7
SDQ_A6
SDQ_A5
SDQ_A4
SDQ_A3
SDQ_A2
SDQ_A1
SDQ_A0
SDM_A0
SDQS_A0
MD_A63
MD_A62
MD_A61
MD_A60
MD_A59
MD_A58
MD_A57
MD_A56
MPD_A7
DQS_L_A7
MD_A55
MD_A54
MD_A53
MD_A52
MD_A51
MD_A50
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DQS_L_A6
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MD_A43
MD_A42
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MD_A40
MPD_A5
DQS_L_A5
MD_A39
MD_A38
MD_A37
MD_A36
MD_A35
MD_A34
MD_A33
MD_A32
MPD_A4
DQS_L_A4
MD_A31
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DQS_L_A3
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MPD_A2
DQS_L_A2
MD_A15
MD_A14
MD_A13
MD_A12
MD_A11
MD_A10
MD_A9
MD_A8
MPD_A1
DQS_L_A1
MD_A7
MD_A6
MD_A5
MD_A4
MD_A3
MD_A2
MD_A1
MD_A0
MPD_A0
DQS_L_A0
C94
C92
2.2U/0805
.01U
SDREFB
Close to pin AP9
MCH1C
SPDG MCH
DDR Channel B
SMYVOL
SMYVOH
SMYRCOMP
SDREFB
SMYRCOMPVOL
SMYRCOMPVOH
SMYRCOMP
SMVREF_B
DCLKB_L2
DCLKB_H2
DCLKB_L1
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DCLKB_H0
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SCMDCLK_B5
SCMDCLK_B4#
SCMDCLK_B4
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SCMDCLK_B2#
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SCMDCLK_B1
SCMDCLK_B0#
SCMDCLK_B0
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SCKE_B2
SCKE_B1
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CSB_L0
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SCS_B2#
SCS_B1#
SCS_B0#
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SCAS_L_B
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SRAS_B#
SCAS_B#
SWE_B#
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MABB_4
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SMAB_B2
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SMAA_B11
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SMAA_B8
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SMAA_B2
SMAA_B1
SMAA_B0
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SDQ_B49
SDQ_B48
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SDQS_B1
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SDQ_B4
SDQ_B3
SDQ_B2
SDQ_B1
SDQ_B0
SDM_B0
SDQS_B0
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MD_B58
MD_B57
MD_B56
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MD_B54
MD_B53
MD_B52
MD_B51
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MD_B49
MD_B48
MPD_B6
DQS_L_B6
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DQS_L_B3
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MD_B16
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DQS_L_B2
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MPD_B1
DQS_L_B1
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MD_B6
MD_B5
MD_B4
MD_B3
MD_B2
MD_B1
MD_B0
MPD_B0
DQS_L_B0
MCH1G
GND
SPDG MCH
CPUGND
CPUGND
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MD_A[0..63]
11 MD_B[0..63]
MD_B[0..63]
10 DQS_L_A[0..7]
DQS_L_A[0..7]
11 DQS_L_B[0..7]
DQS_L_B[0..7]
10 MPD_A[0..7]
MPD_A[0..7]
11 MPD_B[0..7]
MPD_B[0..7]
10 MAAA_[0..12]
MAAA_[0..12]
11 MAAB_[0..12]
MAAB_[0..12]
10 MABA_[1..5]
MABA_[1..5]
11 MABB_[1..5]
MABB_[1..5]
10 CSA_L[0..3]
CSA_L[0..3]
11 CSB_L[0..1]
CSB_L[0..1]
10 CKEA_[0..3]
CKEA_[0..3]
11 CKEB_[0..1]
CKEB_[0..1]
10 DCLKA_L[0..5]
DCLKA_L[0..5]
10 DCLKA_H[0..5]
DCLKA_H[0..5]
11 DCLKB_L[0..2]
DCLKB_L[0..2]
11 DCLKB_H[0..2]
DCLKB_H[0..2]
SWE_L_A
SCAS_L_A
SRAS_L_A
SWE_L_B
SCAS_L_B
SRAS_L_B
SBSA_0
SBSA_1
SBSB_0
SBSB_1
SWE_L_A 10
SCAS_L_A 10
SRAS_L_A 10
SWE_L_B 11
SCAS_L_B 11
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SBSB_0 11
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I (GMCH) Part C
Size B
Document Number
M981
Rev 5.0
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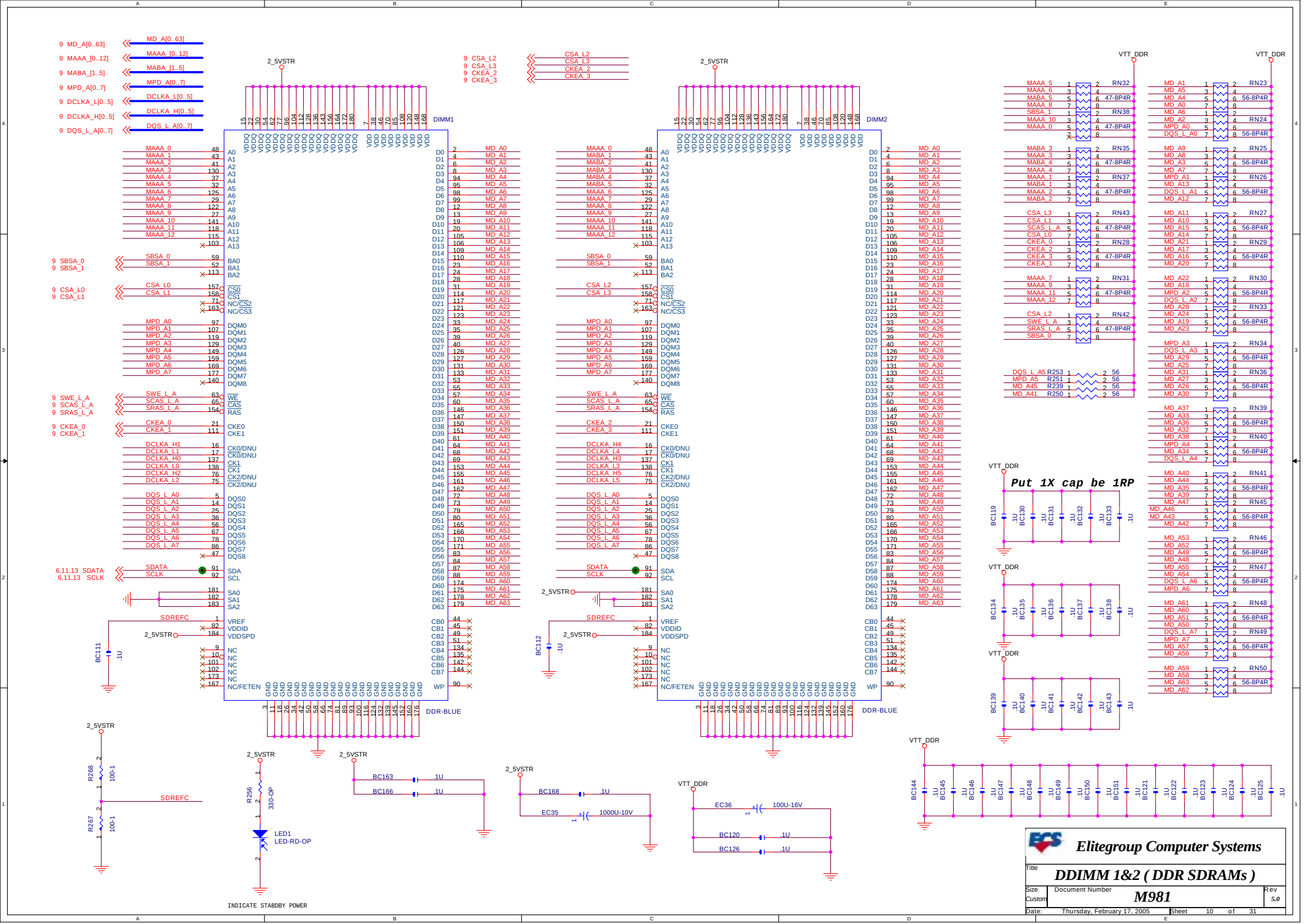
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MD_A[0..63]
9 MAAA_[0..12]
MAAA_[0..12]
9 MABA_[1..5]
MABA_[1..5]
9 MPD_A[0..7]
MPD_A[0..7]
9 DCLKA_L[0..5]
DCLKA_L[0..5]
9 DCLKA_H[0..5]
DCLKA_H[0..5]
9 DQS_L_A[0..7]
DQS_L_A[0..7]
2_5VSTR
DIMM1
9 SBSA_0
9 SBSA_1
9 CSA_L0
9 CSA_L1
9 SWE_L_A
9 SCAS_L_A
9 SRAS_L_A
9 CKEA_0
9 CKEA_1
6,11,13 SDATA
6,11,13 SCLK
SDREFC
BC111
.1U
9 CSA_L2
9 CSA_L3
9 CKEA_2
9 CKEA_3
DIMM2
BC112
DDR-BLUE
R268
100-1
R267
100-1
SDREFC
R256
330-OP
LED1
LED-RD-OP
INDICATE STABDBY POWER
BC163
BC166
BC168
EC35
1000U-10V
EC36
100U-16V
BC120
BC126
VTT_DDR
RN32
RN38
RN35
RN37
RN43
RN28
RN31
RN42
47-8P4R
56-8P4R
R253
R251
R239
R250
56
Put 1X cap be 1RP
BC119
BC130
BC131
BC132
BC133
BC134
BC135
BC136
BC137
BC138
BC139
BC140
BC141
BC142
BC143
BC144
BC145
BC146
BC147
BC148
BC149
BC150
BC151
BC121
BC122
BC123
BC124
BC125
RN23
RN25
RN24
RN26
RN27
RN30
RN34
RN33
RN36
RN39
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Elitegroup Computer Systems
DDIMM 1&2 (DDR SDRAMs)
Size
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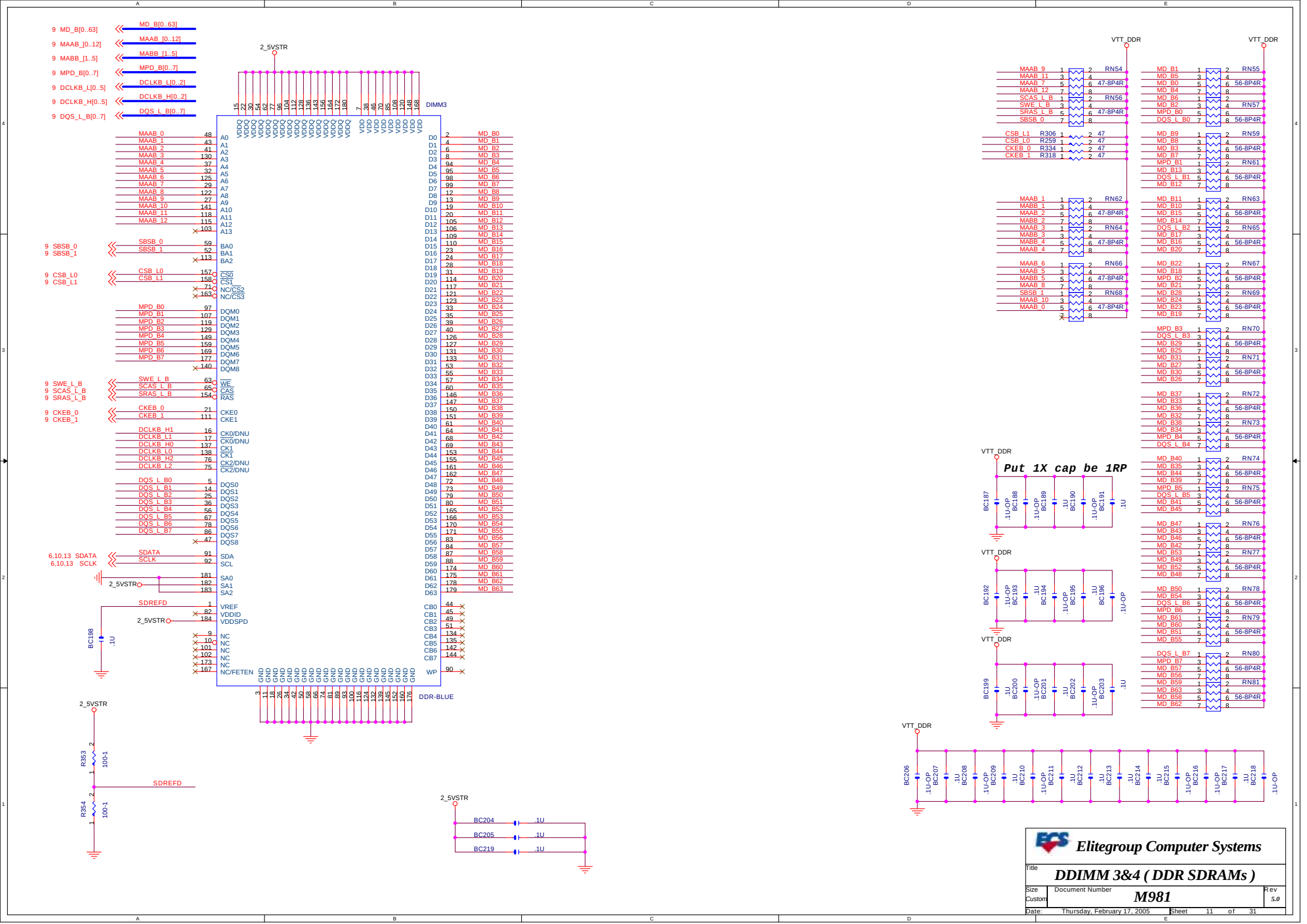
9 MD_B[0..63]
9 MAAB_[0..12]
9 MABB_[1..5]
9 MPD_B[0..7]
9 DCLKB_L[0..2]
9 DCLKB_H[0..2]
9 DQS_L_B[0..7]
MD_B[0..63]
MAAB_[0..12]
MABB_[1..5]
MPD_B[0..7]
DCLKB_L[0..2]
DCLKB_H[0..2]
DQS_L_B[0..7]
2_5VSTR
DIMM3
DDR-BLUE
9 SBSB_0
9 SBSB_1
9 CSB_L0
9 CSB_L1
9 SWE_L_B
9 SCAS_L_B
9 SRAS_L_B
9 CKEB_0
9 CKEB_1
6,10,13 SDATA
6,10,13 SCLK
SDREFD
BC198
.1U
R353
R354
100-1
BC204
BC205
BC219
VTT_DDR
RN54
RN55
RN56
RN57
RN59
RN61
RN62
RN63
RN64
RN65
RN66
RN67
RN68
RN69
RN70
RN71
RN72
RN73
RN74
RN75
RN76
RN77
RN78
RN79
RN80
RN81
47-8P4R
56-8P4R
Put 1X cap be 1RP
BC187
BC188
BC189
BC190
BC191
BC192
BC193
BC194
BC195
BC196
BC199
BC200
BC201
BC202
BC203
BC206
BC207
BC208
BC209
BC210
BC211
BC212
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BC214
BC215
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BC217
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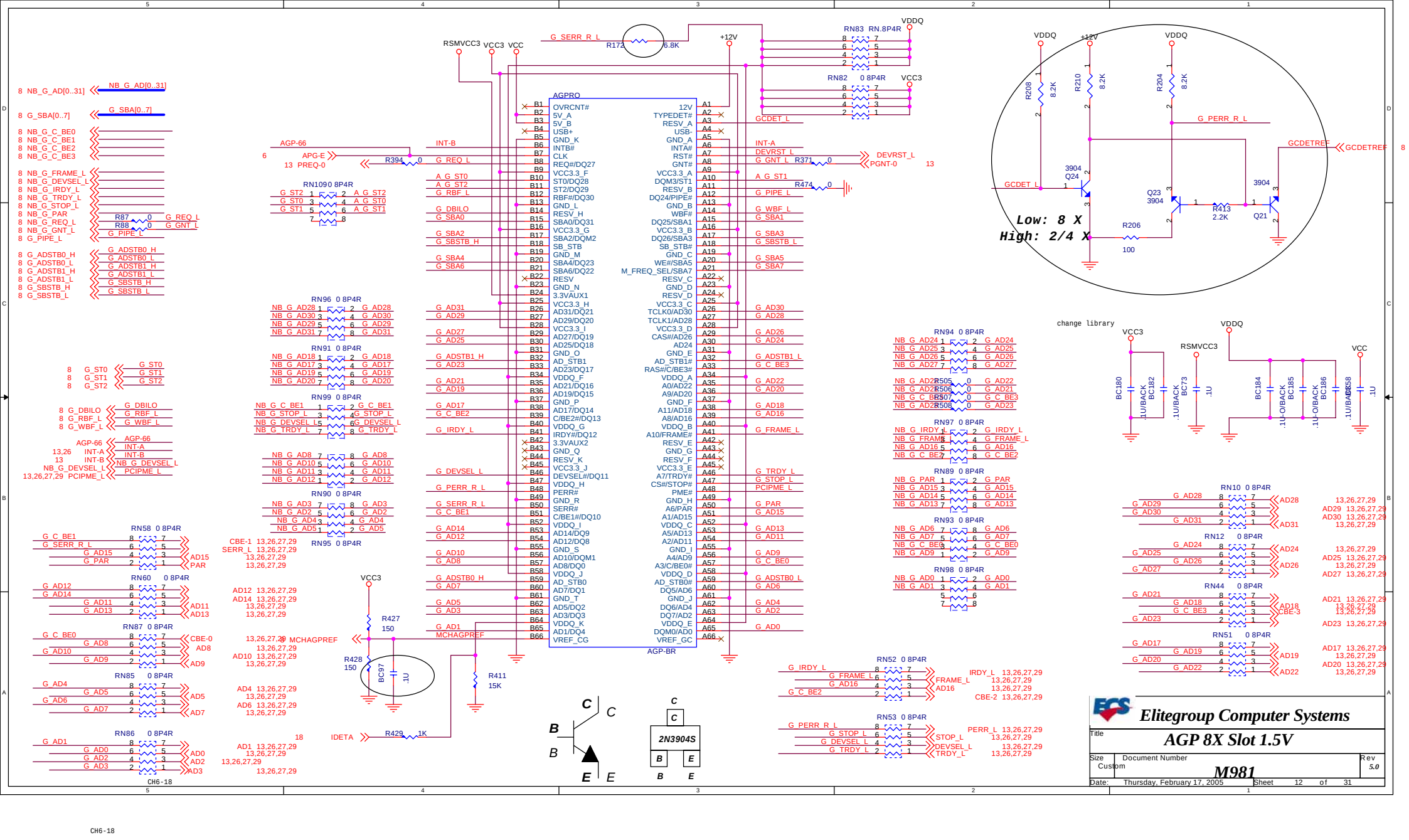
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AGP-BR
2N3904S
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High: 2/4 X
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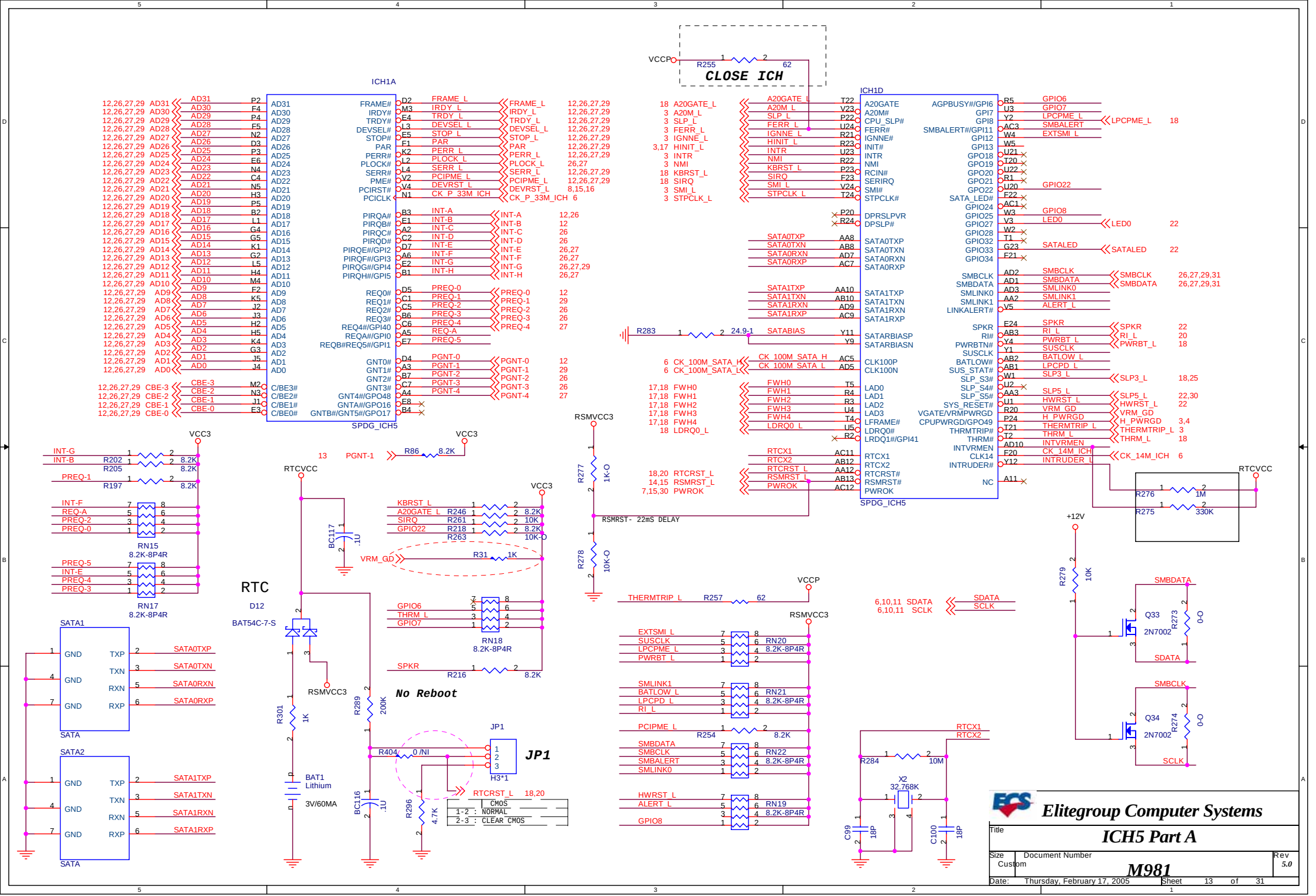
CH6-18


CLOSE ICH
ICH1A
ICH1D
SPDG_ICH5
RTC
D12
BAT54C-7-S
BAT1
Lithium
3V/60MA
No Reboot
JP1
CMOS
1-2 : NORMAL
2-3 : CLEAR CMOS
RSMRST- 22mS DELAY
SATA0
SATA1
SATA2
RN15
8.2K-8P4R
RN17
8.2K-8P4R
RN18
8.2K-8P4R
RN19
8.2K-8P4R
RN20
8.2K-8P4R
RN21
8.2K-8P4R
RN22
8.2K-8P4R
Elitegroup Computer Systems
ICH5 Part A
M981
Custom
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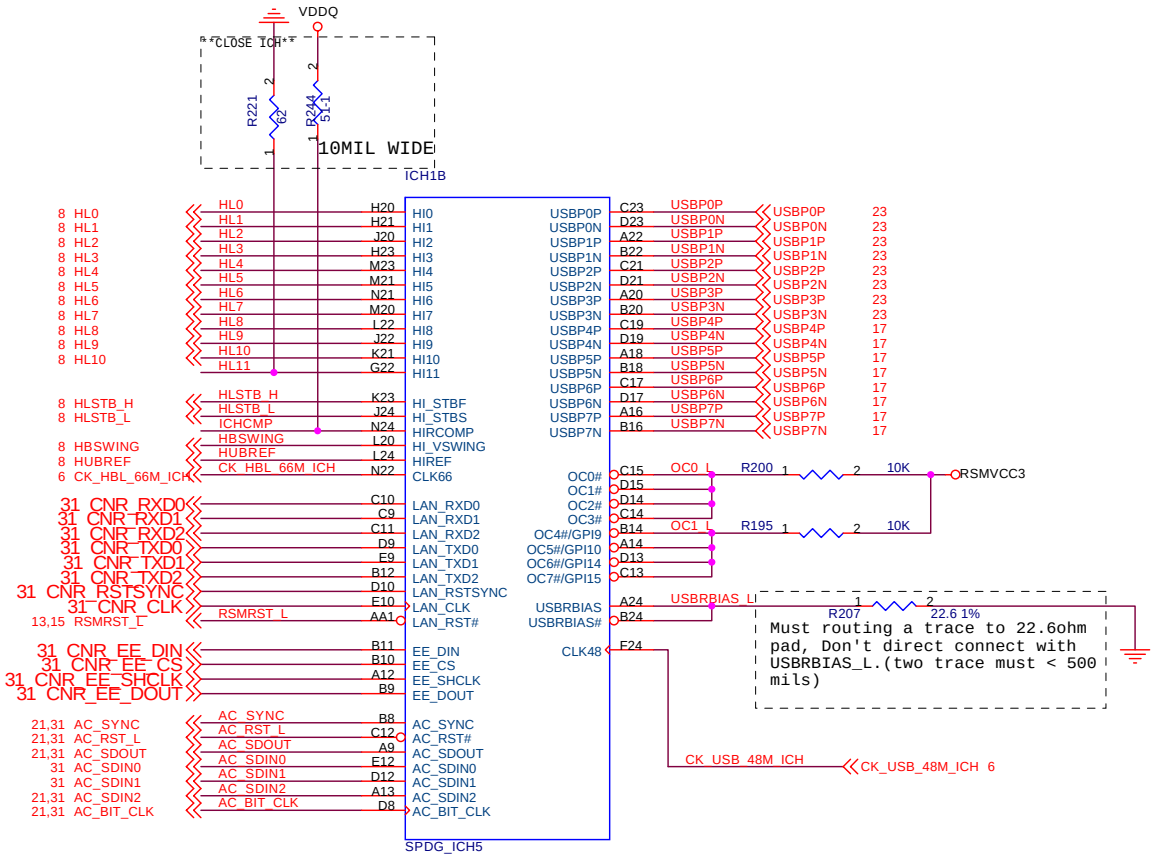

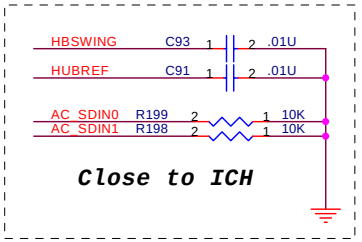

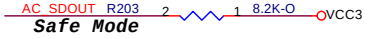

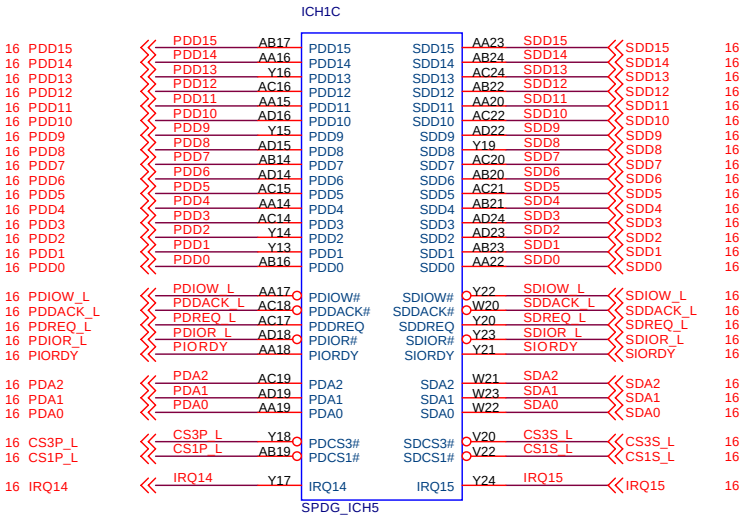

Elitegroup Computer Systems

Title: ICH5 Part B

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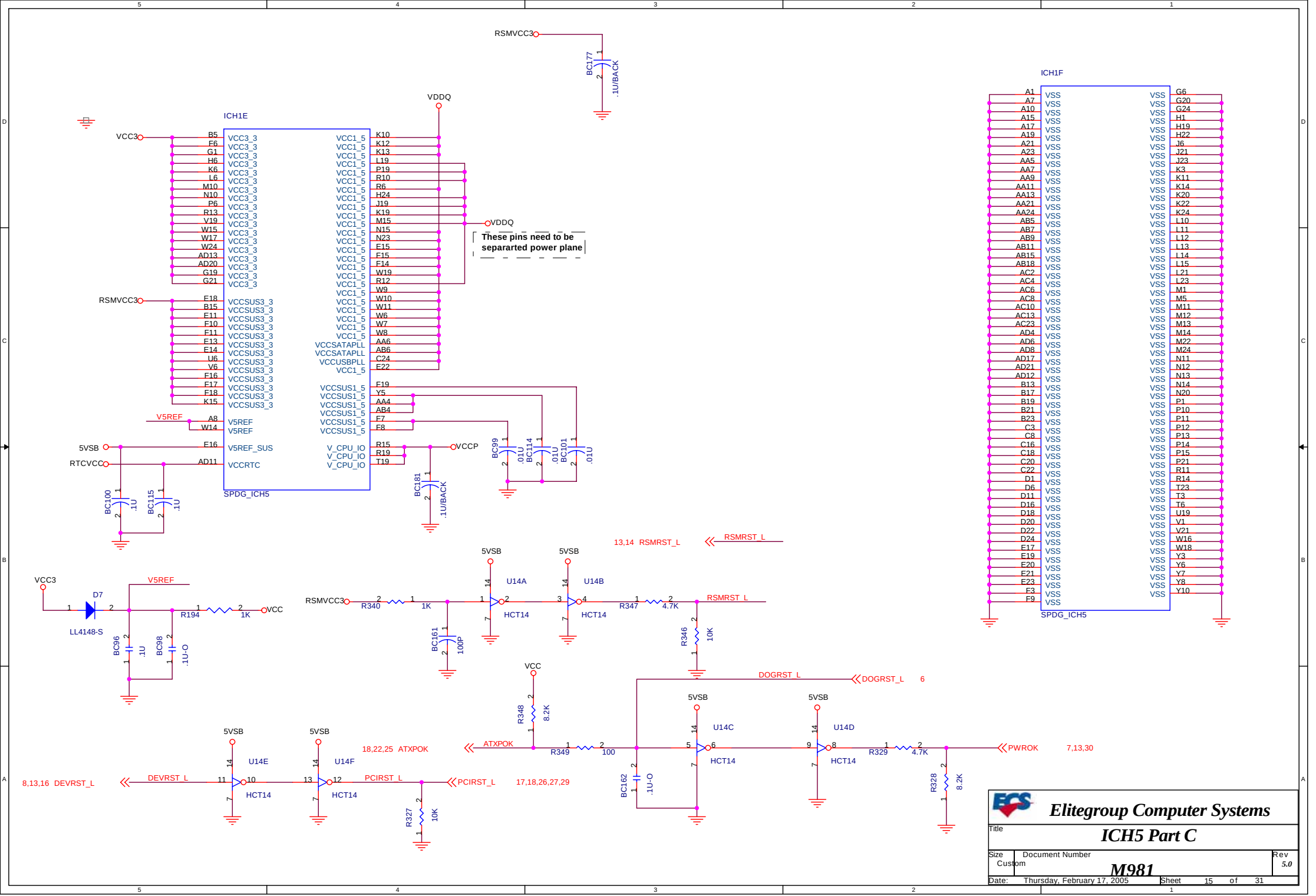

Elitegroup Computer Systems		
Title: ICH5 Part C		
Size: Custom	Document Number: M981	Rev: 5.0
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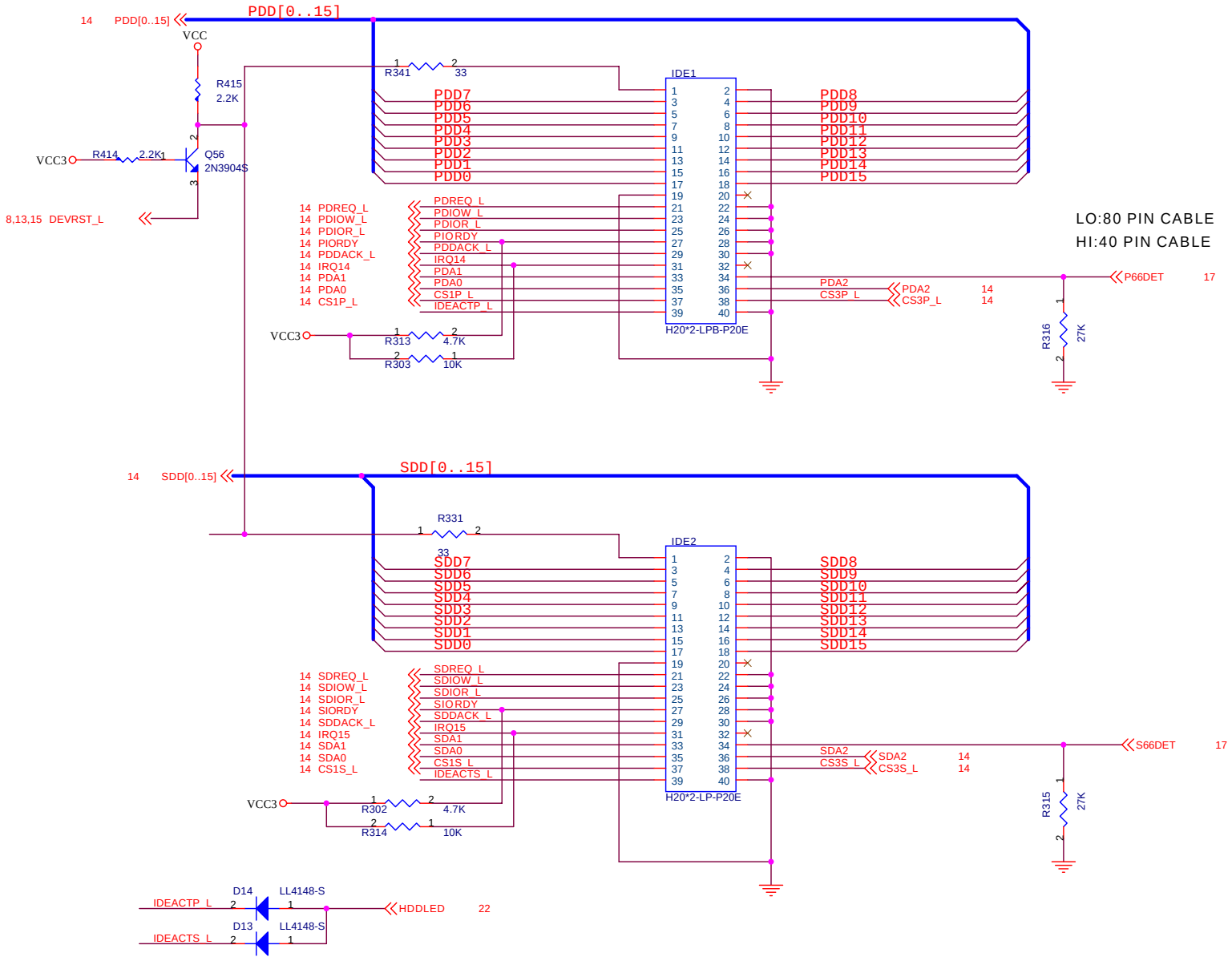

MAX TRACE LENGTH IS 8"

Elitegroup Computer Systems

Title: IDE Connector

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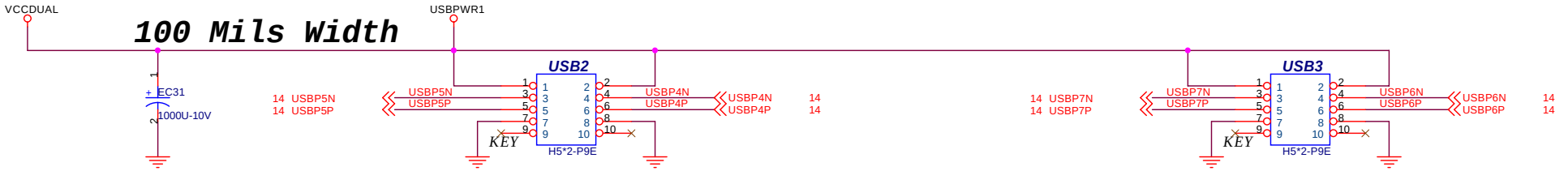
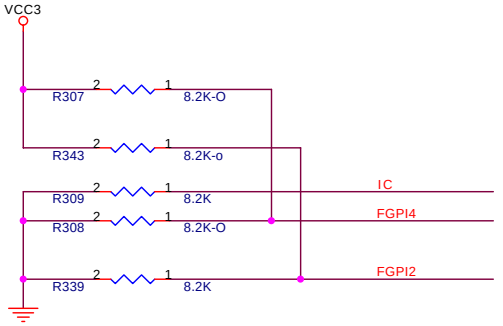
USB PORT INTERFACE





GPI2:

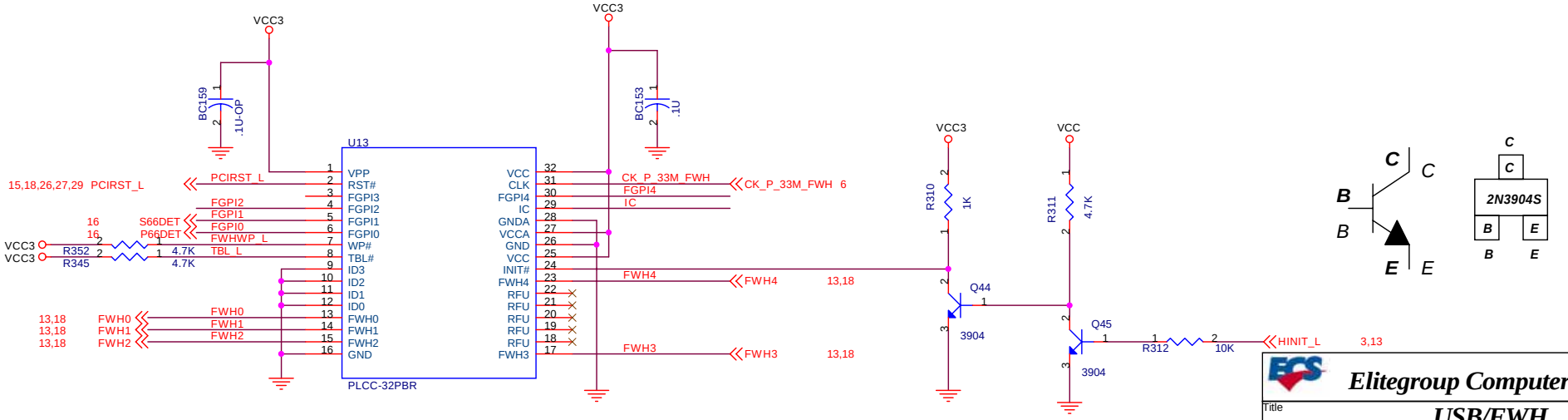
High: w/o S3

Low: w/S3

GPI4:

High: Reseved

Low: Reserved



Elitegroup Computer Systems	
Title	USB/FWH
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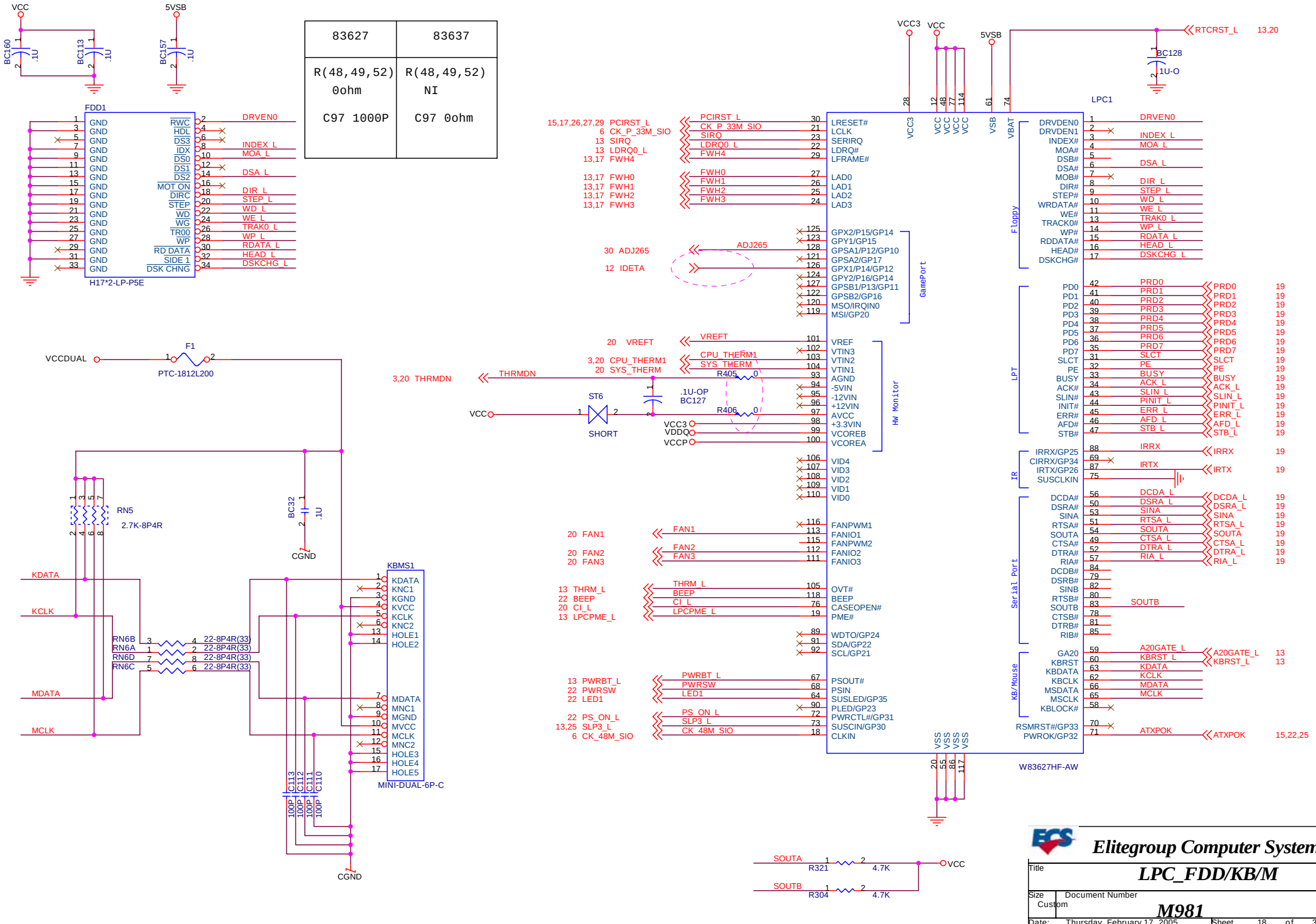
VCC
5VSB
BC160 .1U
BC113 .1U
BC157 .1U
83627 | 83637
R(48,49,52) 0ohm | R(48,49,52) NI
C97 1000P | C97 0ohm
FDD1
H17*2-LP-P5E
DRVEN0
INDEX_L
MOA_L
DSA_L
DIR_L
STEP_L
WD_L
WE_L
TRAK0_L
WP_L
RDATA_L
HEAD_L
DSKCHG_L
VCCDUAL
F1
PTC-1812L200
RN5
2.7K-8P4R
BC32 .1U
CGND
KDATA
KCLK
MDATA
MCLK
RN6B 22-8P4R(33)
RN6A 22-8P4R(33)
RN6D 22-8P4R(33)
RN6C 22-8P4R(33)
KBMS1
KNC1
KGND
KVCC
KCLK
KNC2
HOLE1
HOLE2
MNC1
MGND
MVCC
MCLK
MNC2
HOLE3
HOLE4
HOLE5
MINI-DUAL-6P-C
C113 100P
C112 100P
C111 100P
C110 100P
CGND
VCC3
VCC
5VSB
RTCRST_L 13,20
BC128 1U-O
LPC1
LRESET#
LCLK
SERIRQ
LDRQ#
LFRAME#
LAD0
LAD1
LAD2
LAD3
GPX2/P15/GP14
GPY1/GP15
GPSA1/P12/GP10
GPSA2/GP17
GPX1/P14/GP12
GPY2/P16/GP14
GPSB1/P13/GP11
GPSB2/GP16
MSO/IRQIN0
MSI/GP20
VREF
VTIN3
VTIN2
VTIN1
AGND
-5VIN
-12VIN
+12VIN
AVCC
+3.3VIN
VCOREB
VCOREA
VID4
VID3
VID2
VID1
VID0
FANPWM1
FANIO1
FANPWM2
FANIO2
FANIO3
OVT#
BEEP
CASEOPEN#
PME#
WDTO/GP24
SDA/GP22
SCL/GP21
PSOUT#
PSIN
SUSLED/GP35
PLED/GP23
PWRCTL#/GP31
SUSCIN/GP30
CLKIN
W83627HF-AW
Floppy
GamePort
HW Monitor
LPT
IR
Serial Port
KB/Mouse
ADJ265
IDETA
THRMDN
CPU_THERM1
SYS_THERM
R405 0
R406 0
.1U-OP BC127
ST6
SHORT
SOUTA
SOUTB
R321 4.7K
R304 4.7K
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LPC_FDD/KB/M
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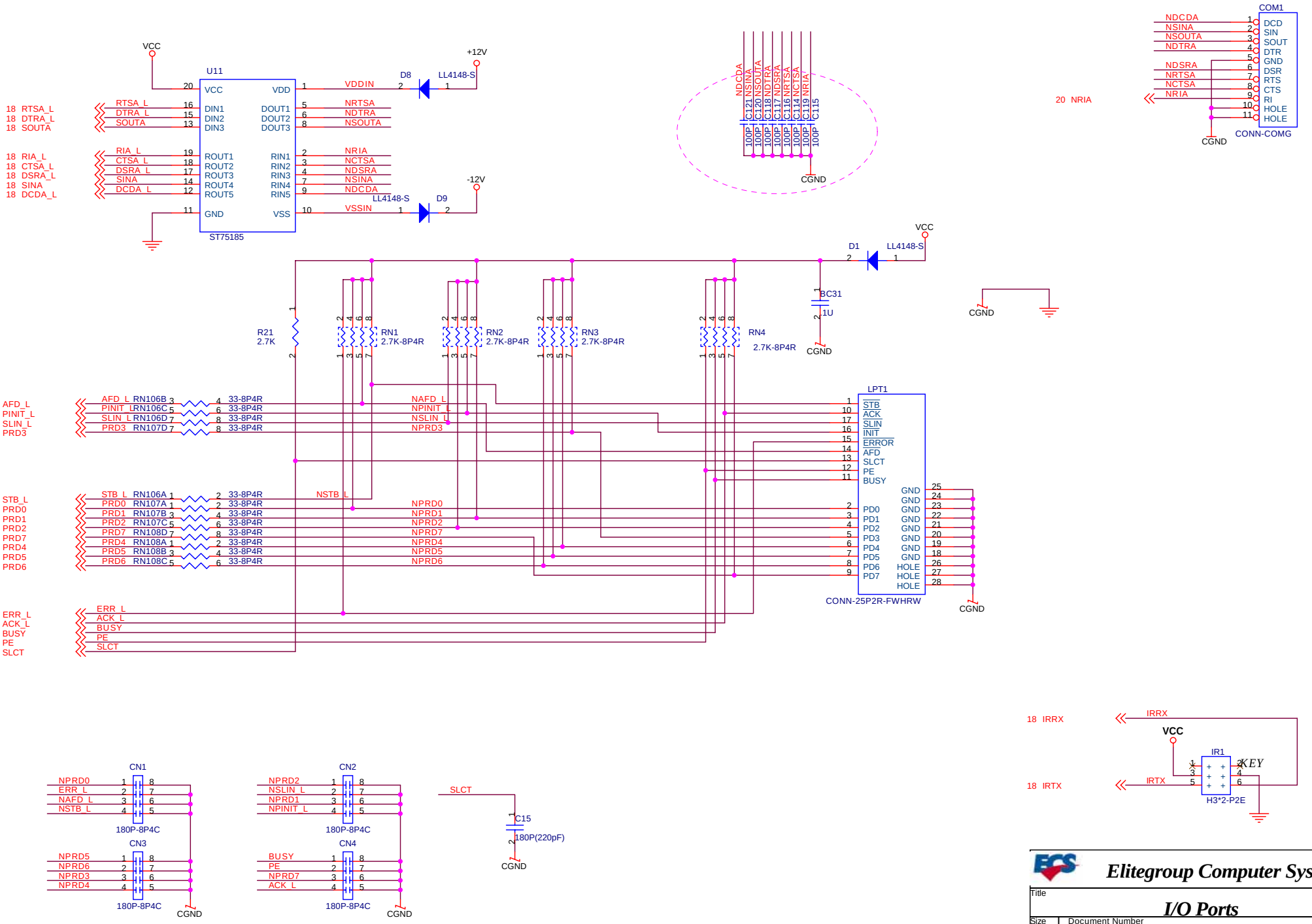

Elitegroup Computer Systems	
Title	I/O Ports
Size: Custom	Document Number: M981
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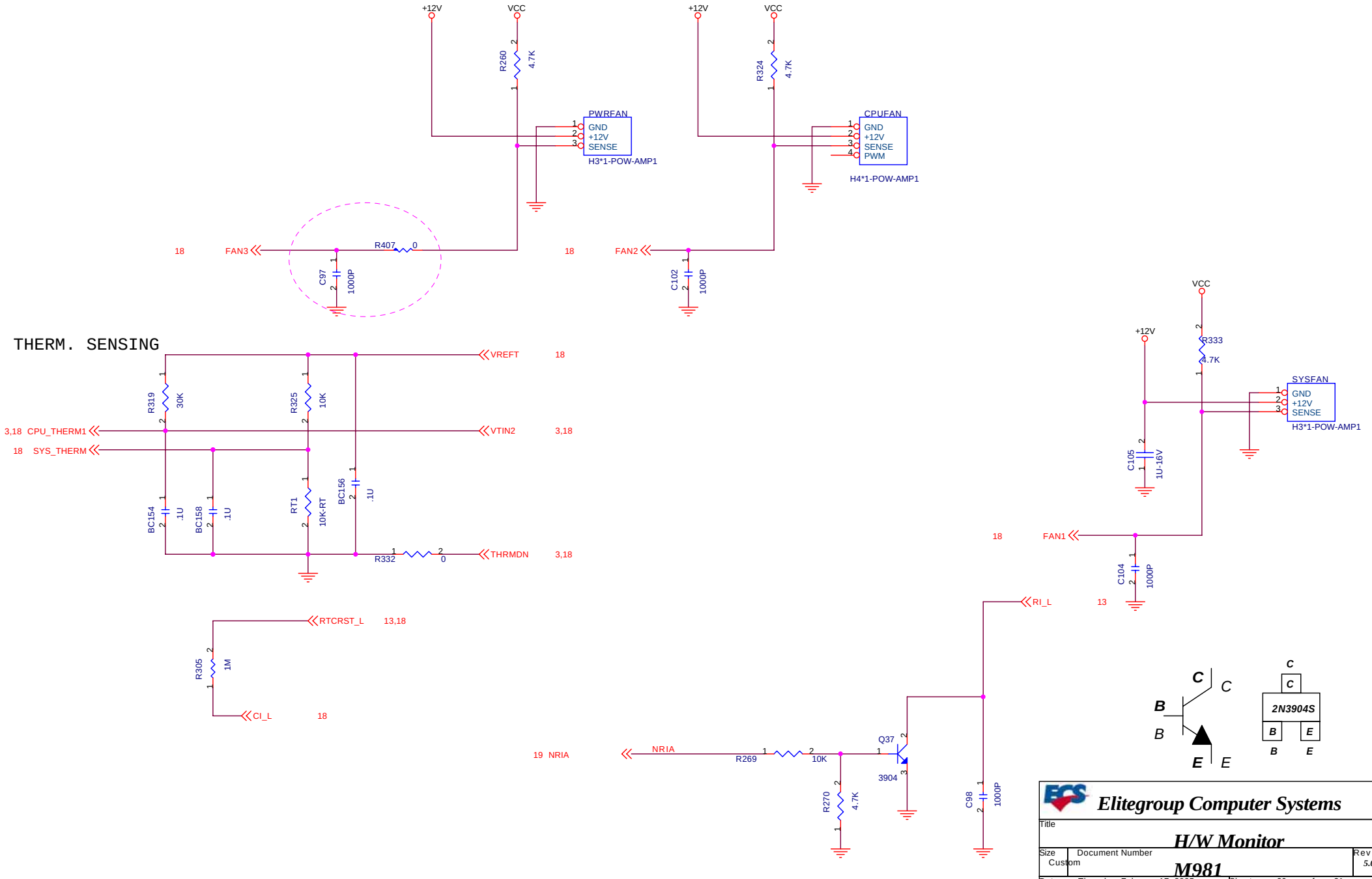

Elitegroup Computer Systems		
Title: H/W Monitor		
Size: Custom	Document Number: M981	Rev: 5.0
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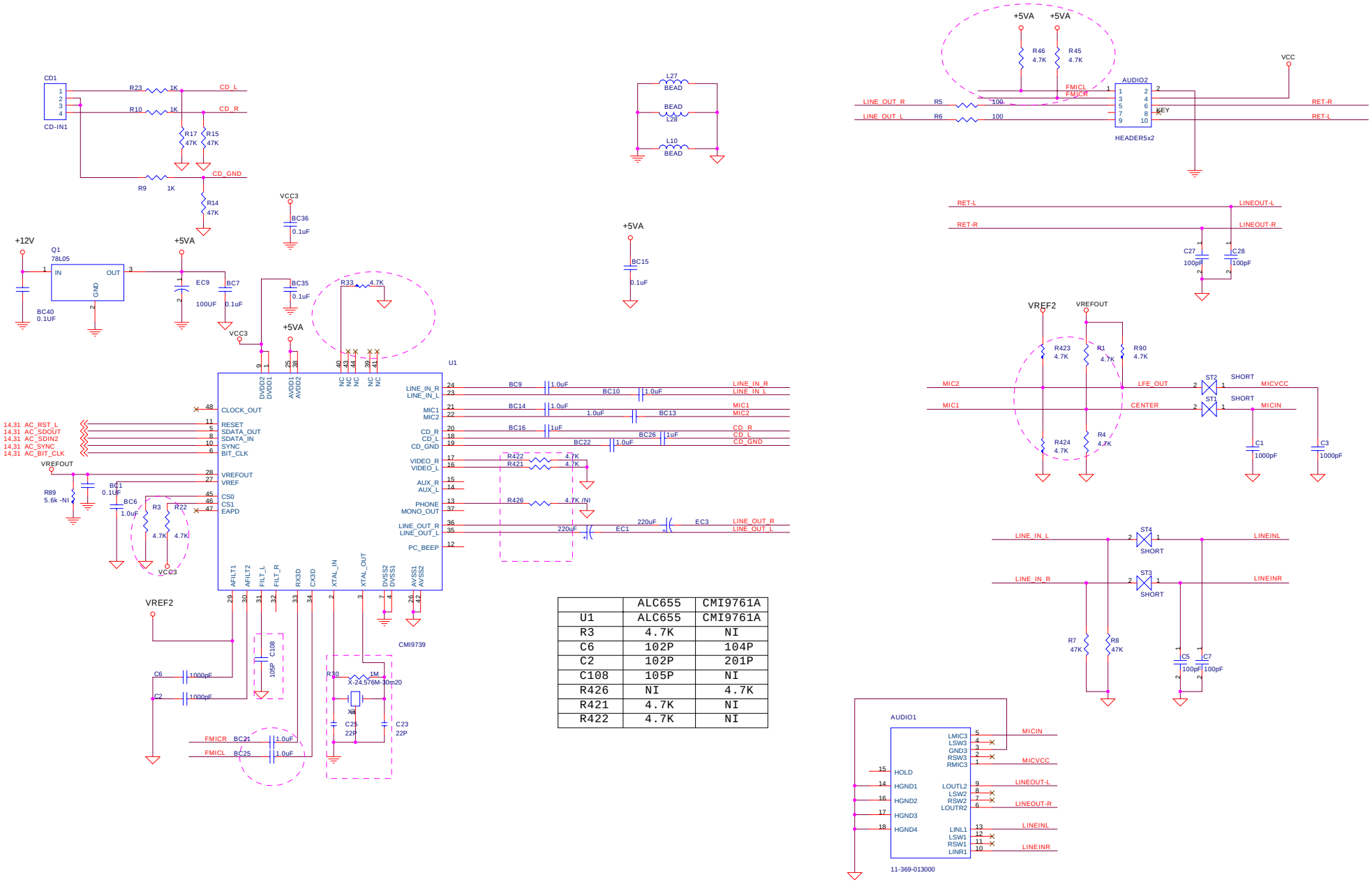

	ALC655	CMI9761A
U1	ALC655	CMI9761A
R3	4.7K	NI
C6	102P	104P
C2	102P	201P
C108	105P	NI
R426	NI	4.7K
R421	4.7K	NI
R422	4.7K	NI

Elitegroup Computer Systems

Title: AC97 Codec

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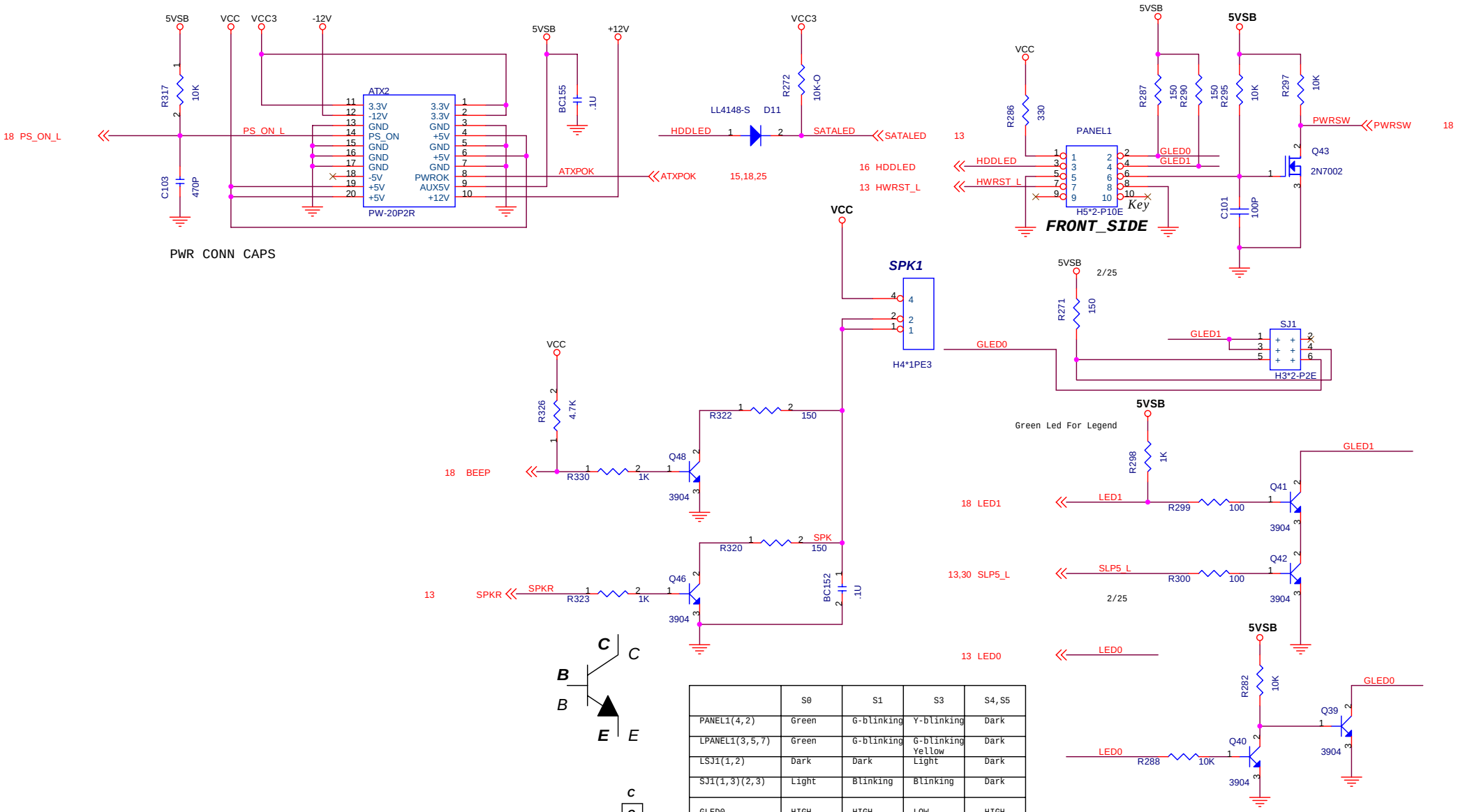
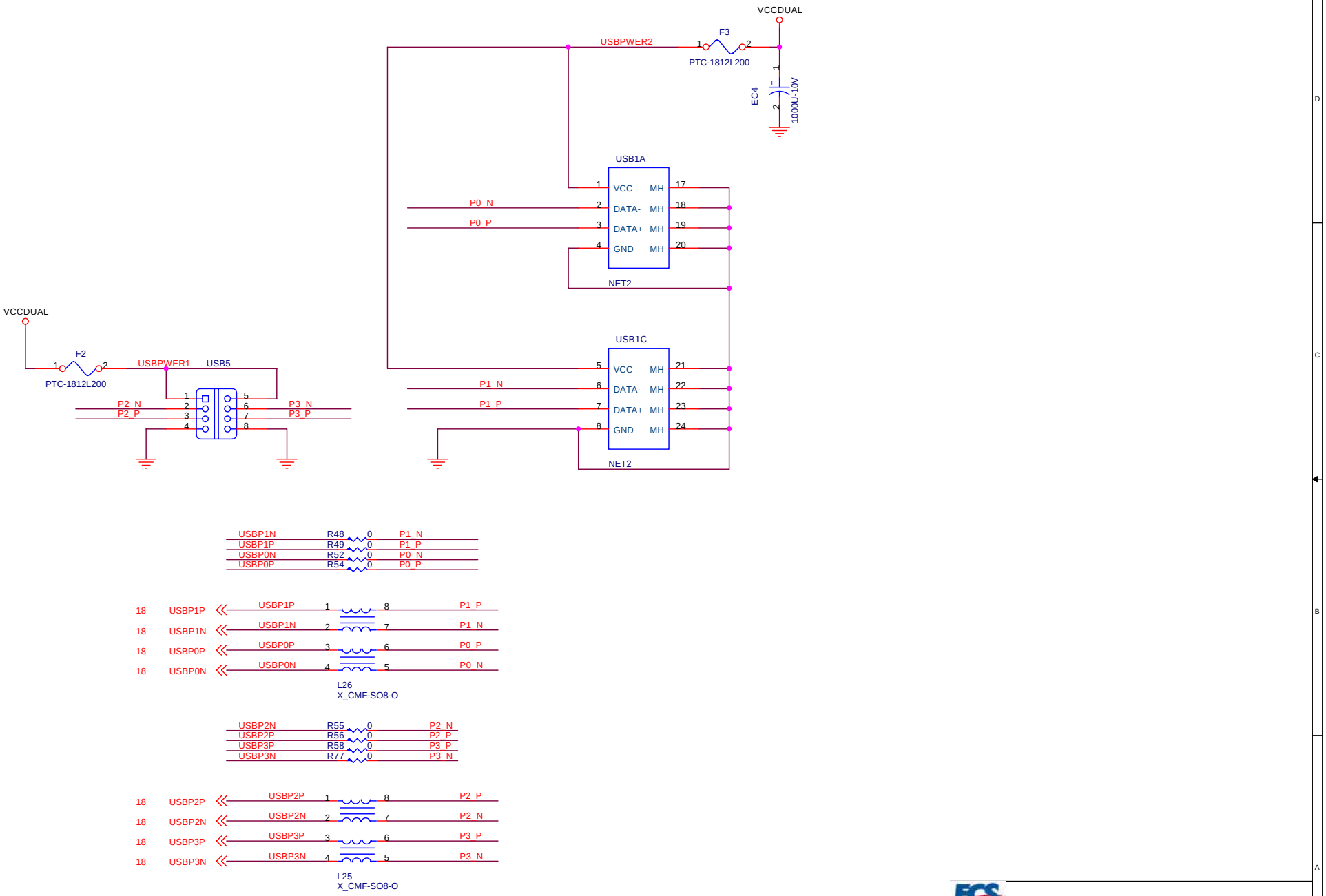


	S0	S1	S3	S4,S5
PANEL1(4,2)	Green	G-blinking	Y-blinking	Dark
LPANEL1(3,5,7)	Green	G-blinking	G-blinking Yellow	Dark
LSJ1(1,2)	Dark	Dark	Light	Dark
SJ1(1,3)(2,3)	Light	Blinking	Blinking	Dark
GLED0 GLED1	HIGH LOW	HIGH SWITCH	LOW SWITCH	HIGH HIGH



Elitegroup Computer Systems

Title: USB

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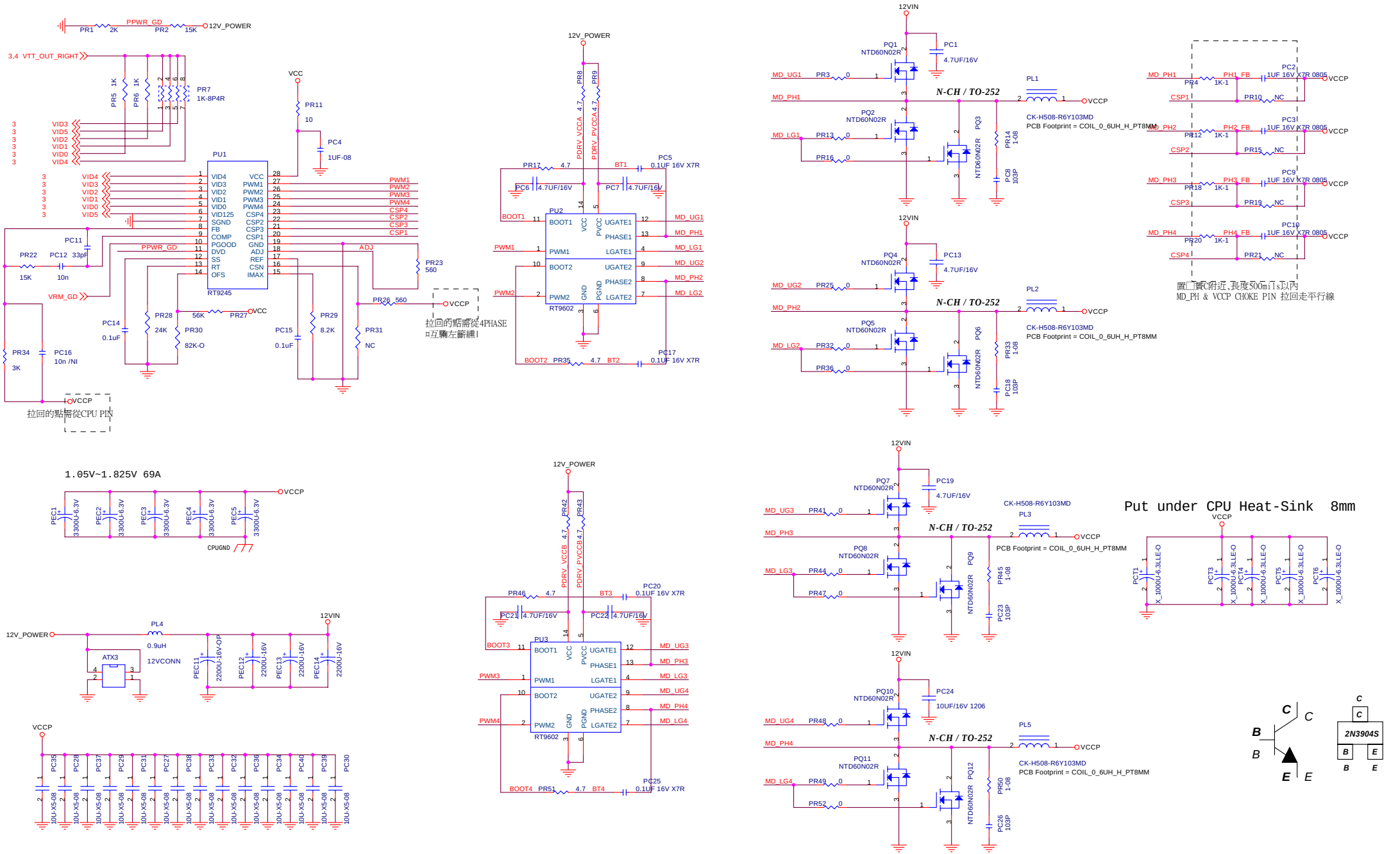

Elitegroup Computer Systems

Title: VOCRE DC-DC

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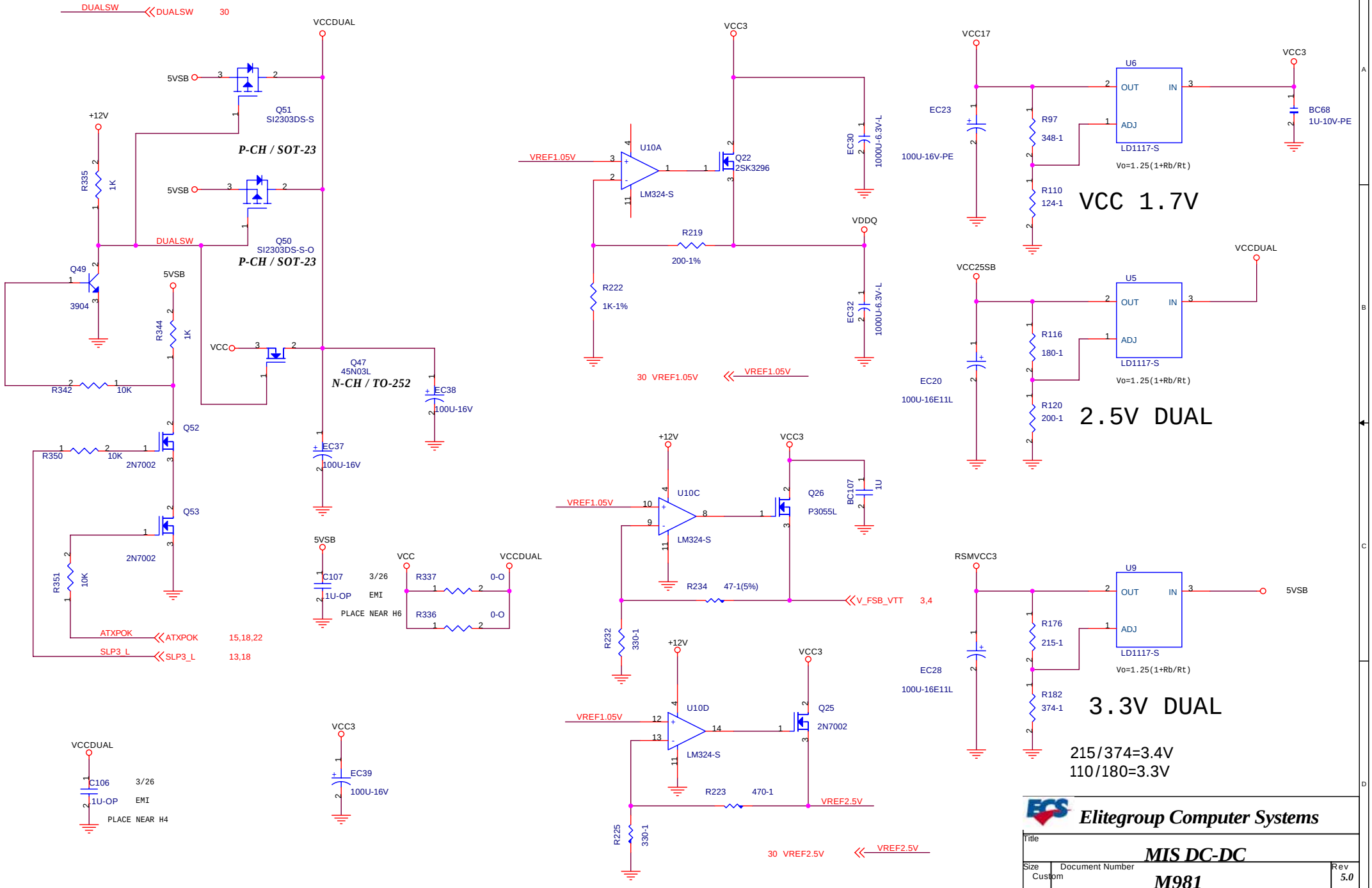

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Title	MIS DC-DC	
Size: Custom	Document Number: M981	Rev: 5.0
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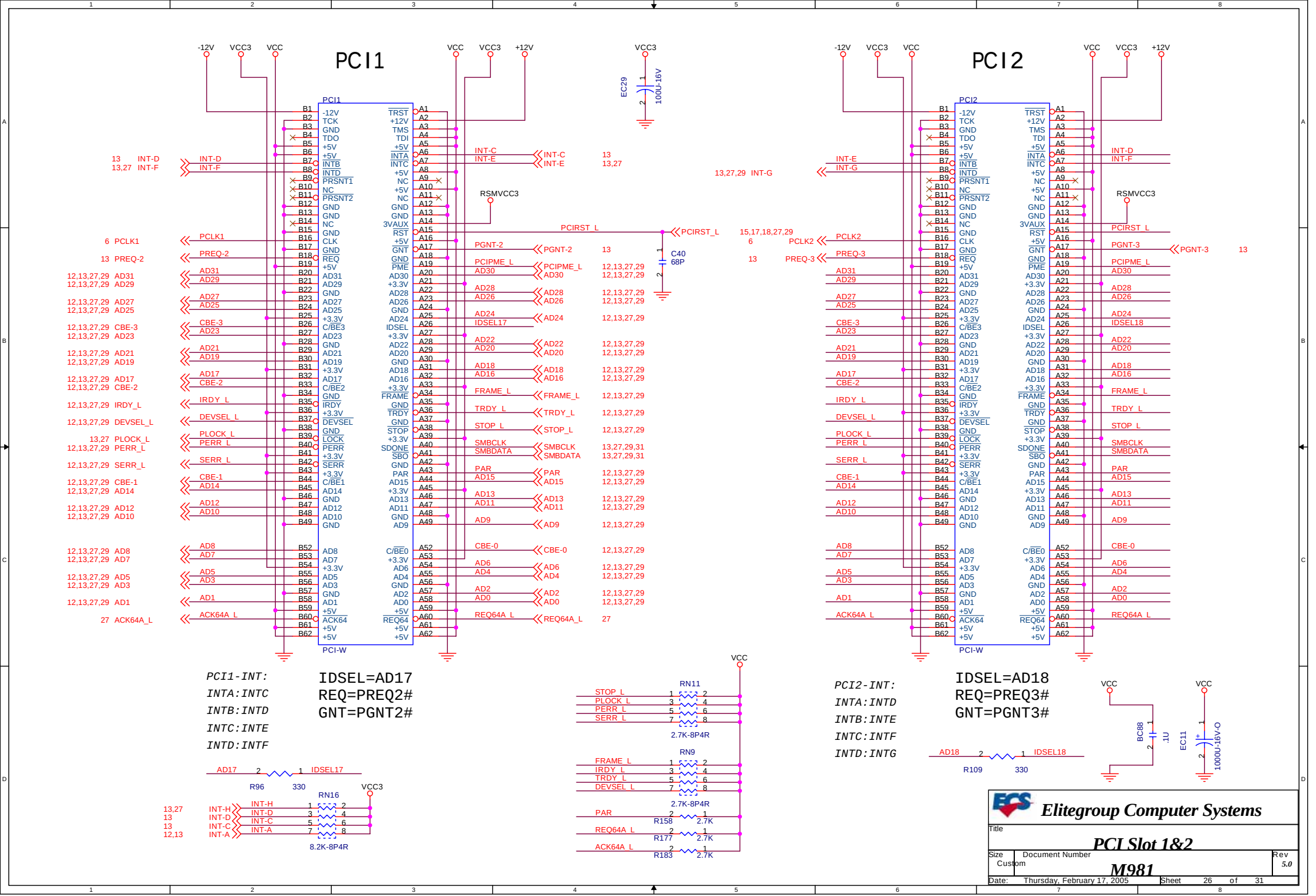
PCI1
PCI2
PCI1-INT:
INTA:INTC
INTB:INTD
INTC:INTE
INTD:INTF
IDSEL=AD17
REQ=PREQ2#
GNT=PGNT2#
PCI2-INT:
INTA:INTD
INTB:INTE
INTC:INTF
INTD:INTG
IDSEL=AD18
REQ=PREQ3#
GNT=PGNT3#
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PCI Slot 1&2
M981
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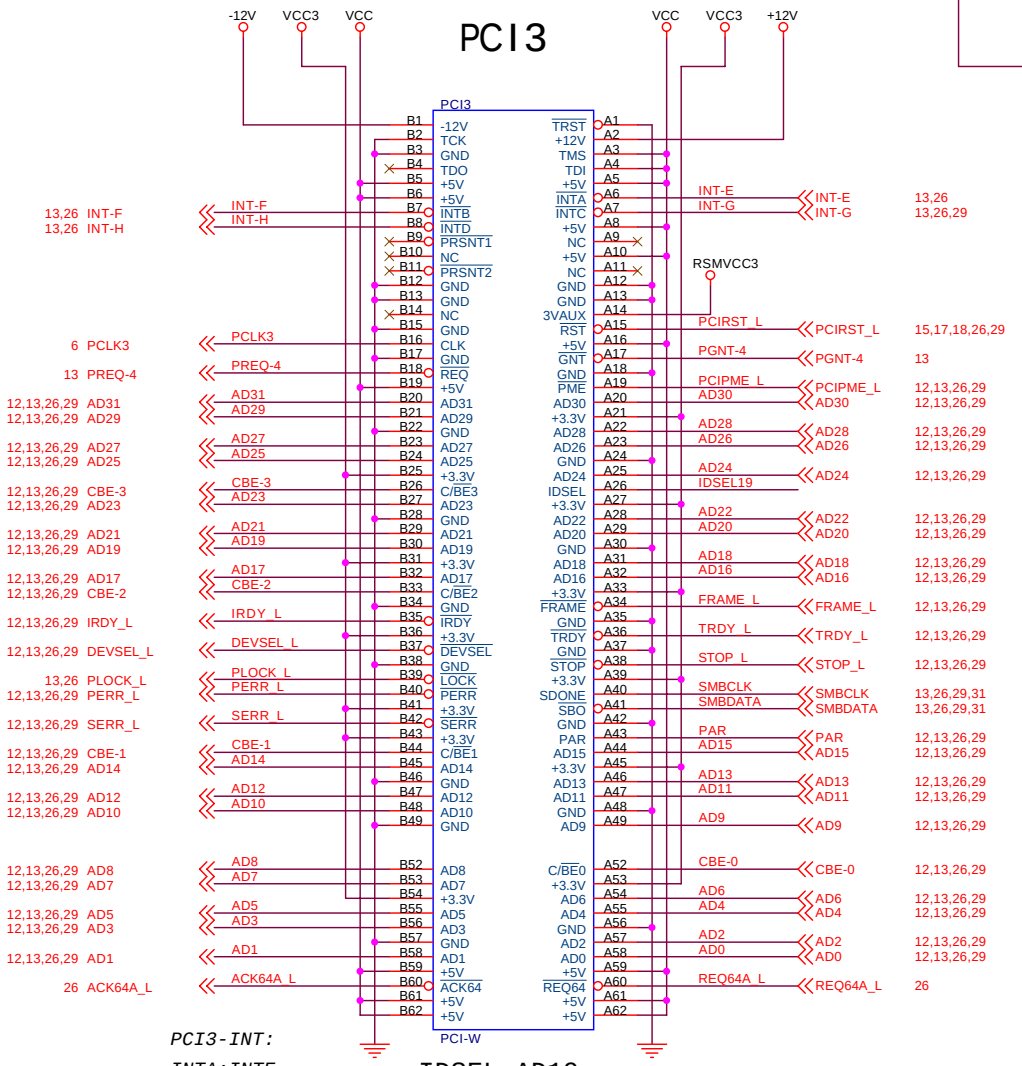

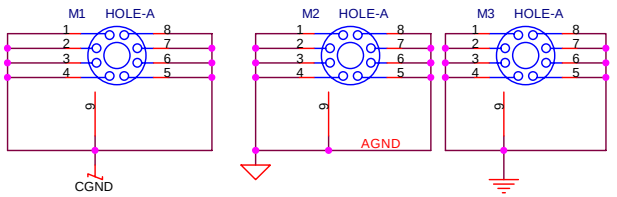

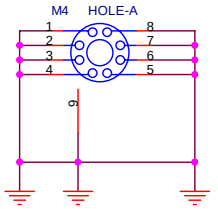

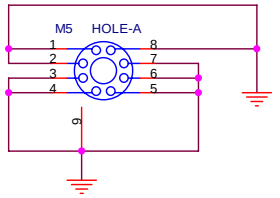

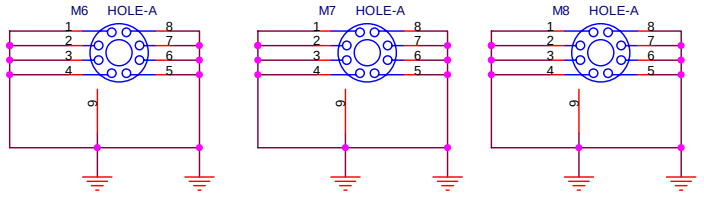

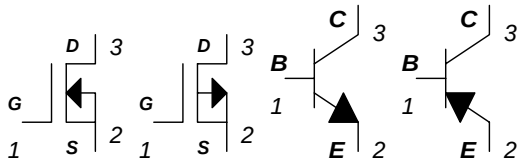

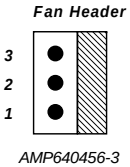

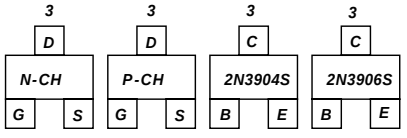

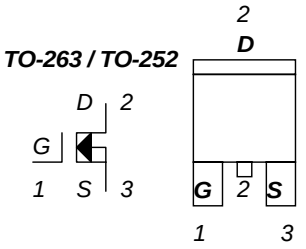

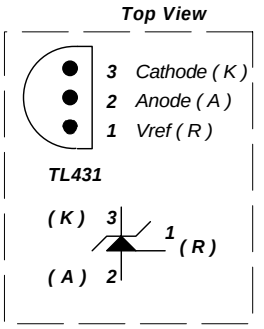

Elitegroup Computer Systems

Title	PCI Slot 3	
Size B	Document Number M981	Rev 5.0
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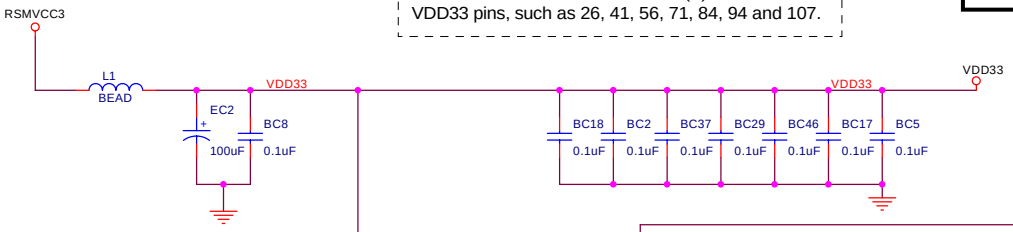
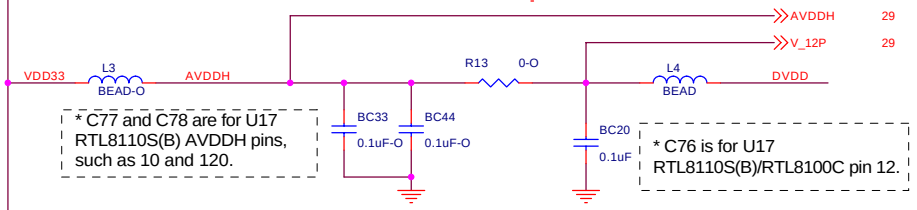
U8 is used for RTL8100C application, U4 and Q8 are used for RTL8110S(B) application.

At RTL8100C application, remove R57 and L7,keep L10. At RTL8110S application, remove L10 and R57, keep L7. At RTL8110SB application, remove L10, keep R57 and L7.

* C40 to C46 are for U17 RTL8110S(B)/RTL8100C VDD33 pins, such as 26, 41, 56, 71, 84, 94 and 107.



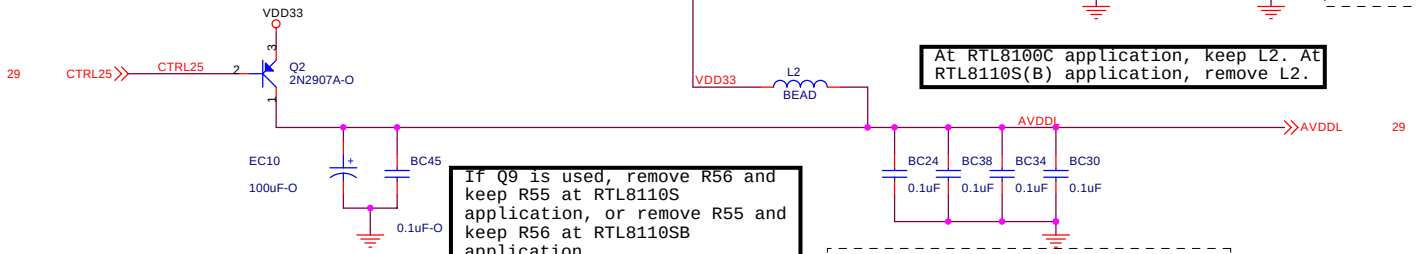
Q6 or U16 is used for RTL8110S(B) application. The 2.5V power can be supplied by external 2.5V regulator, U16. Or by internal regulator in RTL8110S(B) and external BJT, Q6. At RTL8100C application, Q6 and U16 must be removed.



* C77 and C78 are for U17 RTL8110S(B) AVDDH pins, such as 10 and 120.

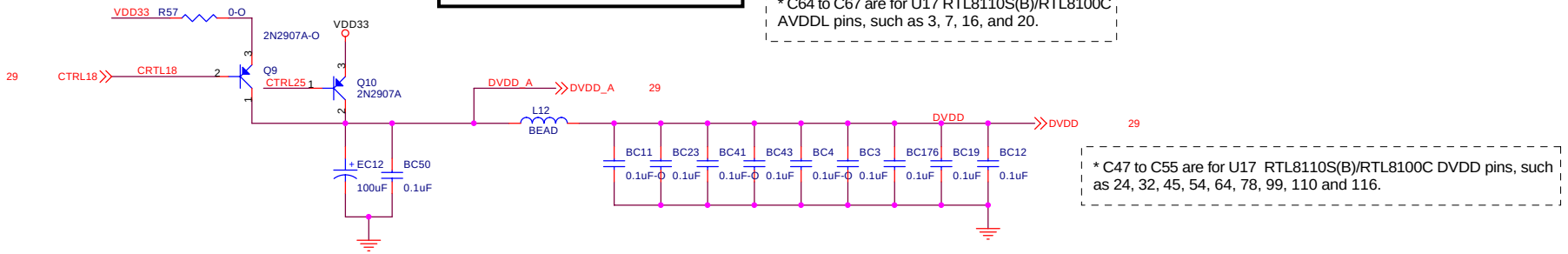
* C76 is for U17 RTL8110S(B)/RTL8100C pin 12.

At RTL8100C application, keep L2. At RTL8110S(B) application, remove L2.



If Q9 is used, remove R56 and keep R55 at RTL8110S application, or remove R55 and keep R56 at RTL8110SB application.

* C64 to C67 are for U17 RTL8110S(B)/RTL8100C AVDDL pins, such as 3, 7, 16, and 20.



* C47 to C55 are for U17 RTL8110S(B)/RTL8100C DVDD pins, such as 24, 32, 45, 54, 64, 78, 99, 110 and 116.

If U10 is used, R58 and R59 can be set to 2.7K and 1.2K to have 1.8V output power at RTL8110S application, or set to and 10K and 390 to have 1.3V output power at RTL8110SB application.

Q9 or U10 is used for RTL8110S(B) application. The DVDD power can be supplied by external adjustable regulator, U10. Or by internal regulator in RTL8110S(B) and external BJT, Q9. At RTL8100C application, Q9 and U10 must be removed.

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Title: Power

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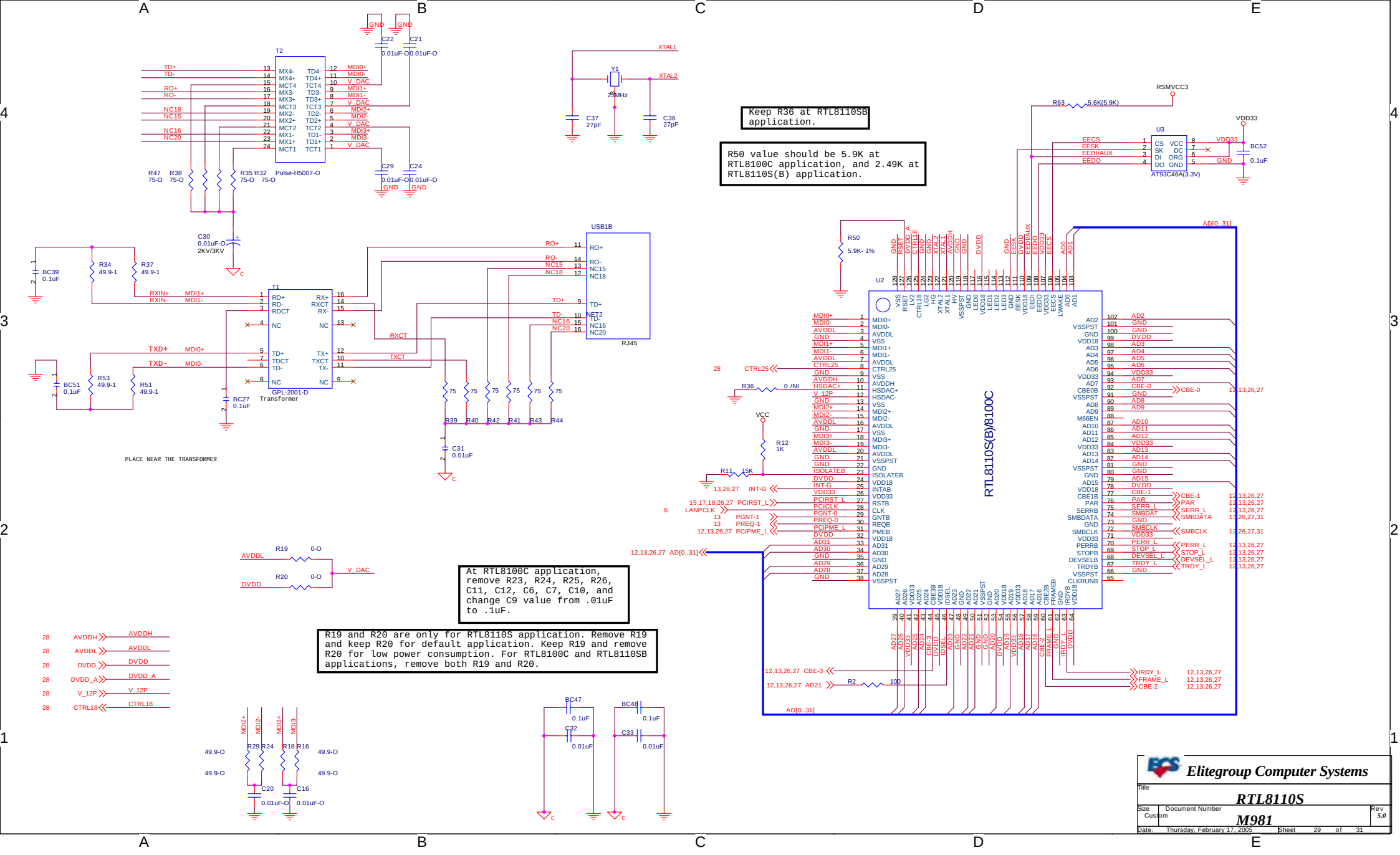
Keep R36 at RTL8110SB application.
R50 value should be 5.9K at RTL8100C application, and 2.49K at RTL8110S(B) application.
At RTL8100C application, remove R23, R24, R25, R26, C11, C12, C6, C7, C10, and change C9 value from .01uF to .1uF.
R19 and R20 are only for RTL8110S application. Remove R19 and keep R20 for default application. Keep R19 and remove R20 for low power consumption. For RTL8100C and RTL8110SB applications, remove both R19 and R20.
PLACE NEAR THE TRANSFORMER
RTL8110S(B)/8100C
Elitegroup Computer Systems
RTL8110S
M981
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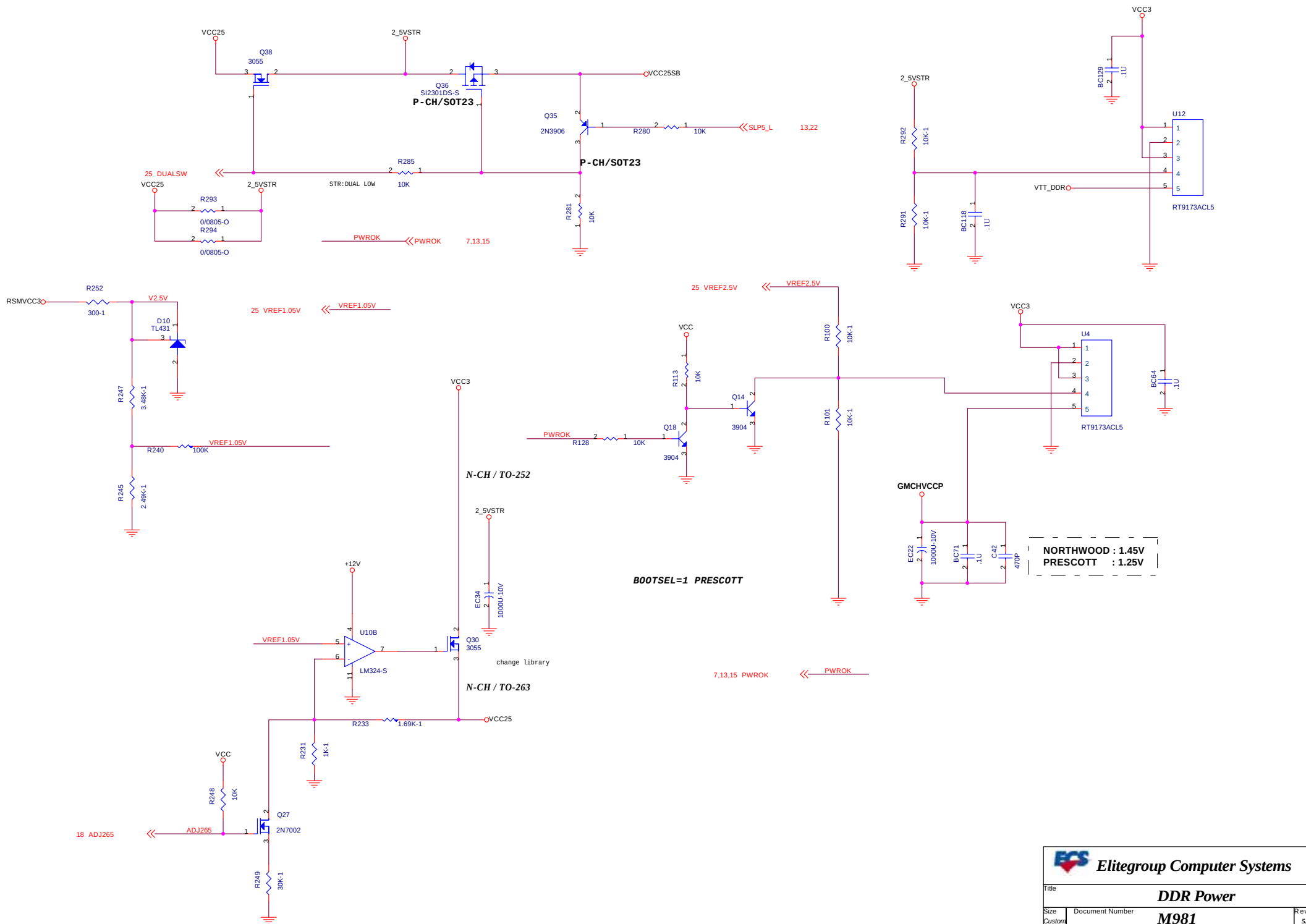

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Title: DDR Power

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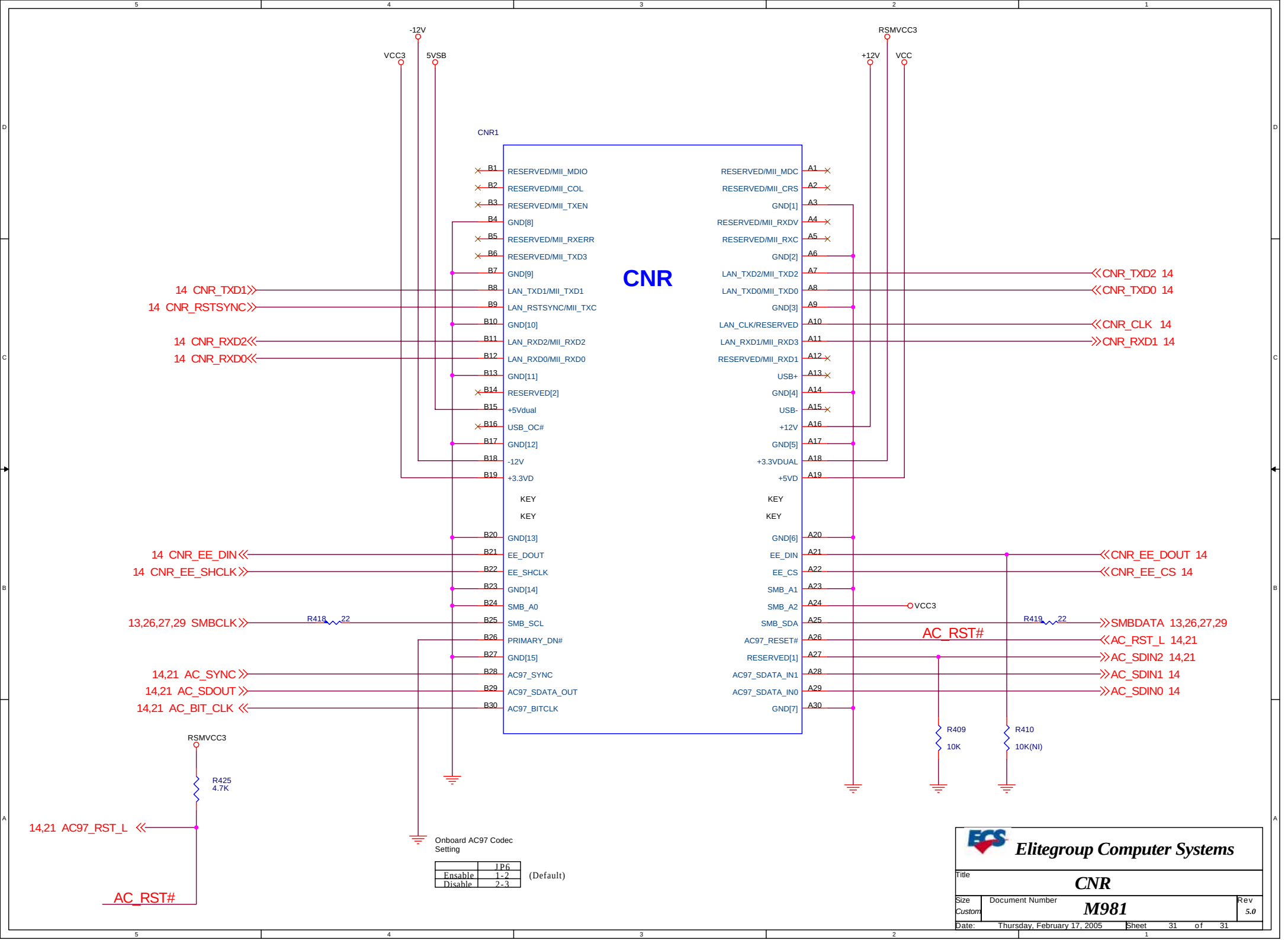
-12V
VCC3
5VSB
RSMVCC3
+12V
VCC
CNR1
CNR
B1 RESERVED/MII_MDIO
B2 RESERVED/MII_COL
B3 RESERVED/MII_TXEN
B4 GND[8]
B5 RESERVED/MII_RXERR
B6 RESERVED/MII_TXD3
B7 GND[9]
B8 LAN_TXD1/MII_TXD1
B9 LAN_RSTSYNC/MII_TXC
B10 GND[10]
B11 LAN_RXD2/MII_RXD2
B12 LAN_RXD0/MII_RXD0
B13 GND[11]
B14 RESERVED[2]
B15 +5Vdual
B16 USB_OC#
B17 GND[12]
B18 -12V
B19 +3.3VD
KEY
KEY
B20 GND[13]
B21 EE_DOUT
B22 EE_SHCLK
B23 GND[14]
B24 SMB_A0
B25 SMB_SCL
B26 PRIMARY_DN#
B27 GND[15]
B28 AC97_SYNC
B29 AC97_SDATA_OUT
B30 AC97_BITCLK
A1 RESERVED/MII_MDC
A2 RESERVED/MII_CRS
A3 GND[1]
A4 RESERVED/MII_RXDV
A5 RESERVED/MII_RXC
A6 GND[2]
A7 LAN_TXD2/MII_TXD2
A8 LAN_TXD0/MII_TXD0
A9 GND[3]
A10 LAN_CLK/RESERVED
A11 LAN_RXD1/MII_RXD3
A12 RESERVED/MII_RXD1
A13 USB+
A14 GND[4]
A15 USB-
A16 +12V
A17 GND[5]
A18 +3.3VDUAL
A19 +5VD
KEY
KEY
A20 GND[6]
A21 EE_DIN
A22 EE_CS
A23 SMB_A1
A24 SMB_A2
A25 SMB_SDA
A26 AC97_RESET#
A27 RESERVED[1]
A28 AC97_SDATA_IN1
A29 AC97_SDATA_IN0
A30 GND[7]
14 CNR_TXD1
14 CNR_RSTSYNC
14 CNR_RXD2
14 CNR_RXD0
13,26,27,29 SMBCLK
14,21 AC_SYNC
14,21 AC_SDOUT
14,21 AC_BIT_CLK
14 CNR_EE_DIN
14 CNR_EE_SHCLK
R418 22
CNR_TXD2 14
CNR_TXD0 14
CNR_CLK 14
CNR_RXD1 14
CNR_EE_DOUT 14
CNR_EE_CS 14
R419 22
SMBDATA 13,26,27,29
AC_RST_L 14,21
AC_SDIN2 14,21
AC_SDIN1 14
AC_SDIN0 14
AC_RST#
VCC3
R409 10K
R410 10K(NI)
RSMVCC3
R425 4.7K
14,21 AC97_RST_L
AC_RST#
Onboard AC97 Codec Setting
JP6
Ensable 1-2 (Default)
Disable 2-3
Elitegroup Computer Systems
Title
CNR
Size Custom
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