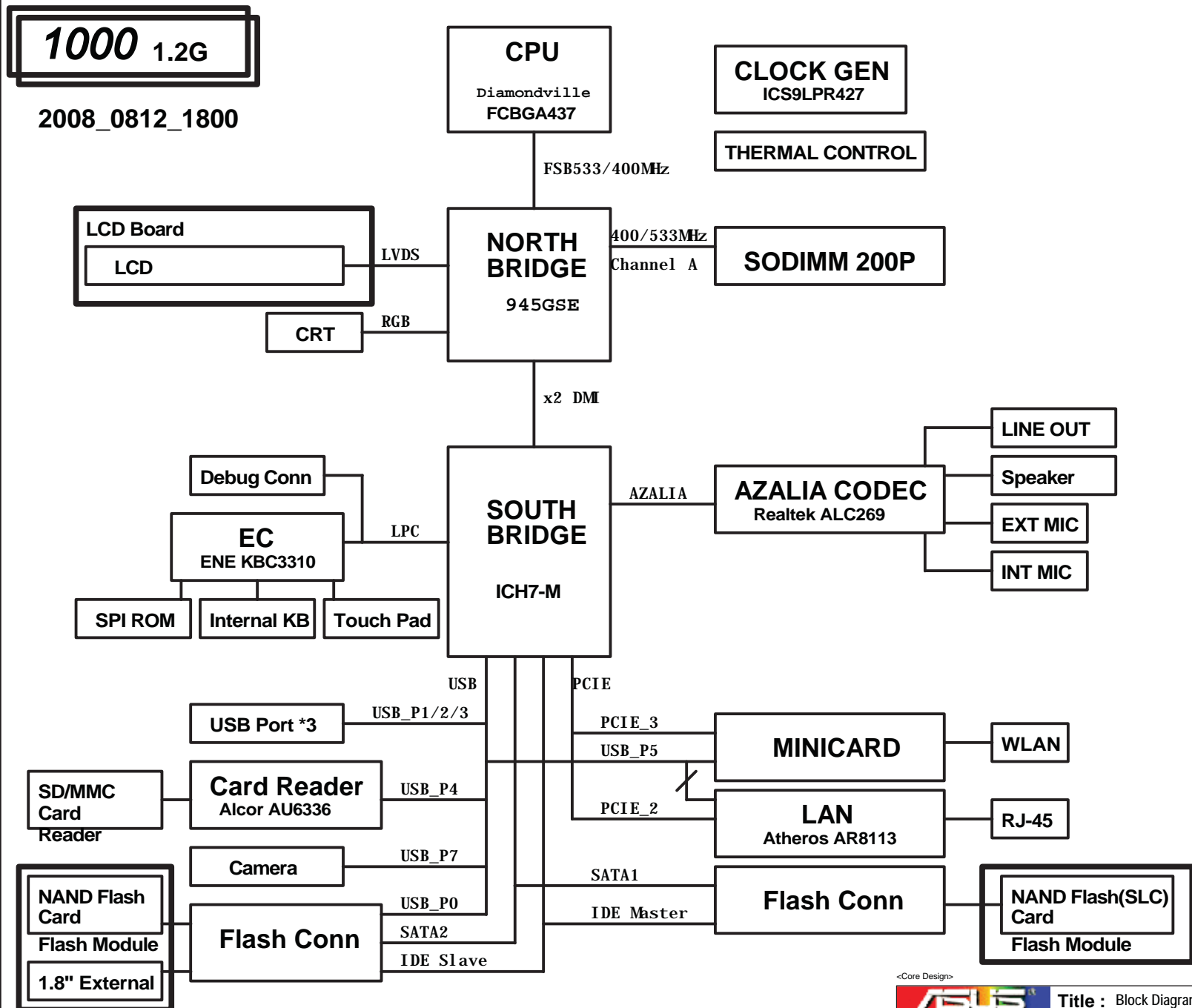


01_Block Diagram
 02_System Setting
 03_Power Sequence
 04_Clock Gen_ICS9LPR426
 05_Diamondville_BUS
 06_Diamondville_PWR
 07_NB-945GMS(HOST)
 08_NB-945GMS(DMI)
 09_NB-945GMS(GRAPHIC)
 10_NB-945GMS(DDR2)
 11_NB-945GMS(PWR)
 12_NB-945GMS(PWR2)
 13_NB-945GMS(GND)
 14_SB-ICH7M(PWR)
 15_SB-ICH7M(1)
 16_SB-ICH7M(2)
 17_SB-ICH7M(3)
 18_DDR2 SODIMM
 19_DDR2 Termination
 20_Onboard VGA
 21_LCD Conn_LID
 22_PCIEx 3.5G & Ext. Antenna
 23_Mini WIFI+ BT
 24_LAN_Atheros AR8113
 25_MDC_RJ11_RJ45
 26_HD + Flash Conn
 27_USB Port
 28_Camera Conn
 29_Card Reader_AU6336C52
 30_Codec_ALC269
 31_Audio_AMP_Jack
 32_EC_ENE KB3310
 33_EC_UART controller
 34_Switch_SPI ROM_Debug Conn
 35_Thermal Sensor_FAN
 36_KB_Touch Pad
 37_LED_THERMTRIP
 38_Discharge
 39_PWR Jack
 40_Srew Hole
 41_EMI
 42_POWER FLOW
 43_Vcore
 44_Power System
 45_Power_+1.8V & VTTDDR
 46_Power_VCCP
 47_Power_+1.5VS & +2.5VS
 48_Power_Charger
 49_EC Pin Define
 49_History



EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

USB 0	Flash Conn
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	Minicard
USB 6	NC
USB 7	Camera


PCIE

PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	NC

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	MODEM
ACZ_SDIN2	NC

<Core Design>



Title : System Setting

Engineer: **Satan_He**

ASUSTek Computer INC.

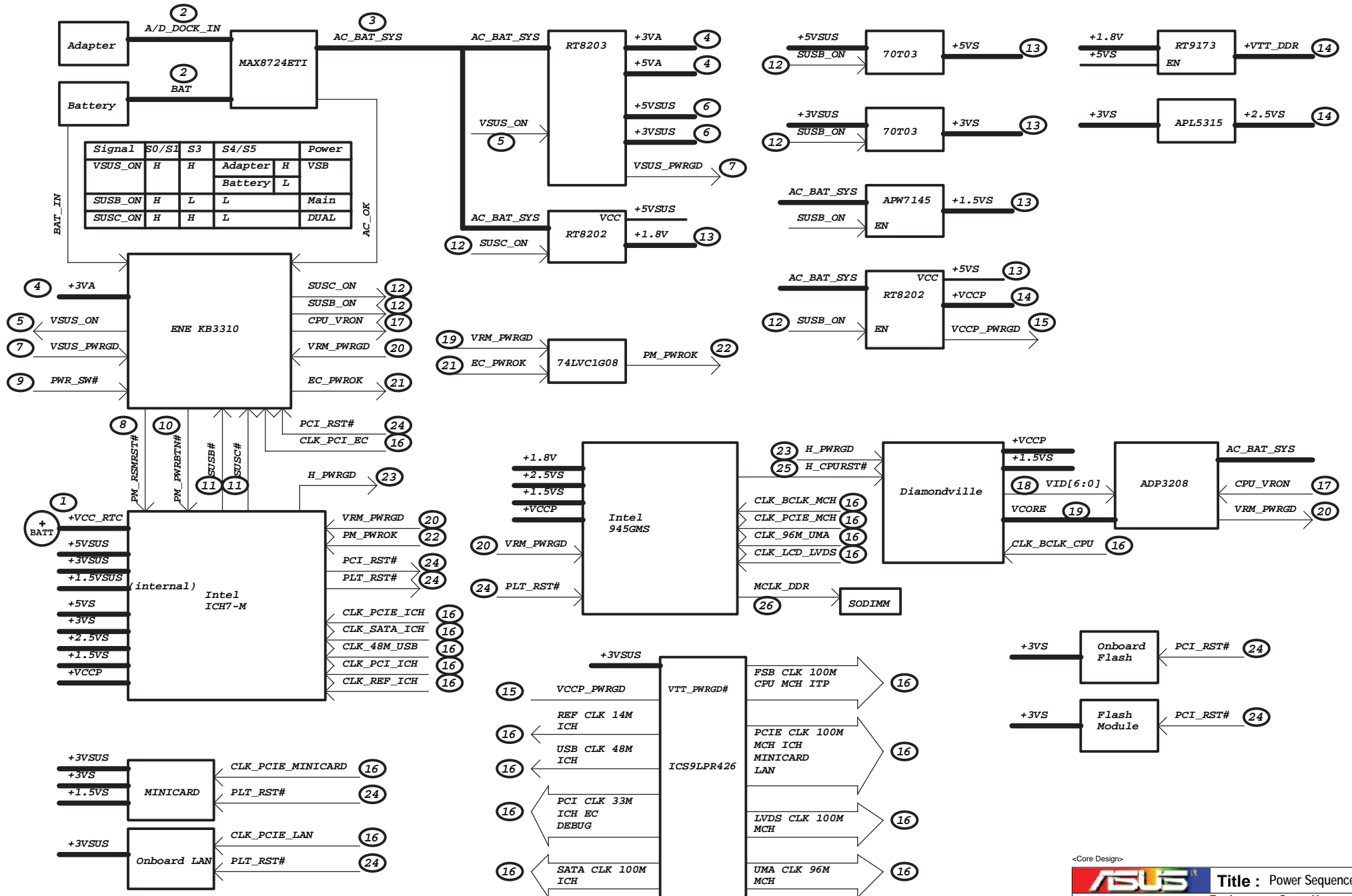
SizeA3

Project Name**1000**

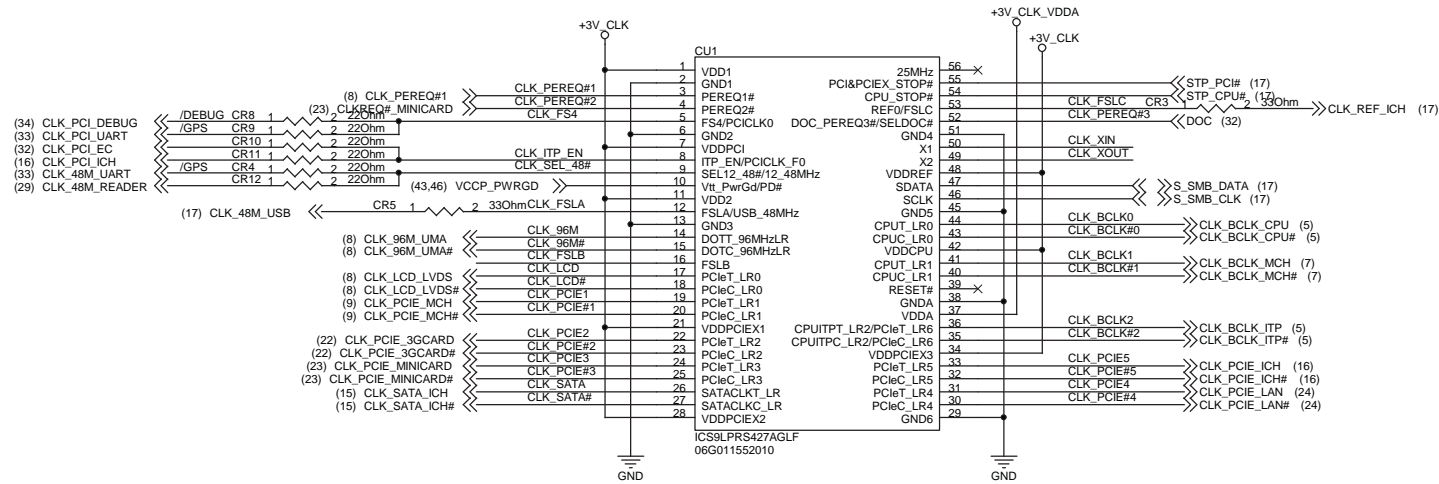
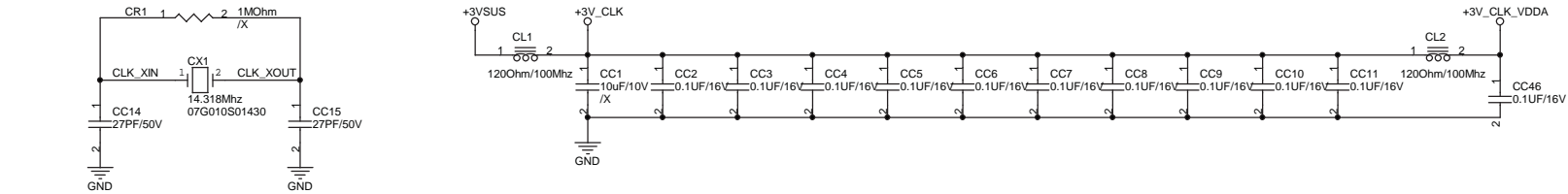
Rev1.0G

Date: Tuesday, August 12, 2008

Sheet2 of 50



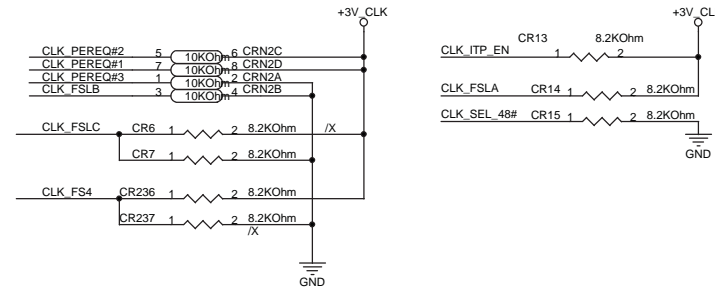
<Core Design>



1:Disable
0:Enable

PEREQ1:PCIEx0 & PCIEx1
PEREQ2:PCIEx2 & PCIEx3 & SATA
PEREQ3:PCIEx4 & PCIEx5 & PCIEx6

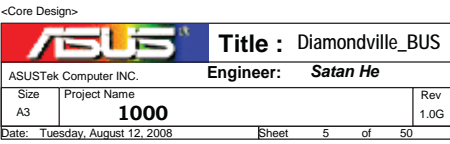
FSC	FSB	FSA	CPU	PCIE	SATA
0	0	1	133	100	100
1	0	1	100	100	100



S_SMB_DATA	CC12	2	1	10PF/50V
S_SMB_CLK	CC13	2	1	10PF/50V

CLK_PCI_I2C	CC36	2	1	10PF/50V
CLK_PCI_EC	CC37	2	1	10PF/50V
CLK_PCI_DEBUG	CC38	2	1	10PF/50V
CLK_REF_I2C	CC39	2	1	10PF/50V
CLK_48M_USB	CC40	2	1	10PF/50V
CLK_48M_UART	CC41	2	1	10PF/50V
CLK_PCI_UART	CC42	2	1	10PF/50V
CLK_48M_READER	CC45	2	1	10PF/50V

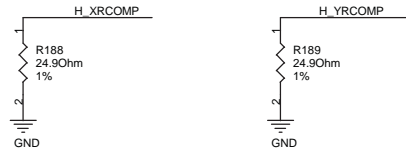
<Core Design>



Power:
+VCCP

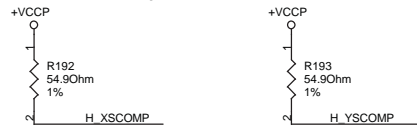
RCOMP

For Calibrating the FSB I/O Buffer



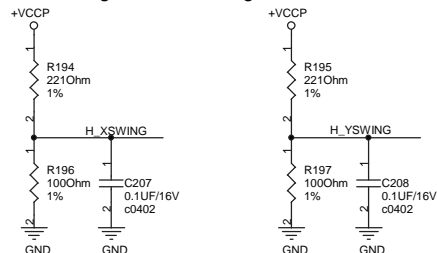
SCOMP

For Slew Rate Compensation on the FSB

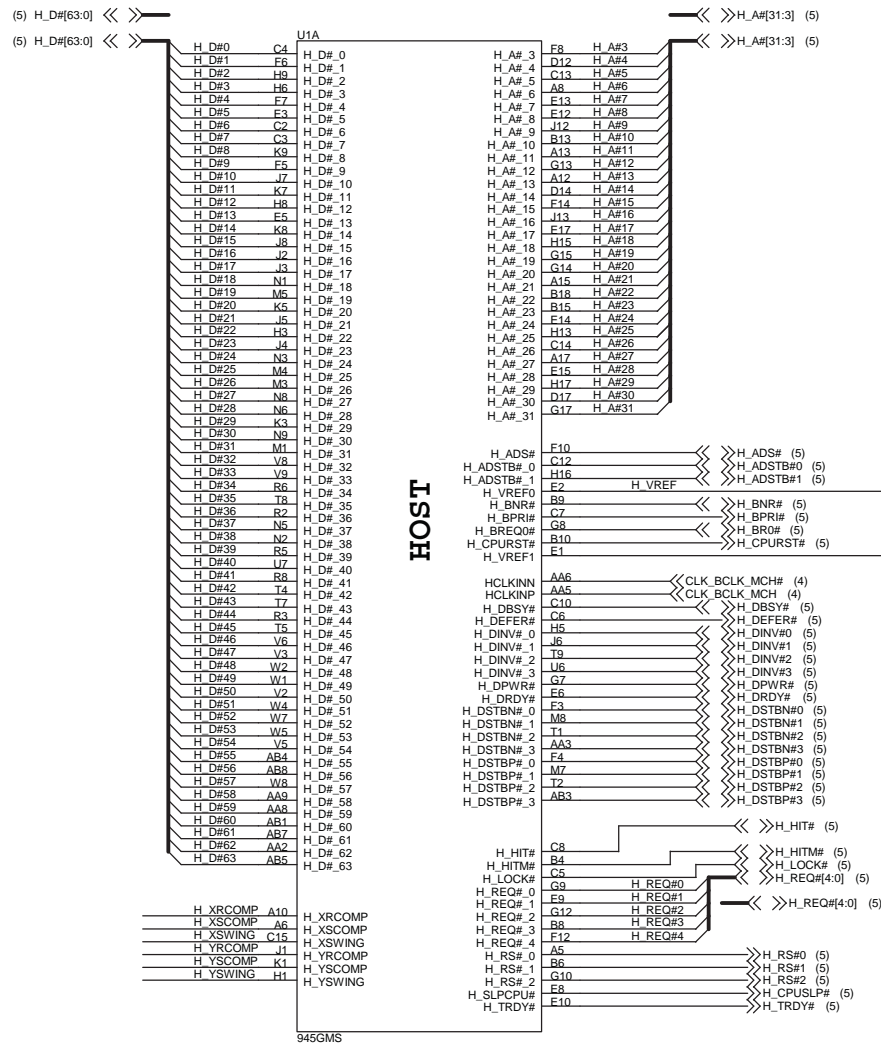


Voltage Swing

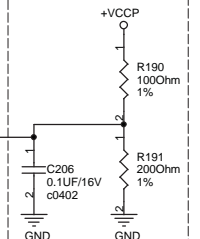
For Providing a Reference Voltage to The FSB RCOMP circuits



Signal voltage level =
0.3125*VCCP
Trace should be 10 mil wide
with 20 mil spacing



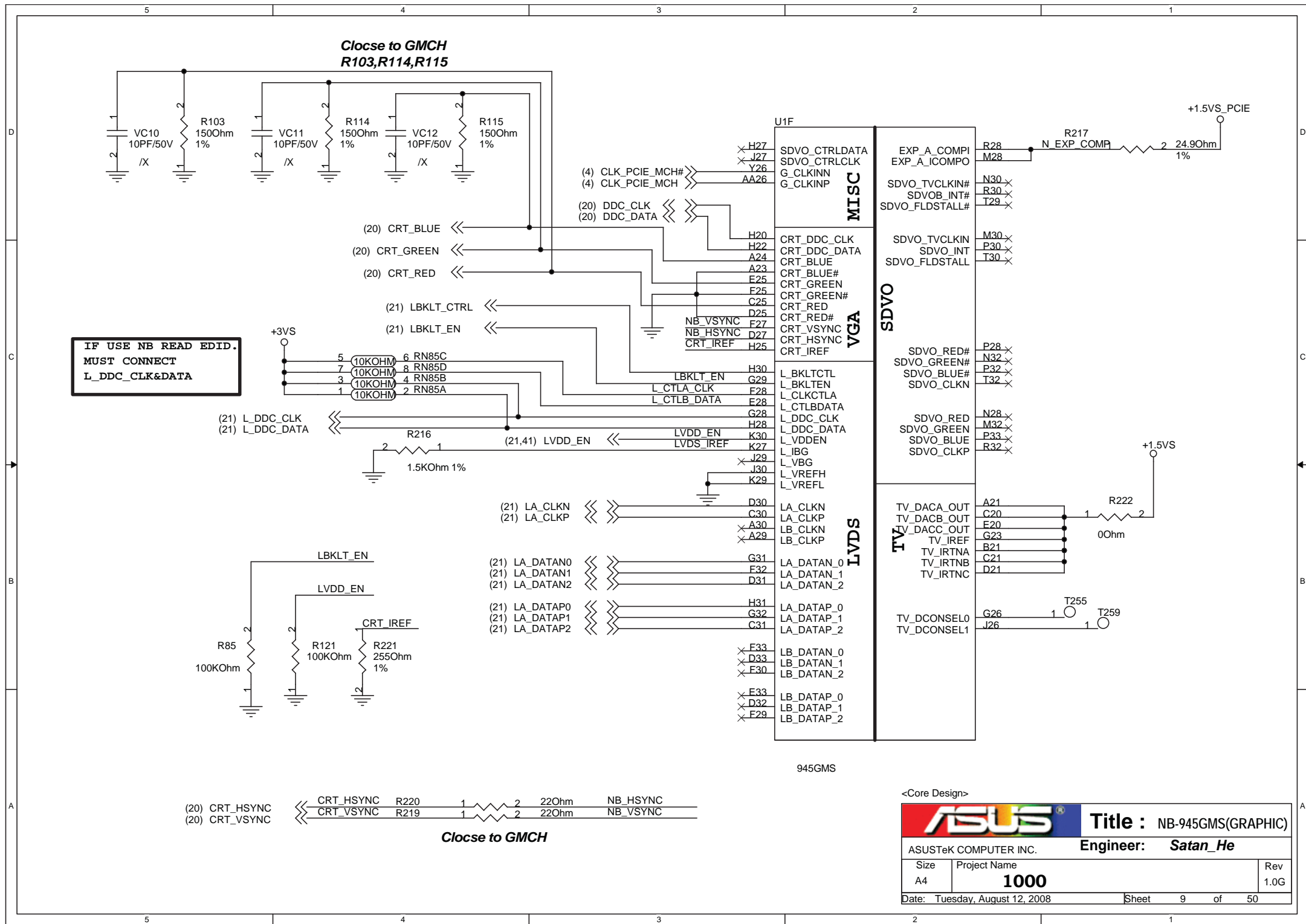
AGTL+ I/O Voltage Reference




Layout Note:
0.1uF should be placed 100mils or less from GMCH pin.

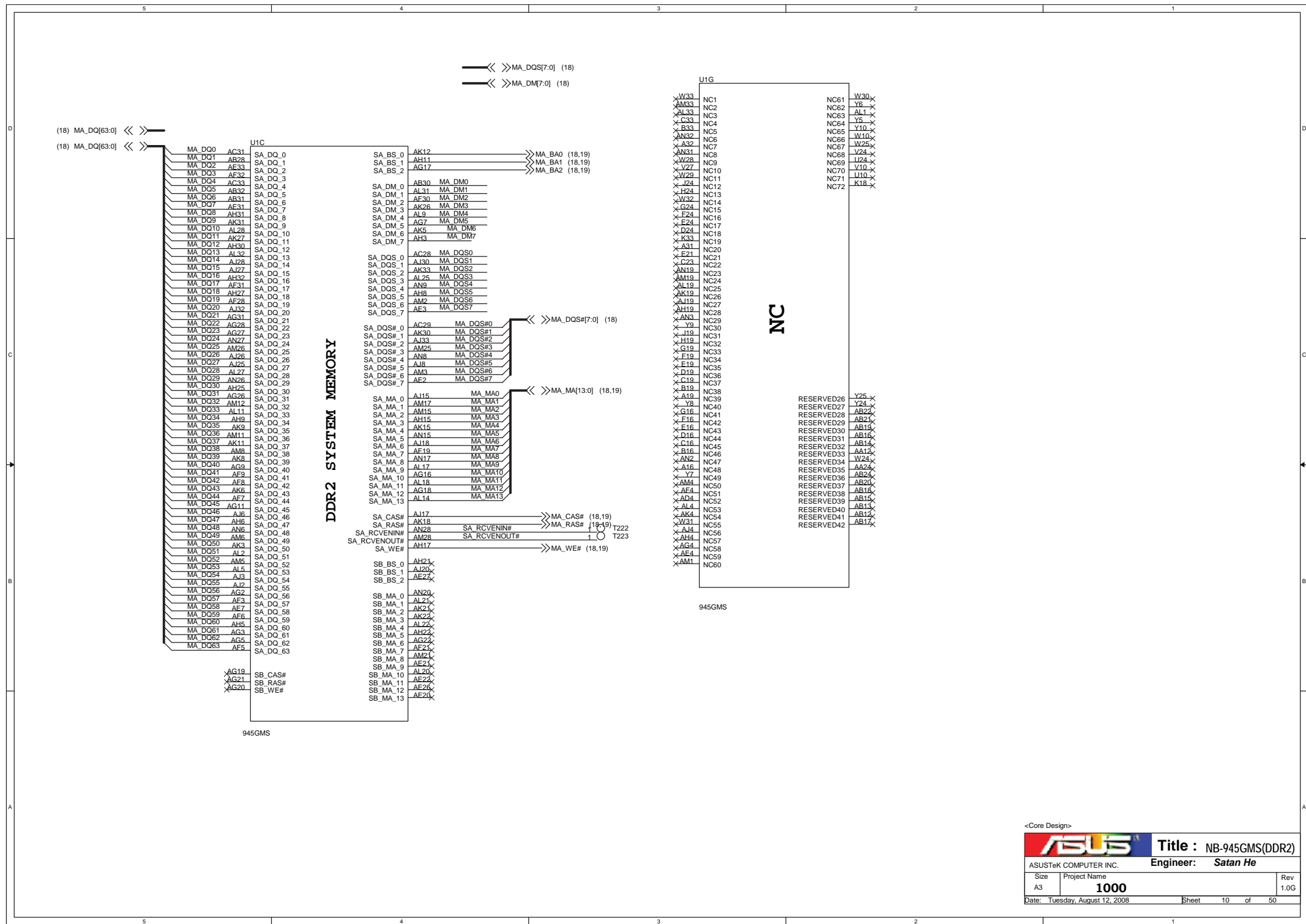
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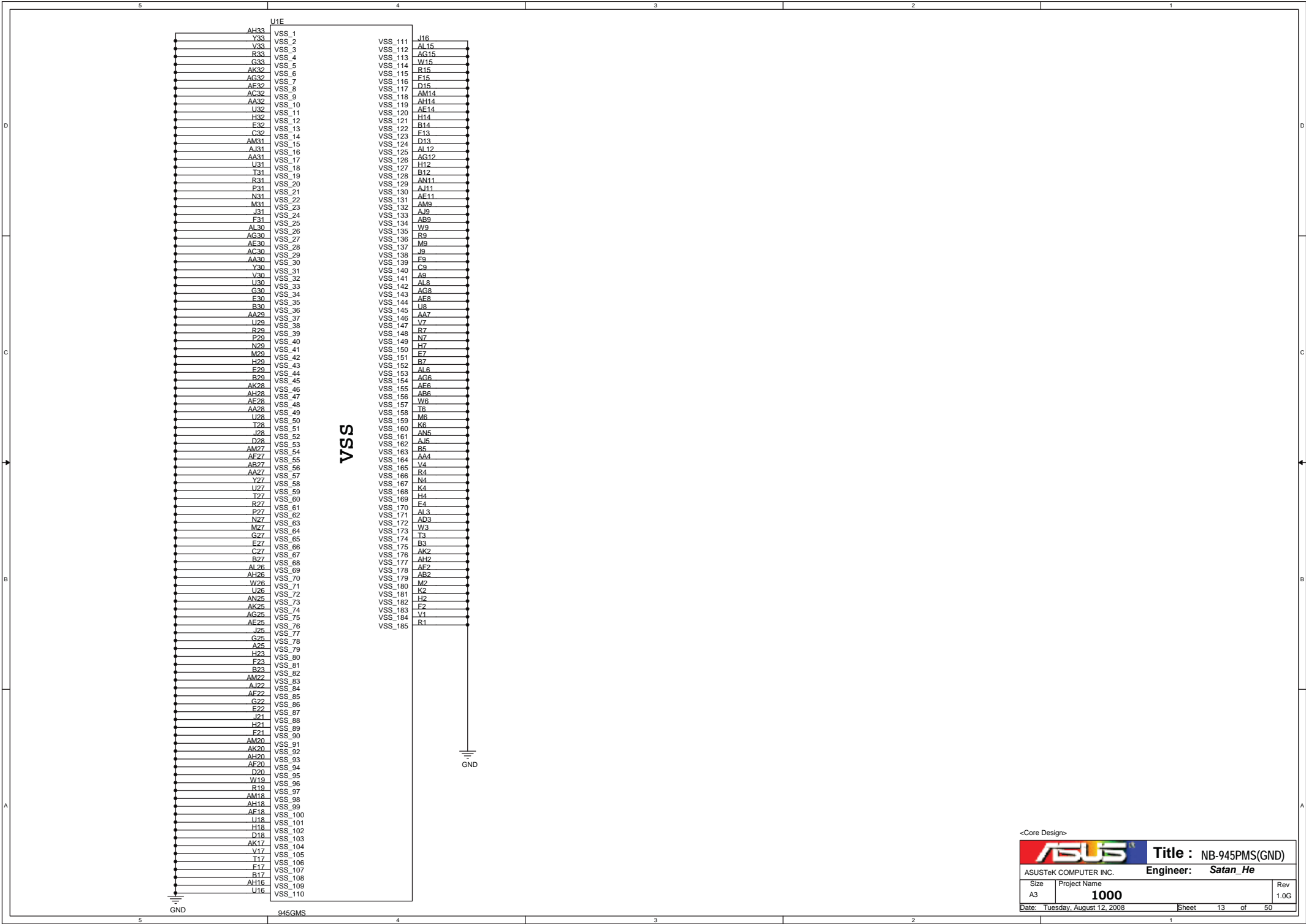
ASUS		Title : NB-945GMS(HOST)	
ASUSTek COMPUTER INC.		Engineer: Satan He	
Size A3	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008		Sheet 7 of 50	




<Core Design>

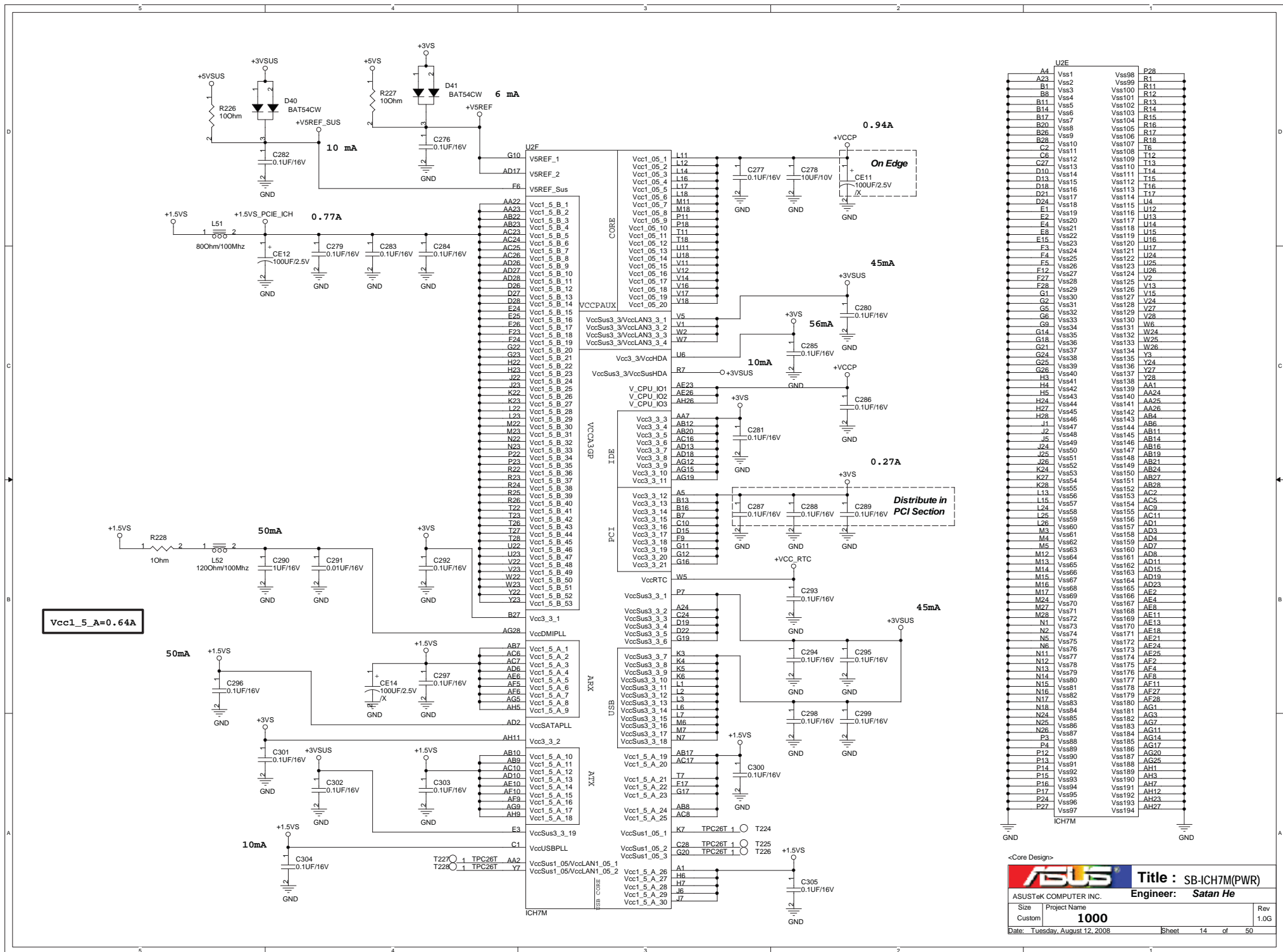
		Title : NB-945GMS(GRAPHIC)	
ASUSTeK COMPUTER INC.		Engineer: <i>Satan_He</i>	
Size A4	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008		Sheet	9 of 50

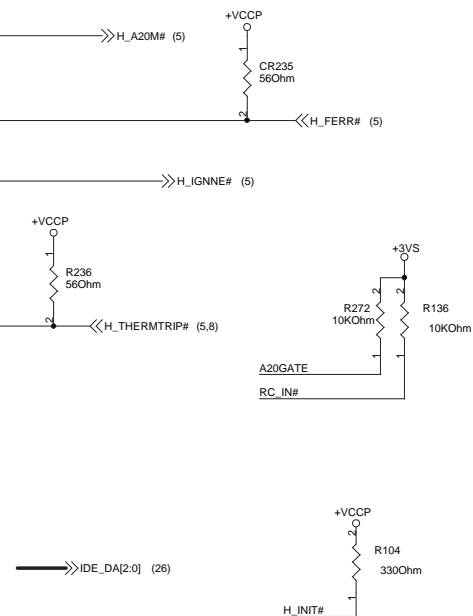
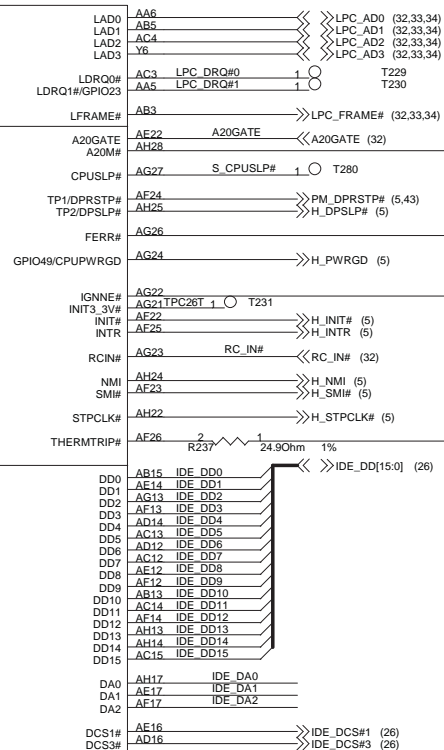
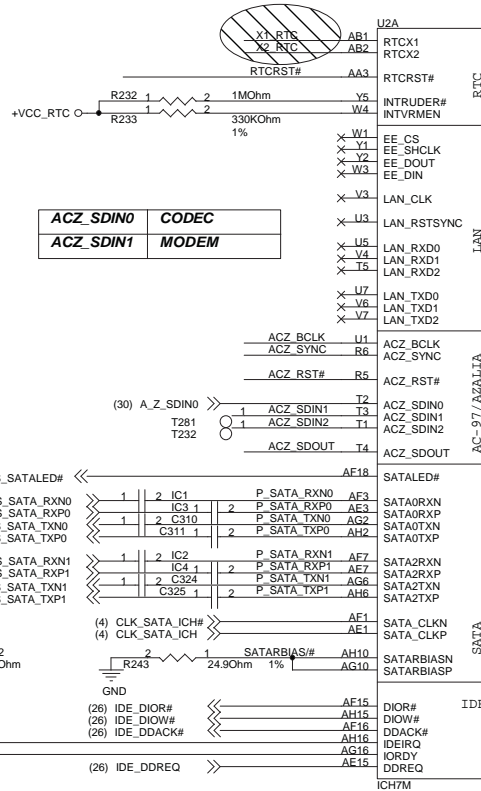
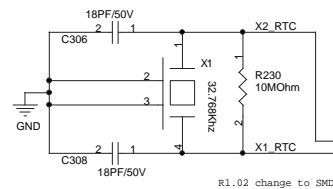


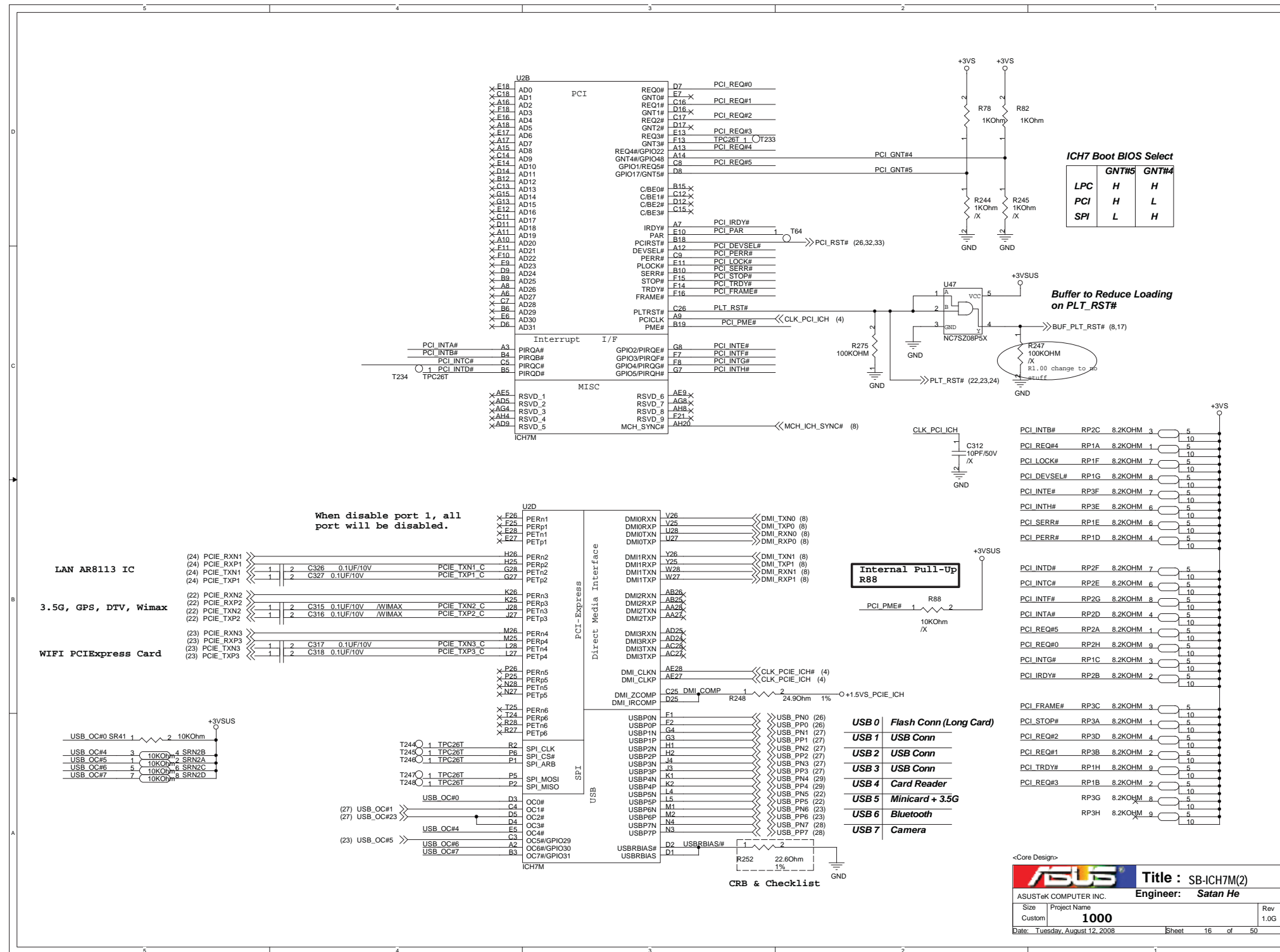


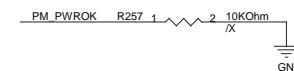
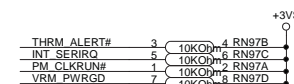
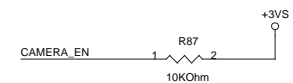
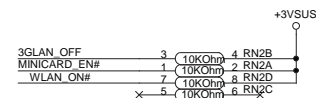
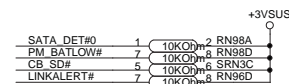
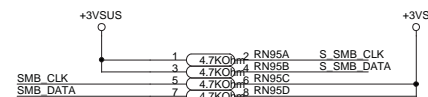
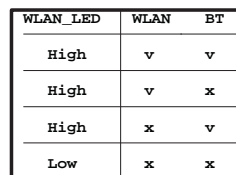
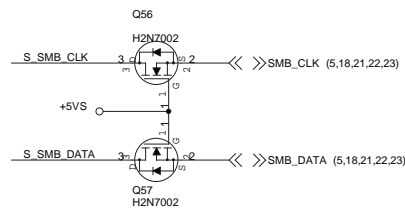
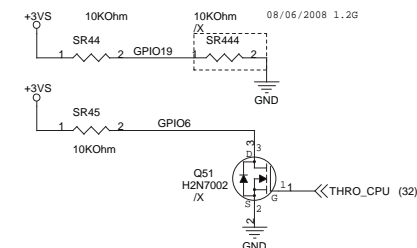
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		Title : NB-945PMS(GND)	
ASUSTeK COMPUTER INC.		Engineer: Satan_He	
Size	Project Name	Rev	
A3	1000	1.0G	
Date: Tuesday, August 12, 2008	Sheet	13	of 50



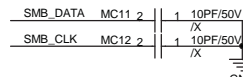
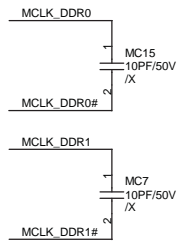






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		Title : SB-1CH7M(3)	
ASUSTeK COMPUTER INC		Engineer: <i>Satan He</i>	
Size Custom	Project Name 1000		Rev 1.0G
Date: Tuesday, August 12, 2008		Sheet	17 of 50

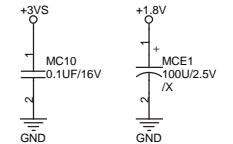
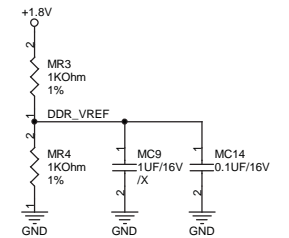
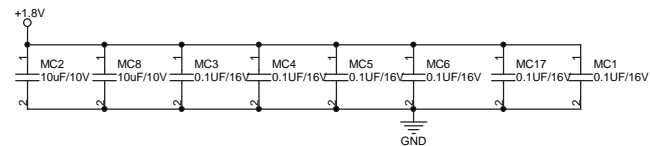


<< >> MA_DQ[63:0] (10)
 << >> MA_DQS[7:0] (10)
 << >> MA_DQS#[7:0] (10)
 << >> MA_DM[7:0] (10)
 << >> MA_MA[13:0] (10,19)
 << >> MA_BA[2:0] (10,19)

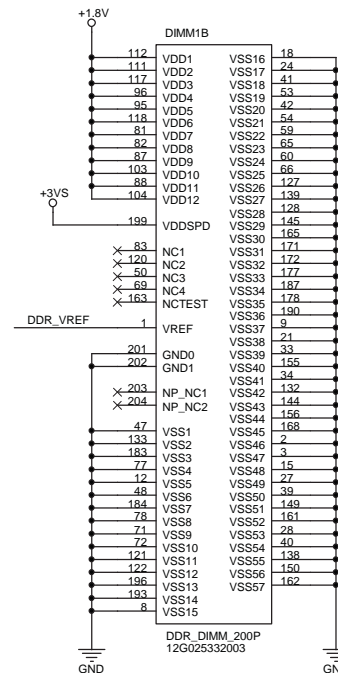
STD Type

DIMM1A									
MA_MA0	102	A0	5	MA_DQ0					
MA_MA1	101	A1	7	MA_DQ1					
MA_MA2	100	A2	17	MA_DQ2					
MA_MA3	99	A3	19	MA_DQ3					
MA_MA4	98	A4	4	MA_DQ4					
MA_MA5	97	A5	6	MA_DQ5					
MA_MA6	94	A6	14	MA_DQ6					
MA_MA7	92	A7	16	MA_DQ7					
MA_MA8	91	A8	23	MA_DQ21					
MA_MA9	90	A9	25	MA_DQ16					
MA_MA10	105	A10/AP	35	MA_DQ18					
MA_MA11	90	A11	37	MA_DQ19					
MA_MA12	89	A12	20	MA_DQ17					
MA_MA13	116	A13	22	MA_DQ20					
	86	A14	36	MA_DQ22					
	84	A15	38	MA_DQ23					
MA_BA2	85	A16_BA2	43	MA_DQ9					
MA_BA0	107	BA0	45	MA_DQ12					
MA_BA1	106	BA1	55	MA_DQ11					
	110	SO#	44	MA_DQ13					
(8,19) MA_CS#0	115	S1#	46	MA_DQ8					
(8) MCLK_DDR0	30	CK0	56	MA_DQ14					
(8) MCLK_DDR0#	32	CK0#	58	MA_DQ15					
(8) MCLK_DDR1	166	CK1	61	MA_DQ24					
(8) MCLK_DDR1#	79	CK1#	63	MA_DQ25					
(8,19) MA_CKE0	80	CKE0	73	MA_DQ26					
(8,19) MA_CKE1	113	CKE1	75	MA_DQ27					
(10,19) MA_CAS#	108	CAS#	62	MA_DQ28					
(10,19) MA_RAS#	109	RAS#	64	MA_DQ29					
(10,19) MA_WE#	198	WE#	74	MA_DQ30					
	200	SA0	76	MA_DQ31					
(5,17,21,22,23) SMB_CLK	197	SA1	123	MA_DQ32					
(5,17,21,22,23) SMB_DATA	195	DQ33	125	MA_DQ33					
		DQ34	135	MA_DQ34					
(8,19) MA_ODT0	114	DQ35	137	MA_DQ35					
(8,19) MA_ODT1	119	DQ36	124	MA_DQ36					
		DQ37	126	MA_DQ37					
		DQ38	134	MA_DQ38					
		DQ39	136	MA_DQ39					
MA_DM0	10	DQ40	141	MA_DQ40					
MA_DM2	26	DQ41	143	MA_DQ41					
MA_DM1	52	DQ42	151	MA_DQ42					
MA_DM3	67	DQ43	153	MA_DQ43					
MA_DM4	130	DQ44	140	MA_DQ44					
MA_DM5	147	DQ45	142	MA_DQ45					
MA_DM6	170	DQ46	152	MA_DQ46					
MA_DM7	185	DQ47	154	MA_DQ47					
		DQ48	157	MA_DQ48					
MA_DQS0	13	DQ49	159	MA_DQ49					
MA_DQS2	31	DQ50	173	MA_DQ50					
MA_DQS1	51	DQ51	175	MA_DQ51					
MA_DQS3	70	DQ52	158	MA_DQ52					
MA_DQS4	131	DQ53	160	MA_DQ53					
MA_DQS5	148	DQ54	174	MA_DQ54					
MA_DQS6	169	DQ55	176	MA_DQ55					
MA_DQS7	188	DQ56	179	MA_DQ56					
MA_DQS#0	11	DQ57	181	MA_DQ57					
MA_DQS#2	29	DQ58	189	MA_DQ58					
MA_DQS#1	49	DQ59	191	MA_DQ59					
MA_DQS#3	68	DQ60	180	MA_DQ60					
MA_DQS#4	129	DQ61	182	MA_DQ61					
MA_DQS#5	146	DQ62	192	MA_DQ62					
MA_DQS#6	167	DQ63	194	MA_DQ63					
MA_DQS#7	186								

DDR_DIMM_200P
12G025332003

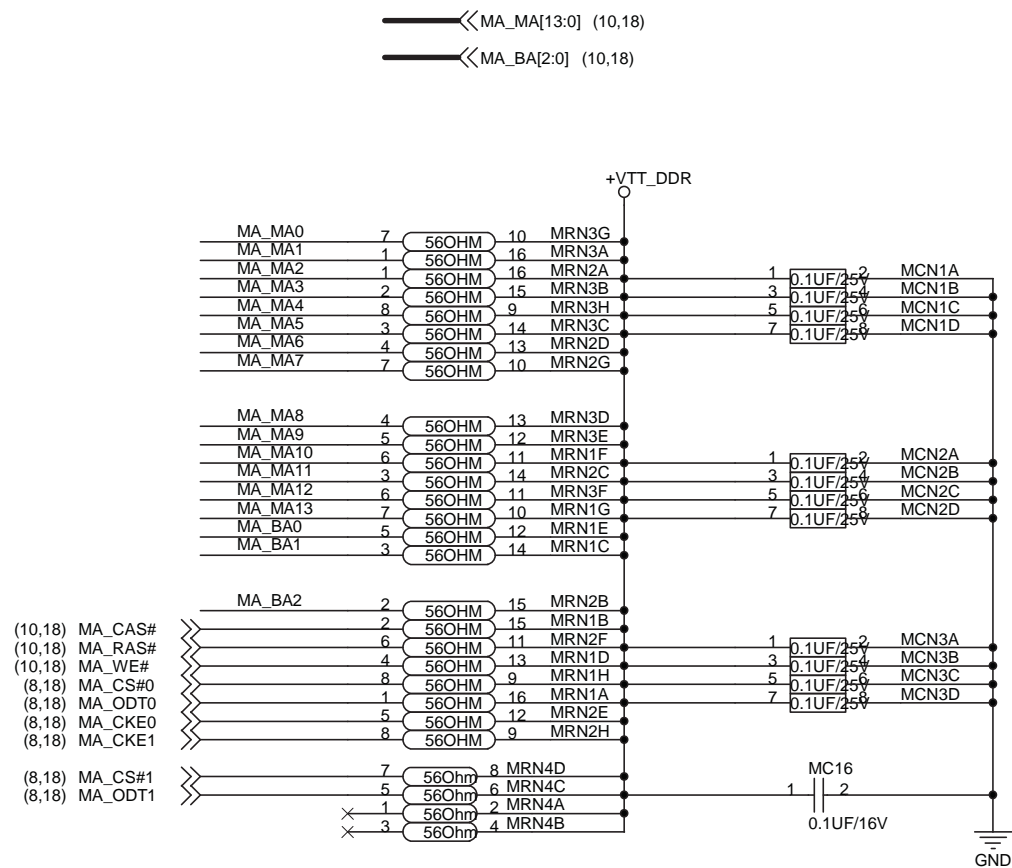


GROUP1
GROUP2
SWAP




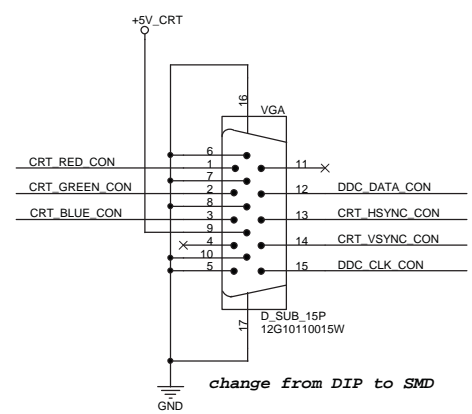
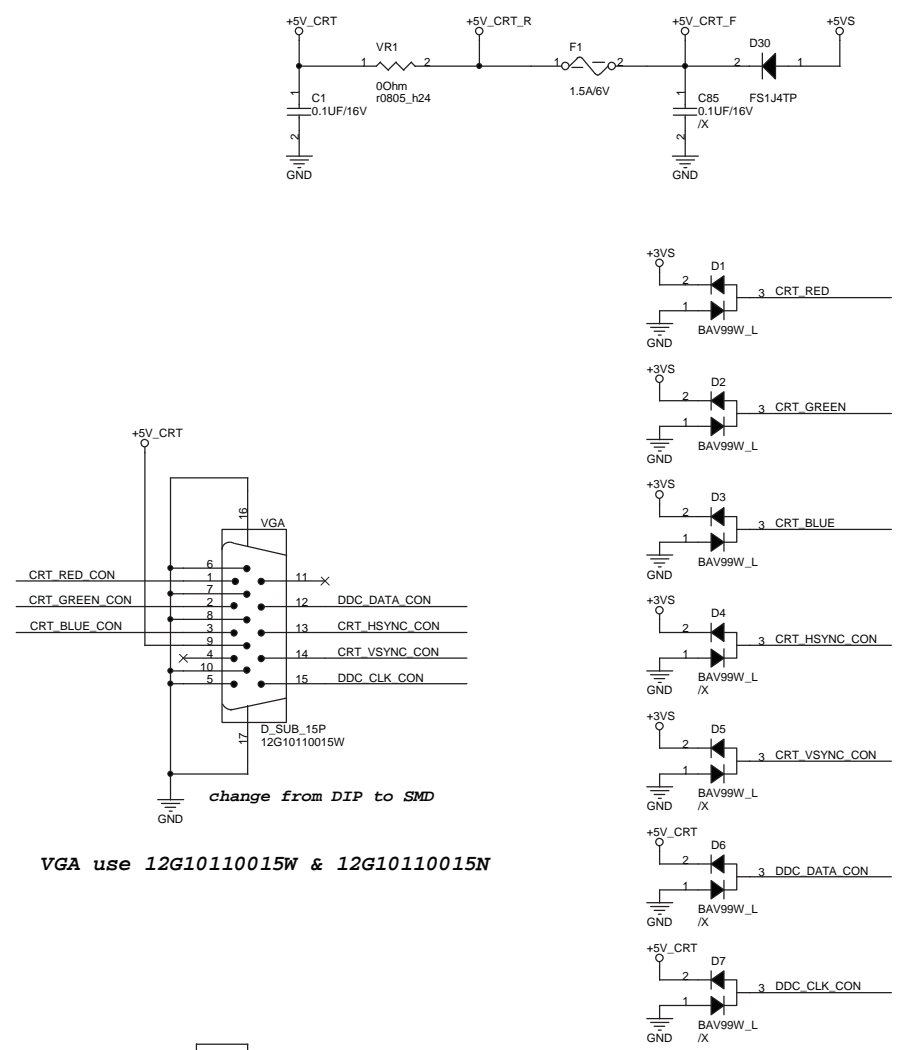
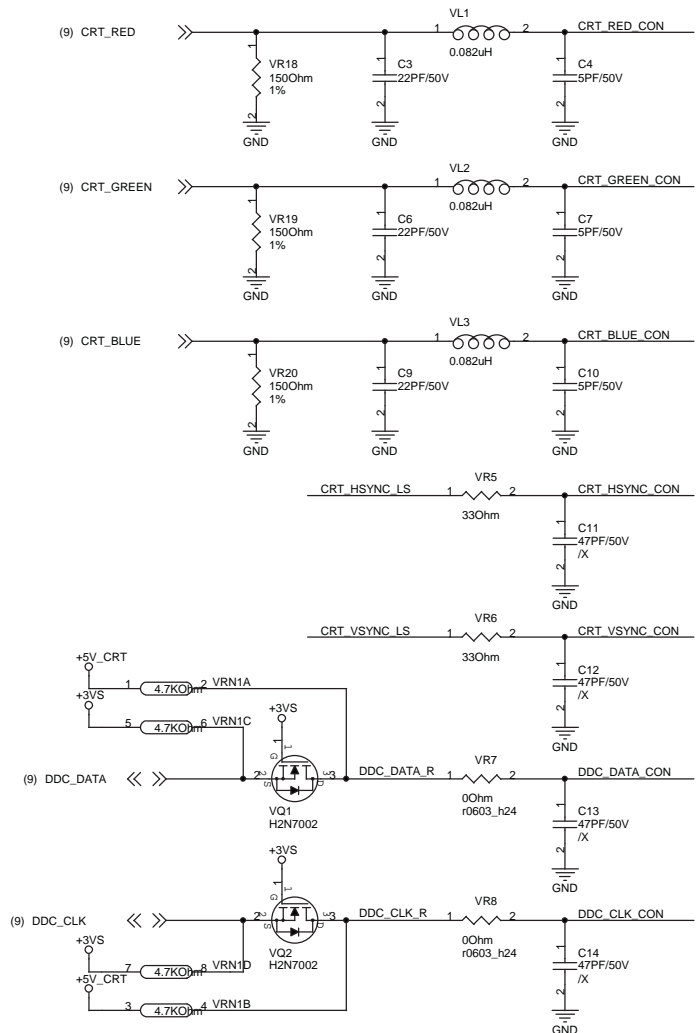
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ASUS		Title : DDR2 SODIMM	
ASUSTek Computer INC.		Engineer: Kell Huang	
Size A3	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008	Sheet	18	of 47

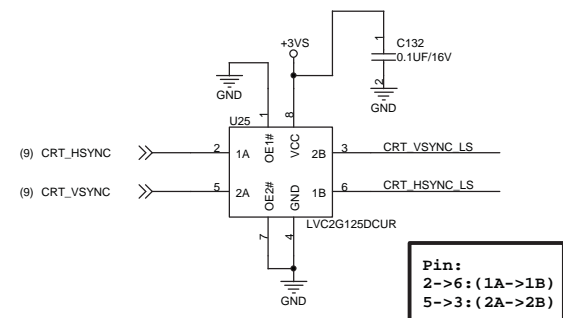


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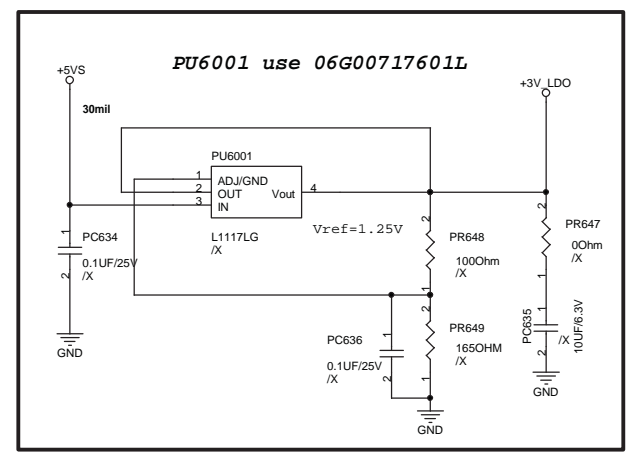
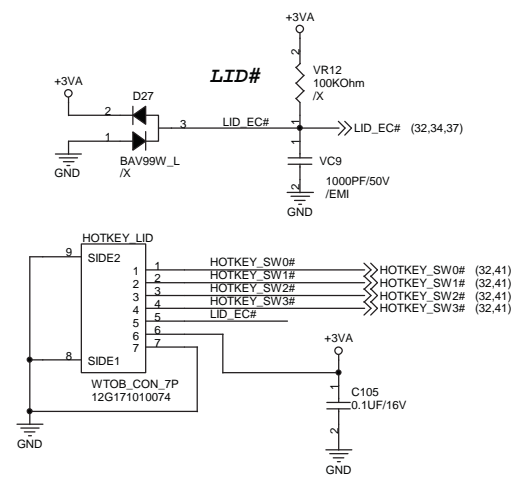
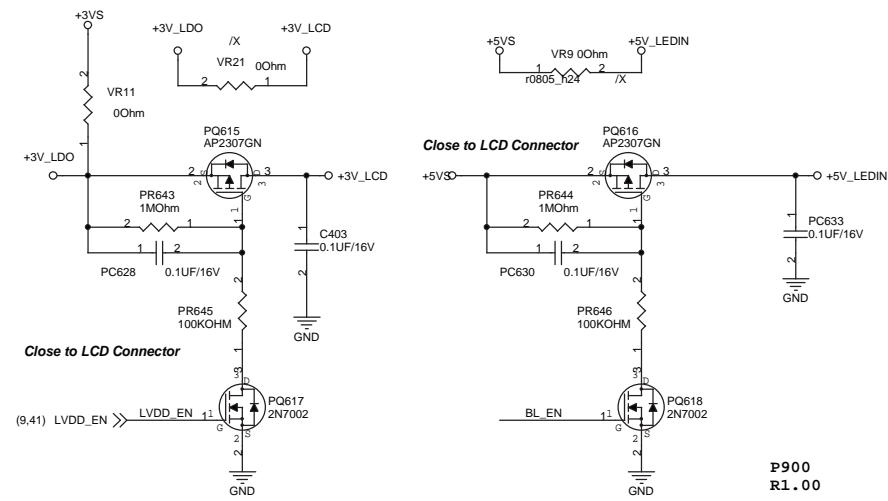
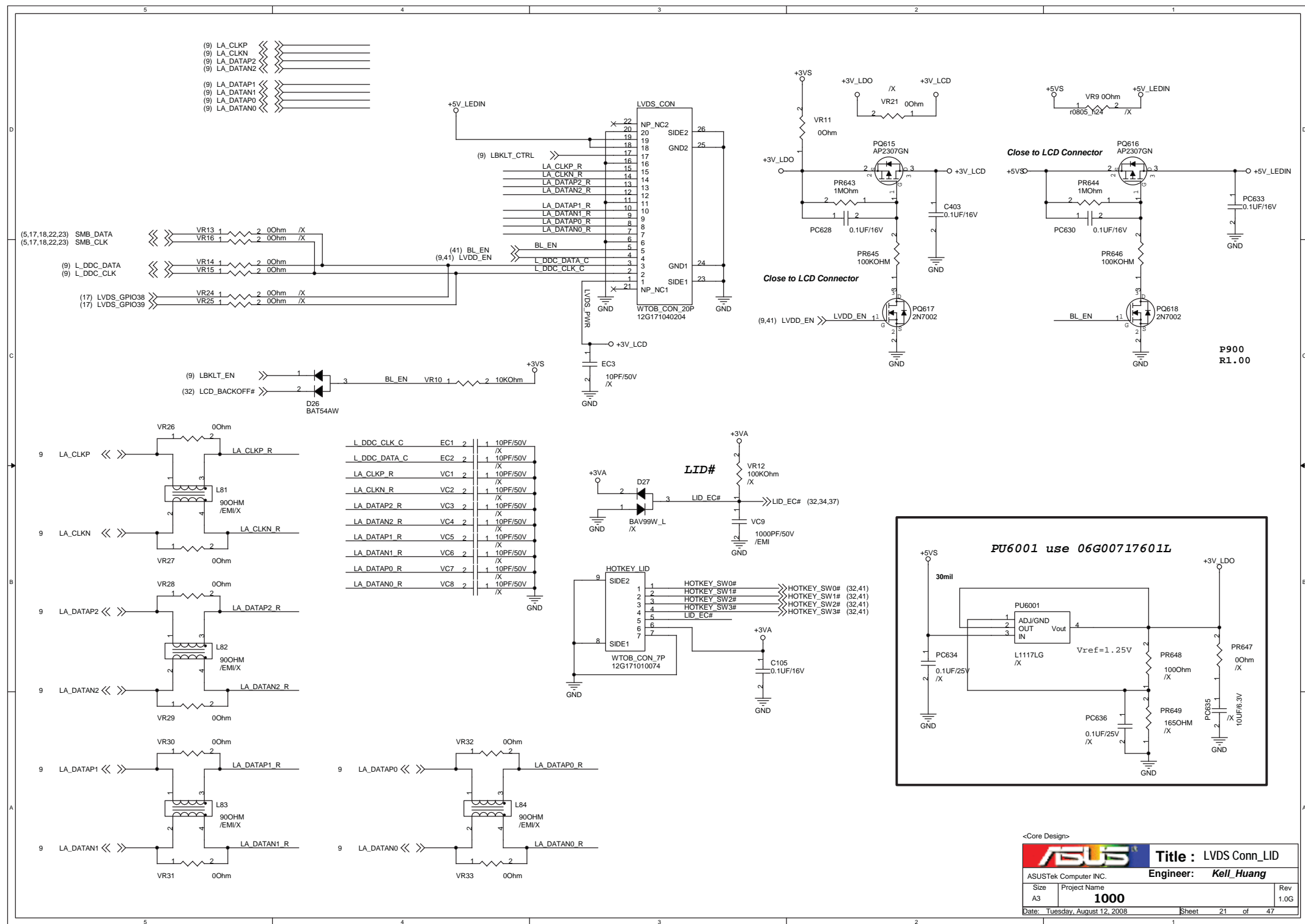
		Title : DDR2_Termination	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A4	Project Name 1000		Rev 1.0G
Date: Tuesday, August 12, 2008		Sheet 19 of 47	



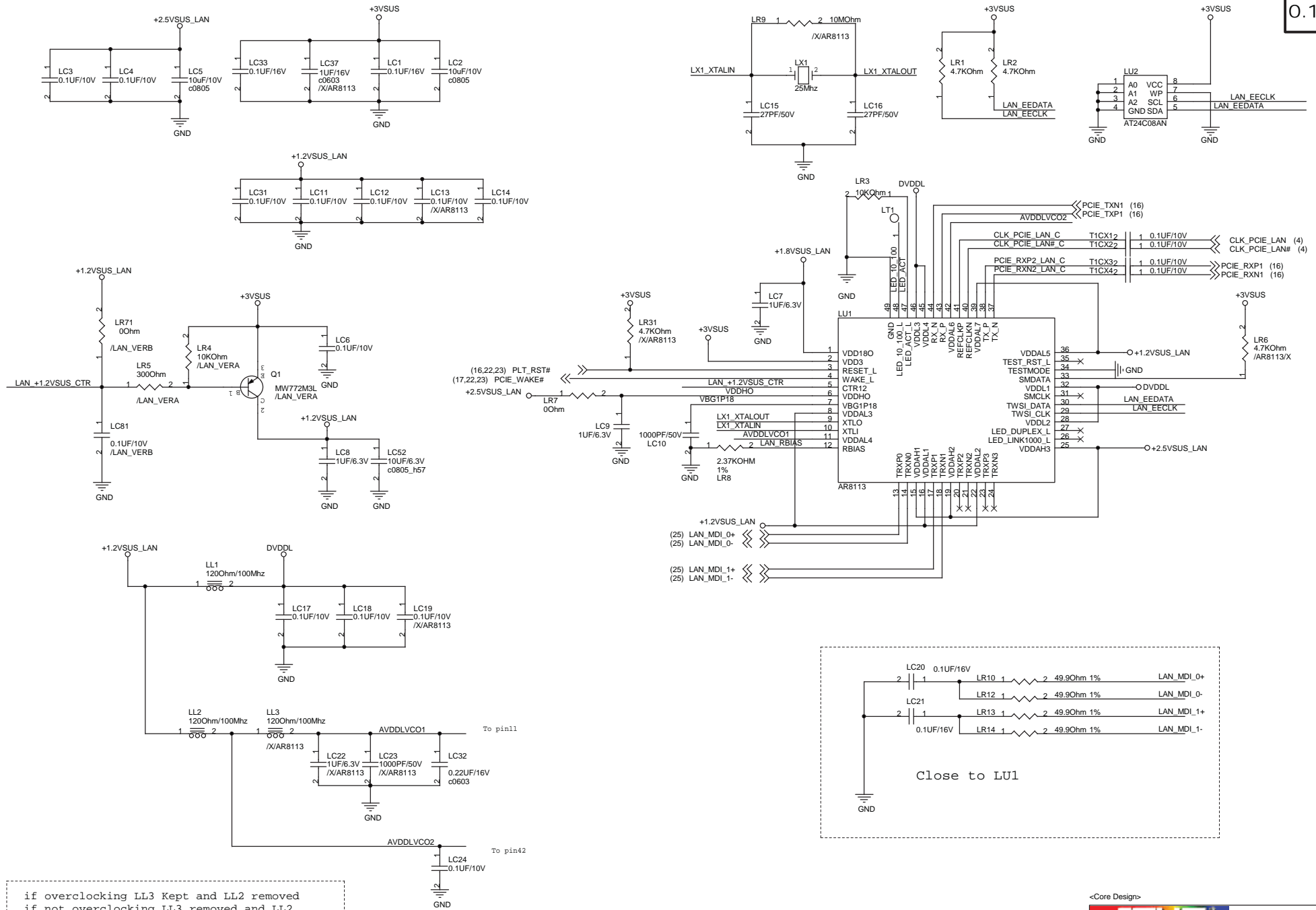
VGA use 12G10110015W & 12G10110015N

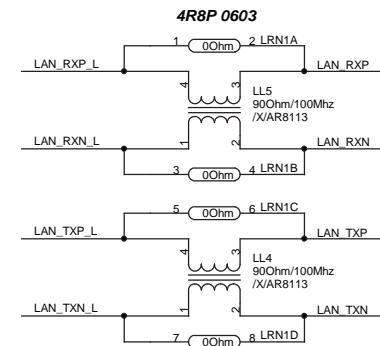
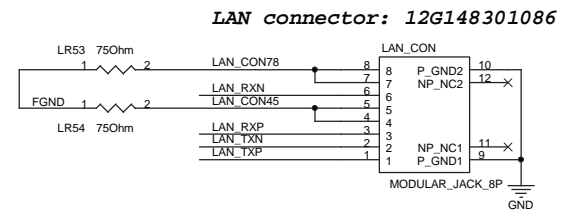
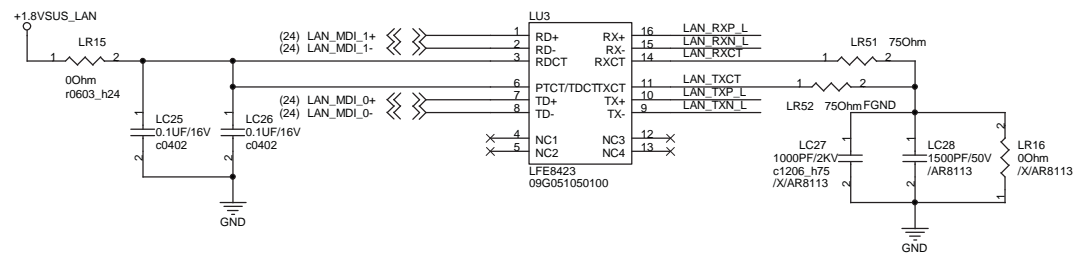


Pin:
2->6: (1A->1B)
5->3: (2A->2B)

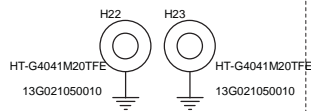


0.1A Beta





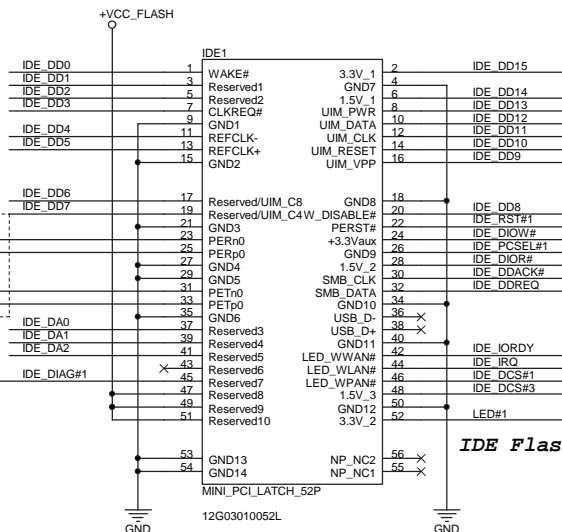
短卡

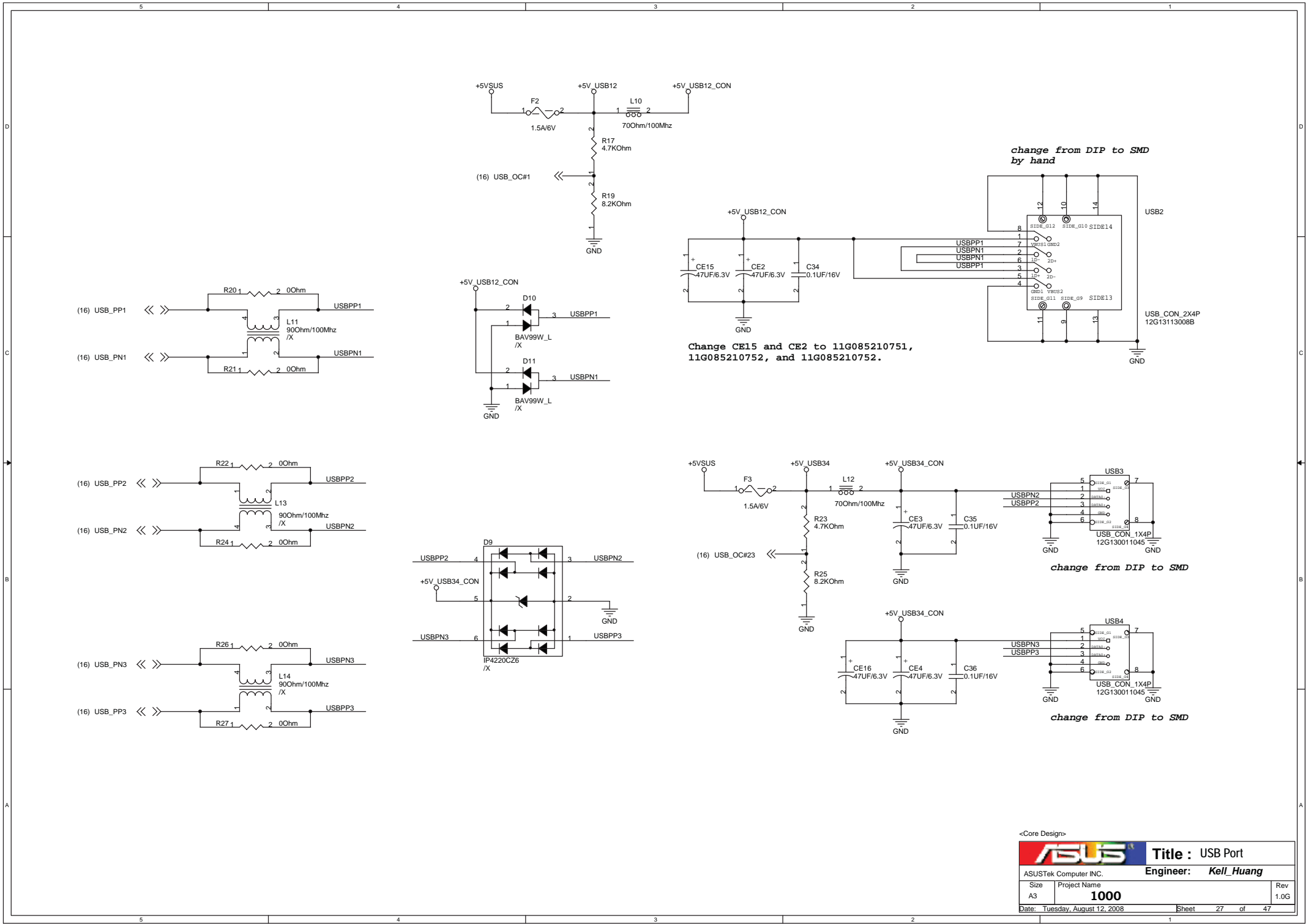


08/07/2008 1.2G

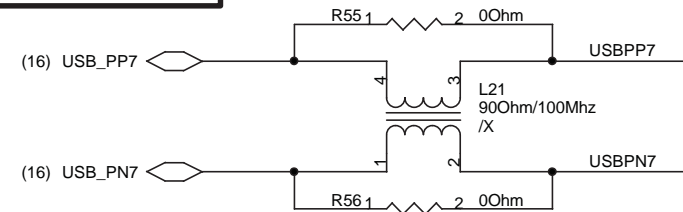
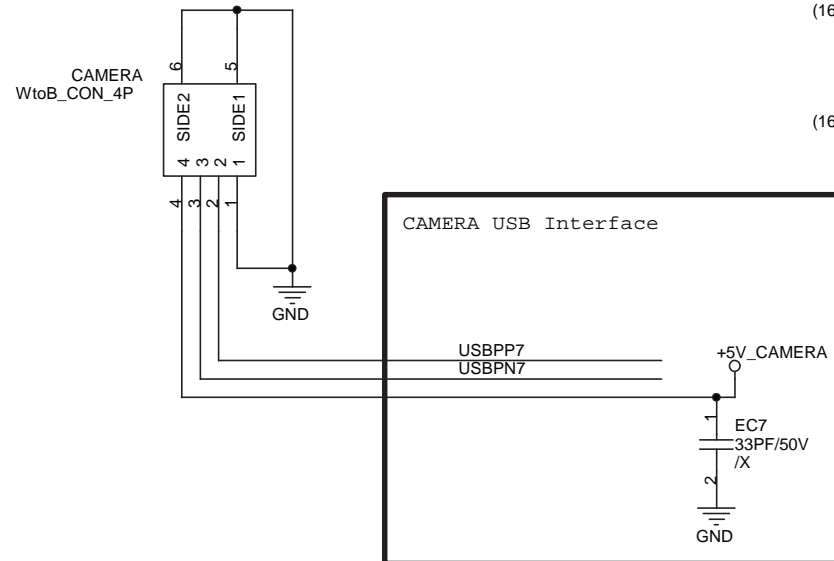
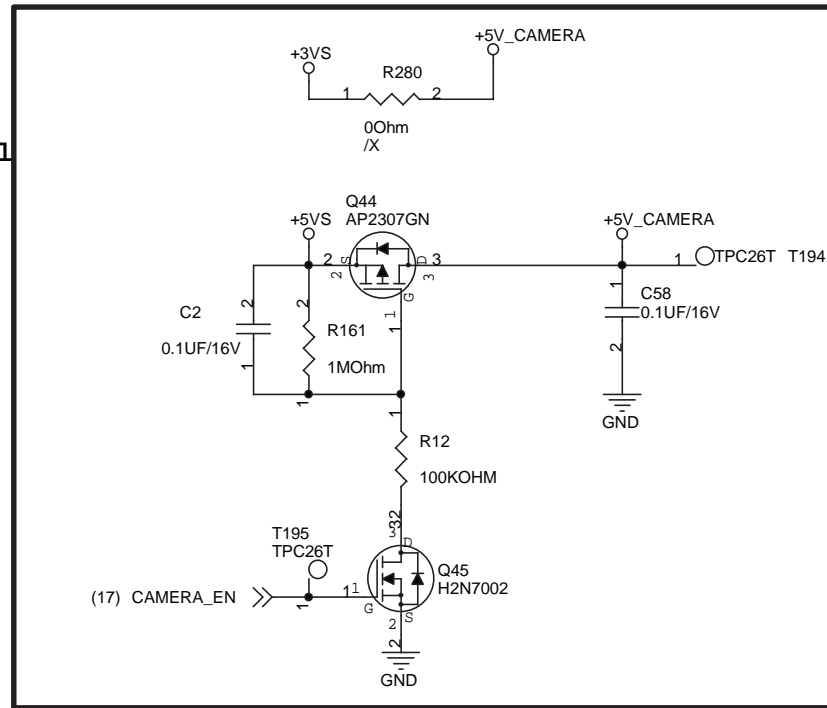
(15) S_SATA_RXN1
(15) S_SATA_RXP1(15) S_SATA_TXN1
(15) S_SATA_TXP1

IDE_DIAG#0 IR20 1 00hm 2 00hm



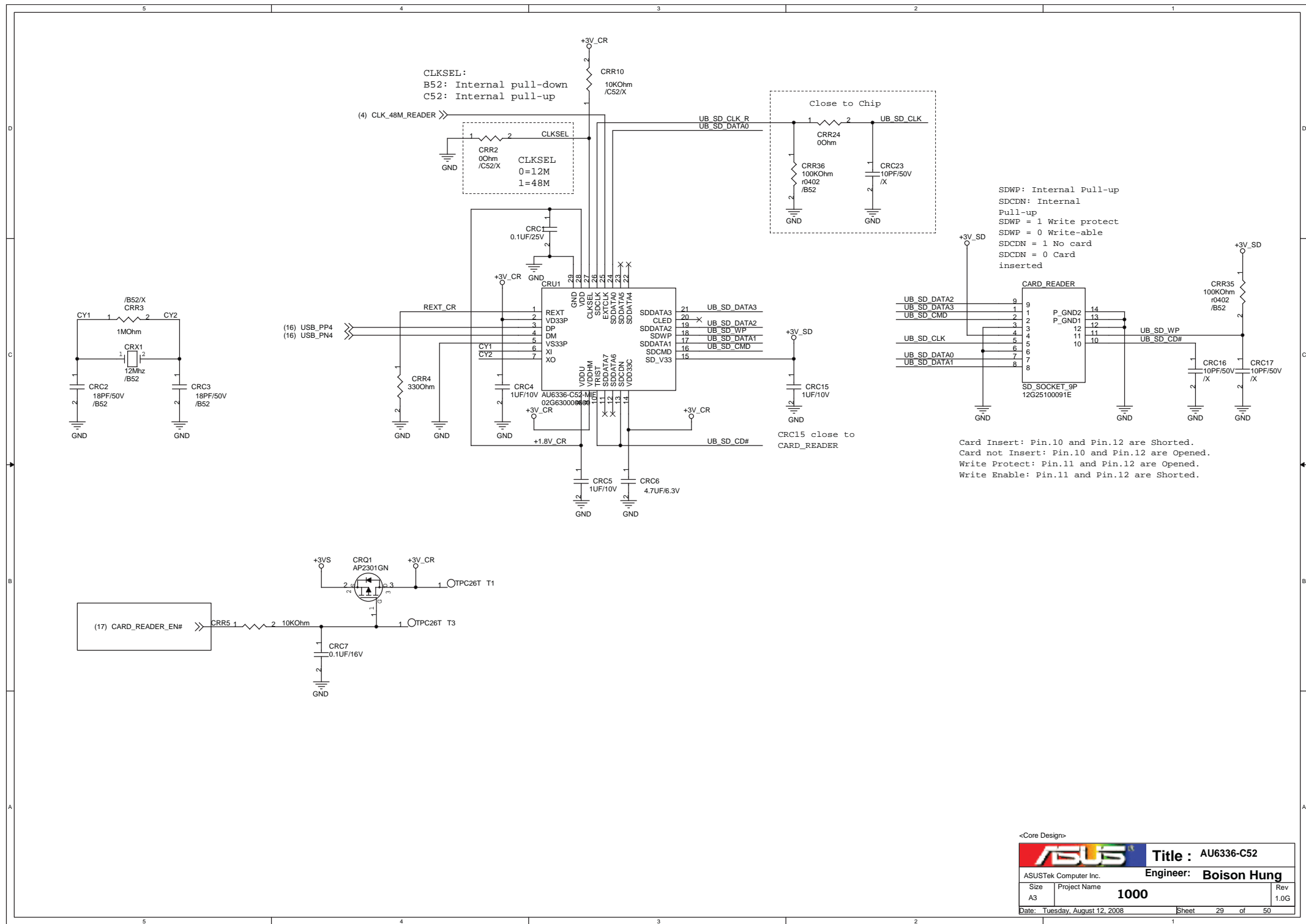


Power Control



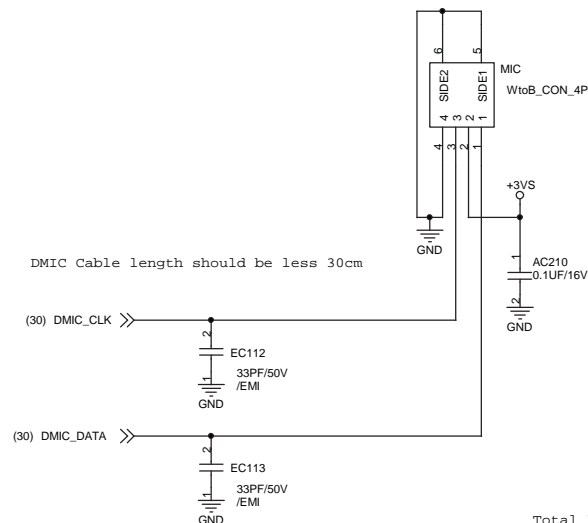
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ASUS		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A4	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008		Sheet 28 of 47	

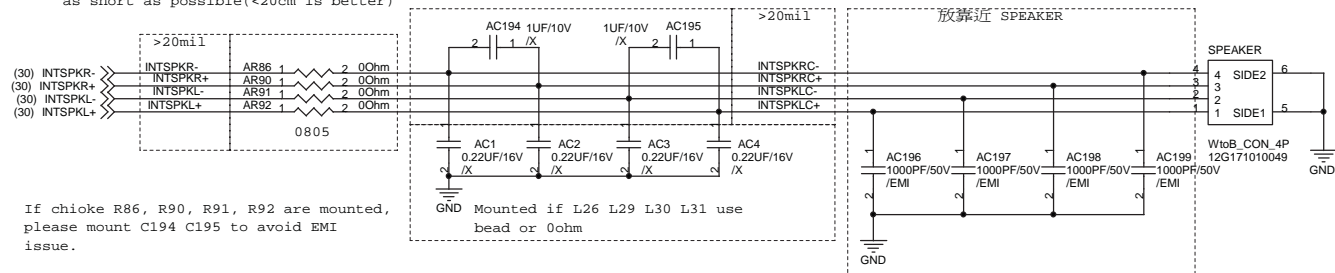


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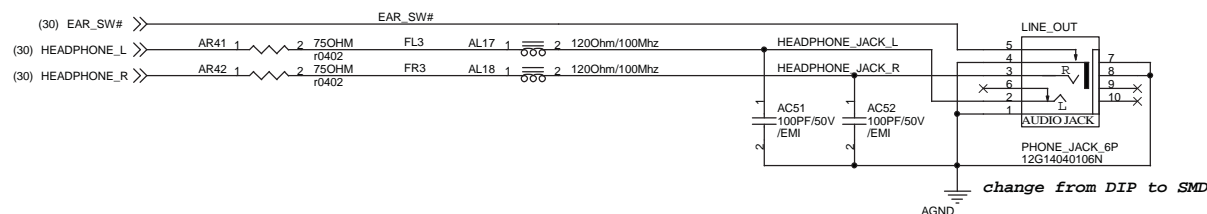
ASUS		Title : AU6336-C52	
ASUSTek Computer Inc.		Engineer: Boison Hung	
Size A3	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008		Sheet 29 of 50	



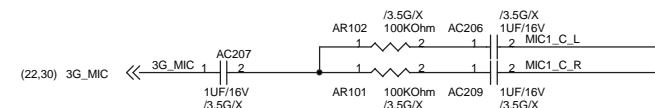
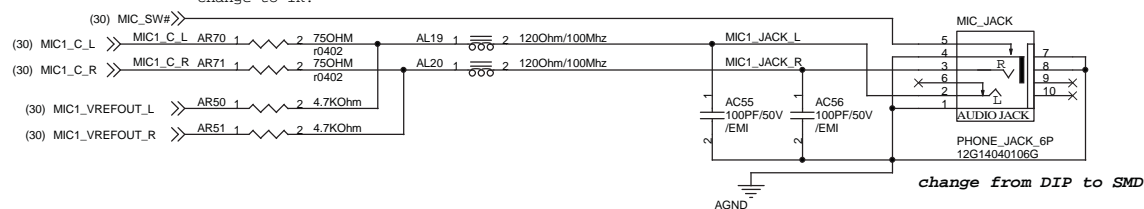
Total length from speakerR+- L+- (pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)



LINE_OUT use 12G14040106N

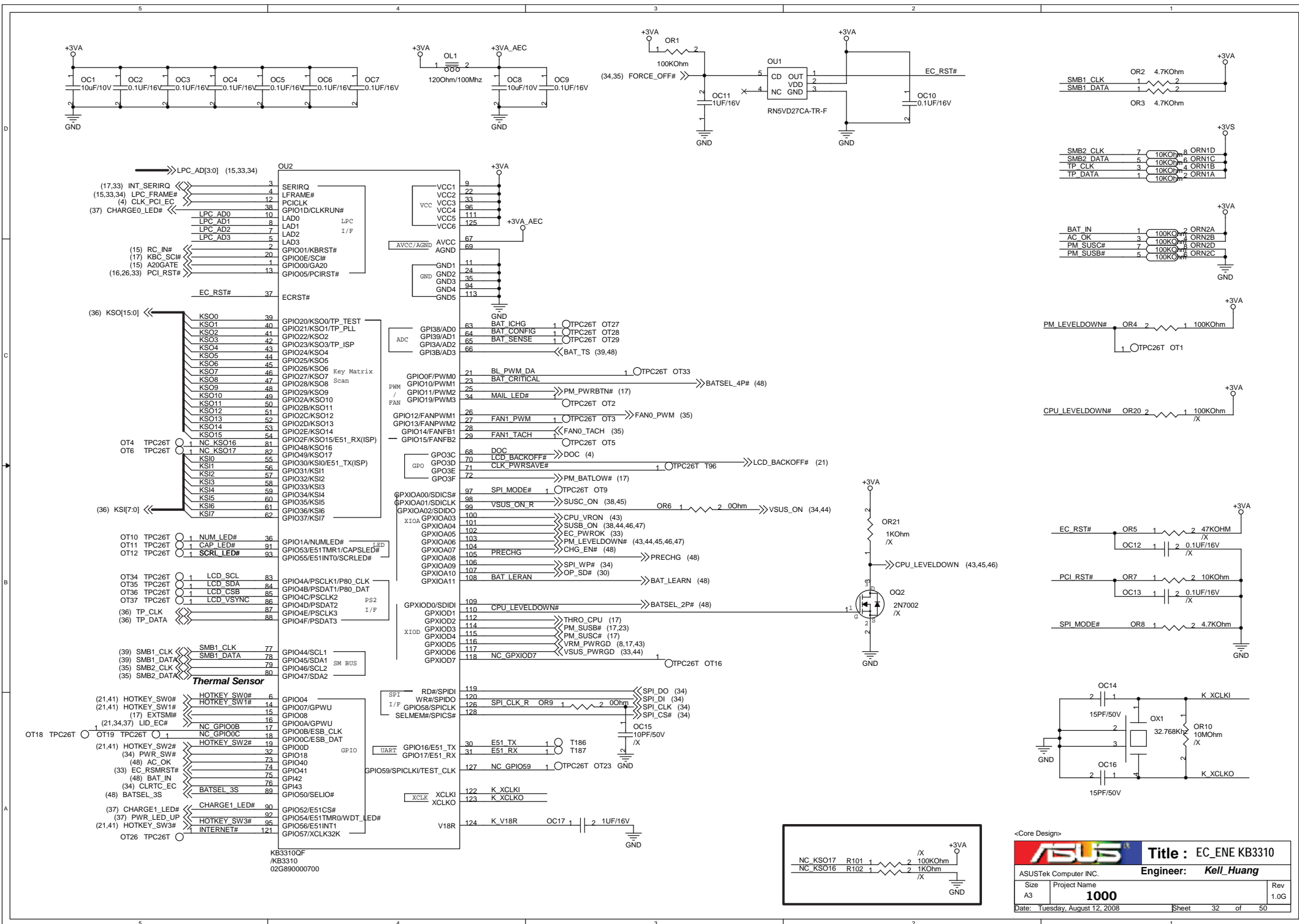


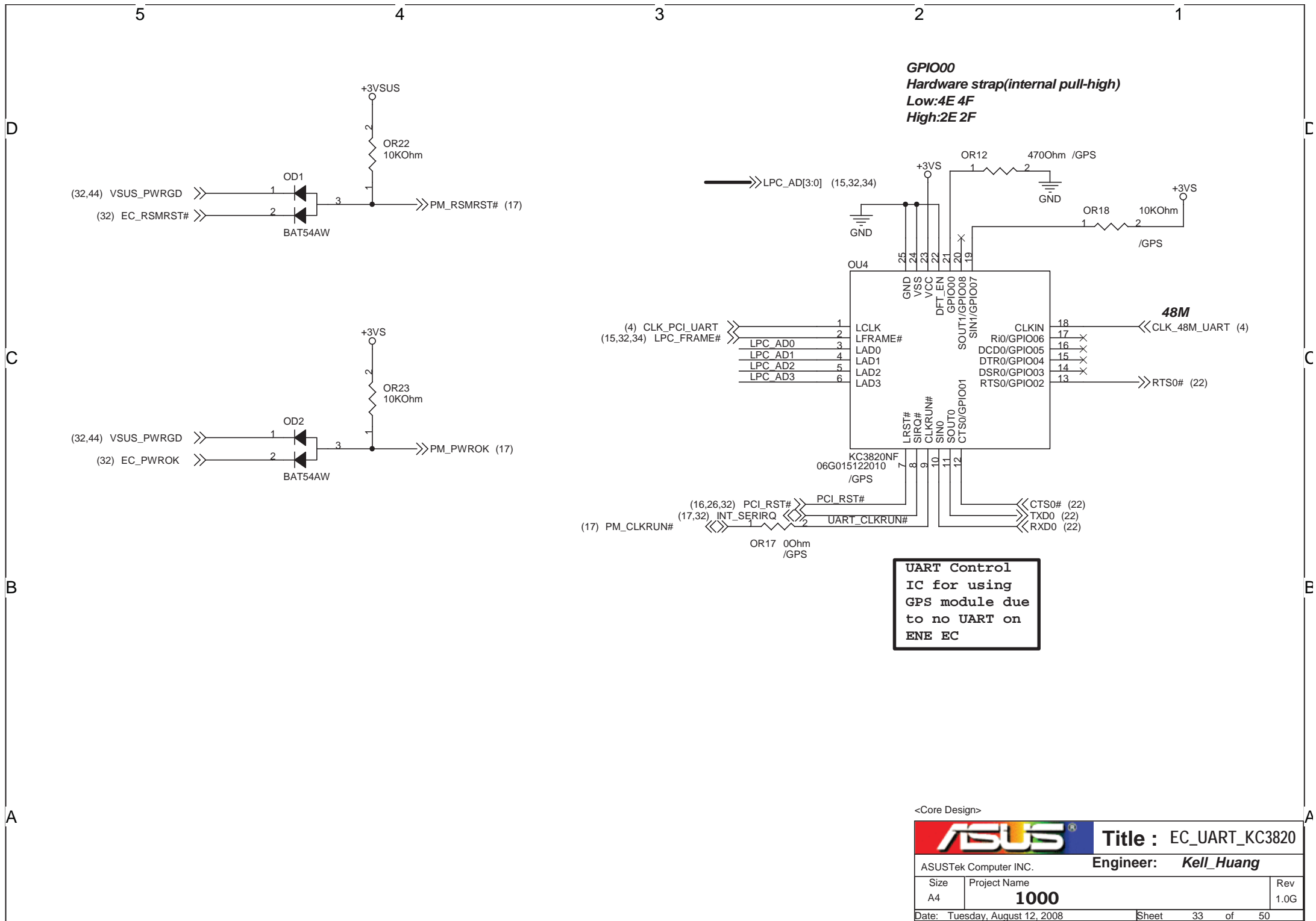
MIC_JACK use 12G14040106G



<Core Design>

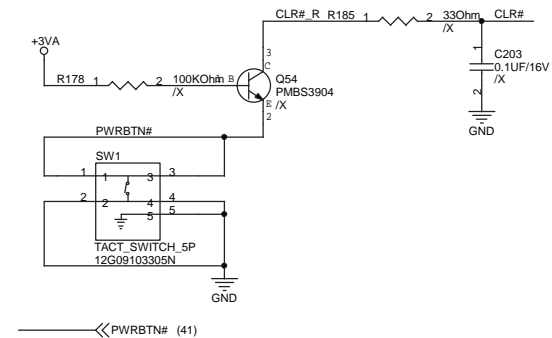
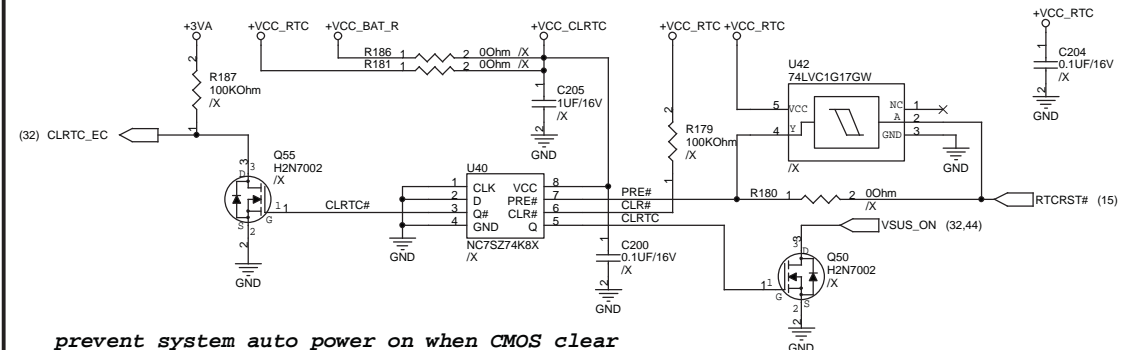
ASUS		Title : ALC269-2	
ASUSTek Computer Inc.		Engineer: MICK	
Size A3	Project Name 1000	Rev 1.0G	
Date: Tuesday, August 12, 2008		Sheet	31 of 50



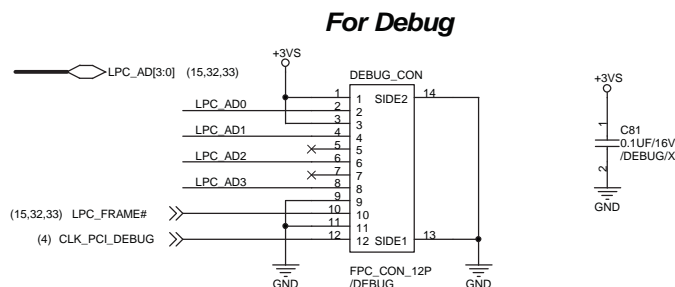


UART Control
IC for using
GPS module due
to no UART on
ENE EC

<Core Design>

[illegible]

```
prevent system auto power on when CMOS clear
```

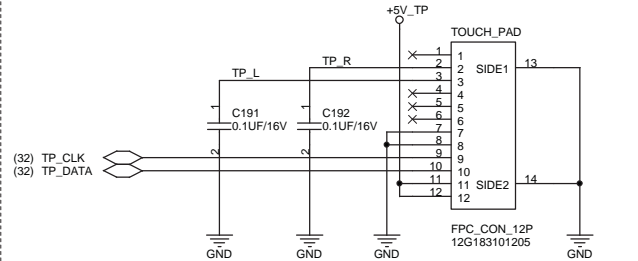


Schematic Diagram of SPI ROM Circuit:

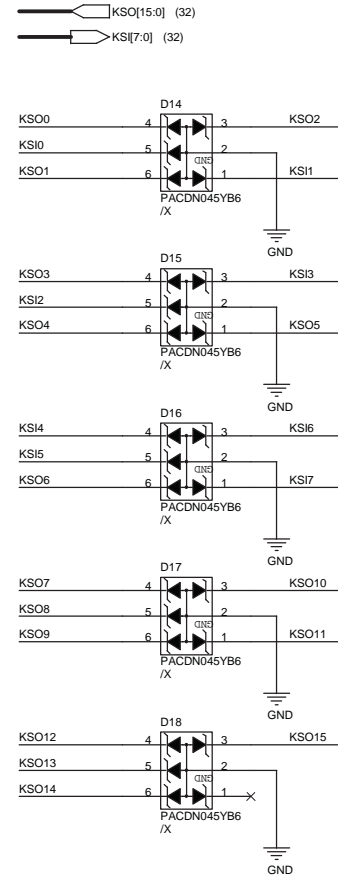
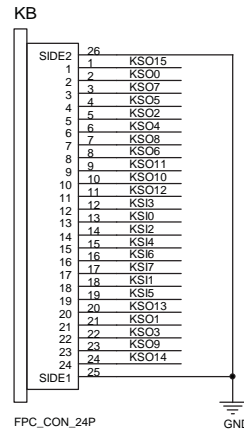
- Power Supply:** +3VA, +3VA_SPI, GND.
- Resistors:** R266 (100k), R267 (100k), R269 (100k), R271 (100k), R79 (10k), R80 (10k), R270 (100k).
- Components:** D43 (BAT54CW), U18 (05G001002900 & 05G00100F130), U18 (SST25VF040B-50-4C).
- Connections:**
 - +3VA_SPI to SPI_HOLD# (R271), SPI_CLK# (R267), SPI_DO# (R269), SPI_DI# (R270).
 - +3VA_SPI to SPI_HOLD# (R79), SPI_CLK# (R80), SPI_DO# (R79), SPI_DI# (R80).
 - +3VA_SPI to SPI_HOLD# (R79), SPI_CLK# (R80), SPI_DO# (R79), SPI_DI# (R80).
 - +3VA_SPI to SPI_HOLD# (R79), SPI_CLK# (R80), SPI_DO# (R79), SPI_DI# (R80).

For Touch-Pad

P900 R1.0G

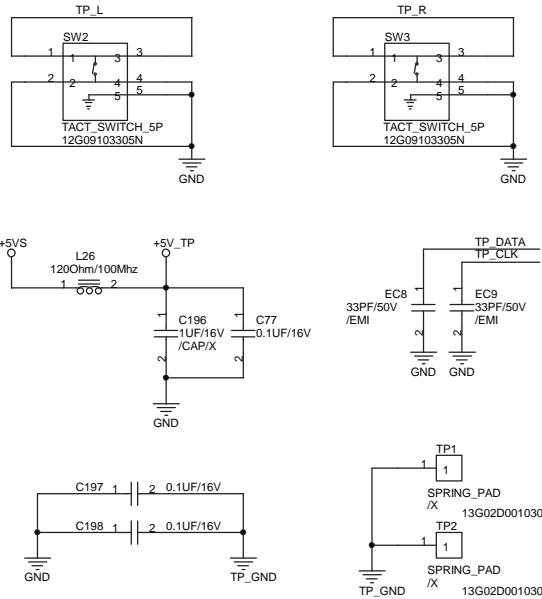


For Keyboard Connector



KSI0	EC10	1	2	33PF/50V
KSI1	EC11	1	2	33PF/50V
KSI2	EC12	1	2	33PF/50V
KSI3	EC13	1	2	33PF/50V
KSI4	EC14	1	2	33PF/50V
KSI5	EC15	1	2	33PF/50V
KSI6	EC16	1	2	33PF/50V
KSI7	EC17	1	2	33PF/50V
KSO0	EC18	1	2	33PF/50V
KSO1	EC19	1	2	33PF/50V
KSO2	EC20	1	2	33PF/50V
KSO3	EC21	1	2	33PF/50V
KSO4	EC22	1	2	33PF/50V
KSO5	EC23	1	2	33PF/50V
KSO6	EC24	1	2	33PF/50V
KSO7	EC25	1	2	33PF/50V
KSO8	EC26	1	2	33PF/50V
KSO9	EC27	1	2	33PF/50V
KSO10	EC28	1	2	33PF/50V
KSO11	EC29	1	2	33PF/50V
KSO12	EC30	1	2	33PF/50V
KSO13	EC31	1	2	33PF/50V
KSO14	EC32	1	2	33PF/50V
KSO15	EC33	1	2	33PF/50V

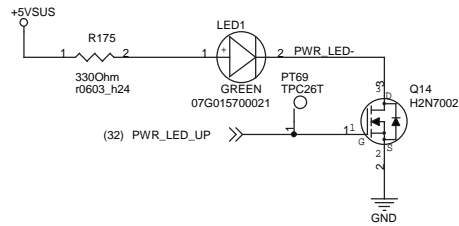
SW2, SW3 use 12G09103305N



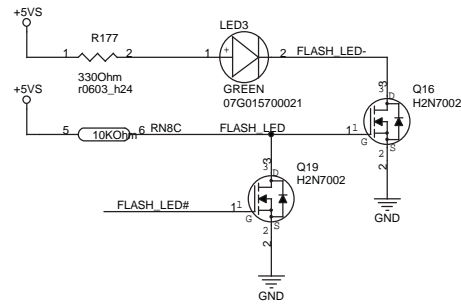
<Core Design>

ASUS		Title : KB_Touch Pad	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name	1000	Rev
A3			1.0G
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for POWER LED

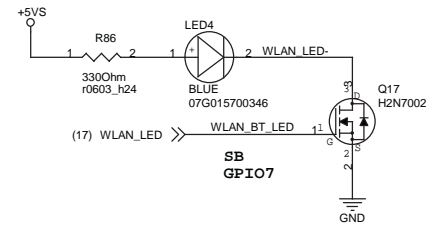


for FLASH LED

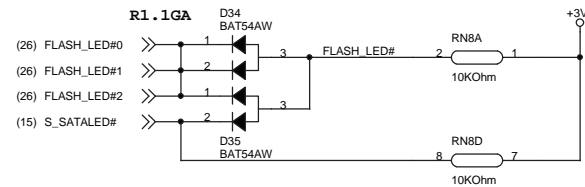
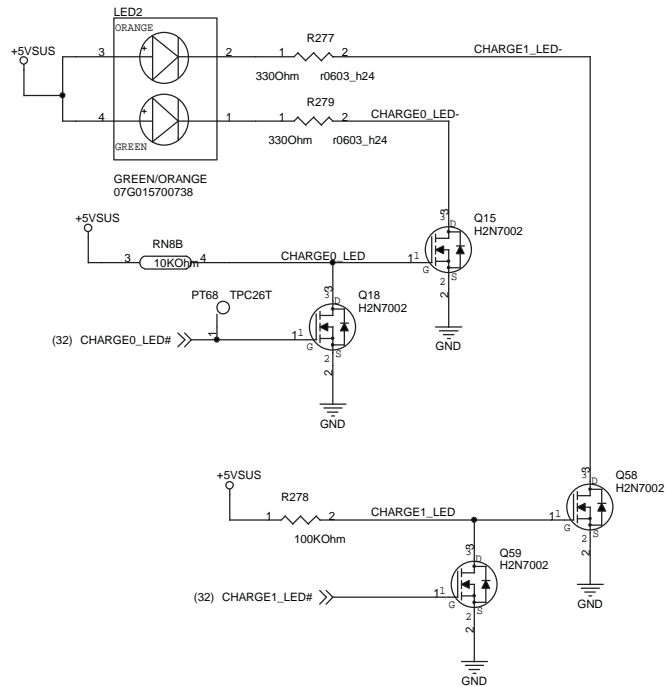


for WLAN/BlueTooth LED

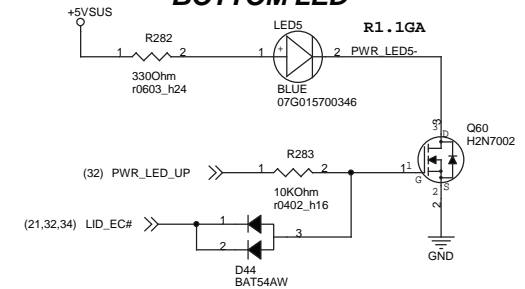
R86 use 4.7K OHm 10G213472003030



for CHARGE LED

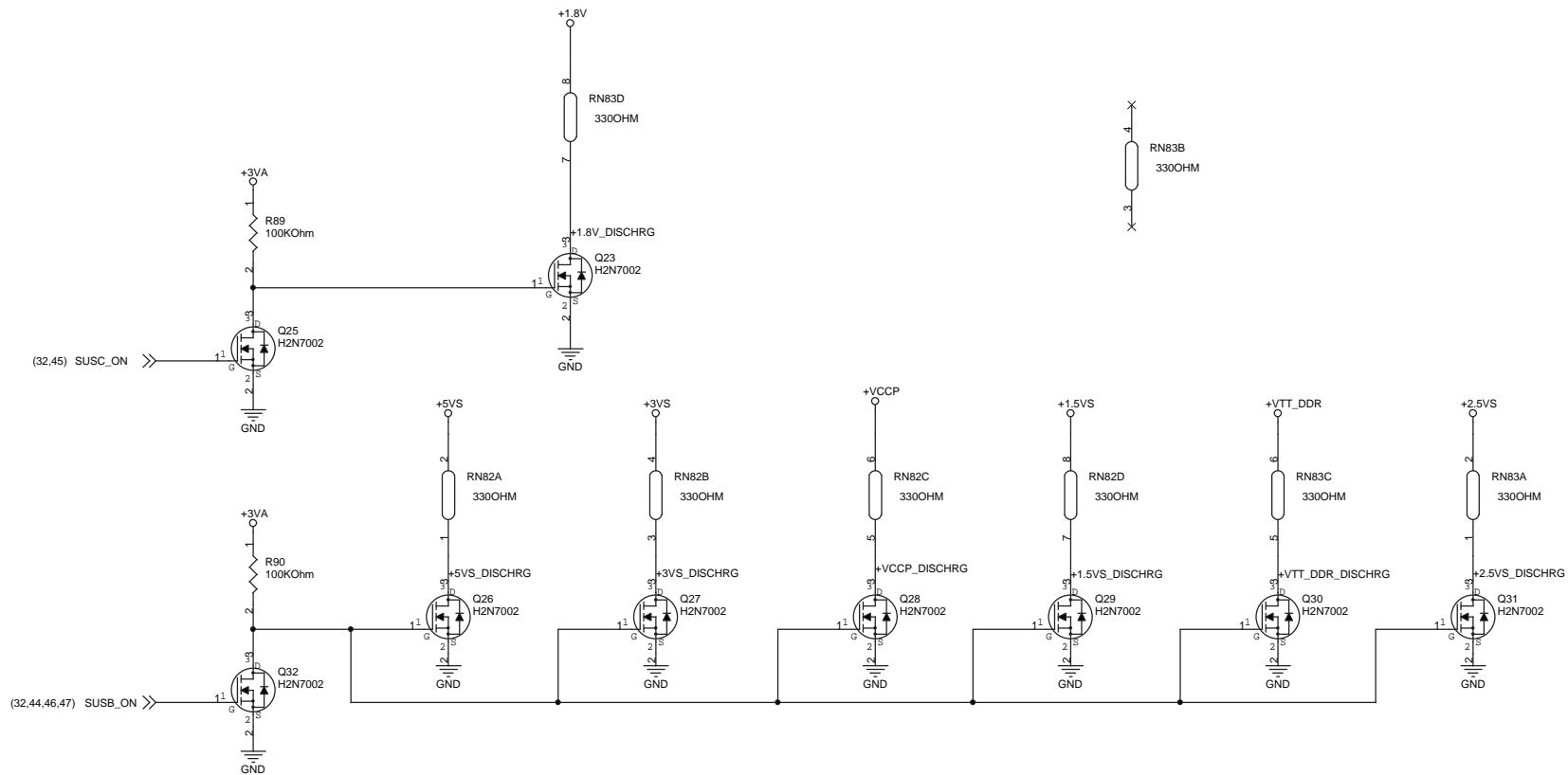


for POWER BOTTOM LED



<Core Design>

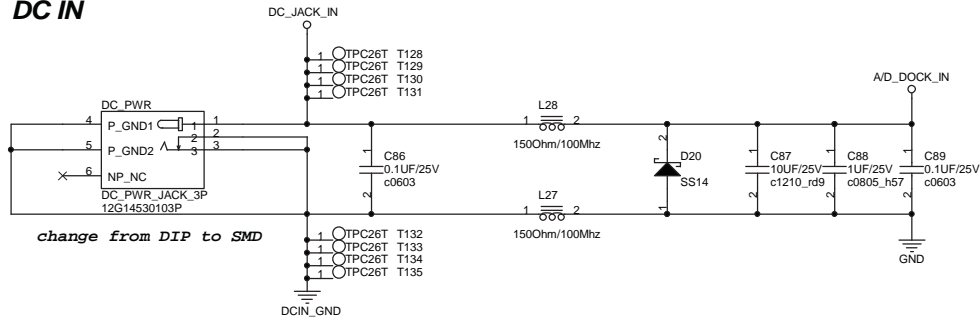
ASUS		Title : LED	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name 1000	Rev 1.0G	
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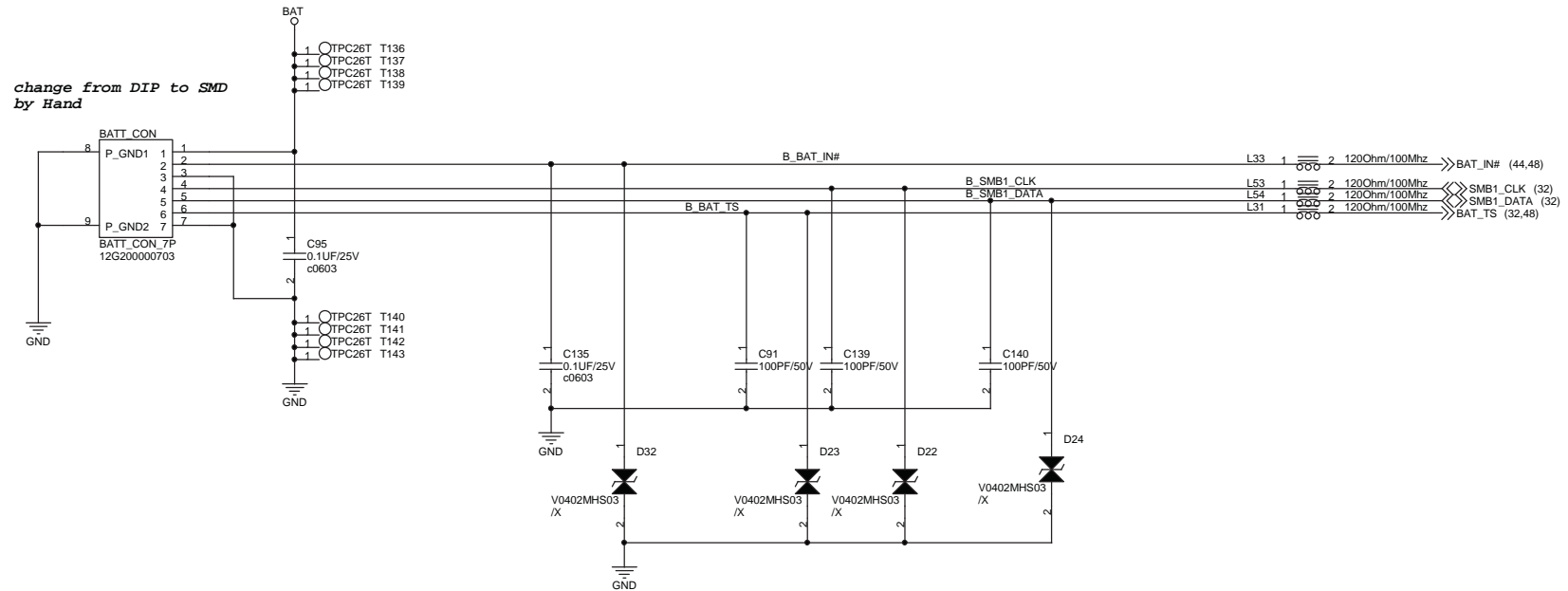
<Core Design>

ASUS		Title : Discharge	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name 1000	Rev 1.0G	
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DC IN

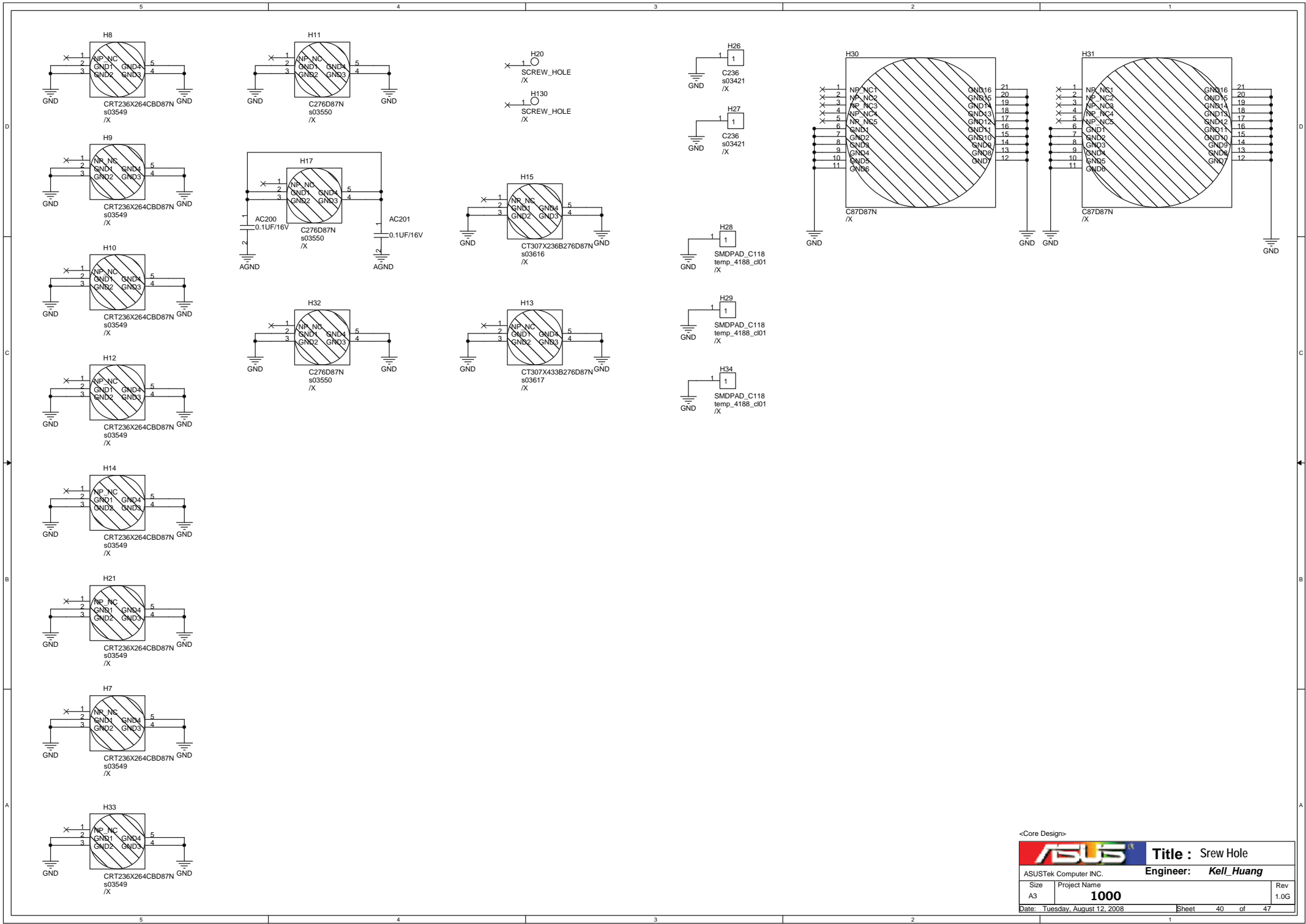


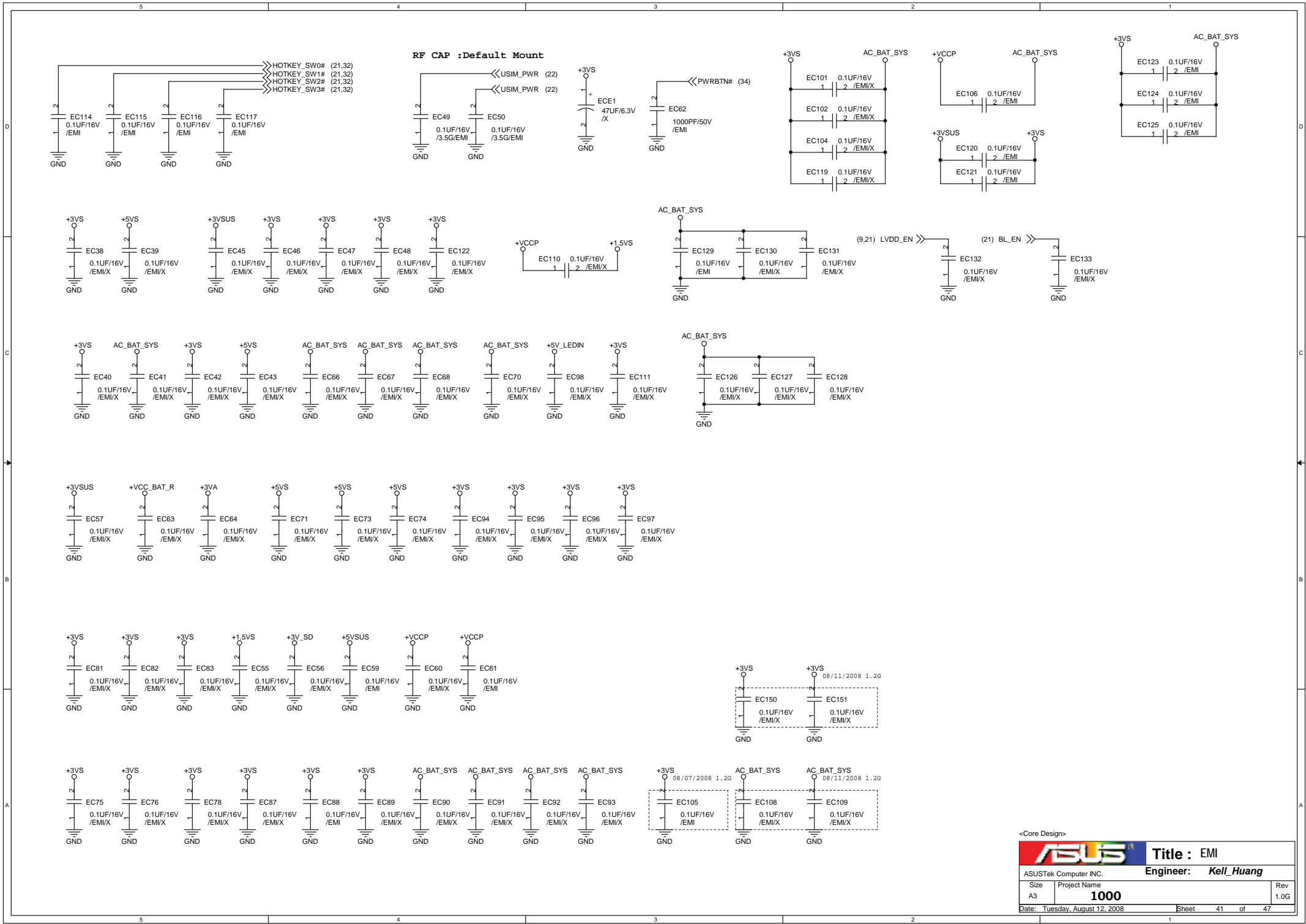
BAT IN

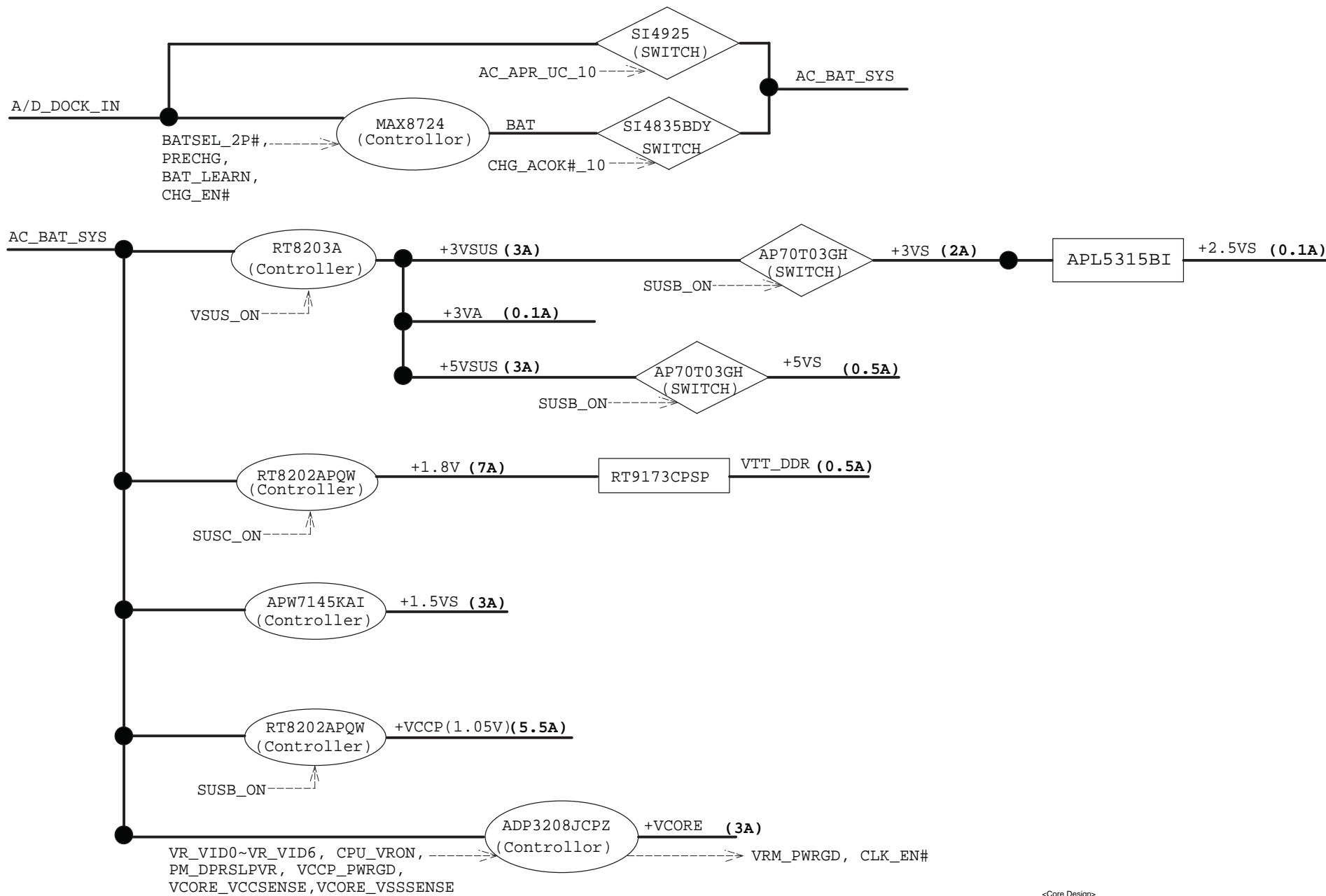


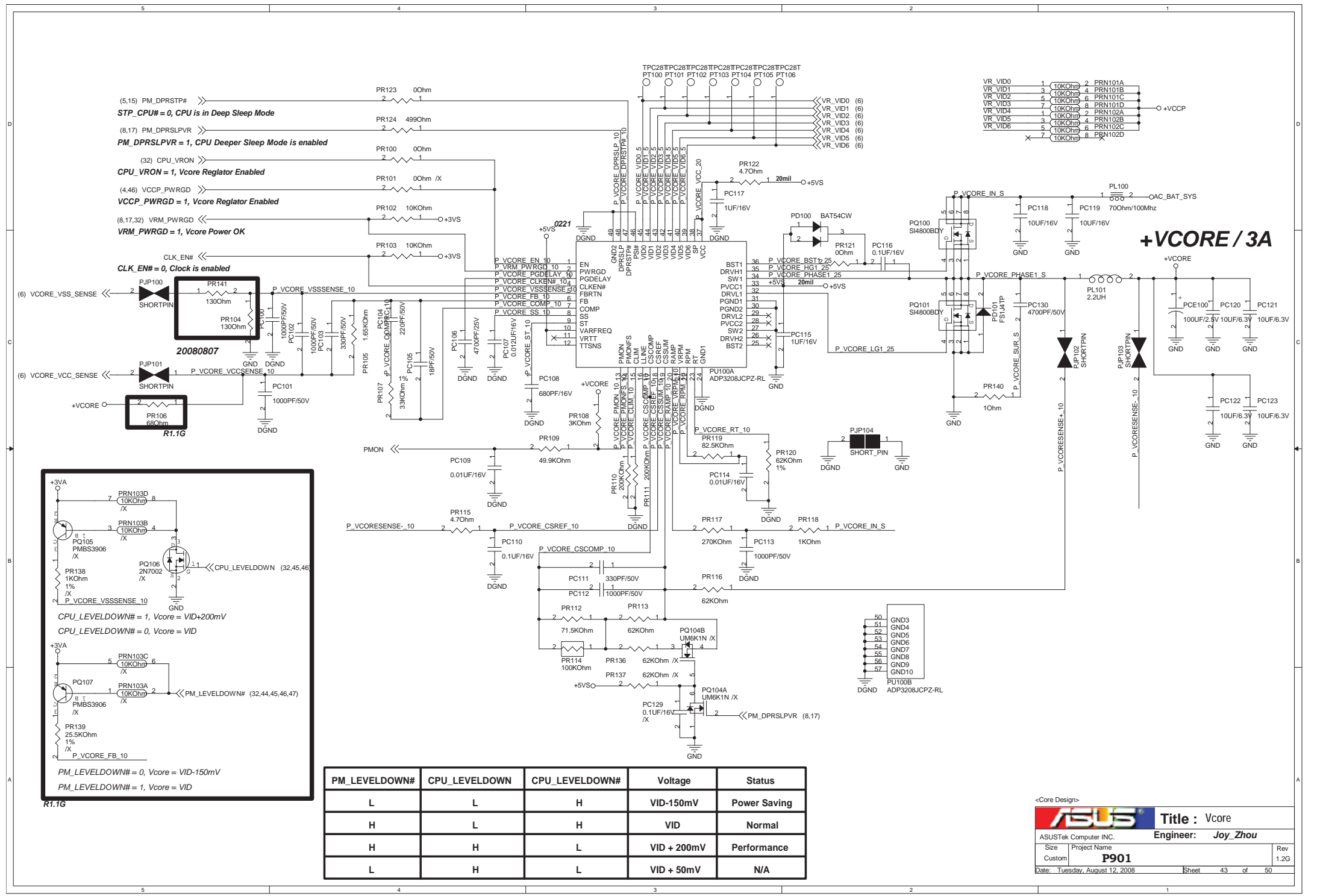
<Core Design>

ASUS		Title : PWR Jack	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name	Rev	
A3	1000		1.0G
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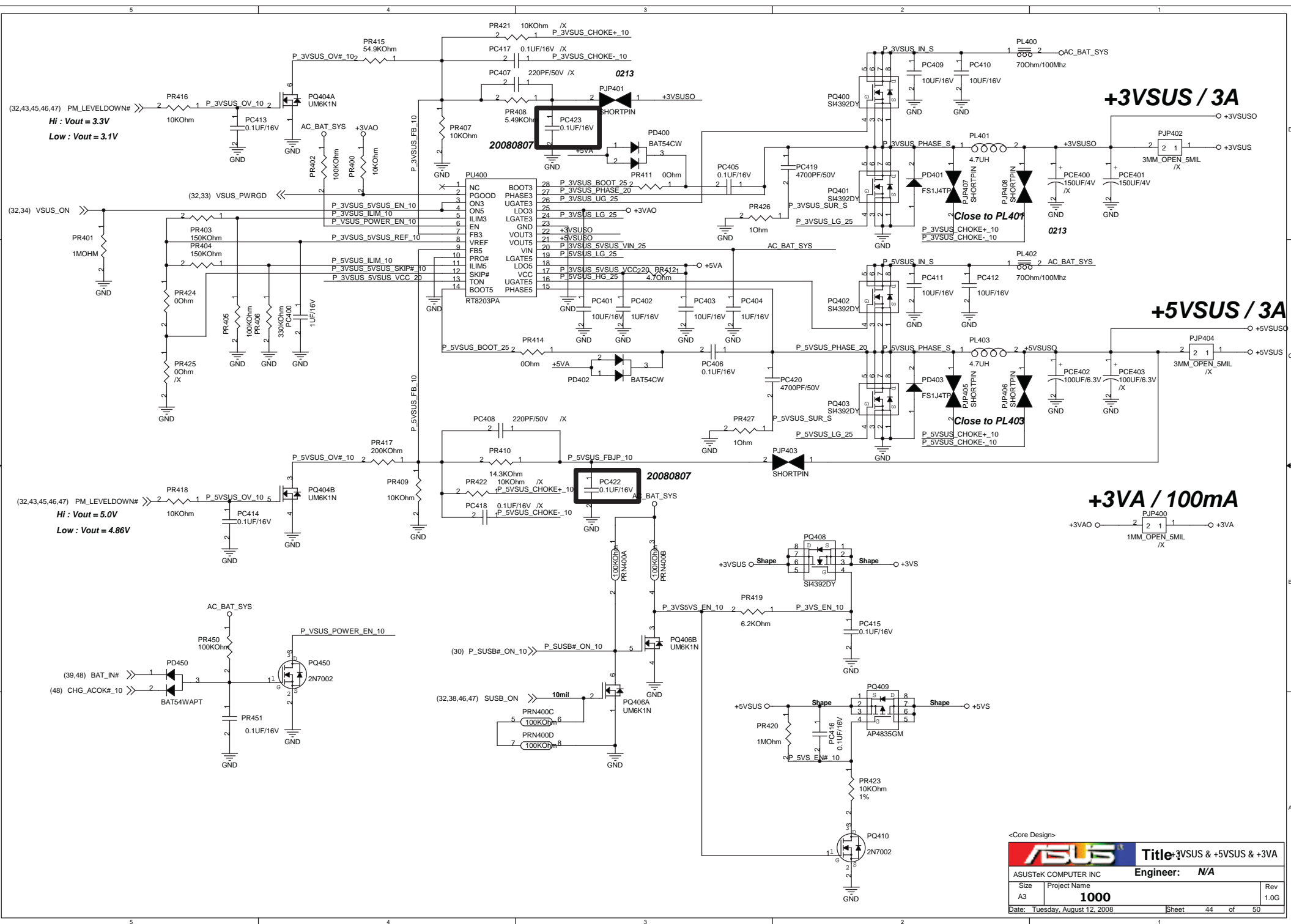








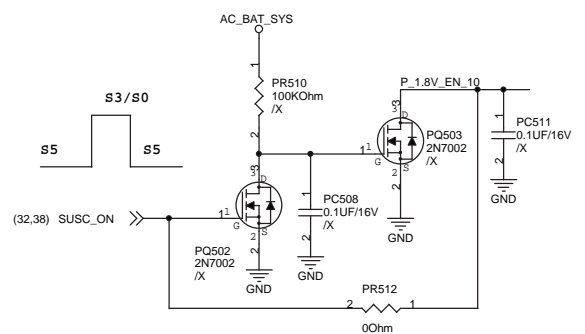
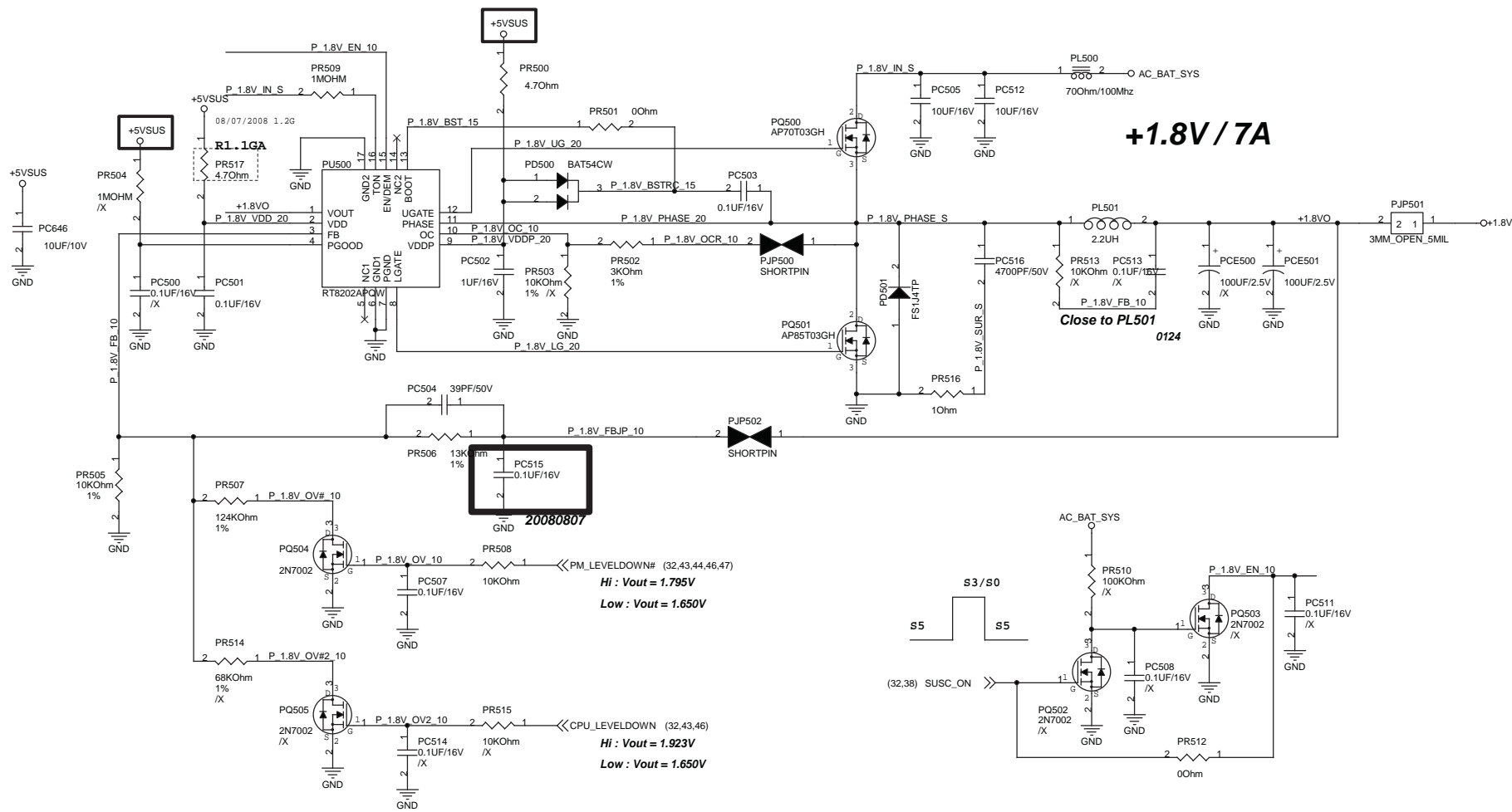
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	VID-150mV	Power Saving
H	L	H	VID	Normal
H	H	L	VID + 200mV	Performance
L	H	L	VID + 50mV	N/A



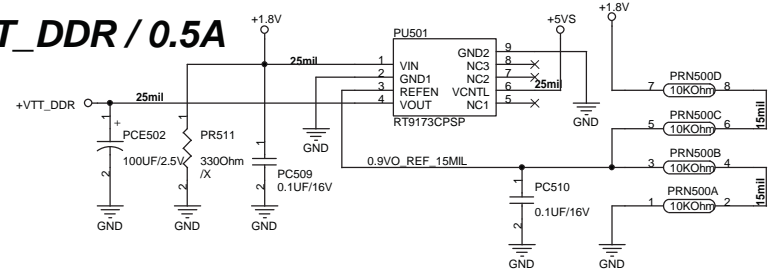
+3VSUS / 3A

+5VSUS / 3A

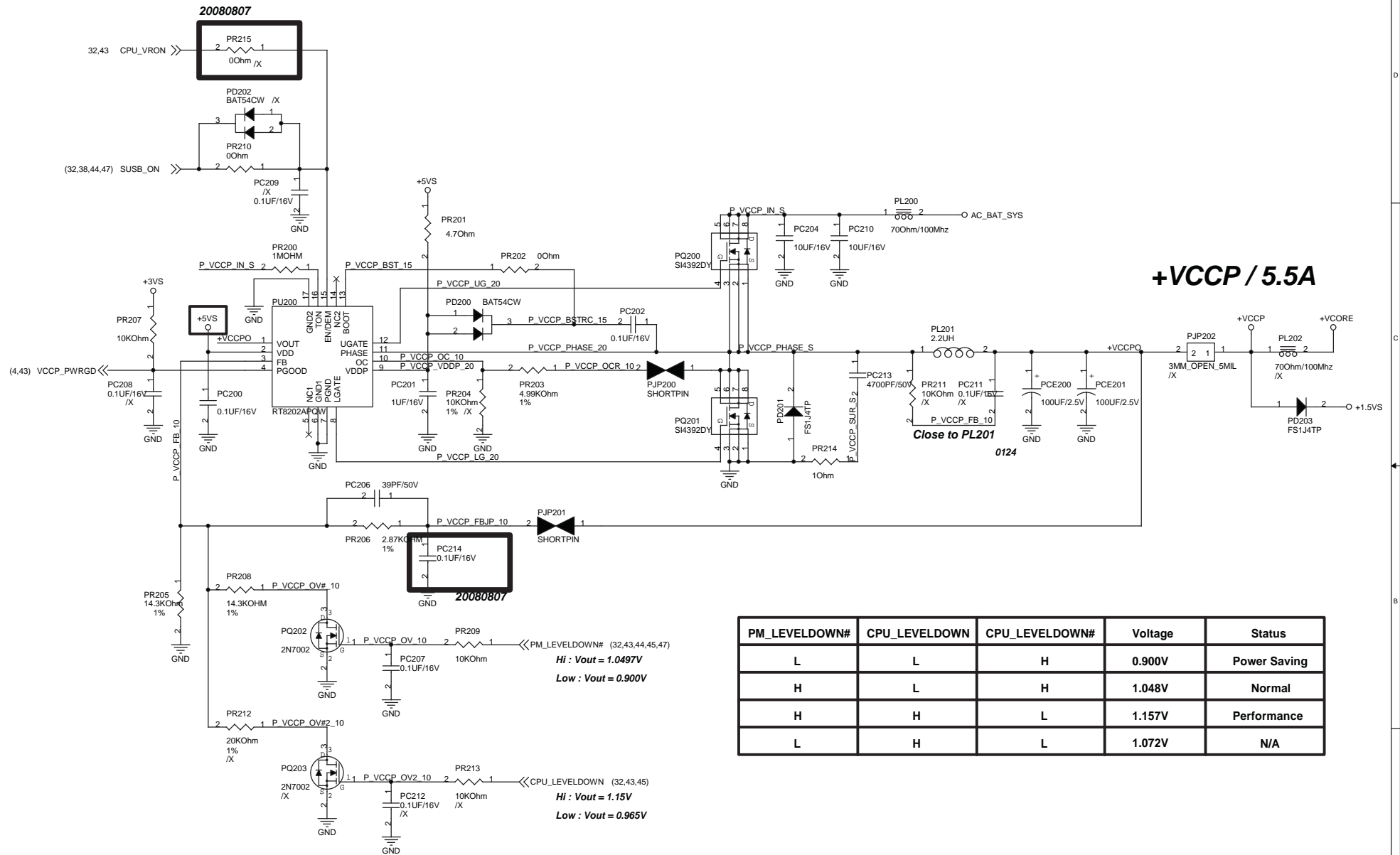
+3VA / 100mA



VTT_DDR / 0.5A



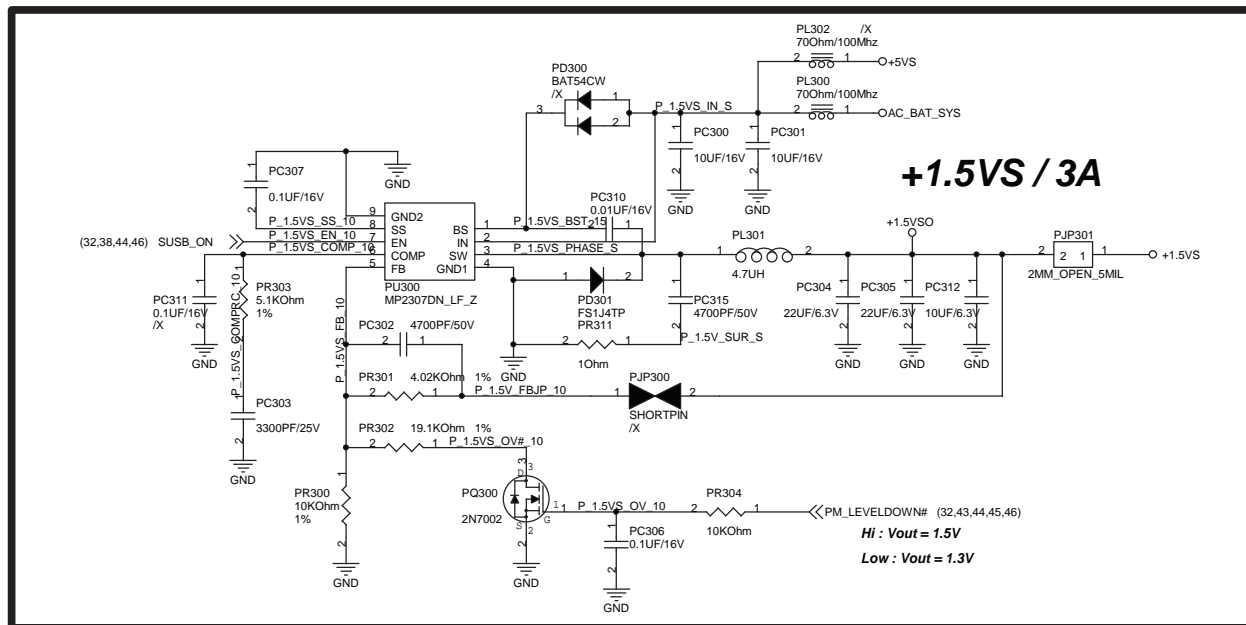
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	1.720V	Power Saving
H	L	H	1.795V	Normal
H	H	L	1.927V	Performance
L	H	L	1.782V	N/A



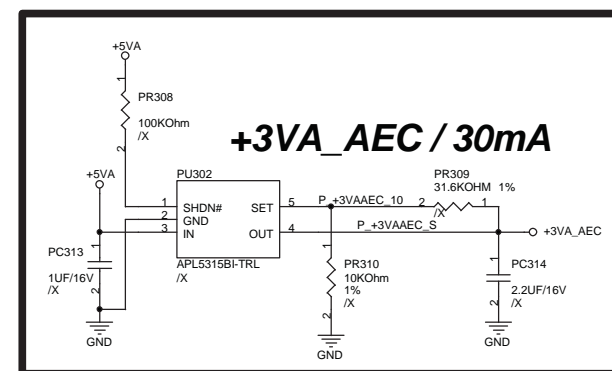
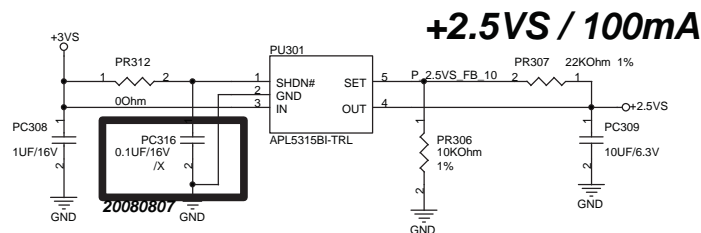
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.900V	Power Saving
H	L	H	1.048V	Normal
H	H	L	1.157V	Performance
L	H	L	1.072V	N/A

<Core Design>

ASUS		Title : VCCP	
ASUSTek Computer INC.		Engineer: Joy_Zhou	
Size A3	Project Name 1000	Rev 1.0G	
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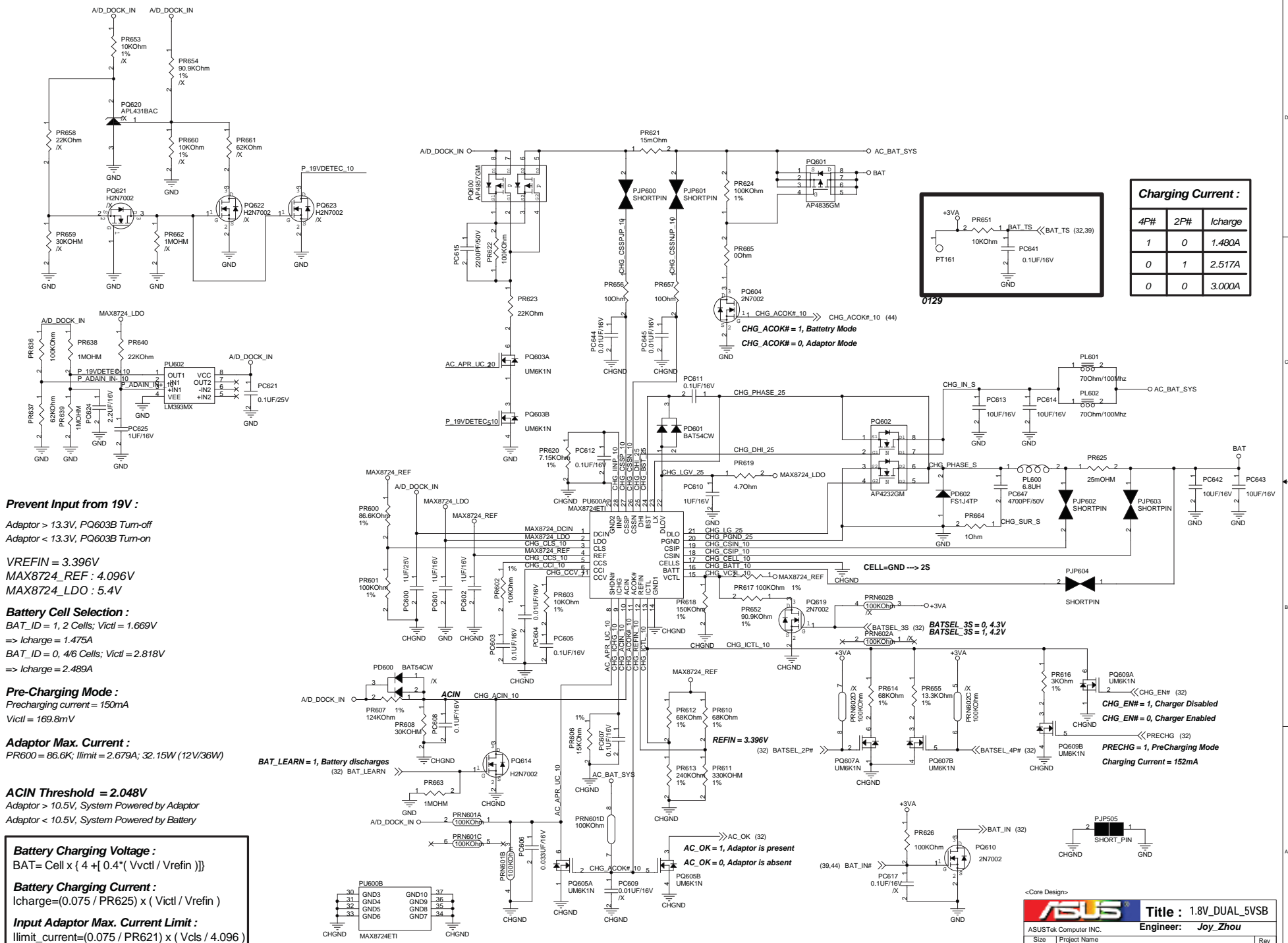


0115



<Core Design>

ASUS		Title : +1.5VS & +2.5VS	
ASUSTek Computer INC.		Engineer: Joy_Zhou	
Size A3	Project Name 1000	Rev 1.0G	
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Prevent Input from 19V :

Adaptor > 13.3V, PQ603B Turn-off
Adaptor < 13.3V, PQ603B Turn-on

VREFIN = 3.396V
MAX8724_REF = 4.096V
MAX8724_LDO = 5.4V

Battery Cell Selection :

BAT_ID = 1, 2 Cells; Vctl = 1.669V
=> Icharge = 1.475A
BAT_ID = 0, 4/6 Cells; Vctl = 2.818V
=> Icharge = 2.489A

Pre-Charging Mode :

Precharging current = 150mA
Vctl = 169.8mV

Adaptor Max. Current :

PR600 = 86.6K; Ilimit = 2.679A; 32.15W (12V/36W)

ACIN Threshold = 2.048V

Adaptor > 10.5V, System Powered by Adaptor
Adaptor < 10.5V, System Powered by Battery

Battery Charging Voltage :

BAT= Cell x { 4 + [0.4* (Vctl / Vrefin)] }

Battery Charging Current :

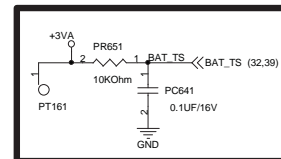
Icharge=(0.075 / PR625) x (Vctl / Vrefin)

Input Adaptor Max. Current Limit :

Ilimit_current=(0.075 / PR621) x (Vcls / 4.096)

Charging Current :

4P#	2P#	Icharge
1	0	1.480A
0	1	2.517A
0	0	3.000A



EC KB3310 GPIO SETTING


Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	EMAIL_SW#	I	Internal pull high
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	BAT_OTP	I	Battery over temperature
15	GPIO08	EXTSMH#	OD	10K pull high to +3VSB
16	GPIO0A	LID_EC#	I	Internal pull high
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	DISTP_SW#	I	Internal pull high
20	GPIO0E/SC#	EXT_SC#	O	10K pull high to +3VSB
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BAT_CRITICAL	I	Battery critical capacity
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debug port
31	GPIO17/E51_RX	E51_RX	I	RS232 debug port
32	GPIO18	PWR_SW#	I	Internal pull high
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	NC	O	
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pull high
56	GPIO31/KSI1	KSI1	I	Internal pull high
57	GPIO32/KSI2	KSI2	I	Internal pull high
58	GPIO33/KSI3	KSI3	I	Internal pull high
59	GPIO34/KSI4	KSI4	I	Internal pull high
60	GPIO35/KSI5	KSI5	I	Internal pull high
61	GPIO36/KSI6	KSI6	I	Internal pull high
62	GPIO37/KSI7	KSI7	I	Internal pull high
63	GPI38/AD0	BAT_ICHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configuration
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/OD	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPI_SO	I	
120	WR#/SPIDO	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSERVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K pull down to GND
75	GPI42	BAT_IN	I	
76	GPI43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/OD	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/OD	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/OD	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/OD	10K pull high to +3V
81	GPIO48/KSO16	KB pin 28	I	for KB type detection
82	GPIO49/KSO17	KB pin 27	I	for KB type detection
83	GPIO4A/PSCLK1	AUO_SCL	O	for AUO, default H at S0
84	GPIO4B/PSDAT1	AUO_SDA	O	for AUO, default L at S0
85	GPIO4C/PSCLK2	AUO_CSB	O	for AUO, default H at S0
86	GPIO4D/PSDAT2	LVDD_EN	I	for AUO 7" Panel
87	GPIO4E/PSCLK3	TP_CLK	I/OD	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/OD	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCRLED_LED#	O	
95	GPIO56	PWR4G_SW#	I	Internal pull high
97	GPX0A00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPX0A01/SDICLK	SUSC_ON	O	
99	GPX0A02/SDIDO	VSUS_ON	O	
100	GPX0A03	CPU_VRON	O	
101	GPX0A04	SUSB_ON	O	
102	GPX0A05	ICH_PWROK	O	
103	GPX0A06	VOLT_CTRL	O	
104	GPX0A07	CHG_EN#	O	Battery charging enabled
105	GPX0A08	PRECHG	O	
106	GPX0A09	SPI_WP#	O	
107	GPX0A10	OP_SD#	O	Audio OP
108	GPX0A11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P~3P
110	GPXID1	NC	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	SUSB#	I	100K pull down to GND
115	GPXID4	SUSC#	I	100K pull down to GND
116	GPXID5	CPUPWR_GD	I	Pull high to +3V
117	GPXID6	VSUS_GD	I	
118	GPXID7	NC	O	
121	GPIO57	INTERNET#	I	Internal pull high
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	

<Core Design>

		Title : EC Pin Define	
ASUSTek Computer INC.		Engineer: Satan He	
Size A3	Project Name 1000	Rev 1.0G	
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<Core Design>



Title : History

ASUSTek Computer INC.

Engineer: *Satan He*

Size
A3

Project Name	
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Rev	
06	

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