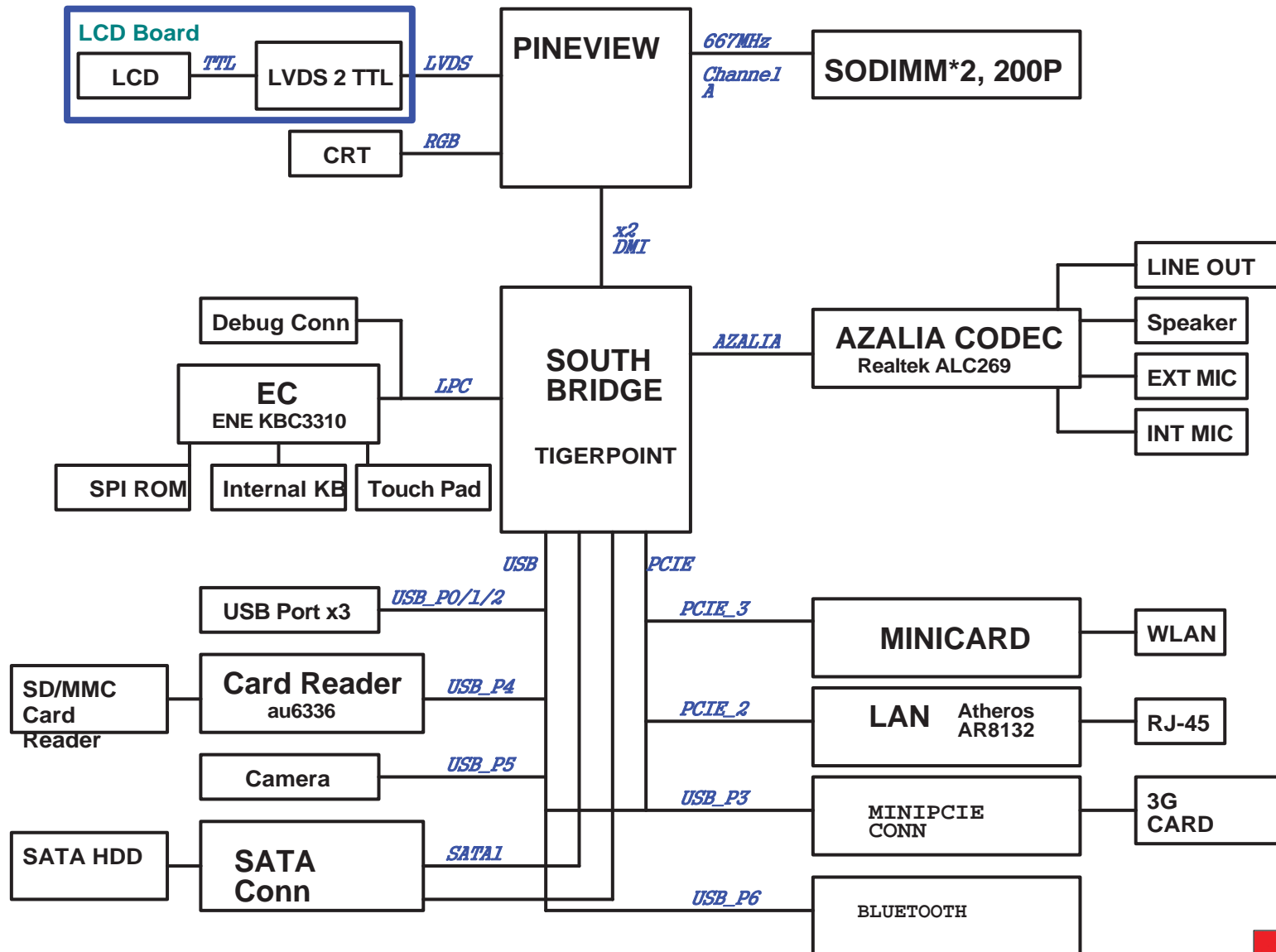


CLOCK GEN
IDT427



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

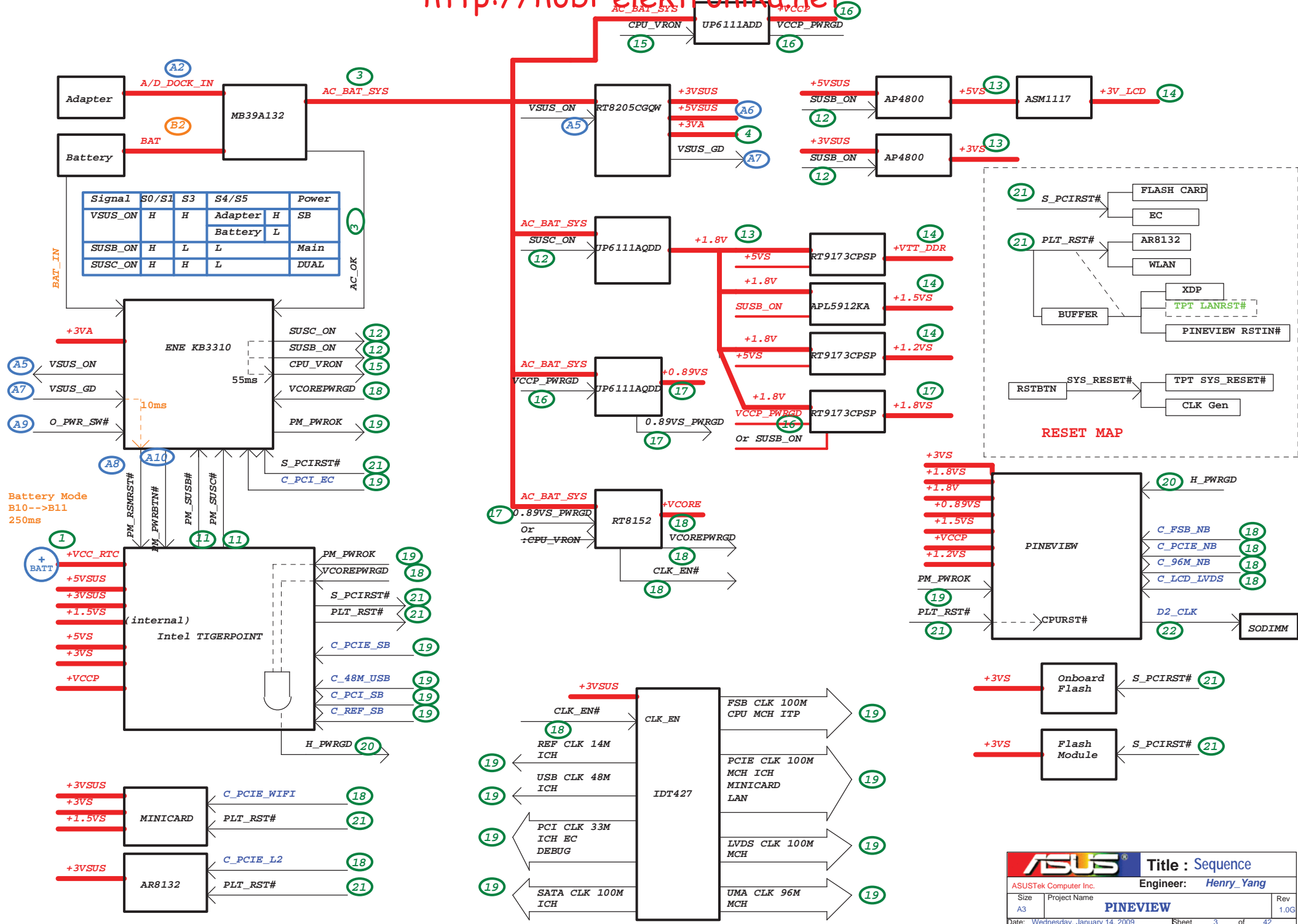
ICH7M GPIO DEFAULT&NOW
SETTING

Pin	Pin Name	Type	Tolerance	Powe Well	Default	Now Setting
AB18	GPIO0/BM_BUSY#	I/O	3.3V	CORE	GPI	BM_BUSY#
C8	GPIO1/REQ5#	I/O	5V	CORE	GPI	REQ5#
G8	GPIO2/PIRQE#	I/OD	5V	CORE	GPI	PIRQE#
F7	GPIO3/PIRQF#	I/OD	5V	CORE	GPI	PIRQF#
F8	GPIO4/PIRQG#	I/OD	5V	CORE	GPI	PIRQG#
G7	GPIO5/PIRQH#	I/OD	5V	CORE	GPI	PIRQH#
AC21	GPIO6	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AC18	GPIO7	I/O	3.3V	CORE	GPI	GPO,WLAN_LED
E21	GPIO8	I/O	3.3V	Resume	GPI	GPI,EXTSM#
E20	GPIO9	I/O	3.3V	Resume	GPI	GPI,No Function,10K Pull +3VS
A20	GPIO10	I/O	3.3V	Resume	GPI	GPO,WLAN_ON#
B23	GPIO11/SMBALERT#	I/O	3.3V	Resume	Native	SMBALERT#
F19	GPIO12	I/O	3.3V	Resume	GPI	GPI,KBC_SC#
E19	GPIO13	I/O	3.3V	Resume	GPI	GPO,VCCP_DOWN
R4	GPIO14	I/O	3.3V	Resume	GPI	GPO,1.5VS_DOWN
E22	GPIO15	I/O	3.3V	Resume	GPI	GPI,No Function,10K Pull +3VSUS
AC22	GPIO16/DPRSLVR	I/O	3.3V	CORE	Native	DPRSLVR
D8	GPIO17/GNT5#	I/O	3.3V	CORE	GPO	BIOS_SEL1
AC20	GPIO18/STPPC#	I/O	3.3V	CORE	GPO	STP_PC#
AH18	GPIO19/SATA1GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AF21	GPIO20/STPCPU#	I/O	3.3V	CORE	GPO	STP_CPU#
AF19	GPIO21/SATA0GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
A13	GPIO22/REQ4#	I/O	3.3V	CORE	Native	REQ4#
AA5	GPIO23/LDRQ1#	I/O	3.3V	CORE	Native	LDRQ1#
R3	GPIO24	I/O	3.3V	Resume	GPO	GPO,MINICARD1_EN#
D20	GPIO25	I/O	3.3V	Resume	GPO	GPO,DUAL_DOWN

Pin	Pin Name	Type	Tolerance	Powe Well	Default	Now Setting
A21	GPIO26	I/O	3.3V	Resume	GPO	GPO,VCORE_DOWN
B21	GPIO27	I/O	3.3V	Resume	GPO	GPO,CARD_READER_EN#
E23	GPIO28	I/O	3.3V	Resume	GPO	GPO,MODEM_EN#
C3	GPIO29/OC5#	I/O	3.3V	Resume	Native	OC5#
A2	GPIO30/OC6#	I/O	3.3V	Resume	Native	OC6#
B3	GPIO31/OC7#	I/O	3.3V	Resume	Native	OC7#
AG18	GPIO32/CLKRUN#	I/O	3.3V	CORE	GPO	CLKRUN#
AC19	GPIO33/AZ_DOCK_EN#	I/O	3.3V	CORE	GPO	GPO,No Function,NC
U2	GPIO34/AZ_DOCK_RST#	I/O	3.3V	CORE	GPO	GPO,No Function,NC
AD21	GPIO35	I/O	3.3V	CORE	GPO	GPO,CAMERA_EN
AH19	GPIO36/SATA2GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AE19	GPIO37/SATA3GP	I/O	3.3V	CORE	GPI	GPI,PCB_ID0
AE20	GPIO38	I/O	3.3V	CORE	GPI	GPI,PCB_ID1
AD20	GPIO39	I/O	3.3V	CORE	GPI	GPI,PCB_ID2
NA	GPIO40	NA	NA	NA	NA	NA
NA	GPIO41	NA	NA	NA	NA	NA
NA	GPIO42	NA	NA	NA	NA	NA
NA	GPIO43	NA	NA	NA	NA	NA
NA	GPIO44	NA	NA	NA	NA	NA
NA	GPIO45	NA	NA	NA	NA	NA
NA	GPIO46	NA	NA	NA	NA	NA
NA	GPIO47	NA	NA	NA	NA	NA
A14	GPIO48/GNT4#	I/O	3.3V	CORE	Native	BIOS_SEL0
AG24	GPIO49/CPUPWRGD	I/O	V_CPU_IO	V_CPU_IO	Native	CPUPWRGD

<http://hobi-elektronika.net>

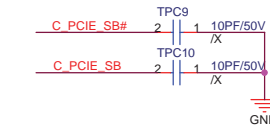
<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



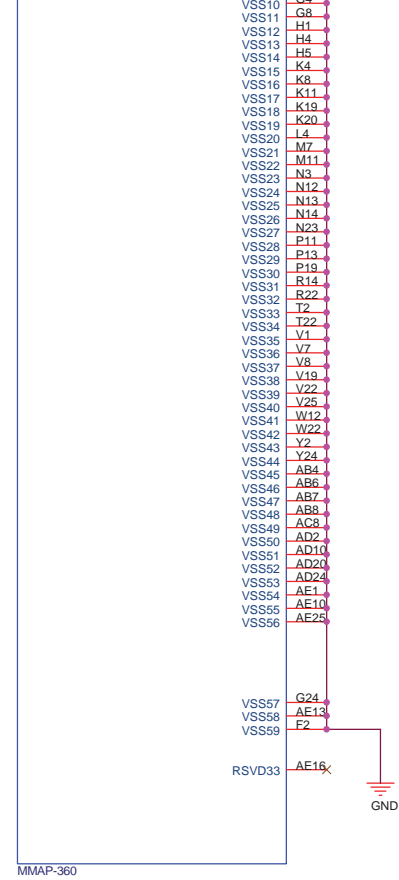
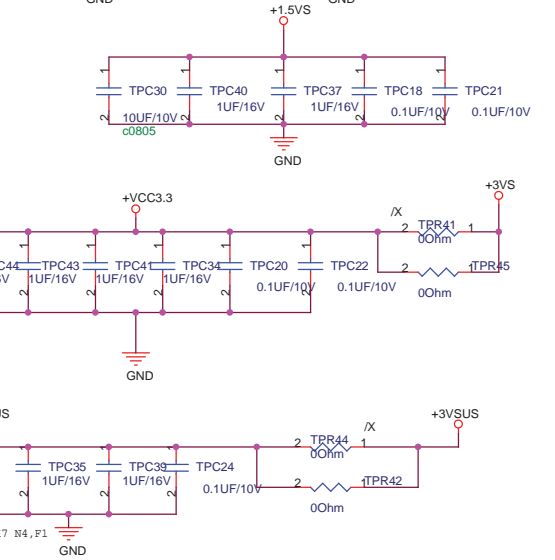
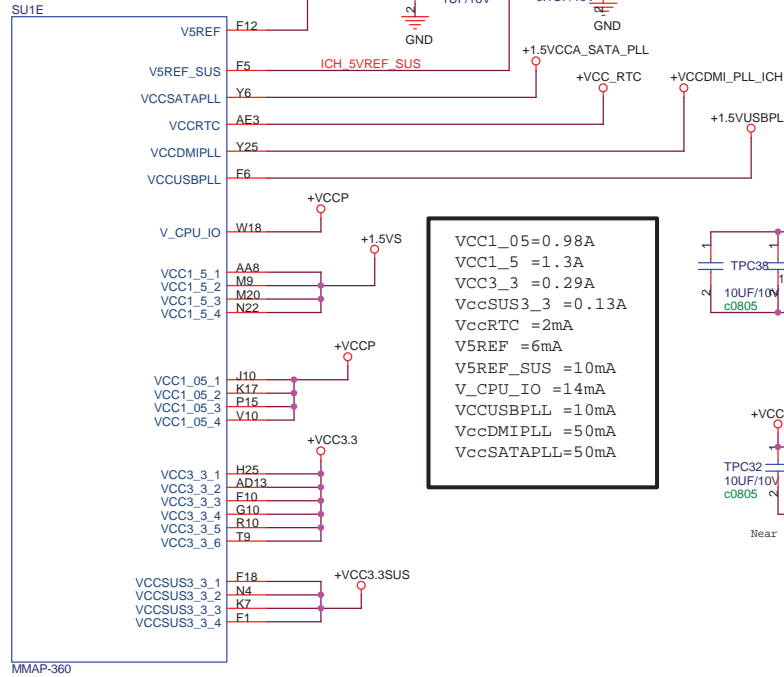
SUI1A			
	TPT55	DEVSEL#	A5
		B15	J12
(12)	C_PCI_SB	A23	
(26)	S_PCIRST#	IRDY#	B7
		PME#	C22
		SERR#	B11
		STOP#	F8
		PLOCK#	A8
		TRDY#	A10
		PERR#	D10
		FRAME#	A16
	TPT67		
	0	A18	
(42)	GNT2#	E16	
		REQ1#	G16
		REQ2#	A20
(42)	BOOTSEL1	G14	
(42)	BOOTSEL2	A2	
(18,41)	MINICARD2_EN#	C15	
(15,18)	3GLAN_OFF	C9	
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8
		PIROA#	B2
		PIROB#	D7
		PIROC#	B3
		PIROD#	H10
		PIROE#	E8
		PIROF#	D6
		PIROG#	F8
		PIROH#	F8



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

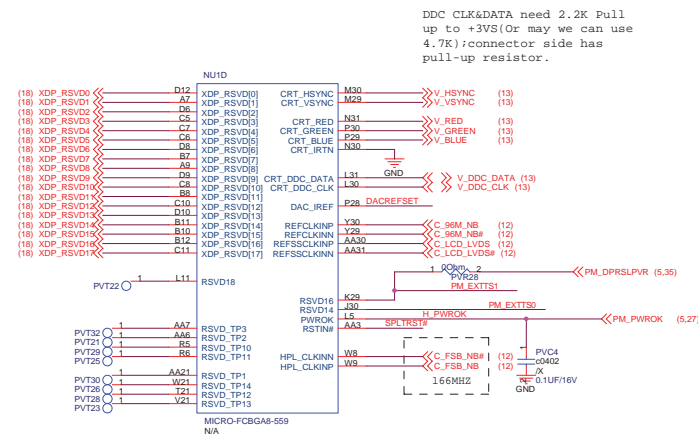
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

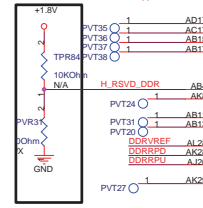
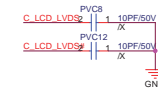
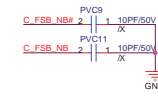
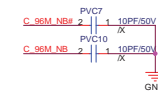
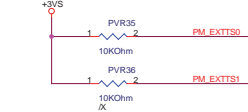
<http://hobi-elektronika.net>



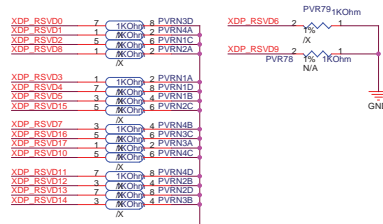
SPLTRST# (5,18)

PVC51
c0402
0.1UF/16V

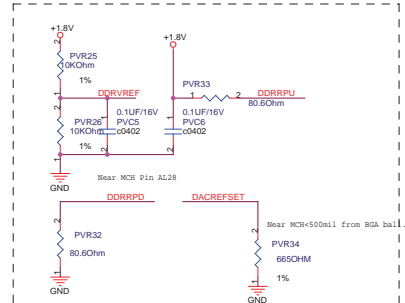
GND

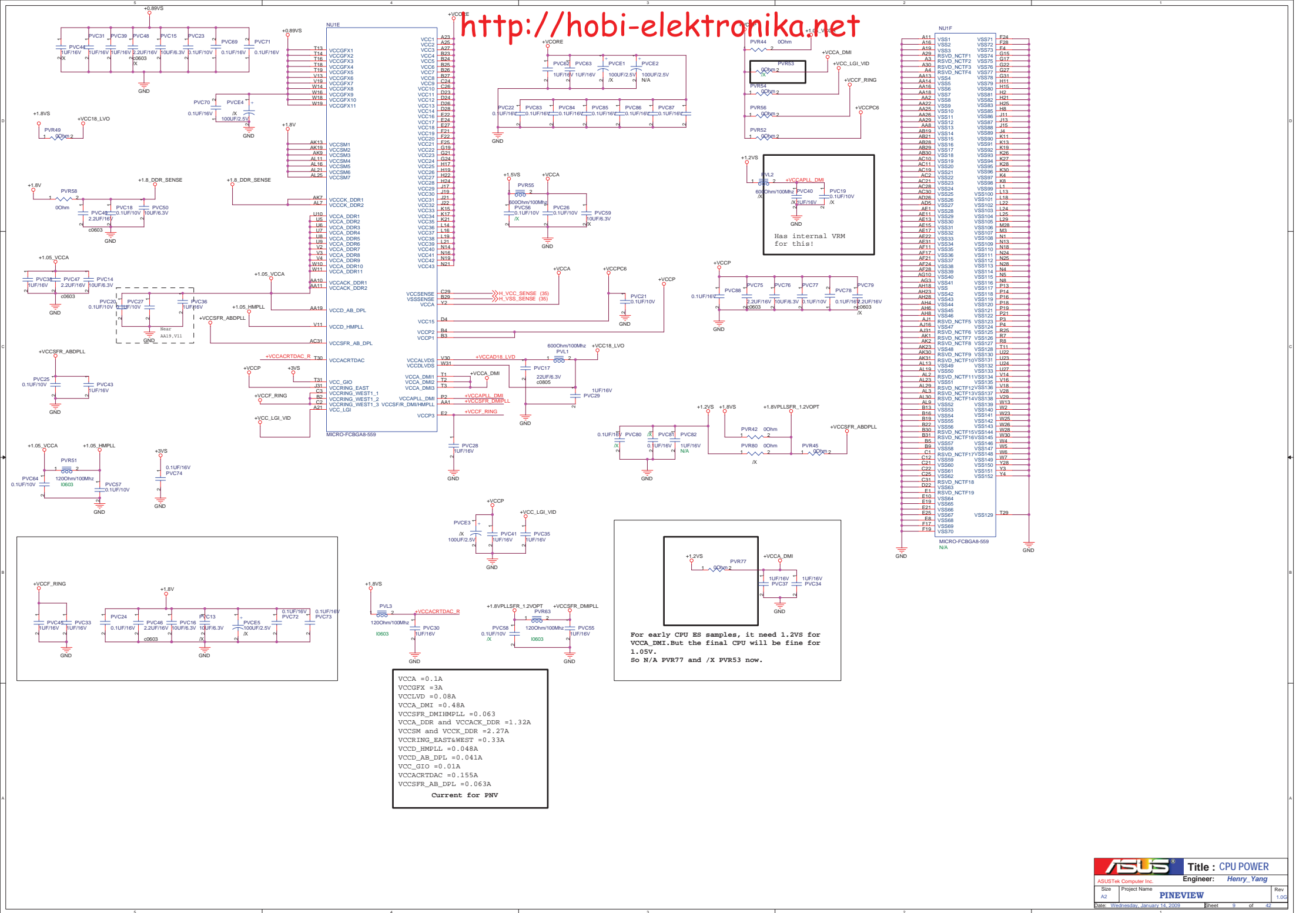


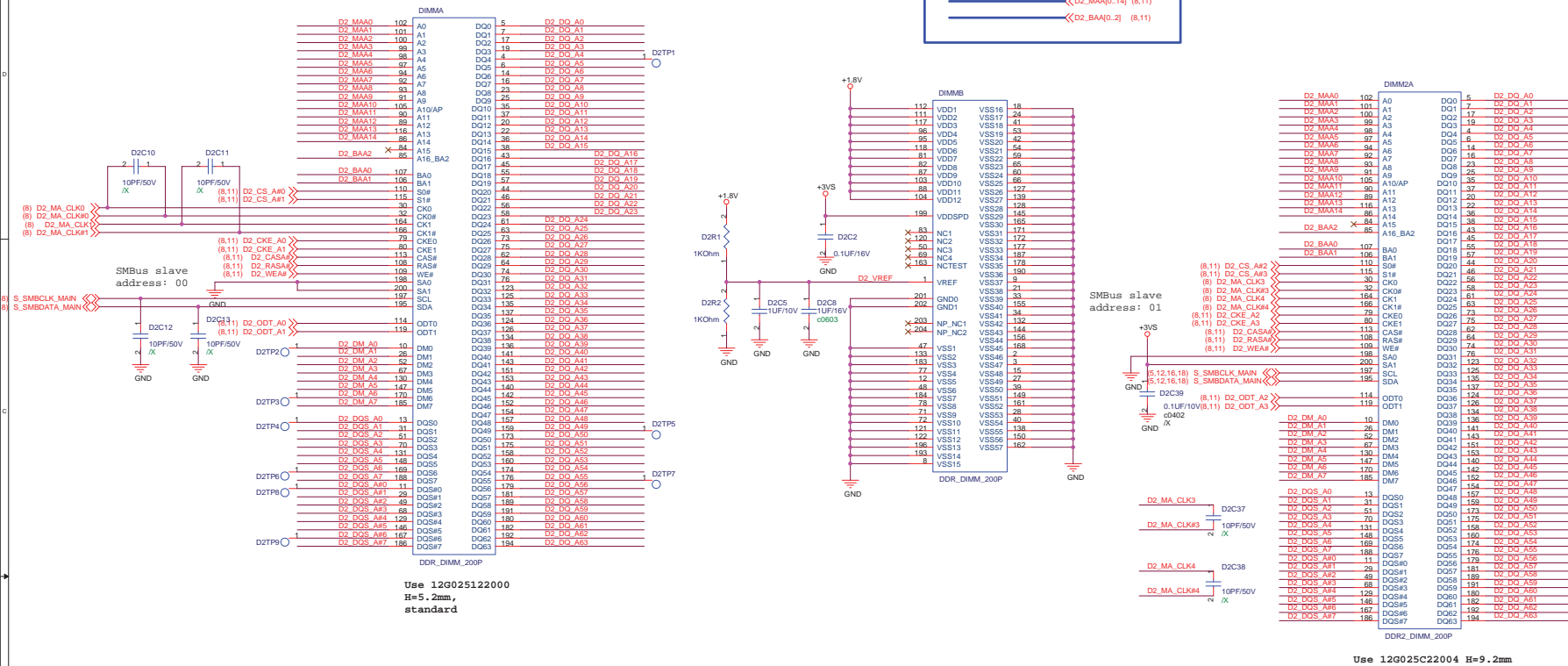
Pull up to 1.8V only for
A0 version ES sample,
later will be pulled down,
so later must N/A PVR31.



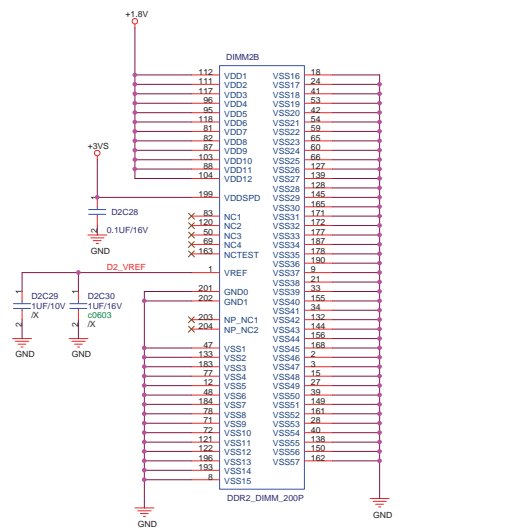
Intel confirm only RSVD9 need resistor.







Use 12G025122000 H=5.2mm, standard



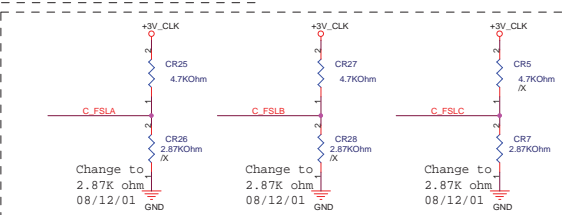
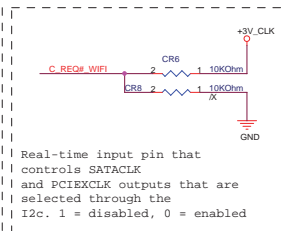
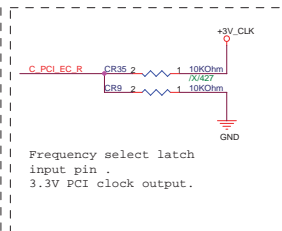
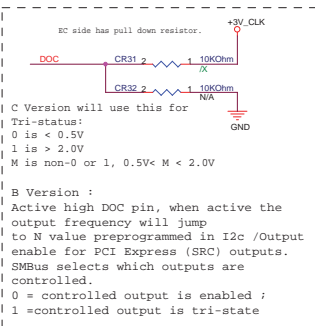
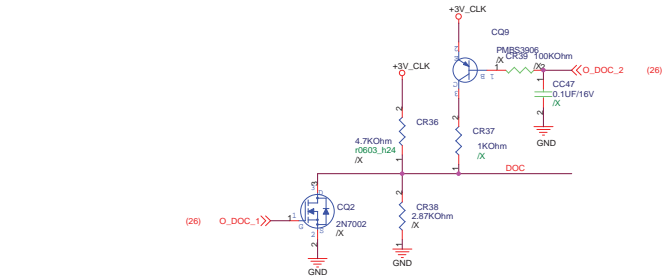
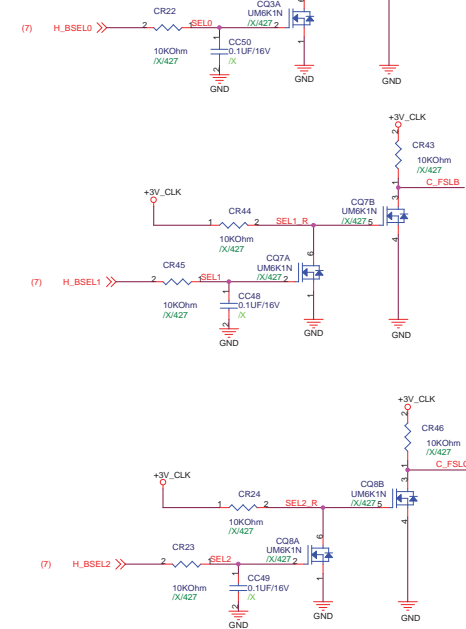
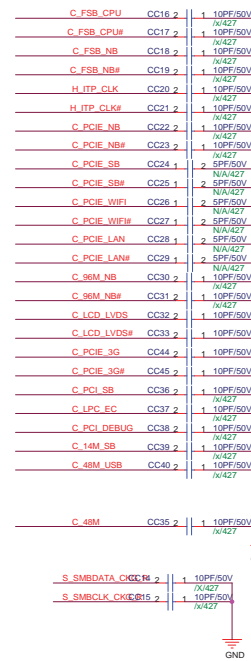
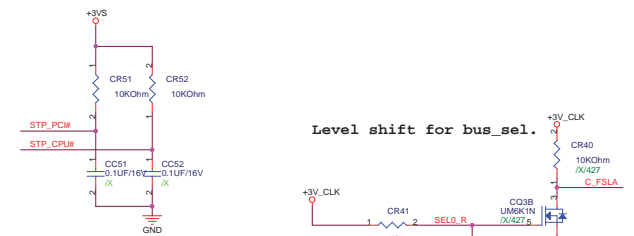
Use 12G025C22004 H=9.2mm



<http://hobi-elektronika.net>

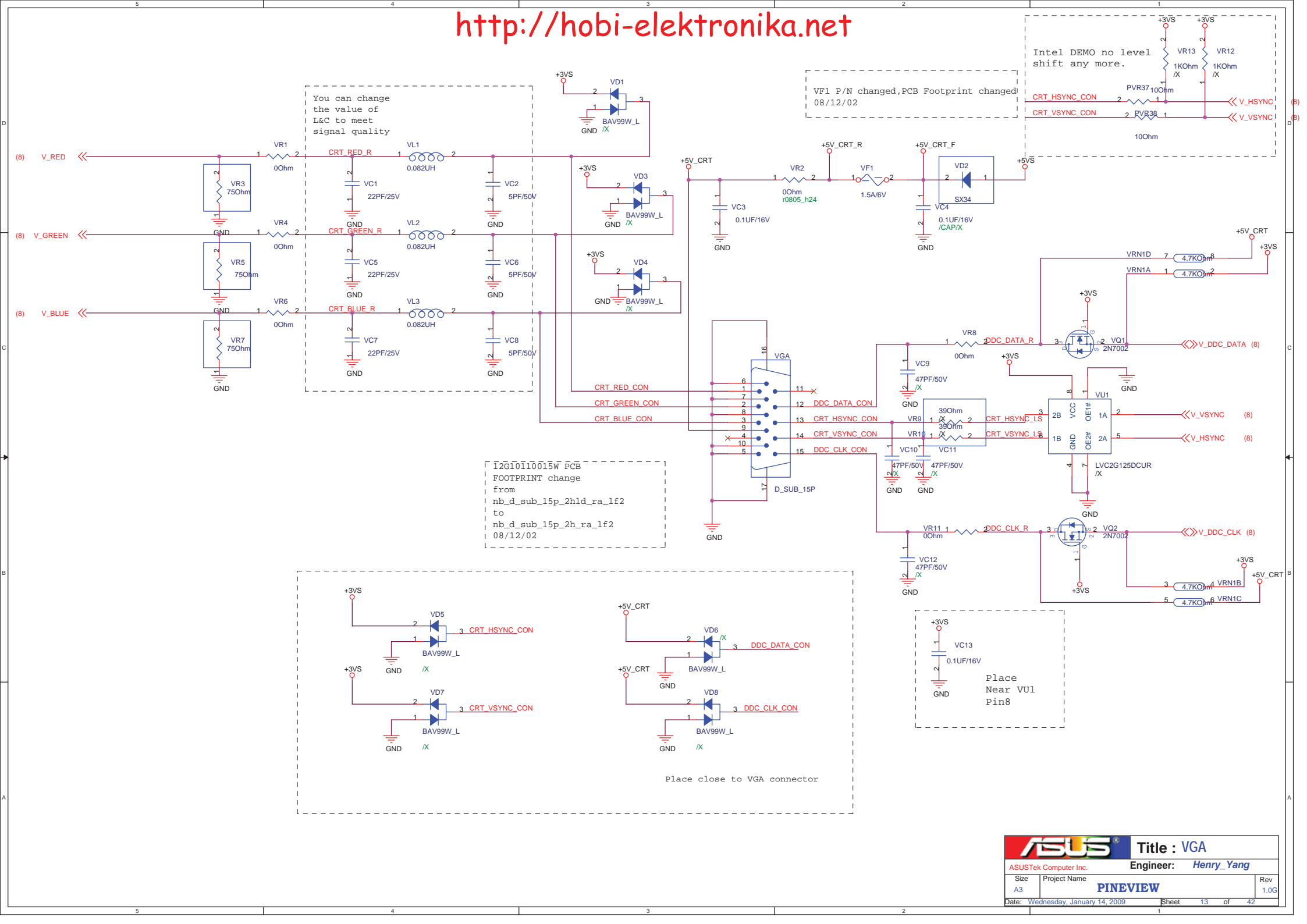
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



FSLC	FSLB	FSLA	CPU(MHZ)
0	0	1	133.33
1	0	1	100
0	1	1	166MHZ

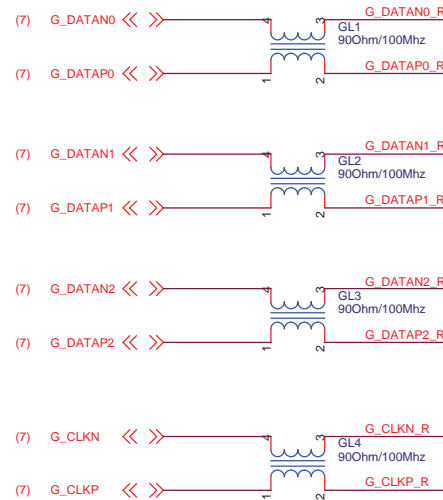
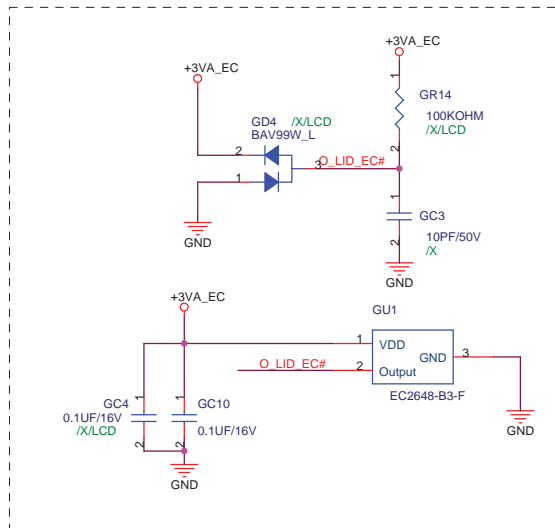
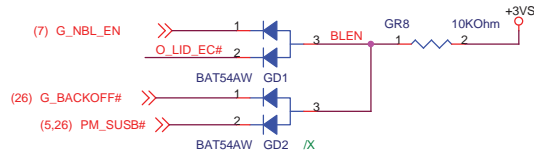
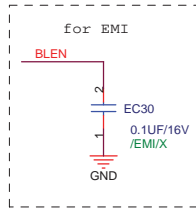
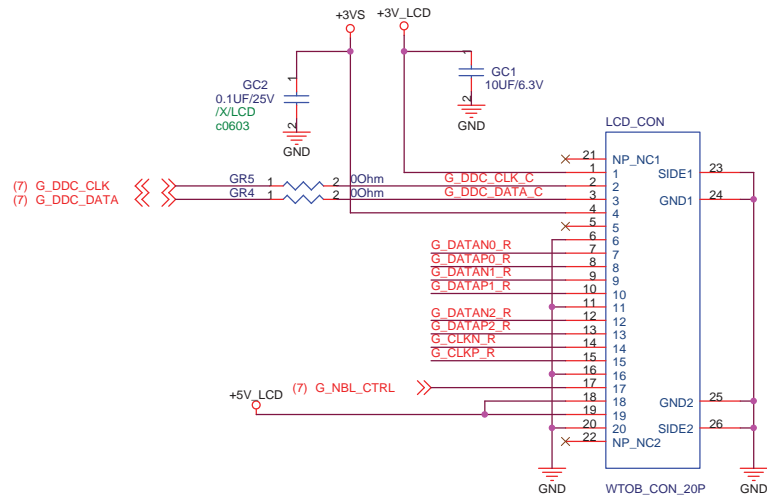
BCLK Select.



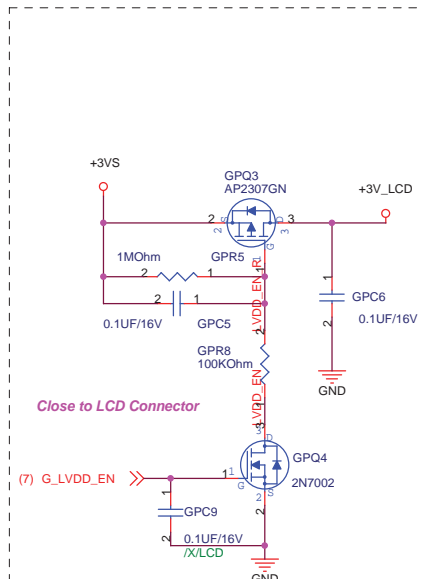
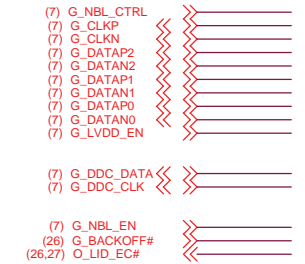
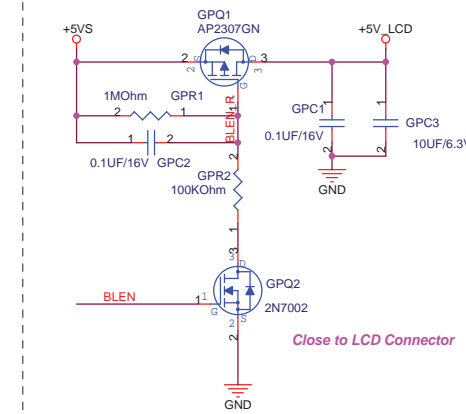
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



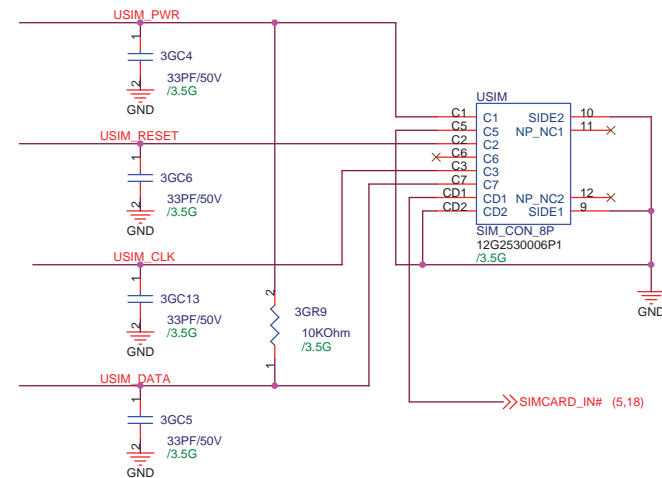
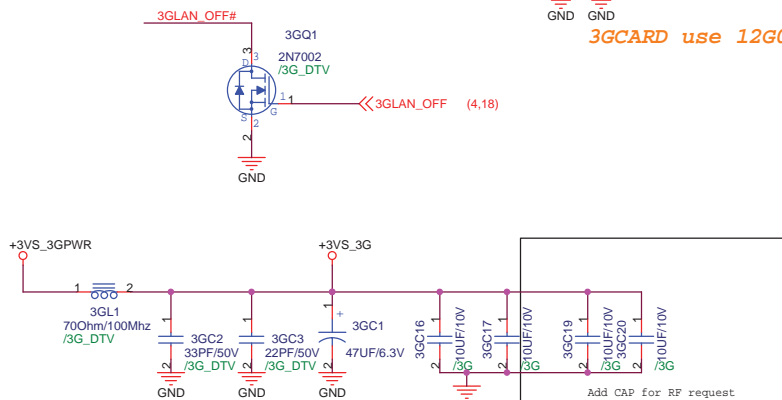
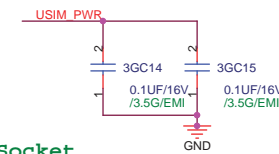
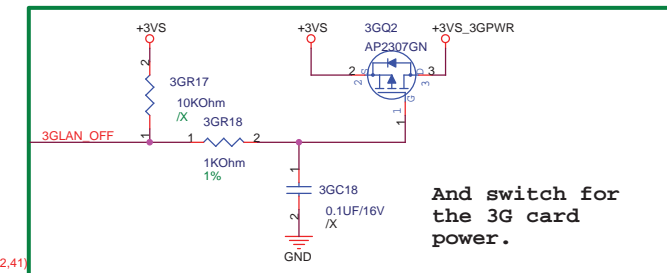
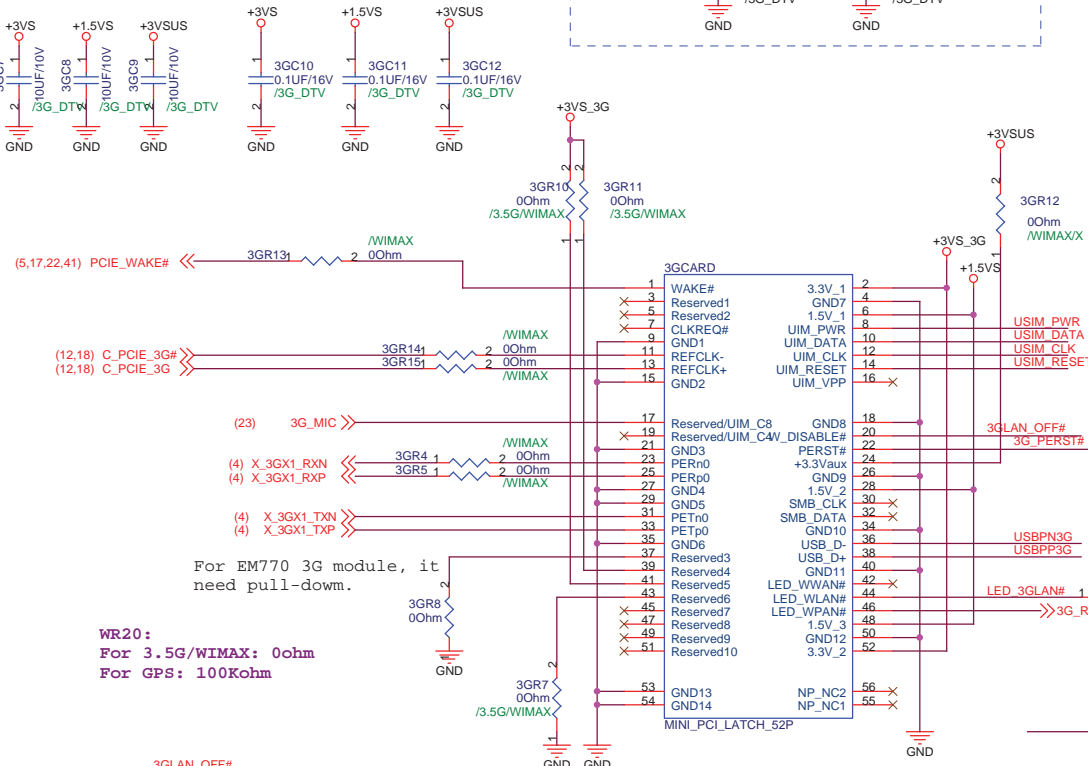
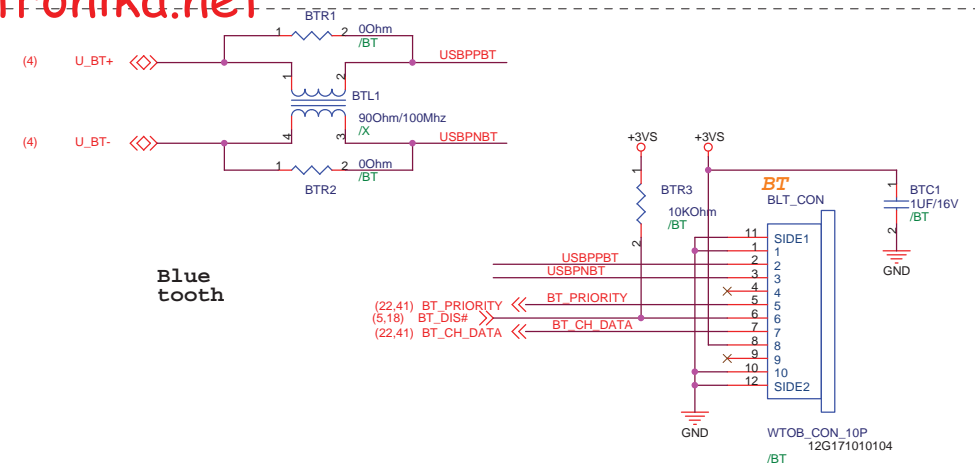
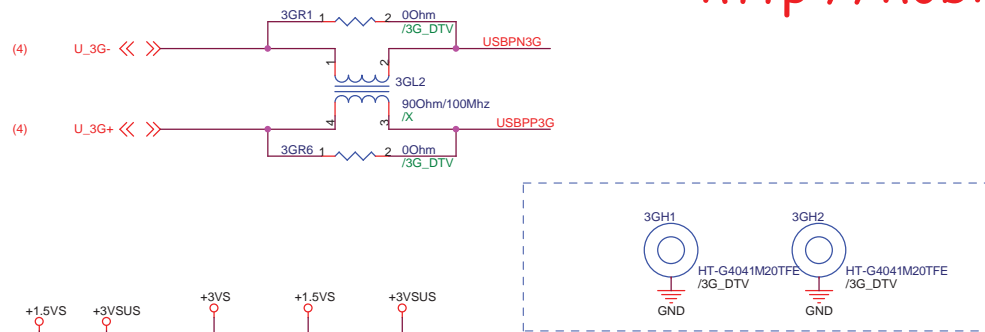
Add for RF request.



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

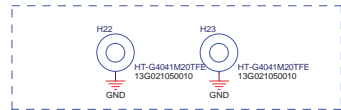
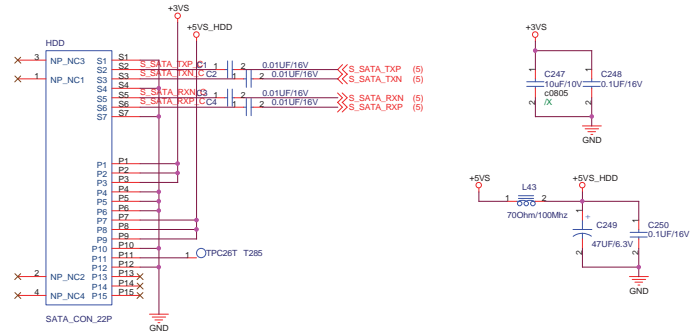


<http://hobi-elektronika.net>

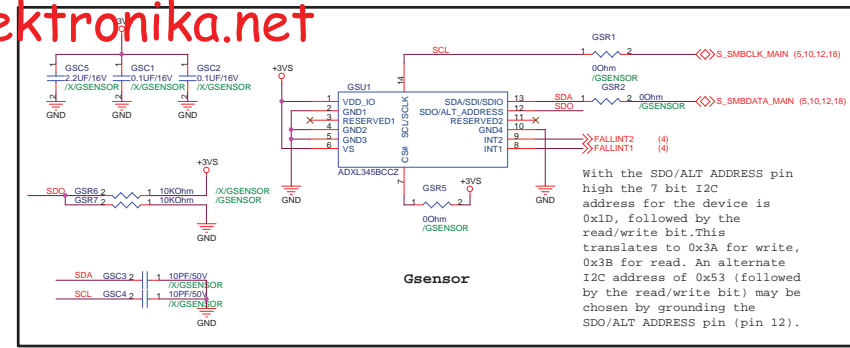
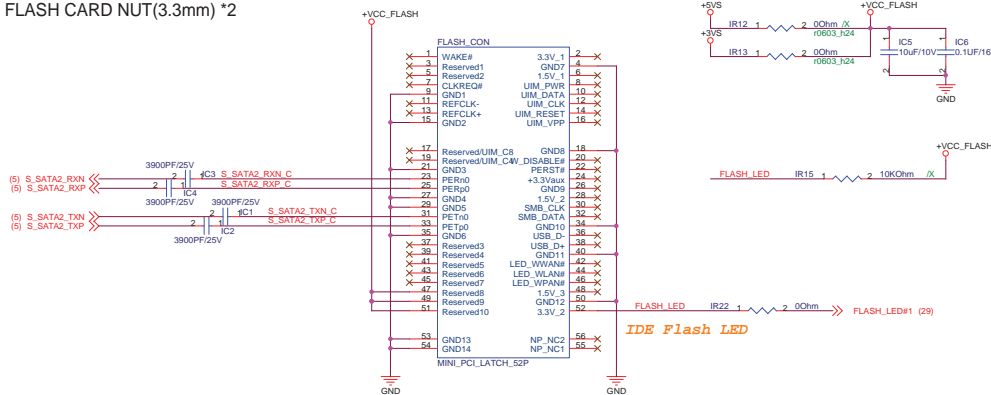
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

SATA HDD Connector

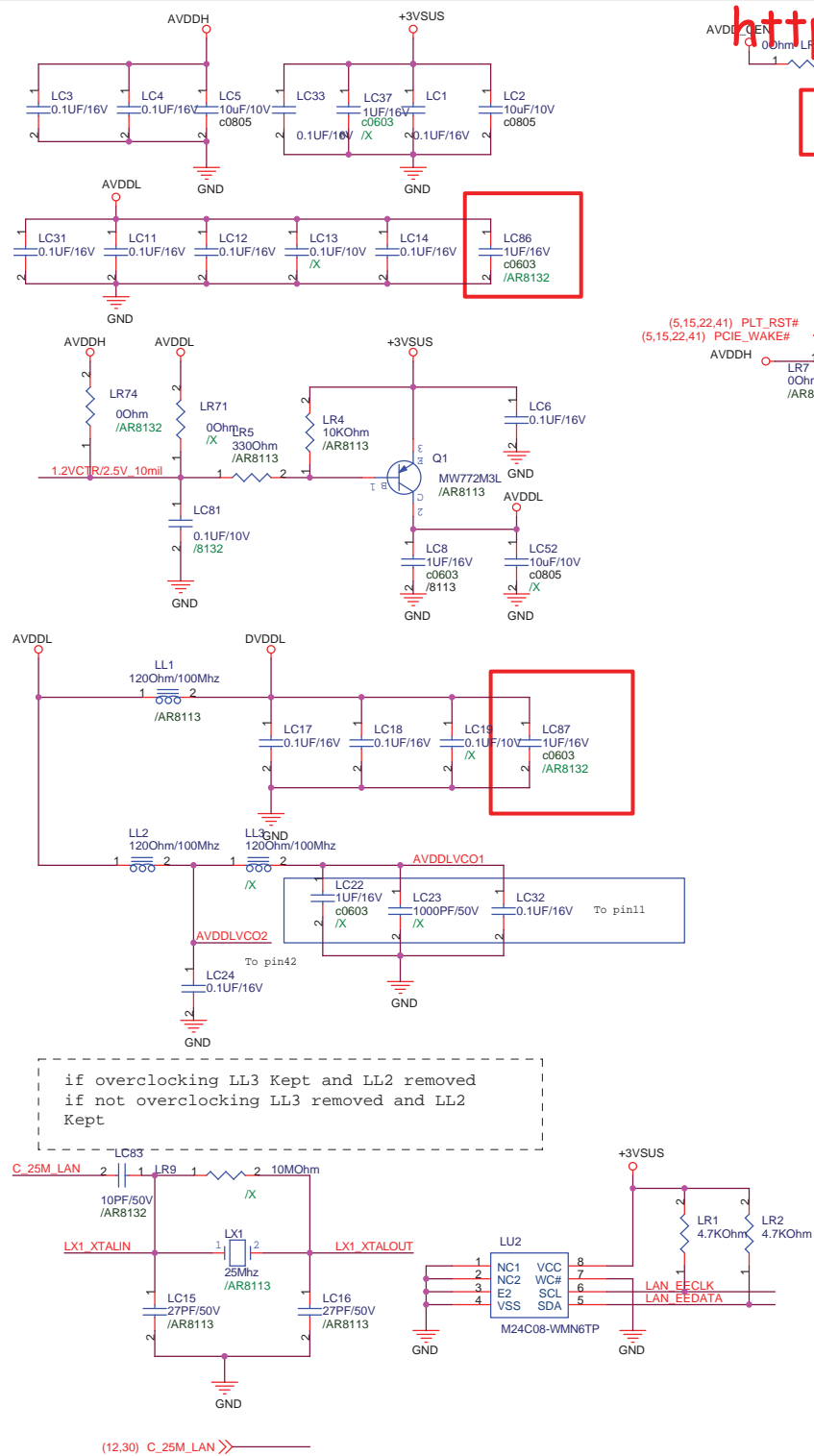


FLASH CARD NUT(3.3mm) *2



With the SDO/ALT ADDRESS pin high the 7 bit I2C address for the device is 0x1D, followed by the read/write bit. This translates to 0x3A for write, 0x3B for read. An alternate I2C address of 0x53 (followed by the read/write bit) may be chosen by grounding the SDO/ALT ADDRESS pin (pin 12).

<Variant Name>



(5,15,22,41) PLT_RST#
(5,15,22,41) PCIE_WAKE#

For
25/48
Trap

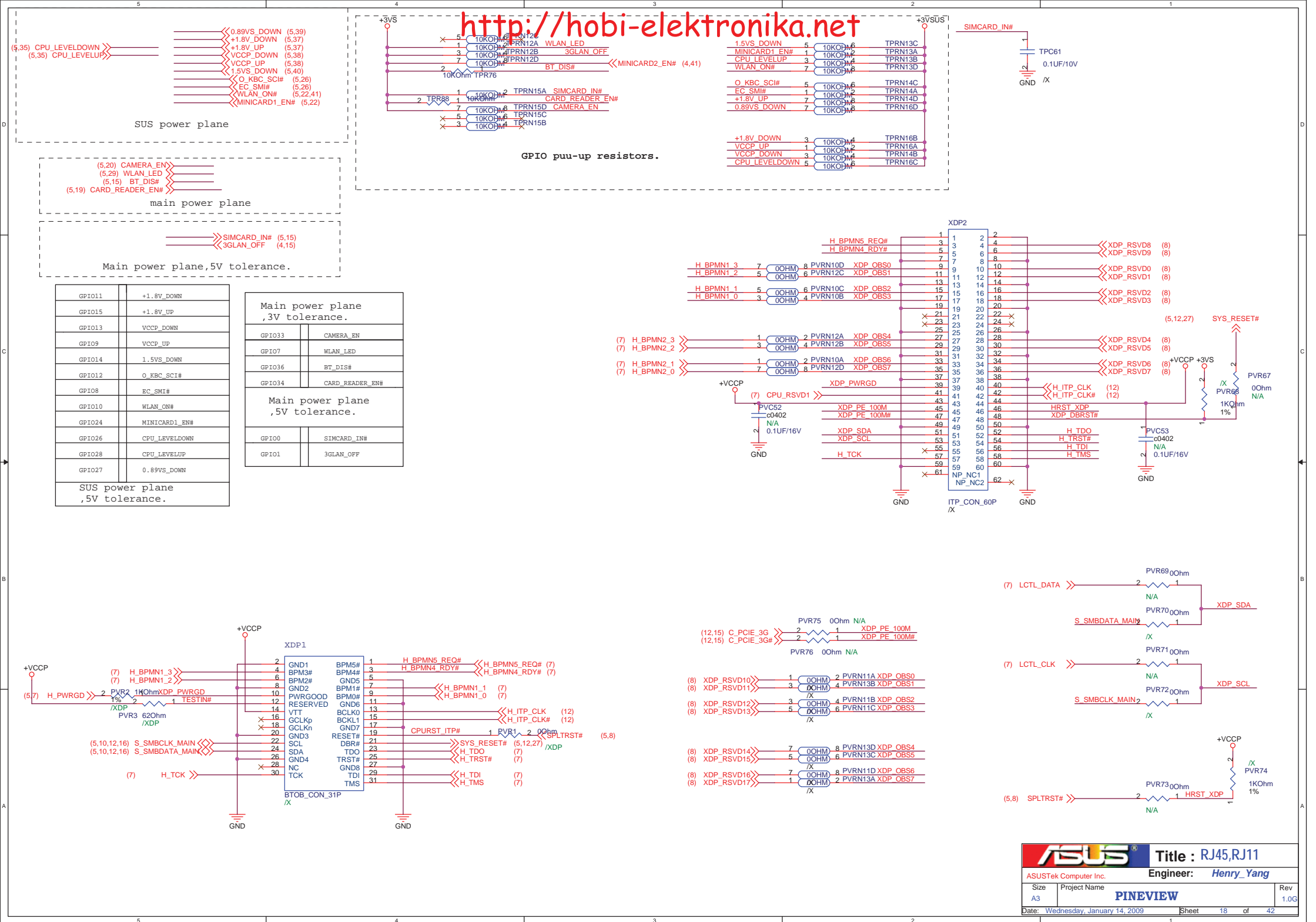
LAN connector: 12G148301086

Close to LU1

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

port work well.

U_CAM+ (4) MR2 1 2 0Ohm M_USBPP1

U_CAM- (4) ML1 90Ohm/100Mhz /X M_USBP1

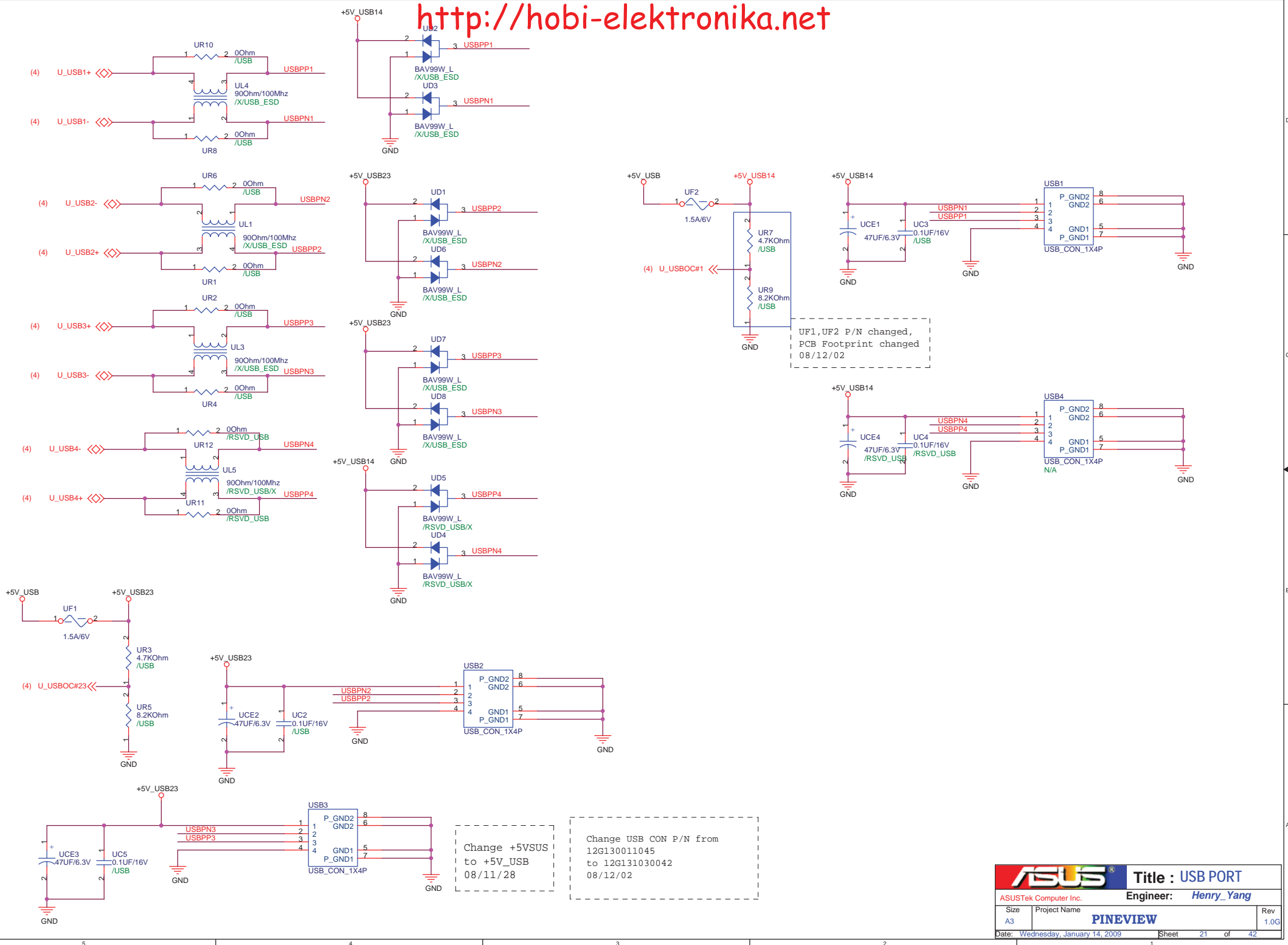
MR1 1 2 0Ohm



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

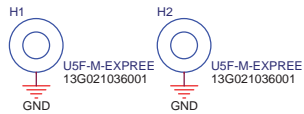
<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

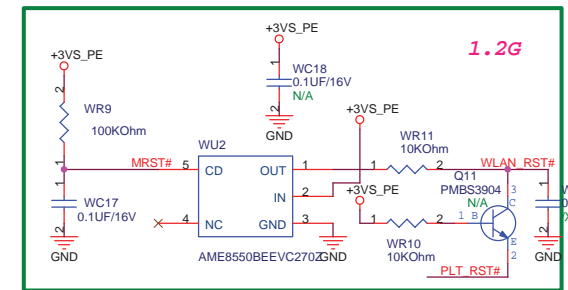
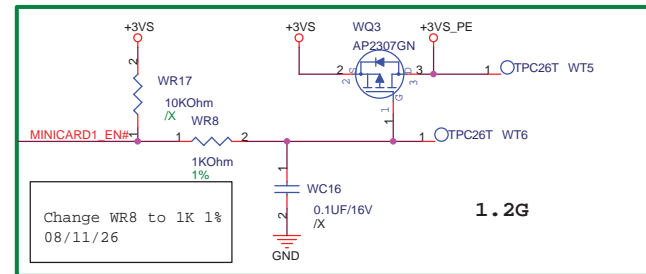
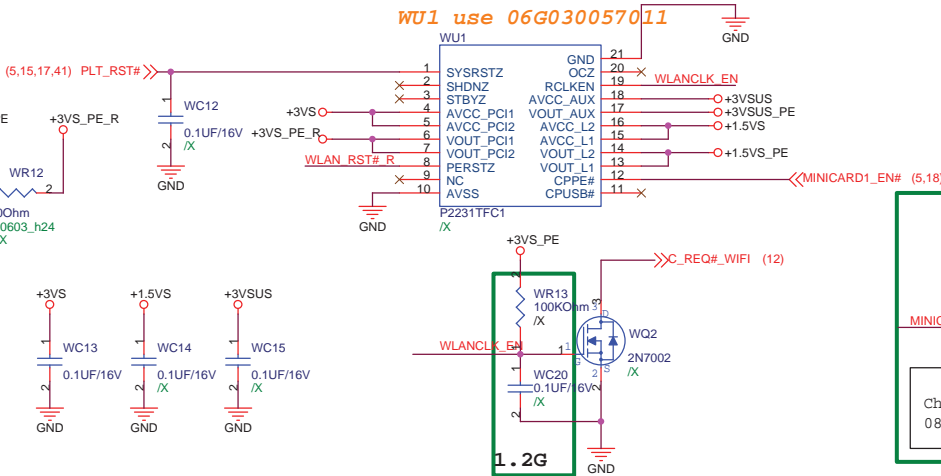
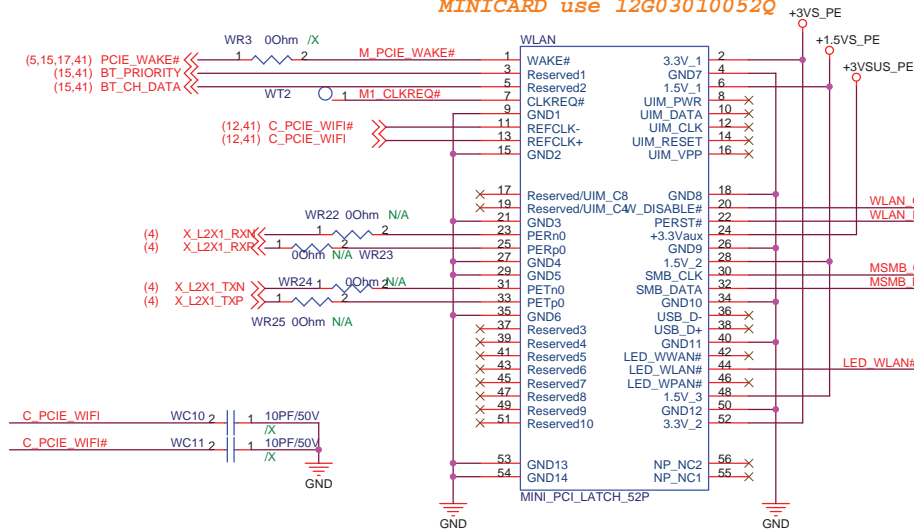
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



MINI CARD NUT(1.6mm) *2

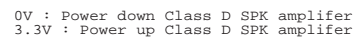
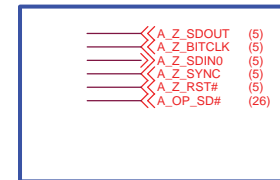
MINICARD use 12G03010052Q



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

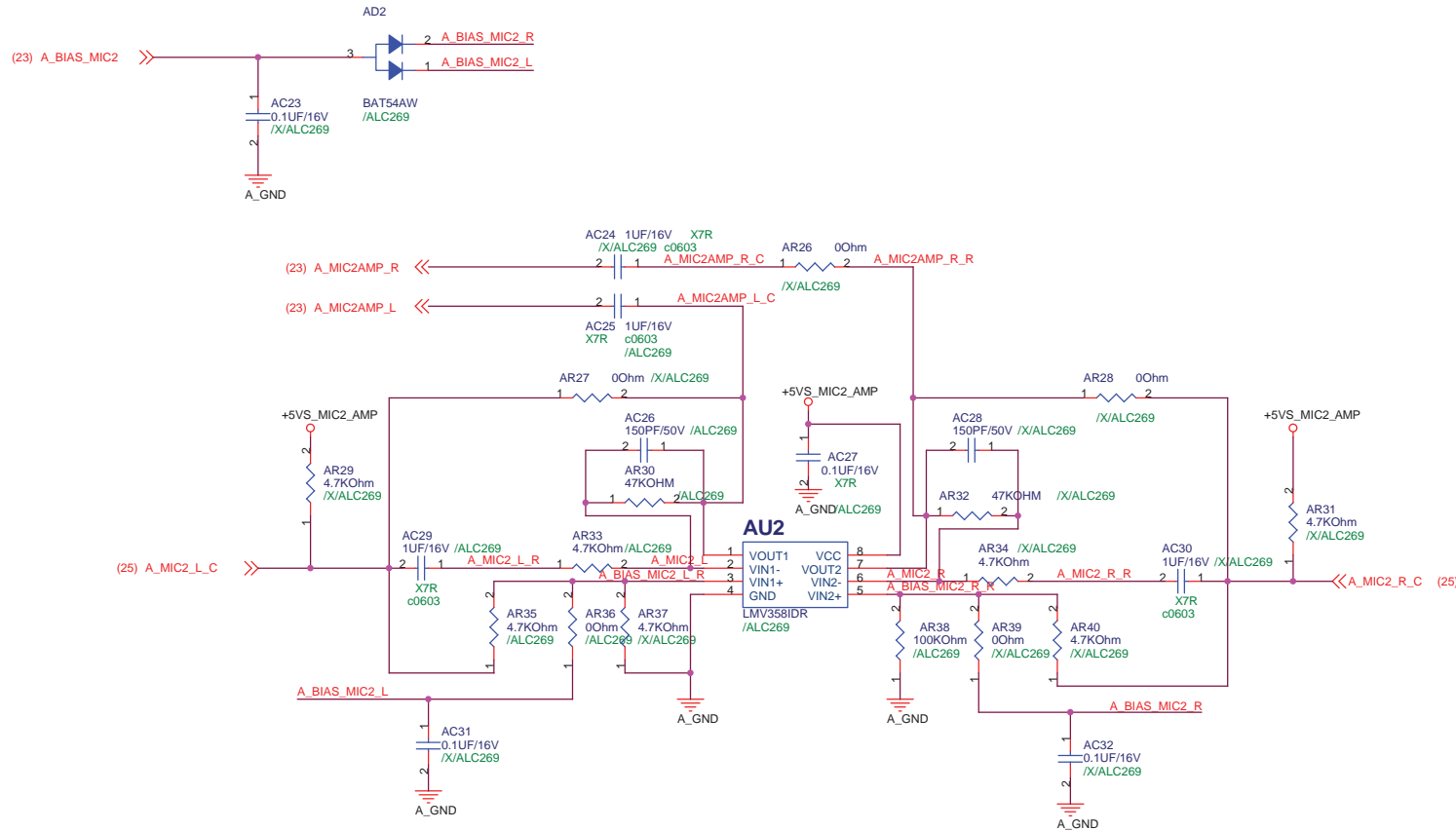
<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

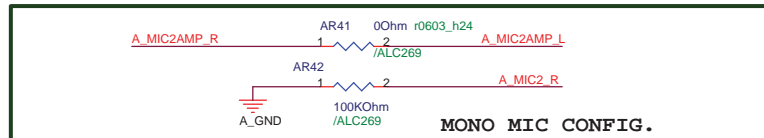
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



Internal MIC Amp.

FL = 33.86kHz, FH = 22.5kHz

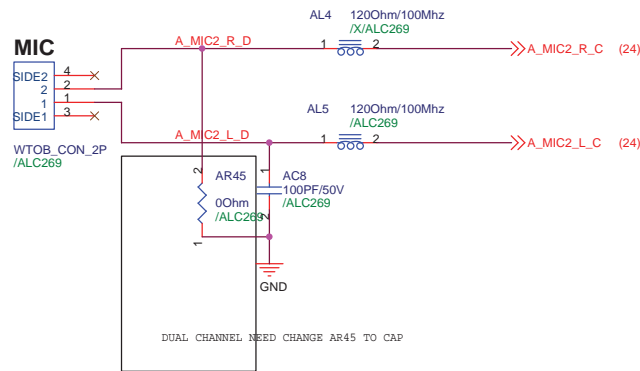


<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

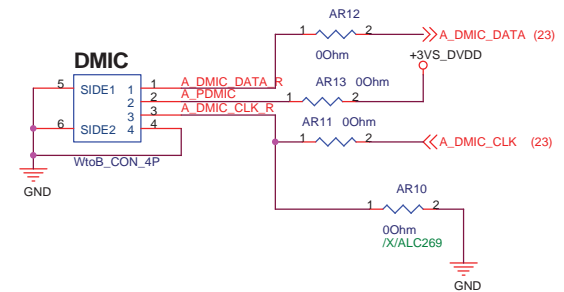
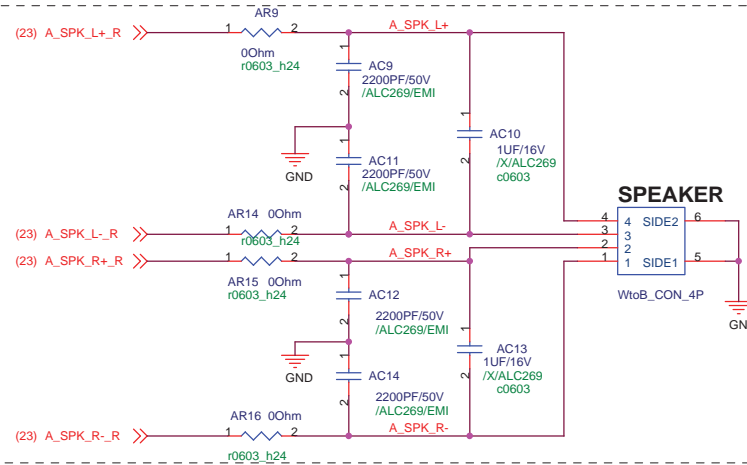
Internal MIC



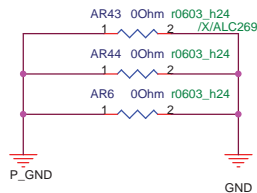
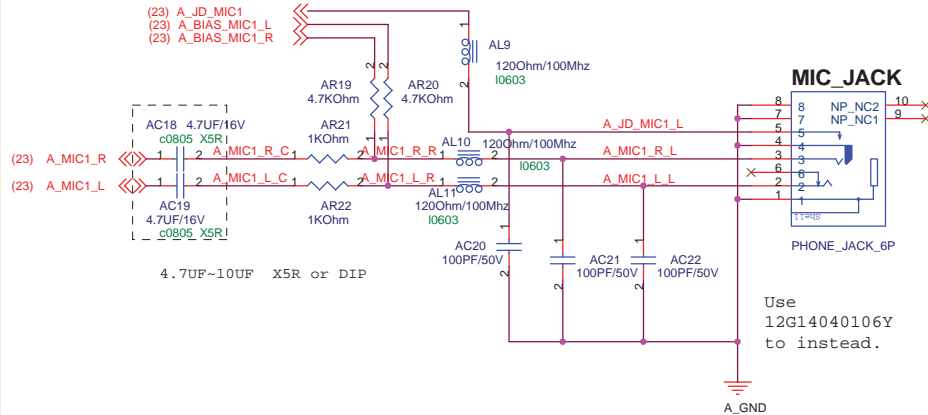
SPEAKER

Demodulation Filter
Placement near
Audio Codec

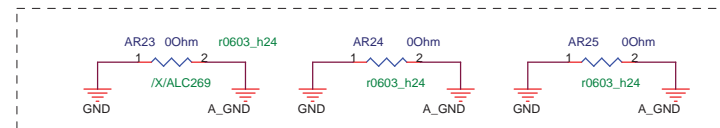
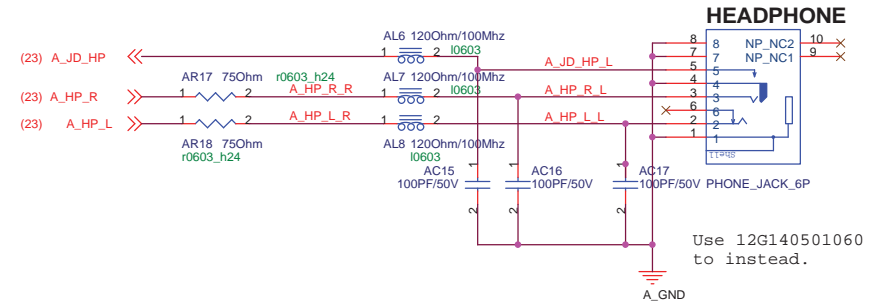
<<Attention>>
you can use LC filter(AR9,AR14,AR15,AR16
mount 8.2uH L ;and mount AC10,AC13) to
eliminate the EMI(please don't use
general beads,because they may influence
the THD+N quality) , AC9/AC11/AC12/AC14
are reserved for EMI fine-tune ; For EMI
issue, All L and C should near to codec



MIC JACK



HEADPHONE



AR24, AR25 can use
0.1uF 11G233310432320
for EMI

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

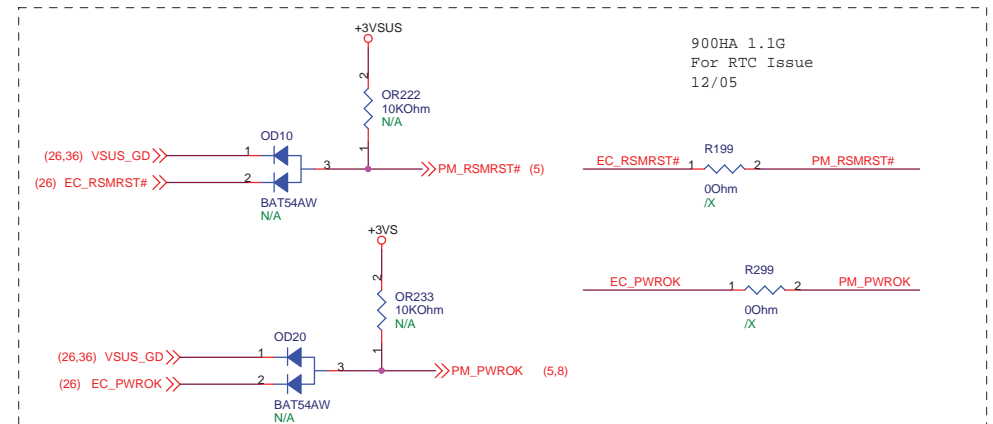
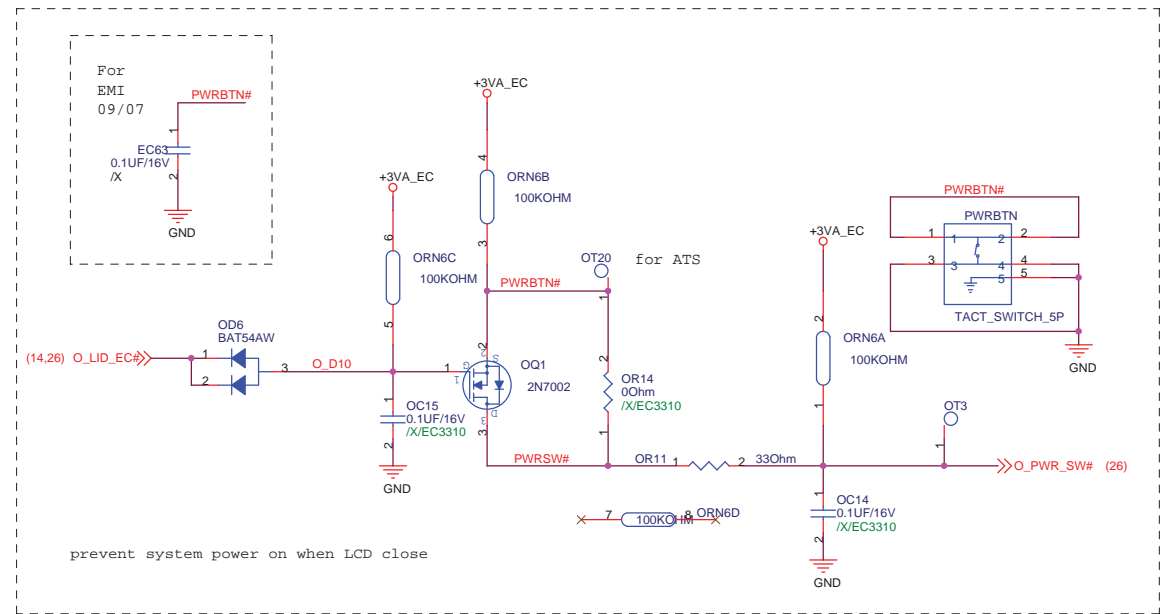
<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

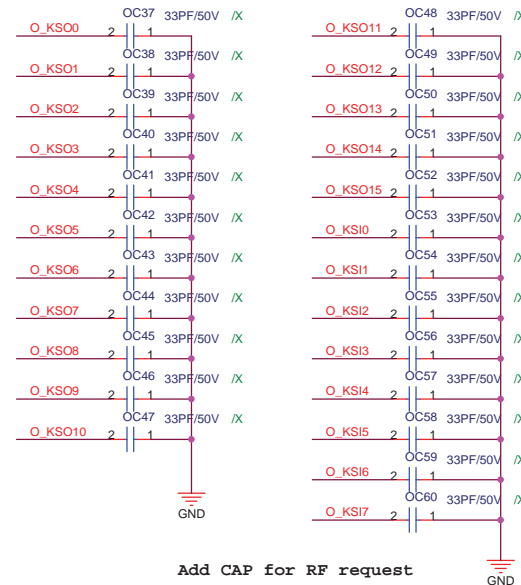
<http://hobi-elektronika.net>



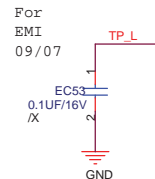
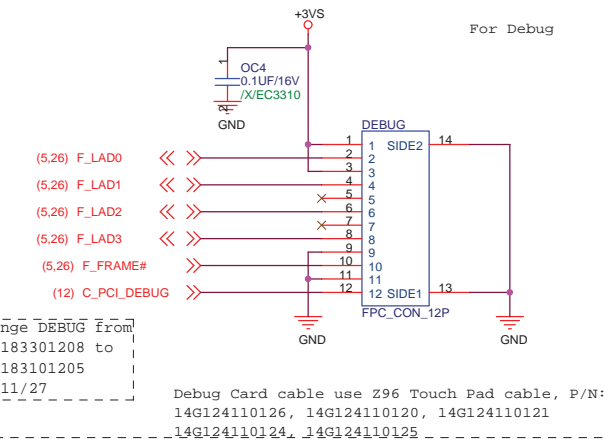
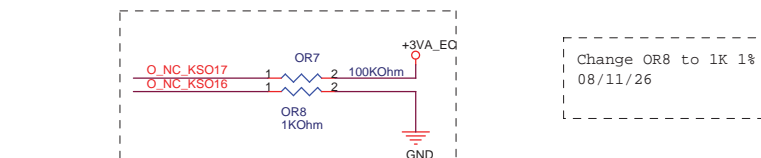
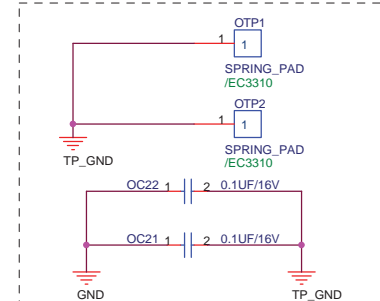
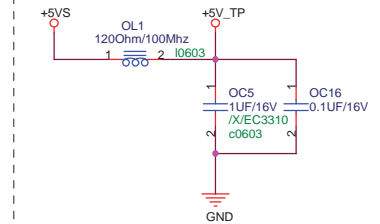
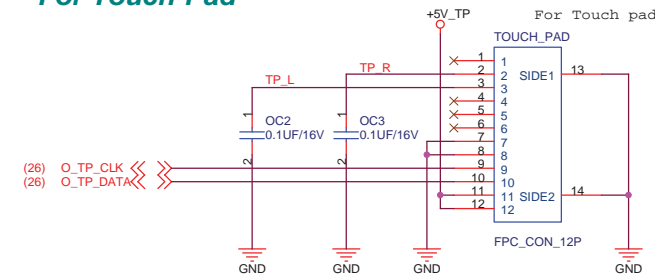
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

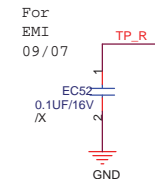
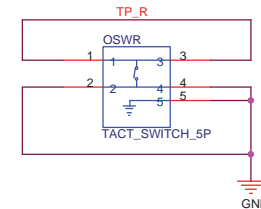
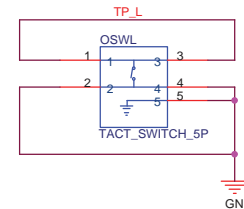
<http://hobi-elektronika.net>



Add CAP for RF request



```
OSW1, OSW2 use
12G09103305N
```



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

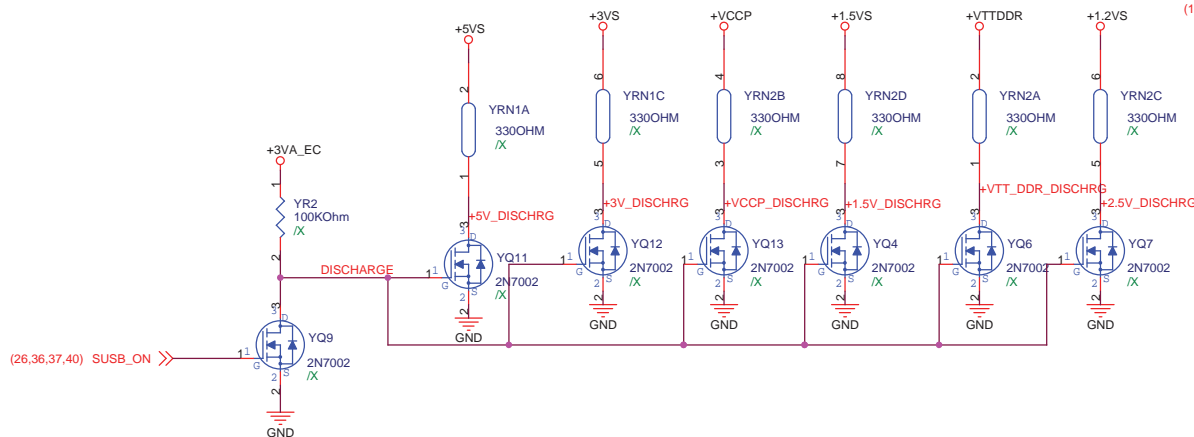
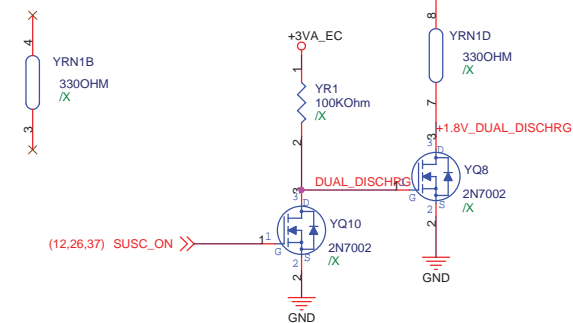
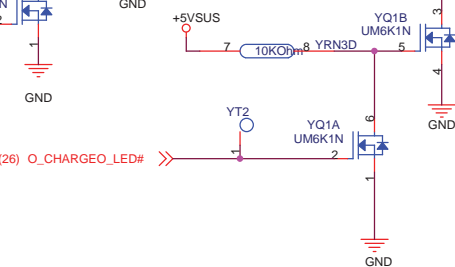
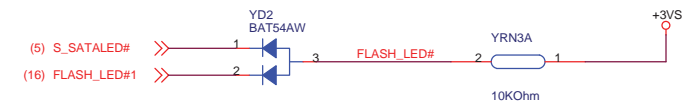
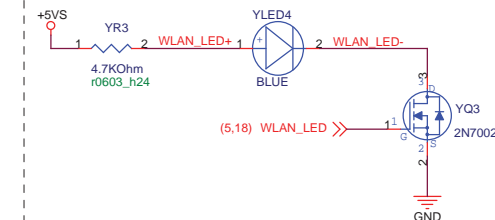
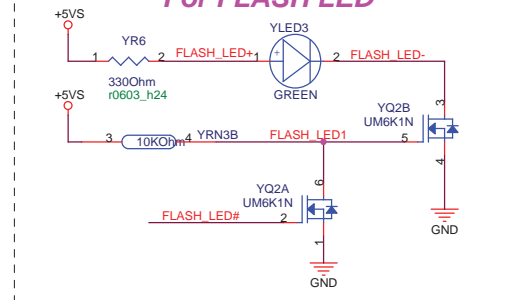
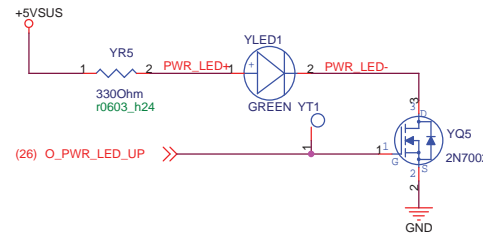
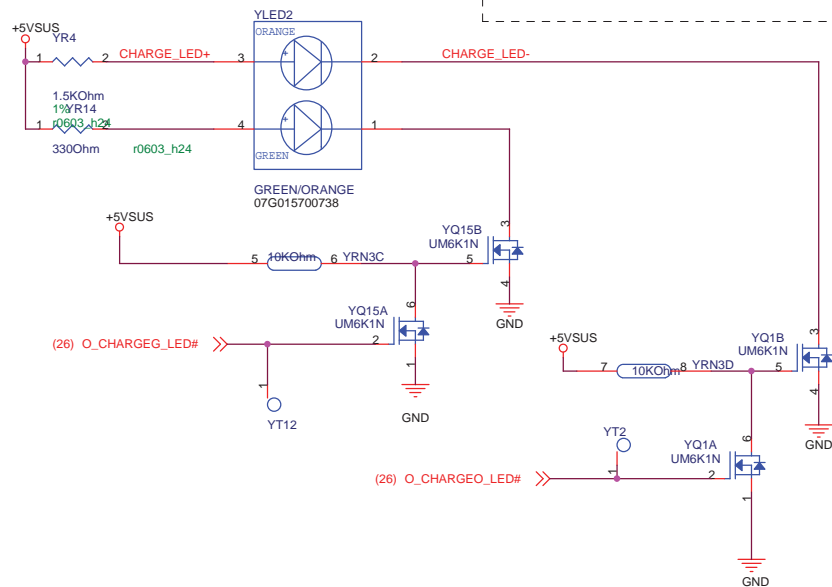
For CHARGE LED

For POWER LED

For FLASH LED

For WLAN LED

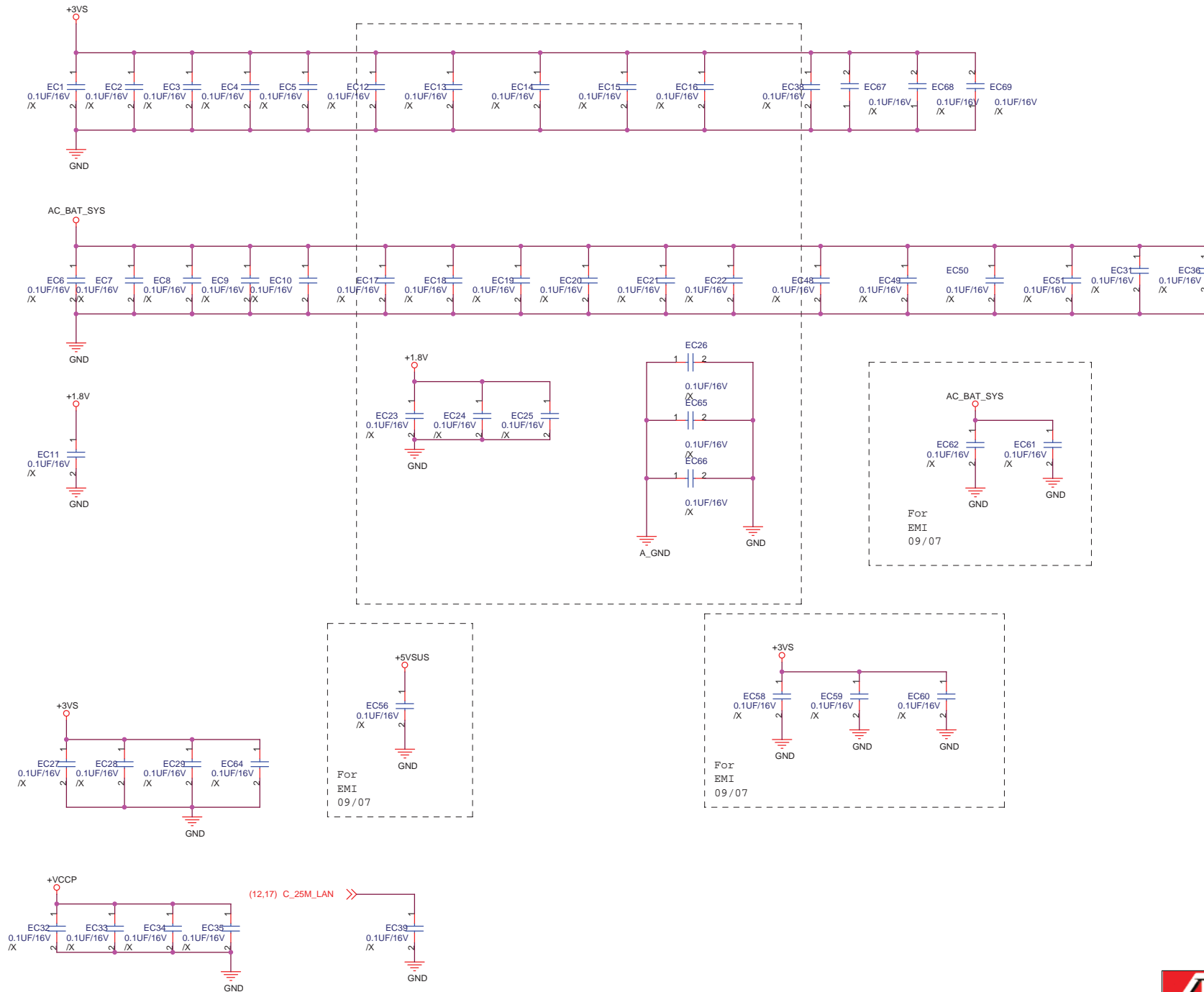
YR3 use 4.7K OHm 10G213472003030



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

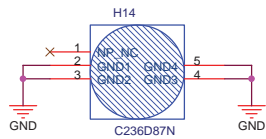
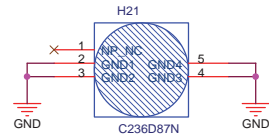
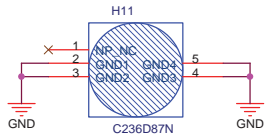
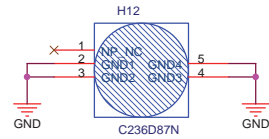
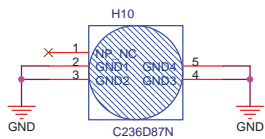
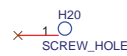
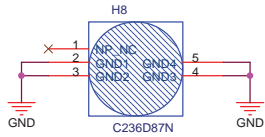
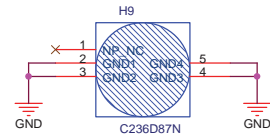
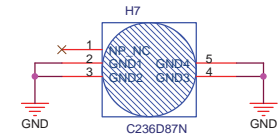
<http://hobi-elektronika.net>



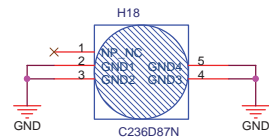
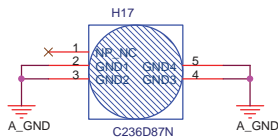
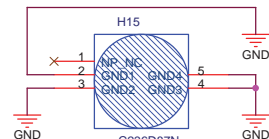
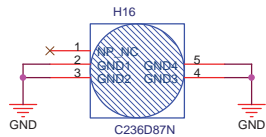
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



Due to 900PV cut a piece of PCB for bluetooth, so we has removed the H13.

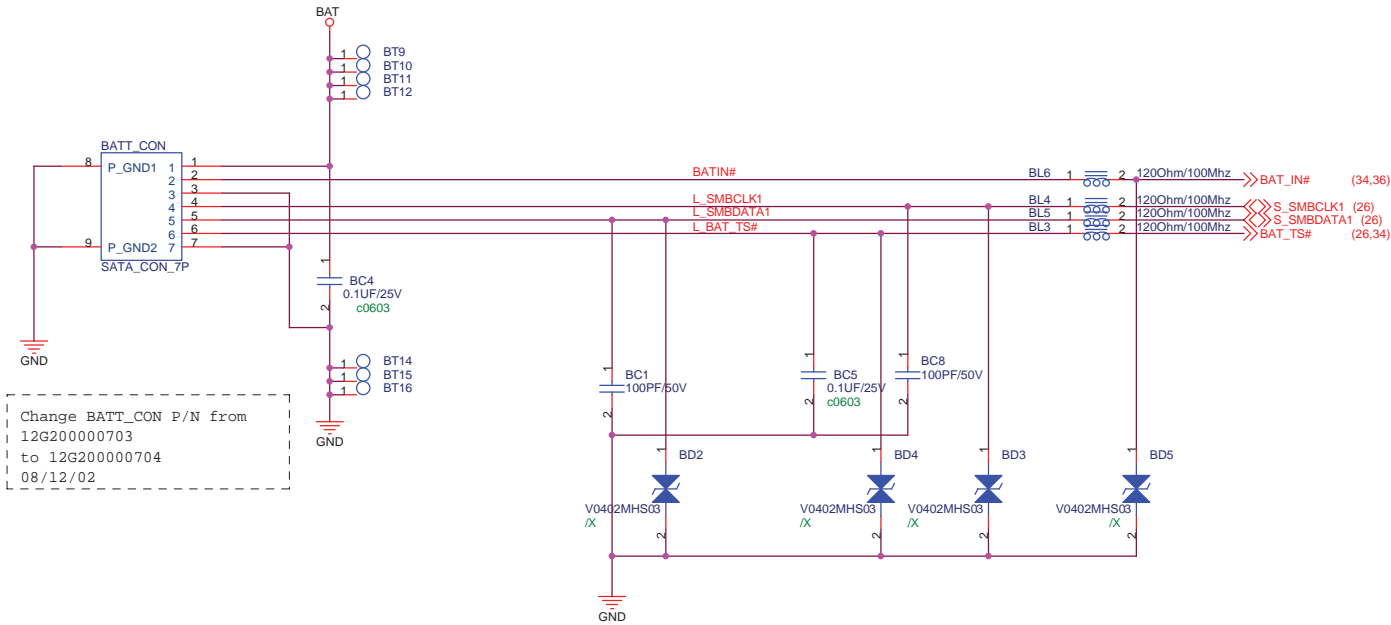
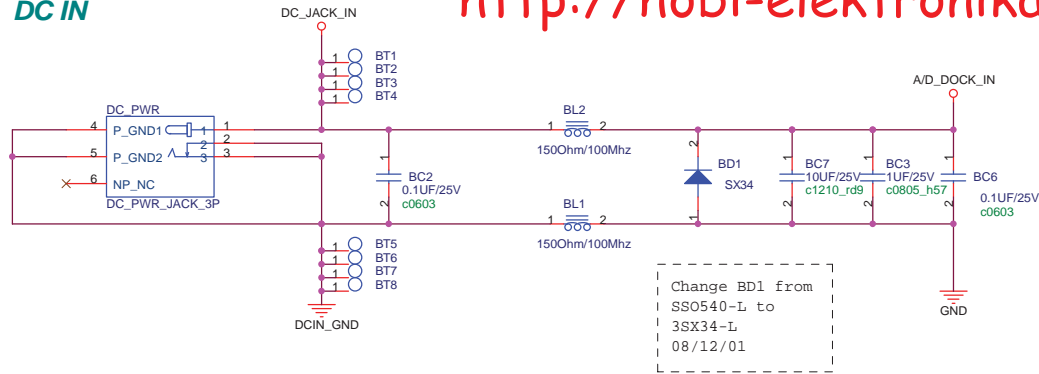


<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

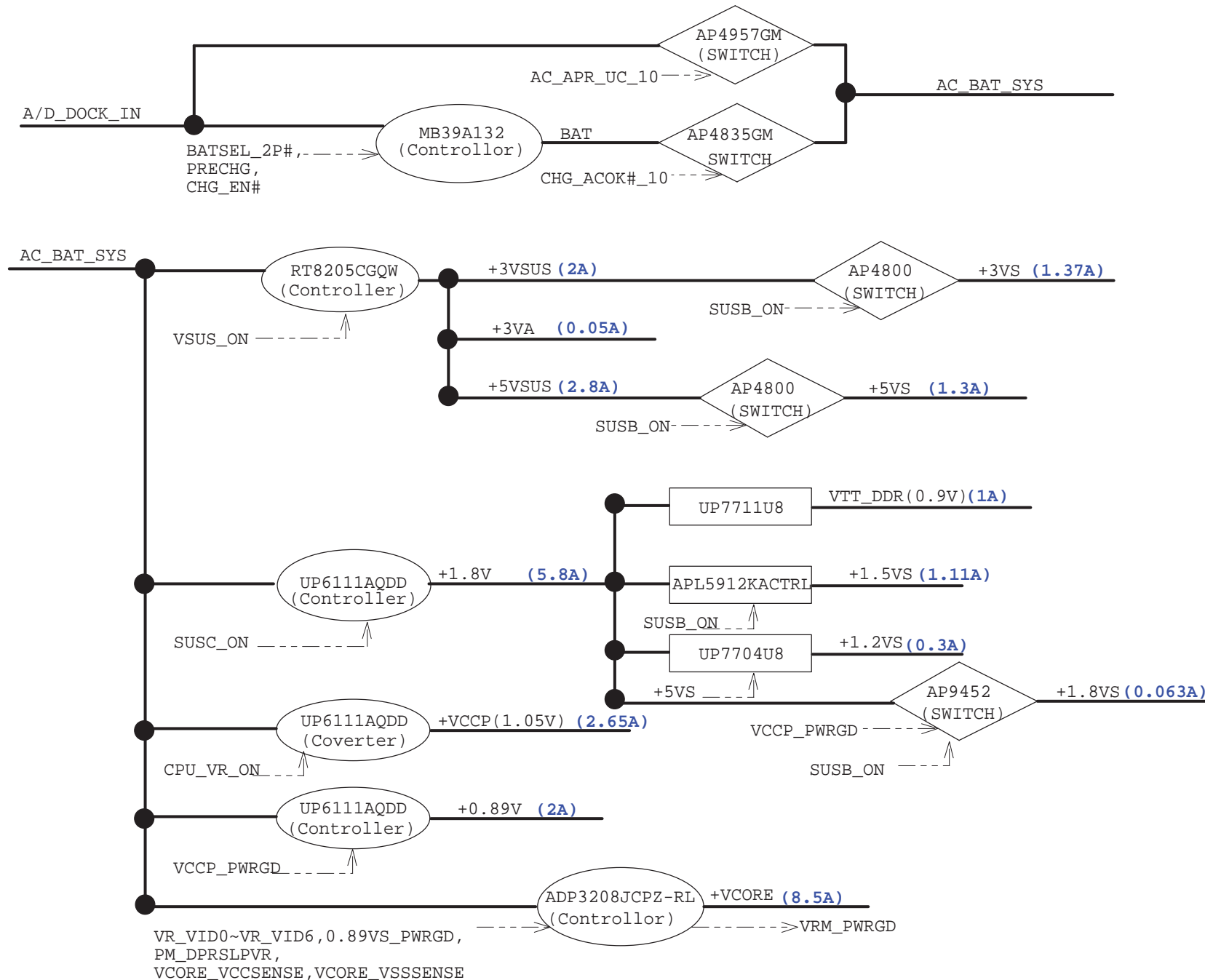
DC IN



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

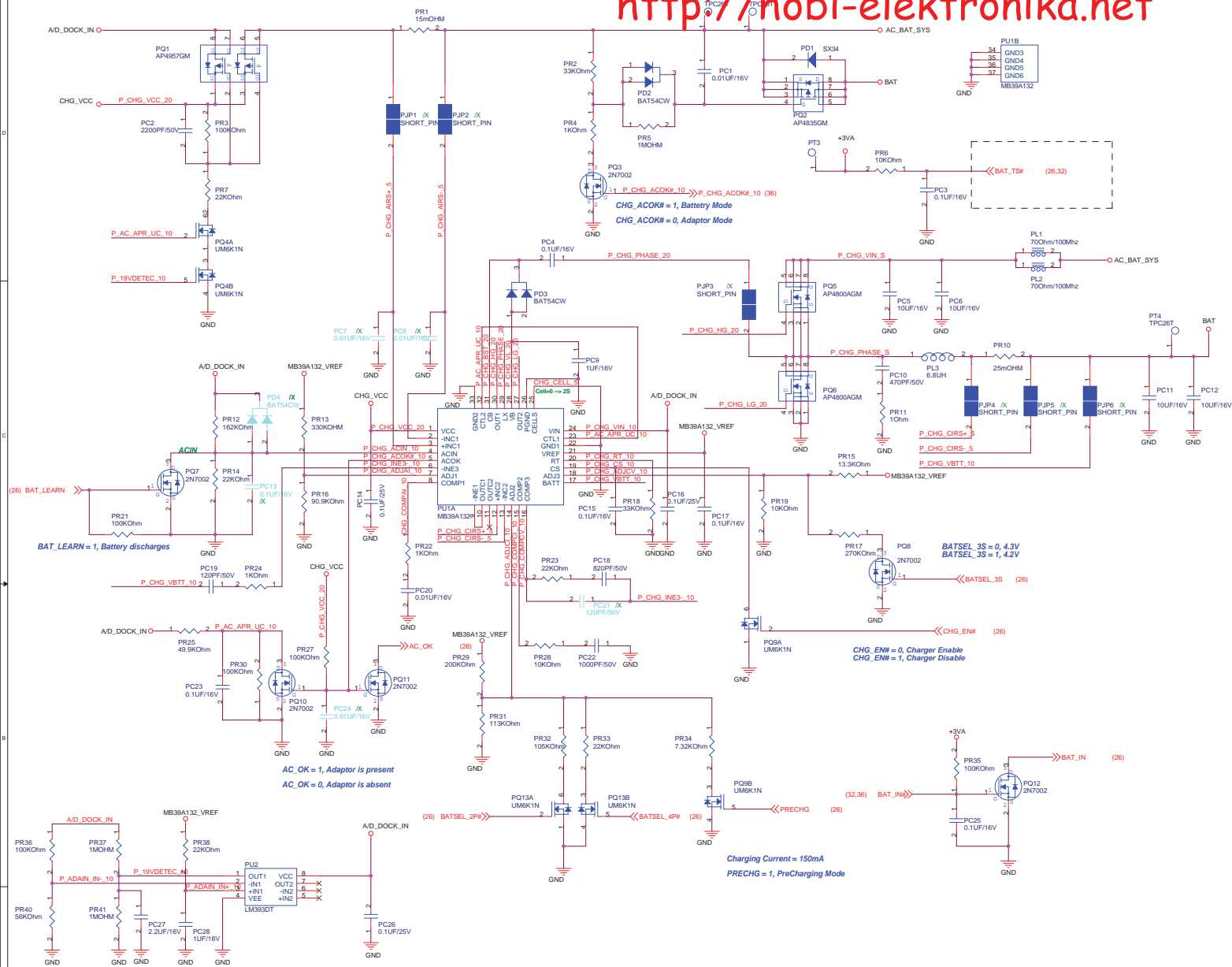
<http://hobi-elektronika.net>



<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



Battery Charging Voltage :

$V_{adj3} > 4.1V \implies V_{bat} = 4.2V / cell$
 $2.2V > V_{adj3} > 1.1V \implies V_{bat} = 2 \cdot V_{adj3} / cell$

Battery Charging Current :

$4.4V > V_{adj2} > 0V \implies I_{chg} = (V_{adj2} - 0.075V) / (25 \cdot R_s)$

Input Adaptor Max. Current Limit :

$I_{limit_current} = (V_{adj1} - 0.075V) / (25 \cdot R_s)$

Pre-Charging Mode :

Precharging current = 146mA
 $V_{adj2} = 166mV$

Adaptor Max. Current :

$PR13=330KOHM$; $PR16=90.9KOHM \implies I_{limit} = 2.679A$

ACIN Threshold = 1.25V

Adaptor >10.45V, System Powered by Adaptor

Adaptor <10.45V, System Powered by Battery

Prevent Input from 19V :

Adaptor > 13.92V, PQ4B Turn-off

Adaptor < 13.92V, PQ4B Turn-on

Battery Cell Selection :

CELLS:VREF,4-Cell;

CELLS:OPEN,3-Cell;

CELLS:GND,2-Cell;

VREF = 5.0V

$f_{osc}(KHz) = 17000 / RT$
 (KOhm)
 =515KHz

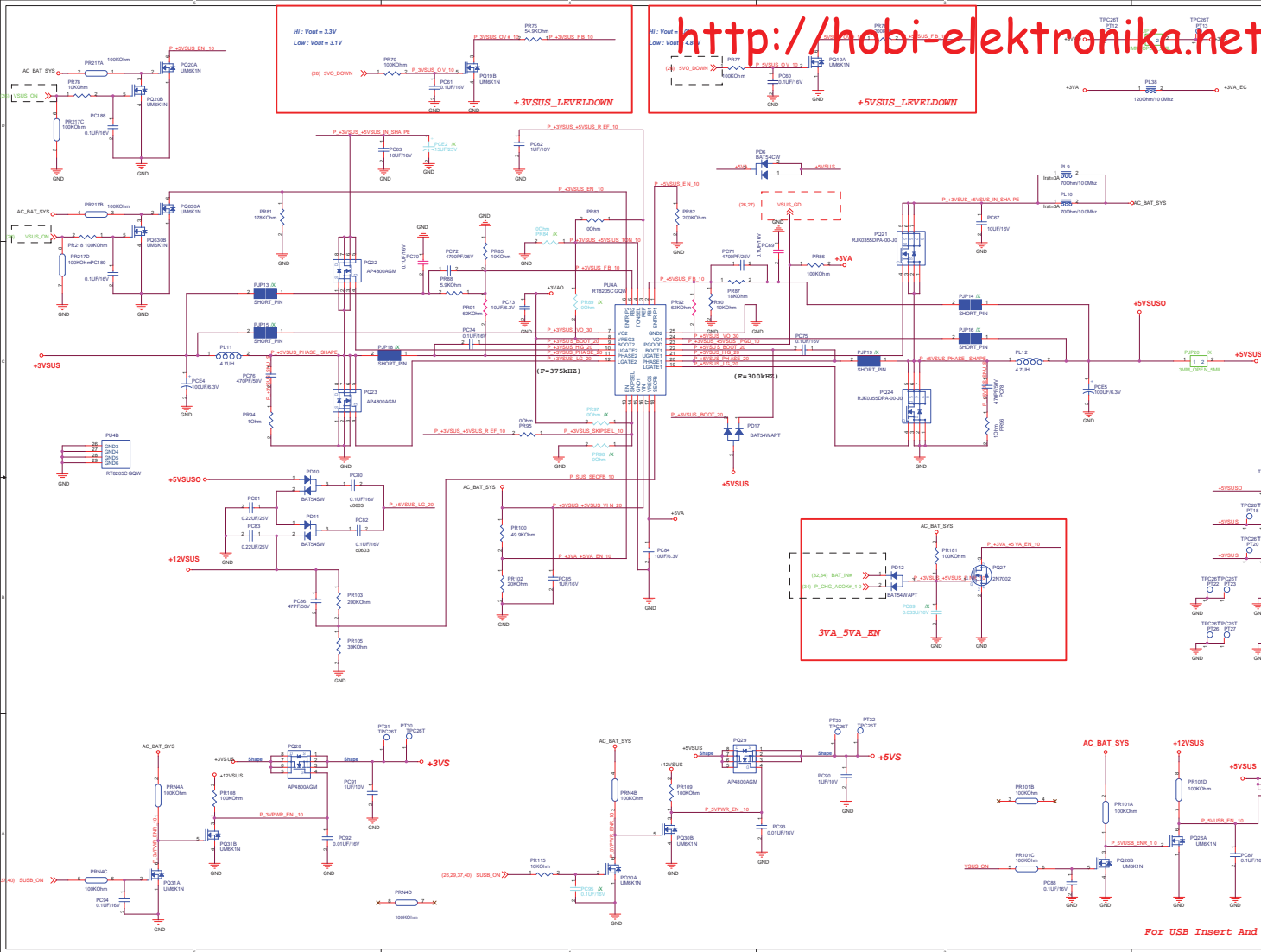
Soft start:

$t_s(s) = 0.23 \cdot CS (uF)$
 =23mS

Charging Current :

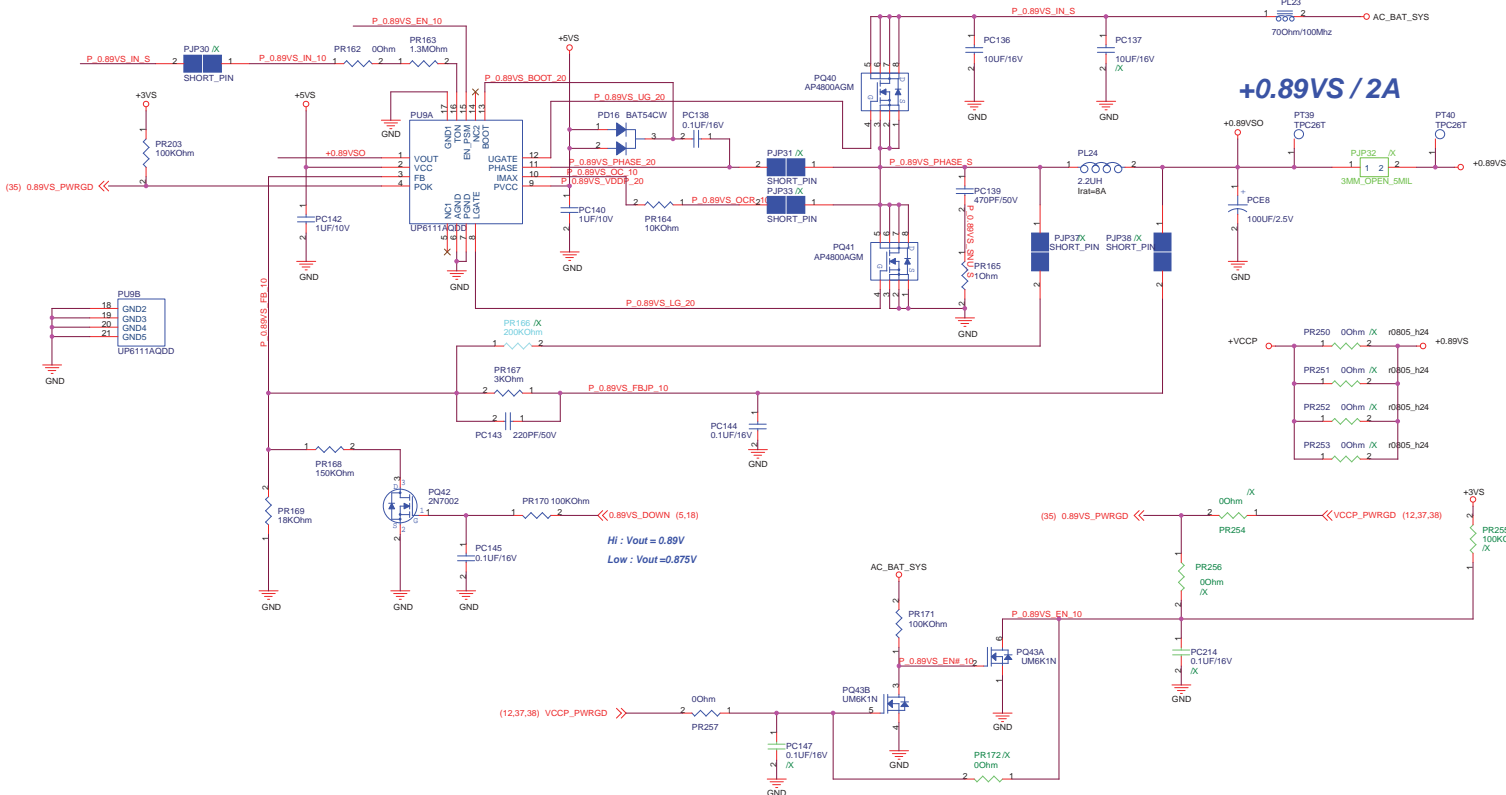
4P#	2P#	I _{charge}
1	0	0.56A
0	1	1.6A
0	0	2.8A





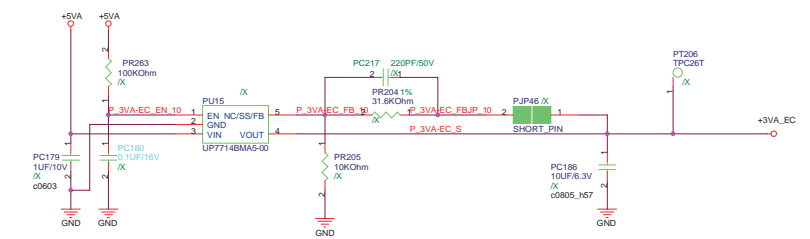
Power stage	+3VSUS	Power stage	+5VSUS
1. I/P Current:	1 in = Vo*Io/(0.75 * Vin) =1.47A	1. I/P Current:	1 in = Vo*Io/(0.75 * Vin) =2.33A
2. Ripple Current:	1 rip =0.99A I spec=2.5A x1 pcs	2. Ripple Current:	1 rip =1.04A I spec=2.5A x1 pcs
3. Dynamic:	1 peak=2A ESR / 1 pcs =25 mohm ΔV =50mV	3. Dynamic:	1 peak=2.8A ESR / 1 pcs =25 mohm ΔV =70mV
4. Inductor Spec:	1 sat=10 A I dc =5.5 A DCR=37 mohm	4. Inductor Spec:	1 sat=10 A I dc =5.5 A DCR=37 mohm
5. MOSFET Spec:	H-side MOSFET: AP4800AGM Rds(ON)= 21 mohm (Vgs=4.5 V) I cont = 9.6 A (T =25 °C) I peak = 40 A (Pause < us) L-side MOSFET: RAP4800AGM Rds(ON)= 21 mohm (Vgs=4.5 V) I cont = 9.6 A (T =25 °C) I peak = 40 A (Pause < us)	5. MOSFET Spec:	H-side MOSFET: RJK0355DPA-00-J0 WPAK Rds(ON)= 11.8 mohm (Vgs=4.5 V) I cont = 30 A (T =25 °C) I peak = 120 A (Pause <10 us) L-side MOSFET: RJK0355DPA-00-J0 WPAK Rds(ON)= 11.8 mohm (Vgs=4.5 V) I cont = 30 A (T =25 °C) I peak = 120 A (Pause <10 us)
Controller	+3VSUS	Controller	+5VSUS
1. Voltage & Current:	+3VSUS=3.3V@2A	1. Voltage & Current:	+5VSUS=5V@2.8A
2. Frequency:	fosc=375KHz	2. Frequency:	fosc=300KHz
3. OCP:	Set PR81=178Kohm Iocp=8.5A	3. OCP:	Set PR82=200Kohm Iocp=16.9A
4. POR:	V on =2.5V	4. POR:	V on =4.35-4.5 V V off =3.9-4.25 V
5. UVP:	V uvp= 70% Vout	5. UVP:	V uvp= 70% Vout
6. OVP:	V ovp=115%Vout	6. OVP:	V ovp=115%Vout
7. Enable Voltage:	V rising = 1V V falling = 0.4 V	7. Enable Voltage:	V rising = 1V V falling = 0.4 V
8. Soft start time:	Tss=2ms	8. Soft start time:	Tss=2ms
9. Phase selection:	/X	9. Phase selection:	/X
10. Inrush Current:	C total = 100 uF I inrush= 0.165 A	10. Inrush Current:	C total = 100 uF I inrush= 0.25 A

For USB Insert And Remove Protection



+0.89VS / 2A

+3VA_AEC / 100mA



+3VA_AEC / 100mA

- | | |
|--|--|
| 1. Dropout Voltage:
$\Delta V = 0.21V$ ($I_o = 0.3A$)
2.OCP:
$I_{ocp} = 480mA$
3. Short Circuit Current Limit:
$I_{sc} = 320mA$
4. Power Dissipation:
$R_{thjc} = 250^{\circ}C/W$
$P_d = 0.4W$ | 5. EN Voltage:
$V_{en} = 2V$
$V_{sd} = 0.4V$
6. Power OK Voltage:
$V_{pokth} = 92\% \cdot V_{out}$
$V_{pokhys} = 8\%$
7. Inrush current:
$T_{ss} = 400\mu s$
$C_{total} = 10\mu F$
$I_{inrush} = 82.5mA$
8. FB Voltage:
$V_{FB} = 0.8V$ |
|--|--|

Power stage

1. I/P Current:

$$I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 0.4A$$

2. Ripple Current:

$$I_{rip} = 0.52A$$

$$I_{spec} = 2.5A \times 1 \text{ pcs}$$

3. Dynamic:

$$I_{peak} = 2A$$

$$ESR = 18 \text{ mohm}$$

$$\Delta V = 36mV$$

4. Inductor Spec:

$$I_{sat} = 14A$$

$$I_{dc} = 8A$$

$$DCR = 18 \text{ mohm}$$

5. MOSFET Spec:

H-side MOSFET: AP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5V)$$

$$I_{cont} = 9.6A \quad (T = 25^{\circ}C)$$

$$I_{peak} = 40A \quad (\text{Pause} < u s)$$

L-side MOSFET: RAP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5V)$$

$$I_{cont} = 9.6A \quad (T = 25^{\circ}C)$$

$$I_{peak} = 40A \quad (\text{Pause} < u s)$$

Controller

1. Voltage & Current:

$$+0.89VS @ 2A$$

2. Frequency:

$$f_{osc} = 251KHz$$

3. OCP:

$$\text{Set } PR164 = 10Kohm$$

$$I_{ocp} = 9.5A$$

4. POR:

$$\text{POR Hysteresis} = 0.2V$$

$$V_{on} = 3.9V$$

5. UVP:

$$V_{uvp} = 70\% V_{out}$$

6. OVP:

$$V_{ovp} = 115\% V_{out}$$

7. Enable Voltage:

$$V = 2.9V$$

8. Soft start time:

$$T_{ss} = 1.2ms$$

9. Phase selection:

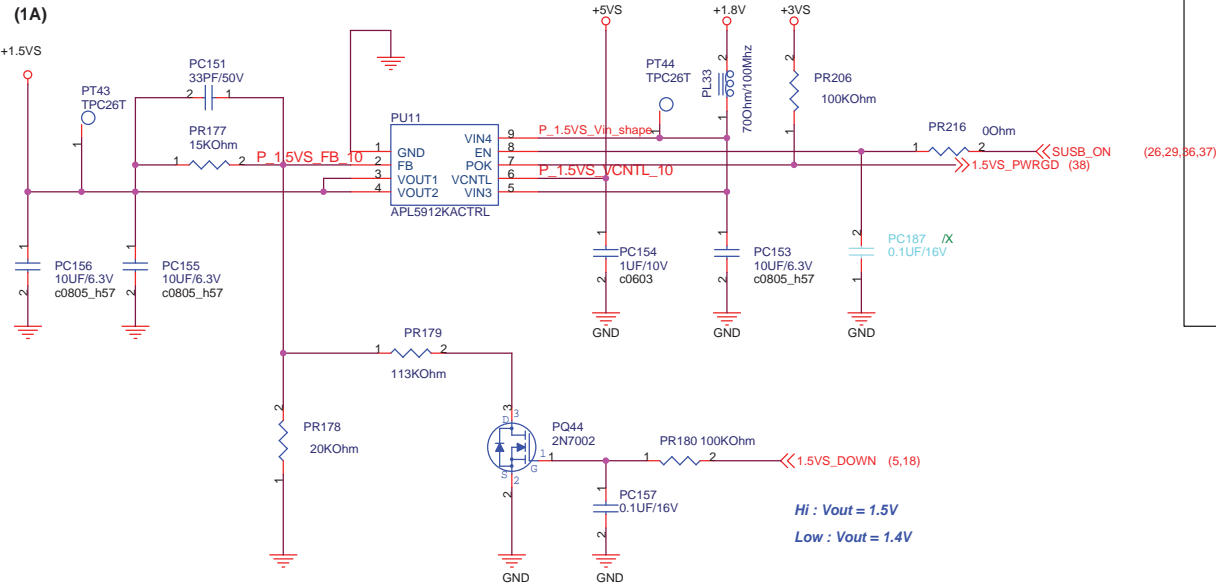
$$/X$$

10. Inrush Current:

$$C_{total} = 100 \mu F$$

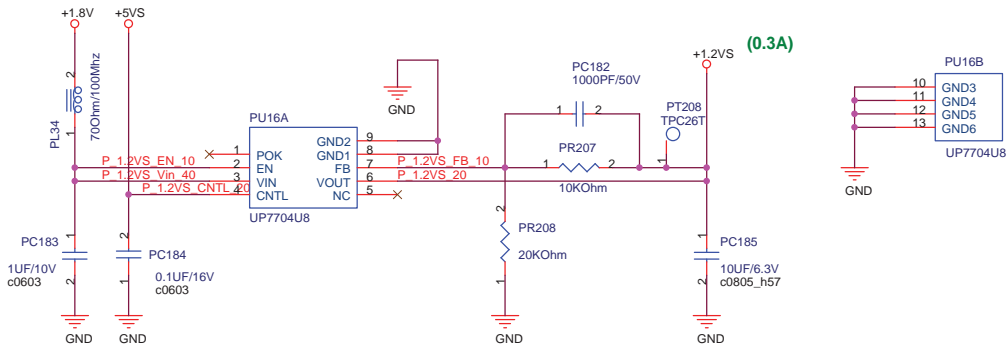
$$I_{inrush} = 0.074A$$

<Variant Name>



- Dropout Voltage:
 $\Delta V = 0.2V$ ($I_o = 5A$)
- Current Limit:
 $I_{limit} = 7A$
- Continue Current:
 $I_{cont} = 6A$
- Power Dissipation:
 $R_{thjc} = 40^\circ C/W$
 $P_d = 3W$

- EN Voltage:
 $V_{rising} = 0.4V$
Hysteresis = 30mV
- Supply Voltage:
 $V_{cc} = 5V$
- Inrush current:
 $T_{ss} = 2ms$
 $C_{total} = 20uF$
 $I_{inrush} = 15mA$



1.2VS @ 0.3A

- Dropout Voltage:
 $\Delta V = 0.3V$ ($I_o = 1A$)
- OCP:
 $I_{ocp} = 1.5A$
- Short Circuit Current Limit:
 $I_{sc} = 300mA$
- Power Dissipation:
 $R_{thjc} = 75^\circ C/W$
 $P_d = 1.33W$

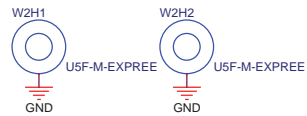
- EN Voltage:
 $V_{en} = 1.6V$
 $V_{sd} = 0.4V$
- Power OK Voltage:
 $V_{pokh} = 92\% \cdot V_{out}$
 $V_{pokhys} = 8\%$
- Inrush current:
 $T_{ss} = 5ms$
 $C_{total} = 10uF$
 $I_{inrush} = 2.4mA$
- FB Voltage:
 $V_{FB} = 0.8V$

<Variant Name>

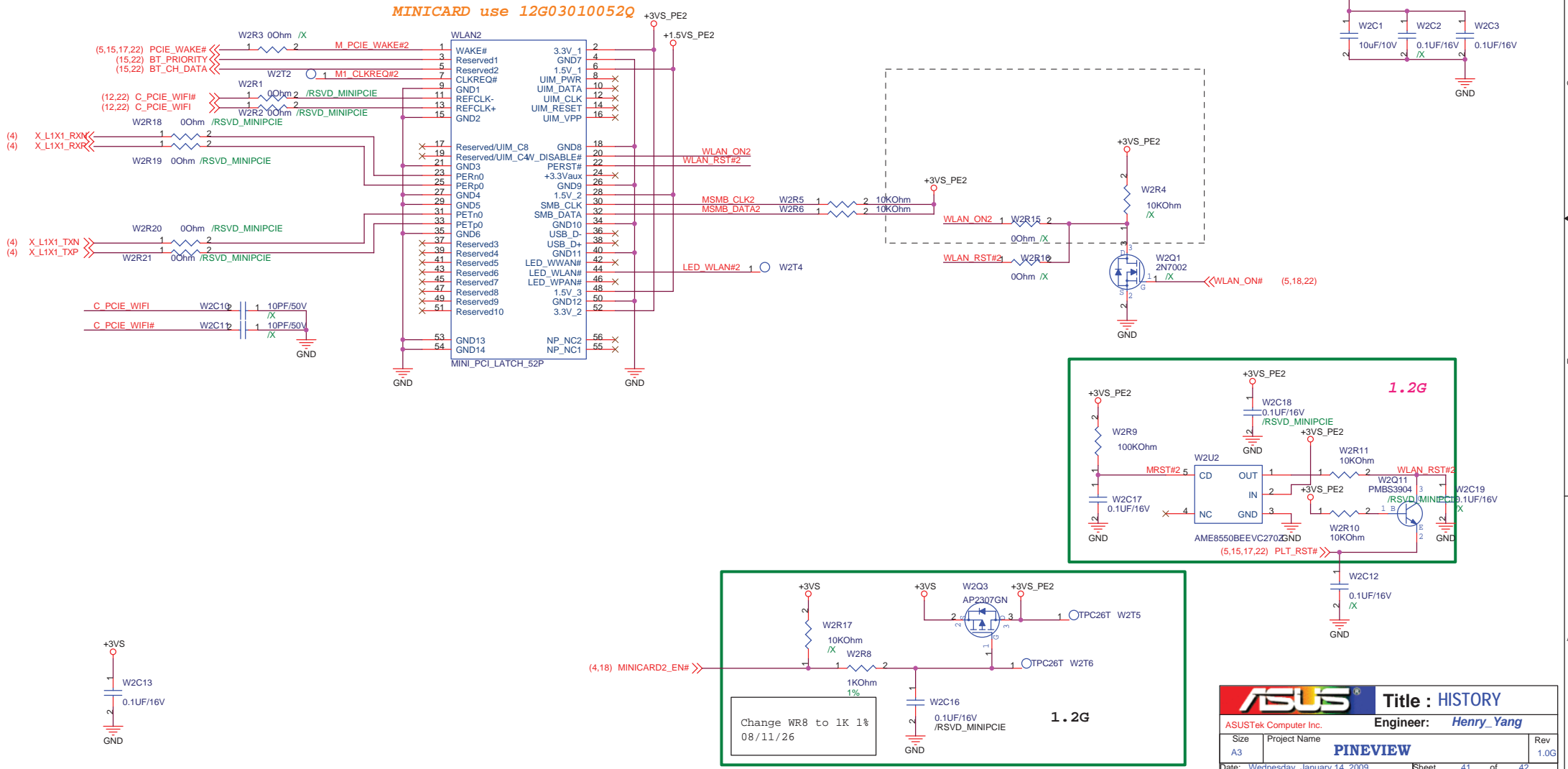
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>



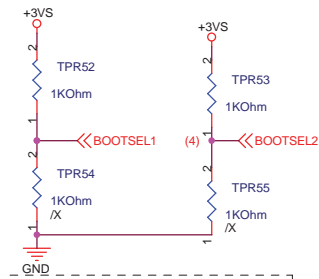
MINI CARD NUT(1.6mm) *2



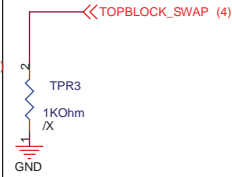
<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

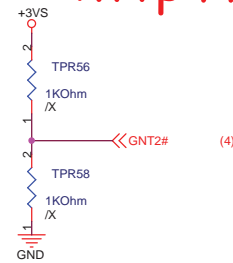
<http://hobi-elektronika.net>



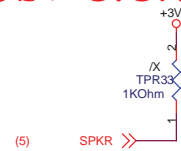
STRAP1#/GPIO48 STRAP2#/GPIO17
Routing
0 1: Flash Cycles Routed to SPI
1 0: Flash Cycles Routed to PCI
1 1: Flash Cycles Routed to LPC



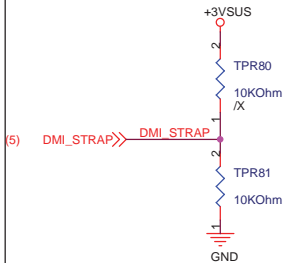
For strapping as
Top-block Swap
override.



Reserved, has a
weak internal
pull-up, cann't
be pulled low

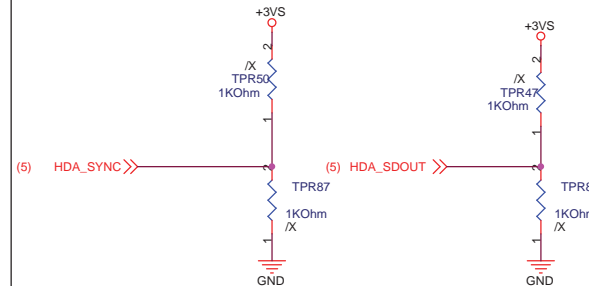


SPKR:For no reboot function.
Has internal pull-down.
If sample high ,it indicates that
the system is strapped to "no
reboot "mode.
(TPT will disable the TCO Timer
system reboot feature).



DMI strap.

1=DMI interface is strapped to
operate in DC coupled mode.
0=DMI interface is strapped to
operate in AC coupled mode.



Check EDS of TPT Section 10.1.30
for details.

HDA_SYNC(PCIE port
config bit 0):
Has weak internal
pull-down.

HDA_SDOUT(PCIE port config bit 1):
Has weak internal pull-down,if not
pulled
low at rising edge of PWROK,sets
bit 1 of RPC.PC.

From 1225.1900 change 3GC1 to 47u,modify screw to
13G021050010.

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>

<http://hobi-elektronika.net>